



CY8CNP101, CY8CNP102

February 2008

Silicon Errata for the nvPSoC CY8CNP101, CY8CNP102 Product Family

This document describes the errata for the nvPSoC, CY8CNP101, CY8CNP102. Details include errata trigger conditions, scope of impact, available workarounds, and silicon revision applicability. Compare this document to the device's data sheet for a complete functional description.

Contact your local Cypress Sales Representative if you have questions.

Part Numbers Affected

Part Number	Device Characteristics
CY8CNP101	All Packages
CY8CNP102	All Packages

nvPSoC Qualification Status

Engineer Sampling

nvPSoC Errata Summary

The following table defines the errata applicability to available nvPSoC family devices. An "X" indicates that the errata pertains to the selected device.

Note Errata items, in the table below, are hyperlinked. Click on any item entry to jump to its description.

Items	CY8CNP101, CY8CNP102	Fix Status
[1]. Standby Current	X	Addressed before the product family enters full production.
[2]. VCAP	X	Addressed before the product family enters full production.
[3]. Test at Room Temperature	X	Addressed before the product family enters full production.

1. Standby Current

• **PROBLEM DEFINITION**

Engineering samples of the nvPSoC CY8CNP101, CY8CNP102 product family do not meet the data sheet standby current specifications of 3 mA. The standby current will be a maximum of 15 mA.

• **TRIGGER CONDITION(S)**

NA

• **SCOPE OF IMPACT**

NA

• **WORKAROUND**

NA

• **FIX STATUS**

This issue will be addressed before the nvPSoC CY8CNP101, CY8CNP102 product family enters full production.

2. VCAP

• **PROBLEM DEFINITION**

Engineering samples of the nvPSoC CY8CNP101, CY8CNP102 product family do not meet the data sheet specifications. VCap: 68 μ F (maximum). The required Vcap is 200 μ F (minimum) to 400 μ F (maximum).

• **TRIGGER CONDITION(S)**

NA

• **SCOPE OF IMPACT**

Capacitor value needs to change.

• **WORKAROUND**

NA

• **FIX STATUS**

This issue will be addressed before the nvPSoC CY8CNP101, CY8CNP102 product family enters full production.

3. Test at Room Temperature

• **PROBLEM DEFINITION**

The data sheet specifications (AC/DC Electrical Specifications) for the nvPSoC CY8CNP101, CY8CNP102 product family are tested and guaranteed **only** at room temperature.

• **TRIGGER CONDITION(S)**

NA

• **SCOPE OF IMPACT**

NA

• **WORKAROUND**

NA

• **FIX STATUS**

This issue will be addressed before the nvPSoC CY8CNP101, CY8CNP102 product family enters full production.



Document History Page

Document Title: Silicon Errata for the nvPSoC CY8CNP101, CY8CNP102 Product Family				
Document Number: 001-44873				
Rev.	ECN No.	Issue Date	Orig. of Change	Description of Change
**	2397787	04/17/2008	GVCH/HMT	New spec.

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