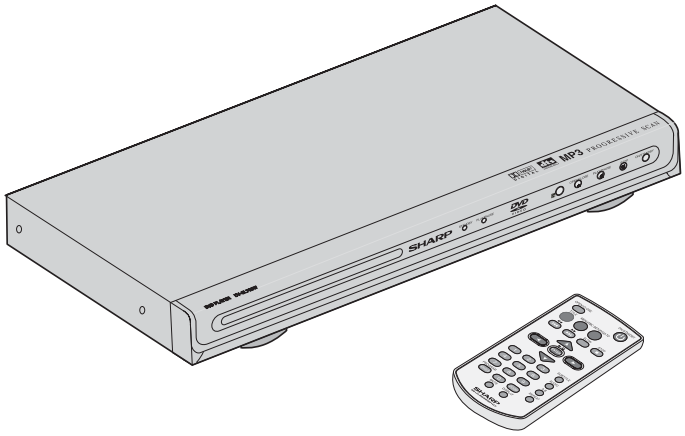
SHARP SERVICE MANUAL

No. S9501DVSL700W



DVD PLAYER

MODEL DV-SL700W

• In the interests of user-safety (Required by safety regulations in some countries) the set should be restored to its original condition and only parts identical to those specified be used.

CONTENTS

Parts marked with "⚠" are important for maintaining the safety of the set. Be sure to replace these parts with specified ones for maintaining the safety and performance of the set.

SHARP CORPORATION

This document has been published to be used for after sales service only.
The contents are subject to change without notice.

SAFETY PRECAUTION FOR SERVICE MANUAL

● This product is classified as a CLASS 1 LASER PRODUCT.

● **WARNINGS:**

The AEL (Accessible Emission Level) of Laser Power Output for this model is specified to be lower than Class 1 Requirements. However, the following precautions must be observed during servicing to protect your eyes against exposure to the Laser beam.

(1) When the cabinet has been removed, the power is turned on without a compact disc, and the Pickup is on a position outer than the lead-in position, the Laser will light for several seconds to detect a disc. Do not look into the Pickup Lens.

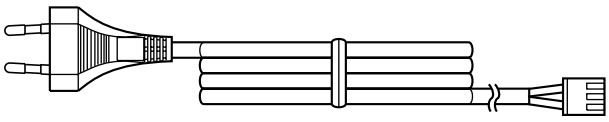
(2) The Laser Power Output of the Pickup inside the unit and replacement service parts have already been adjusted prior to shipping.

(3) No adjustment to the Laser Power should be attempted when replacing or servicing the Pickup.

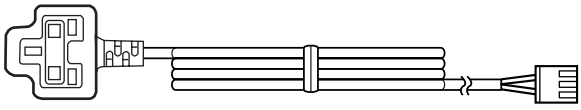
(4) Under no circumstances look directly into the Pickup Lens at any time.

(5) CAUTION - Use of controls or adjustments, or performance of procedures other than those specified herein may result in hazardous radiation exposure.

AC POWER SUPPLY CORD



92LVPE00327911



92LVPE41A25310

SPECIFICATIONS

As part of our policy of continuous improvement, SHARP reserves the right to make design and specification changes for product improvement without prior notice. The performance specification figures indicated are nominal values of production units. There may be some deviations from these values in individual units.

● **General**

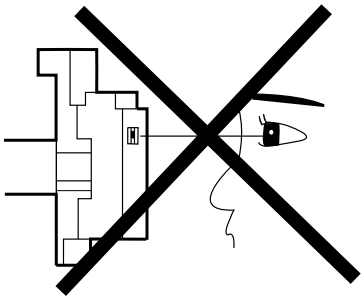
Power source	AC110 - 240 V, 50/60 Hz
Power consumption	Power on: 15 W
Dimensions	Width: 360 mm (14-3/8”) Height: 37 mm (1-1/2”) Depth: 200 mm (7-7/8”)
Weight	1.5 kg (3.3 lbs)
Audio output terminals	Analogue output (OUT): RCA type x 1 pair (L/R)
Video output terminals	S-video output: S-terminal x 1, component (Y, P_B,P_R) Video output: RCA type x 1
Digital output terminal	COAXIAL

● **DVD Player**

Signal system	PAL/NTSC colour	
Supported disc types	DVD (with the same region number on the back of the unit or ALL), SVCD, VCD, audio CD, CD-R, CD-RW, MP3, JPEG	
Video signal	Horizontal resolution: 500 lines S/N ratio: 70dB	
Audio signal	**Frequency characteristics**	Linear PCM DVD: 20 Hz to 20 kHz (sampling rate: 48 kHz) 20 Hz to 20 kHz (sampling rate: 96 kHz) CD: 20 Hz to 20 kHz
	S/N ratio	CD: 94 dB (1 kHz)
	Dynamic range	Linear PCM DVD: 95 dB CD: 94 dB
	Total harmonic distortion ratio	0.01% maximum

LASER BEAM SAFETY PRECAUTIONS

This DVD player uses a pickup that emits a laser beam.

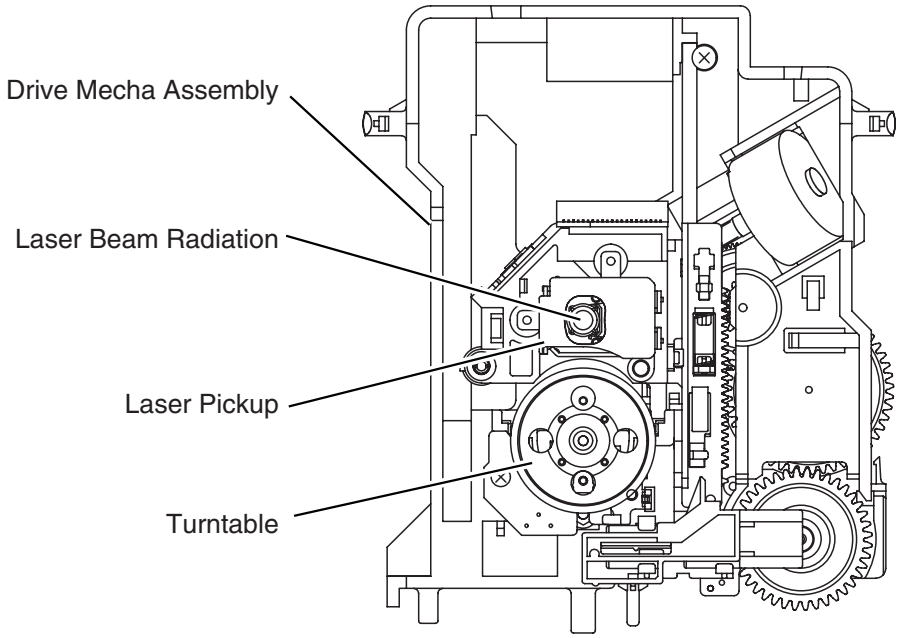


Do not look directly at the laser beam coming from the pickup or allow it to strike against your skin.

The laser beam is emitted from the location shown in the figure. When checking the laser diode, be sure to keep your eyes at least 30cm away from the pickup lens when the diode is turned on. Do not look directly at the laser beam.

The laser beam is emitted from the location shown in the figure. When checking the laser diode, be sure to keep your eyes at least 30cm away from the pickup lens when the diode is turned on. Do not look directly at the laser beam.

Caution: Use of controls and adjustments, or doing procedures other than those specified herein, may result in hazardous radiation exposure.



STANDARD NOTES FOR SERVICING

1. Removal

With Hot-Air Flat Pack-IC Desoldering Machine:

(1) Prepare the hot-air flat pack-IC desoldering machine, then apply hot air to the Flat Pack-IC (about 5 to 6 seconds). (Fig. S-1-1)

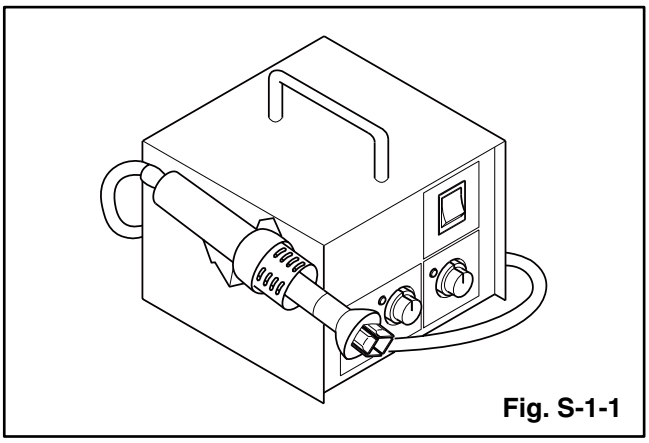


Fig. S-1-1

(2) Remove the flat pack-IC with tweezers while applying the hot air.

(3) Bottom of the flat pack-IC is fixed with glue to the CBA; when removing entire flat pack-IC, first apply soldering iron to center of the flat pack-IC and heat up. Then remove (glue will be melted). (Fig. S-1-6)

(4) Release the flat pack-IC from the CBA using tweezers. (Fig. S-1-6)

Caution:

1. Do not supply hot air to the chip parts around the flat pack-IC for over 6 seconds because damage to the chip parts may occur. Put masking tape around the flat pack-IC to protect other parts from damage. (Fig. S-1-2)
2. The flat pack-IC on the CBA is affixed with glue, so be careful not to break or damage the foil of each pin or the solder lands under the IC when removing it.

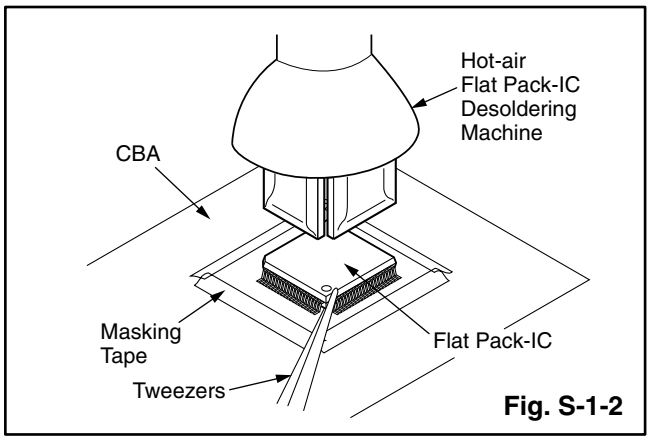


Fig. S-1-2

(1) Using desoldering braid, remove the solder from all pins of the flat pack-IC. When you use solder flux which is applied to all pins of the flat pack-IC, you can remove it easily. (Fig. S-1-3)

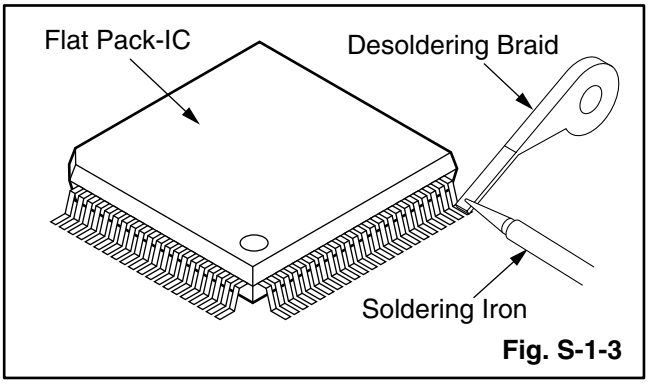


Fig. S-1-3

(2) Lift each lead of the flat pack-IC upward one by one, using a sharp pin or wire to which solder will not adhere (iron wire). When heating the pins, use a fine tip soldering iron or a hot air desoldering machine. (Fig. S-1-4)

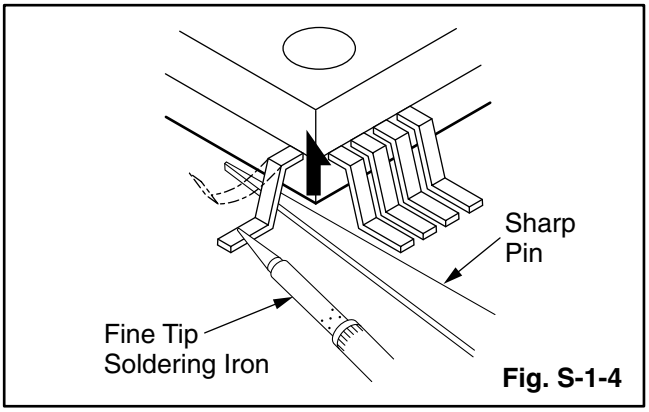


Fig. S-1-4

(3) Bottom of the flat pack-IC is fixed with glue to the CBA; when removing entire flat pack-IC, first apply soldering iron to center of the flat pack-IC and heat up. Then remove (glue will be melted). (Fig. S-1-6)

(4) Release the flat pack-IC from the CBA using tweezers. (Fig. S-1-6)

With Iron Wire:

(1) Using desoldering braid, remove the solder from all pins of the flat pack-IC. When you use solder flux which is applied to all pins of the flat pack-IC, you can remove it easily. (Fig. S-1-3)

(2) Affix the wire to a workbench or solid mounting point, as shown in Fig. S-1-5.

(3) While heating the pins using a fine tip soldering iron or hot air blower, pull up the wire as the solder melts so as to lift the IC leads from the CBA contact pads as shown in Fig. S-1-5.

(4) Bottom of the flat pack-IC is fixed with glue to the CBA; when removing entire flat pack-IC, first apply soldering iron to center of the flat pack-IC and heat up. Then remove (glue will be melted). (Fig. S-1-6)

(5) Release the flat pack-IC from the CBA using tweezers. (Fig. S-1-6)

Note:

When using a soldering iron, care must be taken to ensure that the flat pack-IC is not being held by glue. When the flat pack-IC is removed from the CBA, handle it gently because it may be damaged if force is applied.

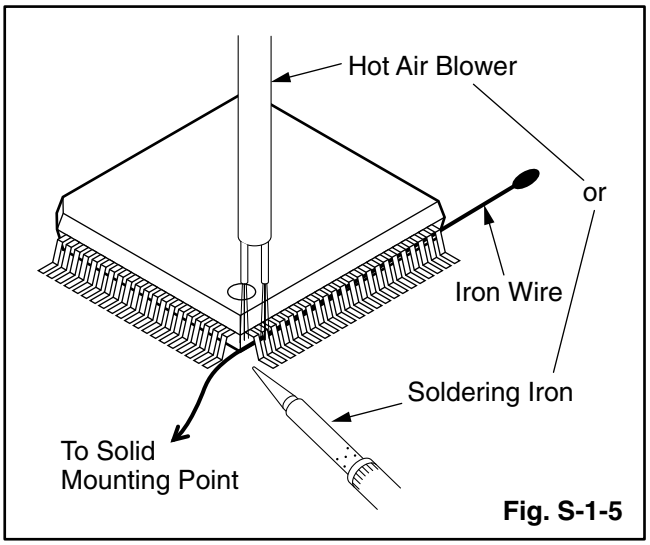


Fig. S-1-5

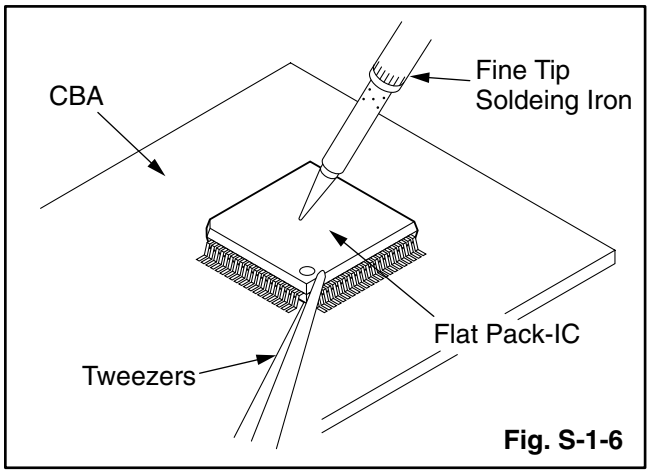


Fig. S-1-6

2. Installation

(1) Using desoldering braid, remove the solder from the foil of each pin of the flat pack-IC on the CBA so you can install a replacement flat pack-IC more easily.

(2) The "●" mark on the flat pack-IC indicates pin 1. (See Fig. S-1-7.) Be sure this mark matches the 1 on the PCB when positioning for installation. Then presolder the four corners of the flat pack-IC. (See Fig. S-1-8.)

(3) Solder all pins of the flat pack-IC. Be sure that none of the pins have solder bridges.

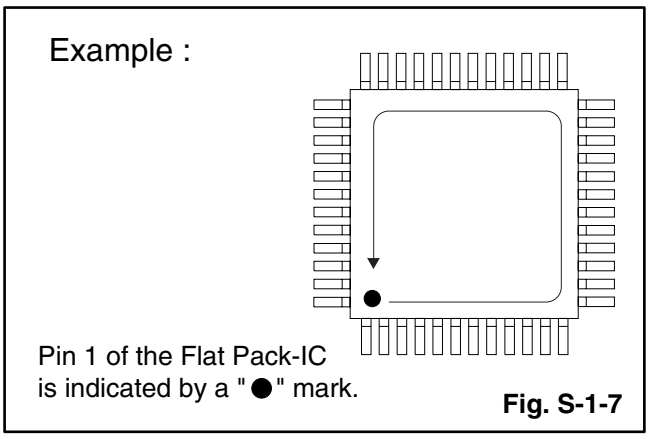


Fig. S-1-7

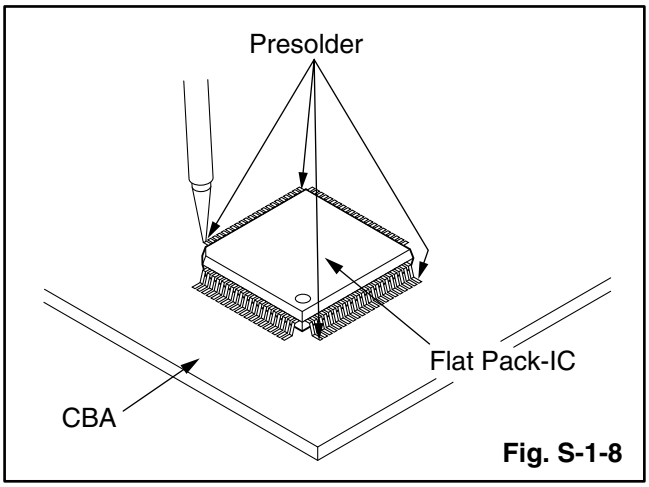


Fig. S-1-8

Instructions for Handling Semi-conductors

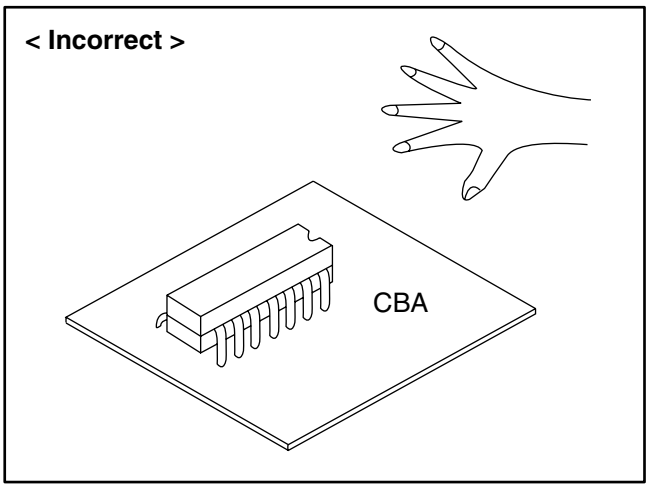
Electrostatic breakdown of the semi-conductors may occur due to a potential difference caused by electrostatic charge during unpacking or repair work.

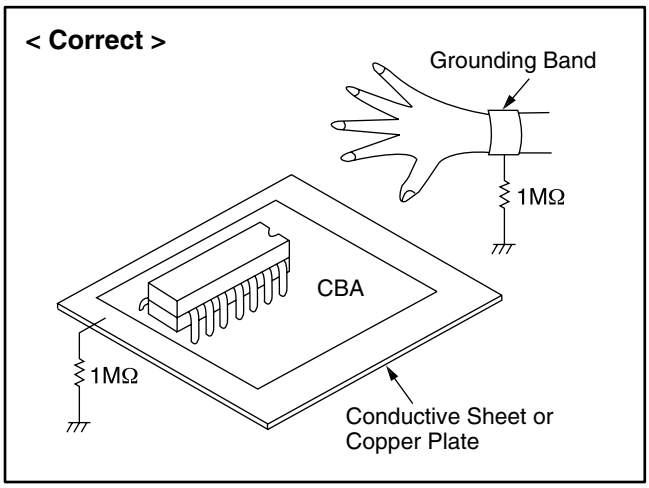
1. Ground for Human Body

Be sure to wear a grounding band (1MΩ) that is properly grounded to remove any static electricity that may be charged on the body.

2. Ground for Workbench

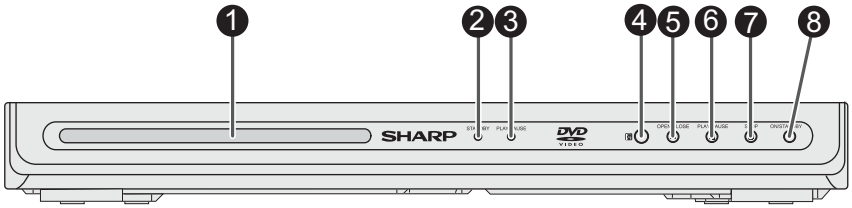
Be sure to place a conductive sheet or copper plate with proper grounding (1MΩ) on the workbench or other surface, where the semi-conductors are to be placed. Because the static electricity charge on clothing will not escape through the body grounding band, be careful to avoid contacting semi-conductors with your clothing.





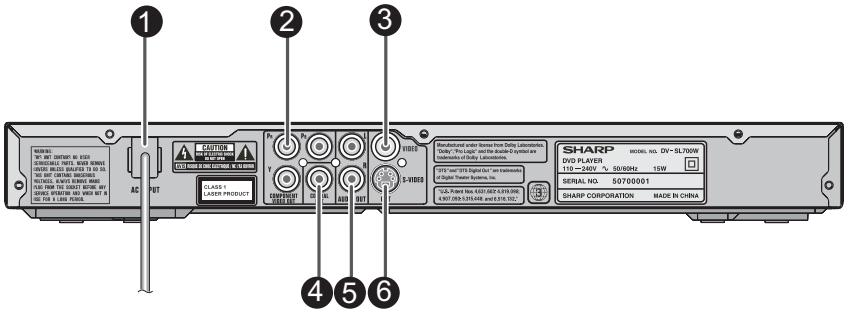
OPERATING CONTROLS AND FUNCTIONS

■ Main unit (front panel)



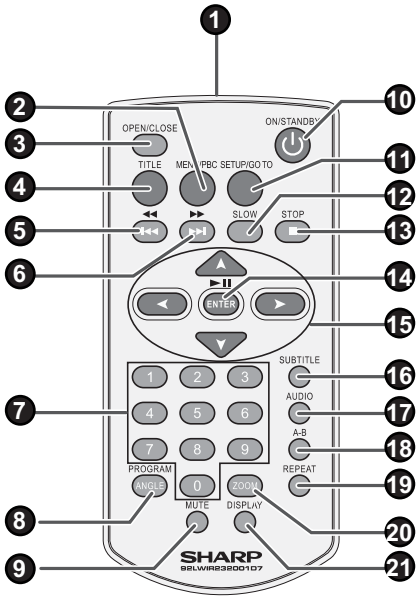
1. Disc Tray
2. STANDBY Indicator
3. PLAY/PAUSE Indicator
4. Remote Sensor
5. Disc Tray OPEN/CLOSE Button
6. Play or Pause Button
7. STOP Button
8. ON/STANDBY

■ Main unit (rear panel)



1. AC Cord Lead
2. Component Video Output Sockets
3. Video Output Socket
4. Digital Output Socket(Coaxial)
5. Audio Output Sockets
6. S-Video Output Socket

■ Remote control



1. Remote Control Transmitter
2. MENU/PBC Button
3. Disc Tray Open/ Close Button
4. Title Select Button
5. Fast Backward/Chapter (track) skip down
6. Fast Forward/Chapter (track) skip up
7. Direct Number Buttons
8. PROGRAM / ANGLE Button
9. MUTE Button
10. ON/STANDBY Button
11. SETUP/GO TO Button
12. SLOW Button
13. STOP Button
14. ENTER / PLAY / PAUSE Button
15. Cursor Up and Down Buttons
16. Subtitle Select Button
17. Audio Select Button
18. Repeat A - B Button
19. Repeat Play Button
20. ZOOM Button
21. DISPLAY Button

BLOCK DIAGRAMS

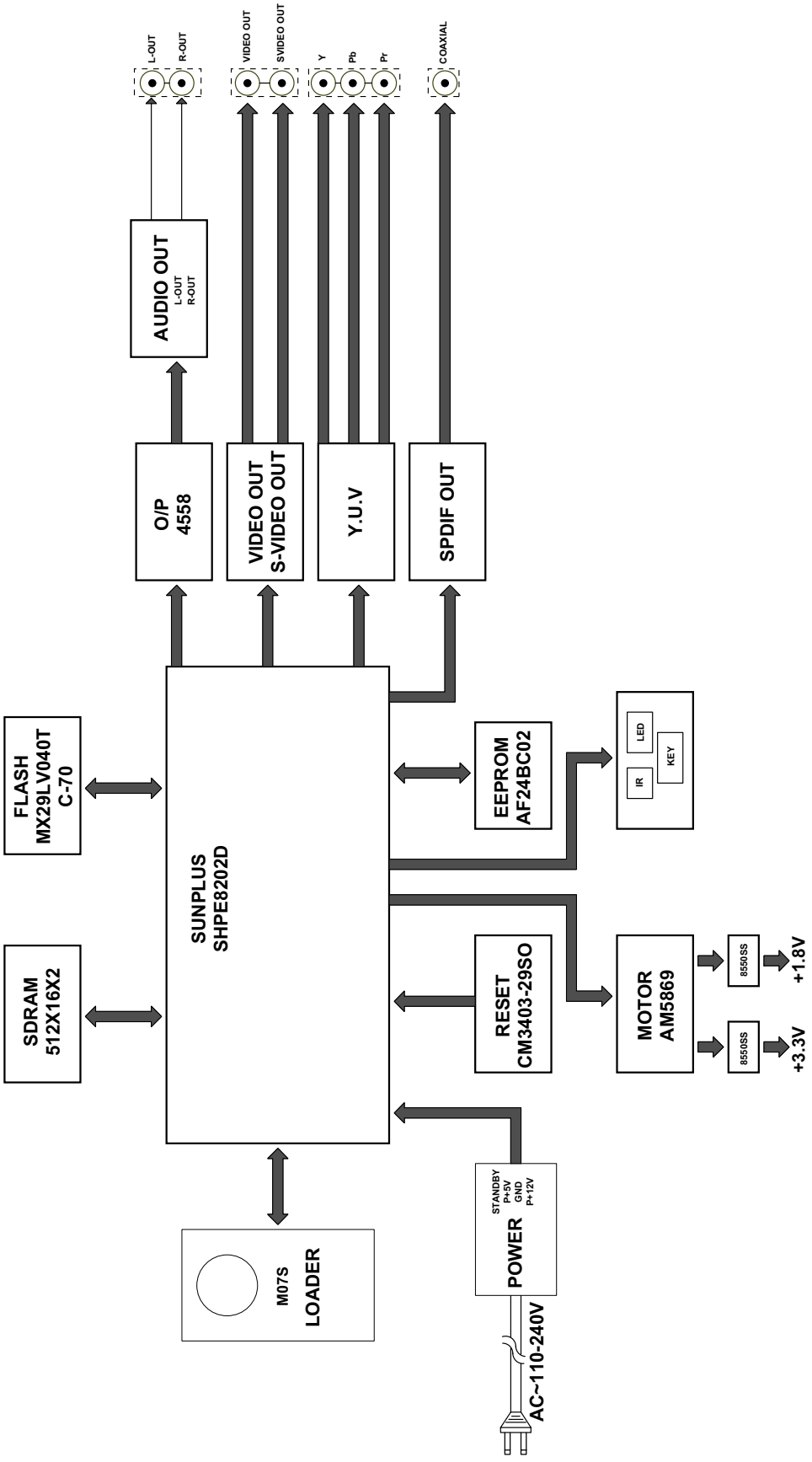


Figure 1: BLOCK DIAGRAM

SCHEMATIC DIAGRAMS

CONTROL PCB SCHEMATIC

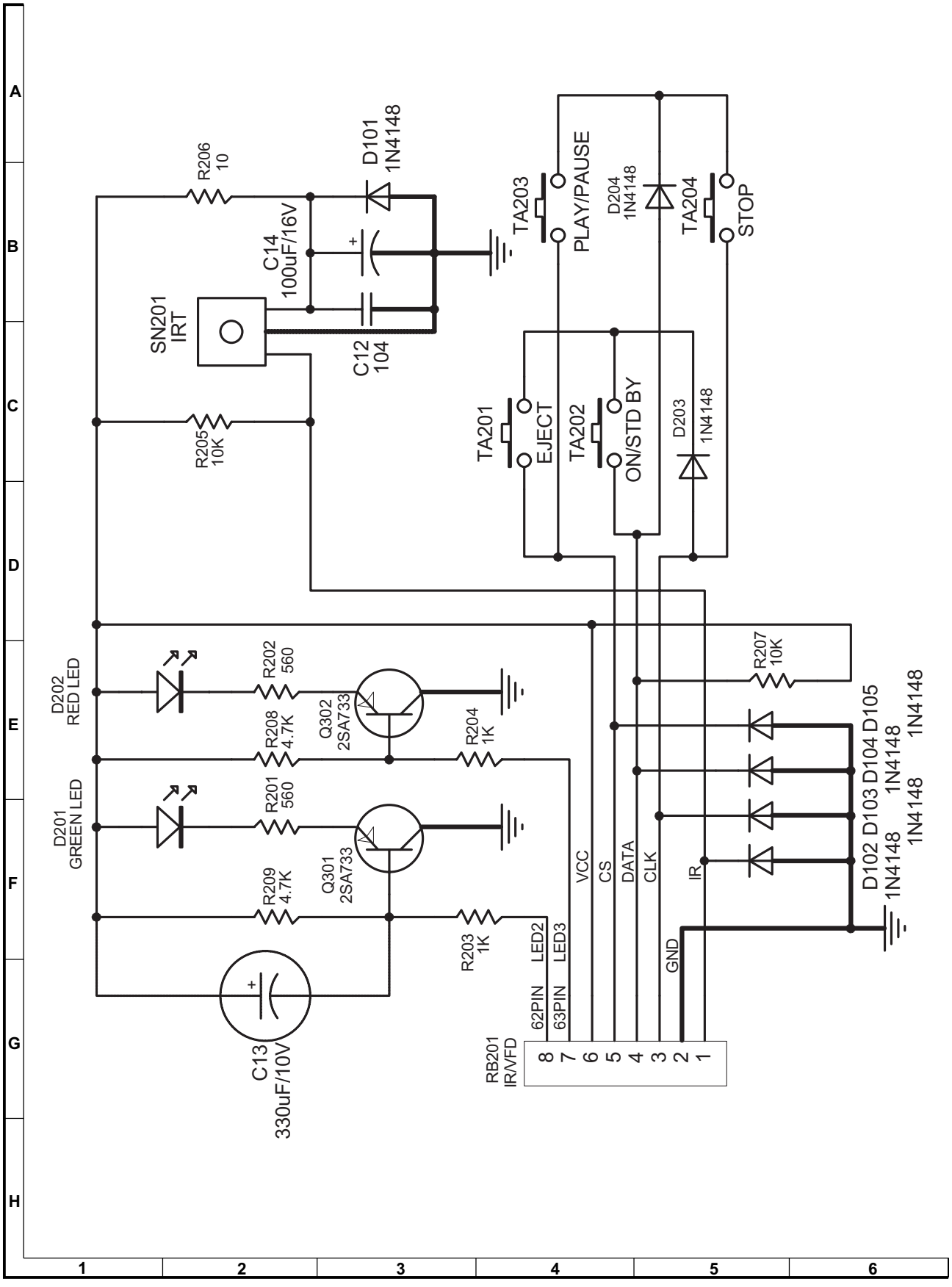


Figure 1: CONTROL PCB SCHEMATIC

MAIN PCB SCHEMATIC

Pickup head parameter

						Motor Driver AM5668				Motor Driver AT5660			
	R40	R39	R14	R15	R53	R62	C32	R63	C33	R62	C32	R63	C33
Sony	4.7	4.7	100	100	20K	12K	1500P	68K	1000P	3.3K	2700P	39K	1000P
Sanyo	5.1	5.1	0	0	2.2K	33K	1000P	68K	1000P	20K	1000P	39K	1000P
Samsung	5.1	5.1	0	0	2.2K	12K	1500P	68K	1000P	3.3K	2700P	39K	1000P
Mitsumi	5.1	5.1	0	0	2.2K	12K	1500P	68K	1000P	3.3K	2700P	39K	1000P

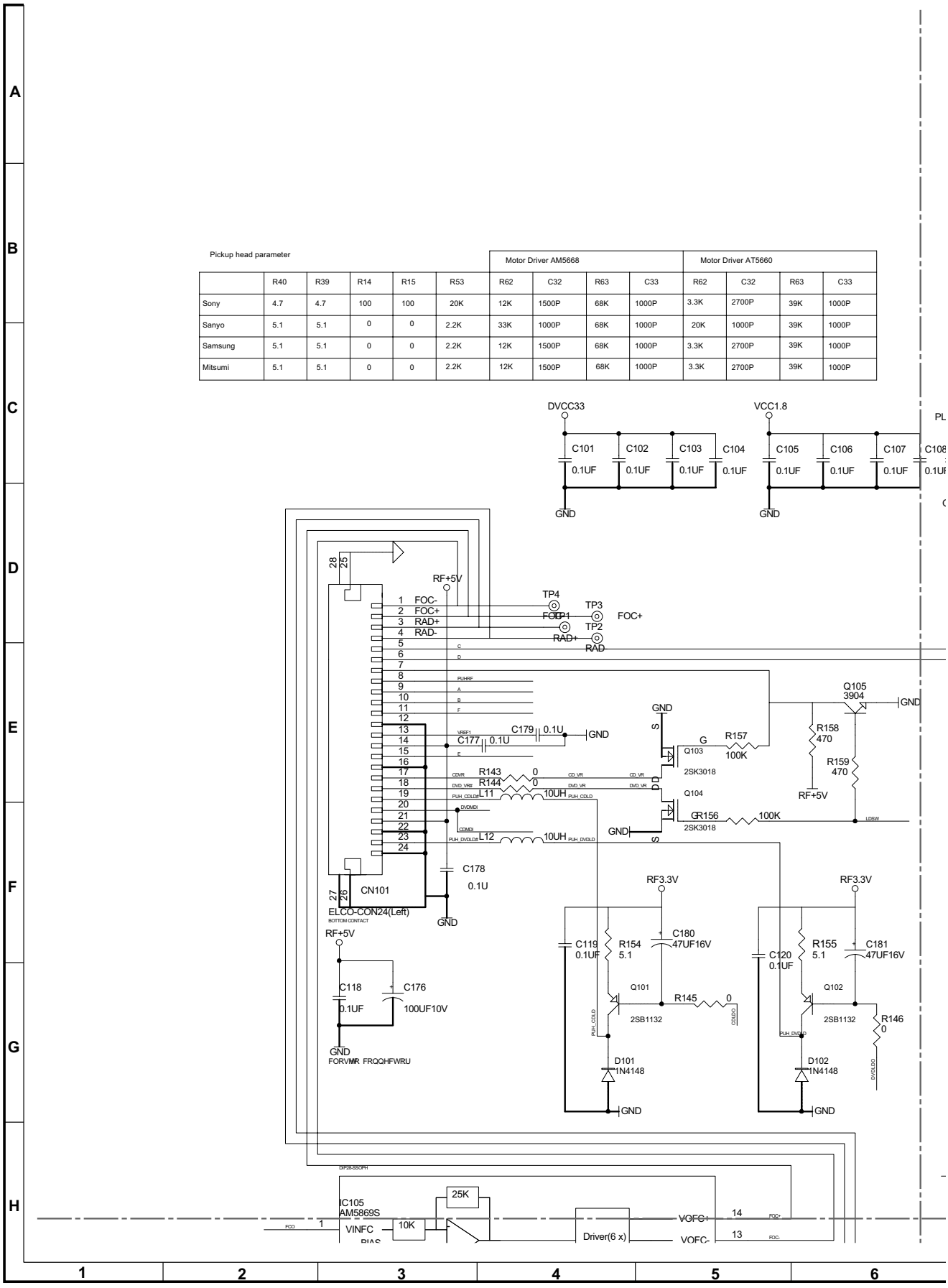


Figure 2: MAIN PCB SCHEMATIC (1/8)

MAIN PCB SCHEMATIC

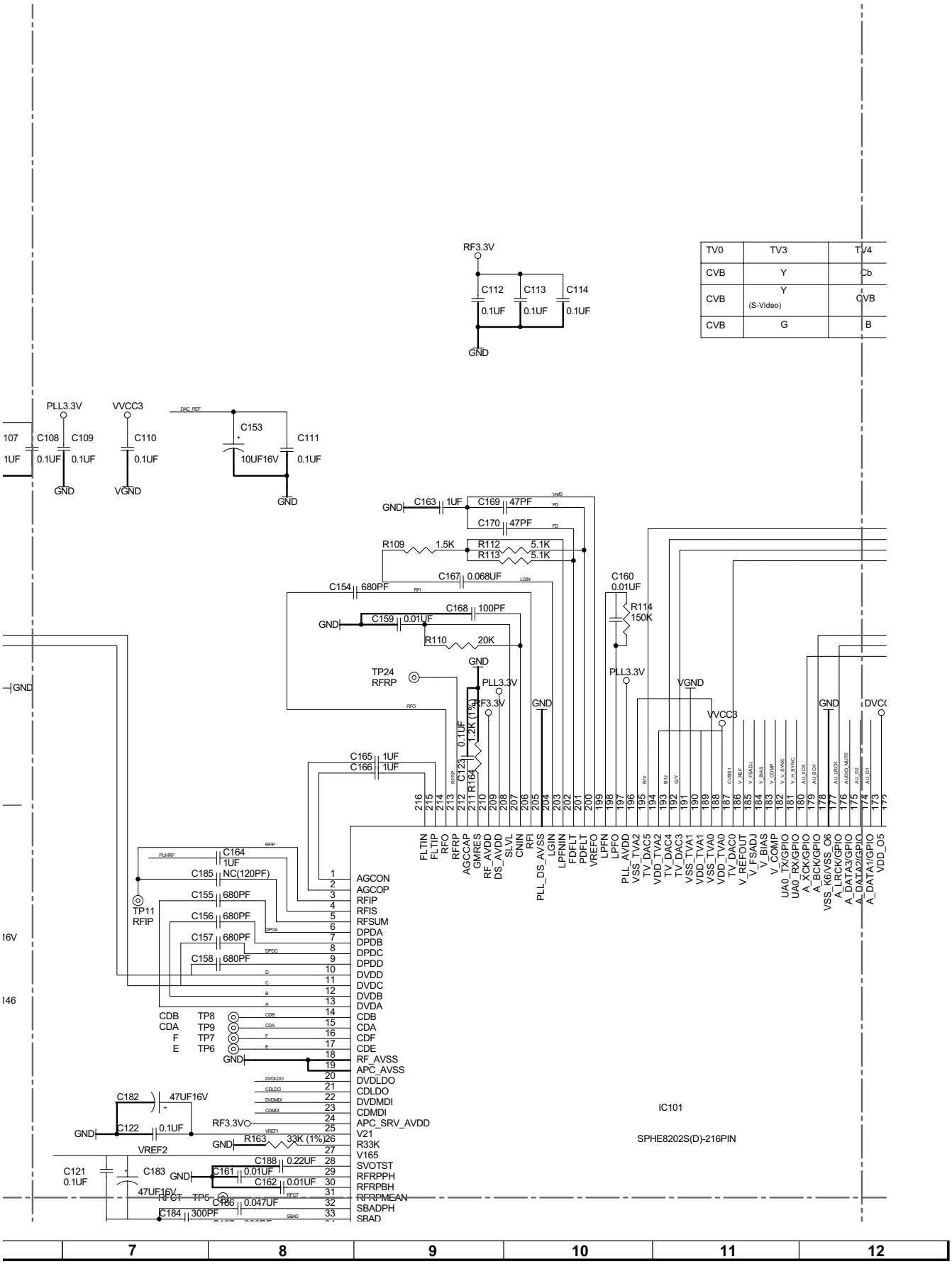


Figure 2: MAIN PCB SCHEMATIC (2/8)

MAIN PCB SCHEMATIC

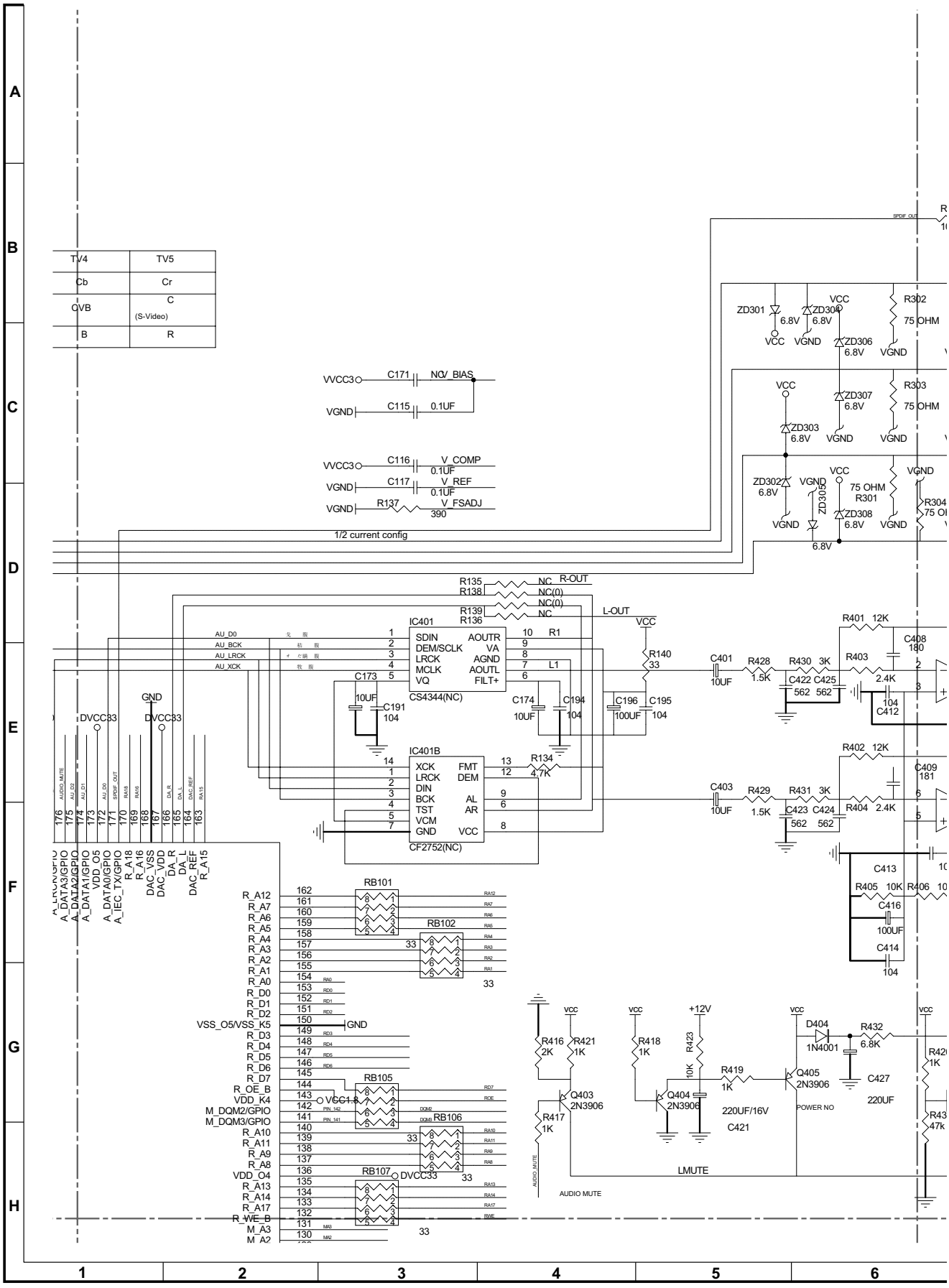
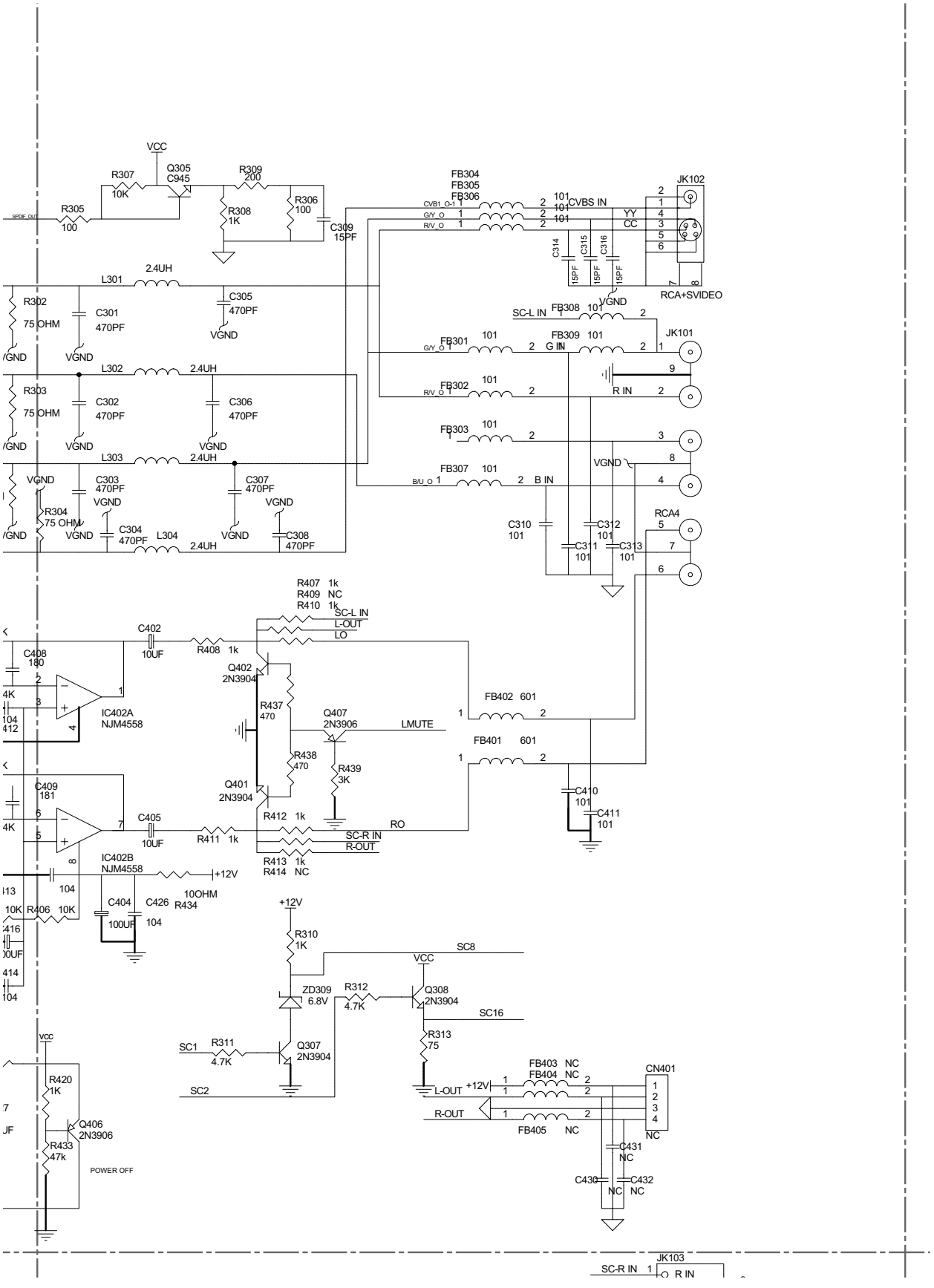


Figure 2: MAIN PCB SCHEMATIC (3/8)

MAIN PCB SCHEMATIC



	7	8	9	10	11	12

Figure 2: MAIN PCB SCHEMATIC (4/8)

MAIN PCB SCHEMATIC

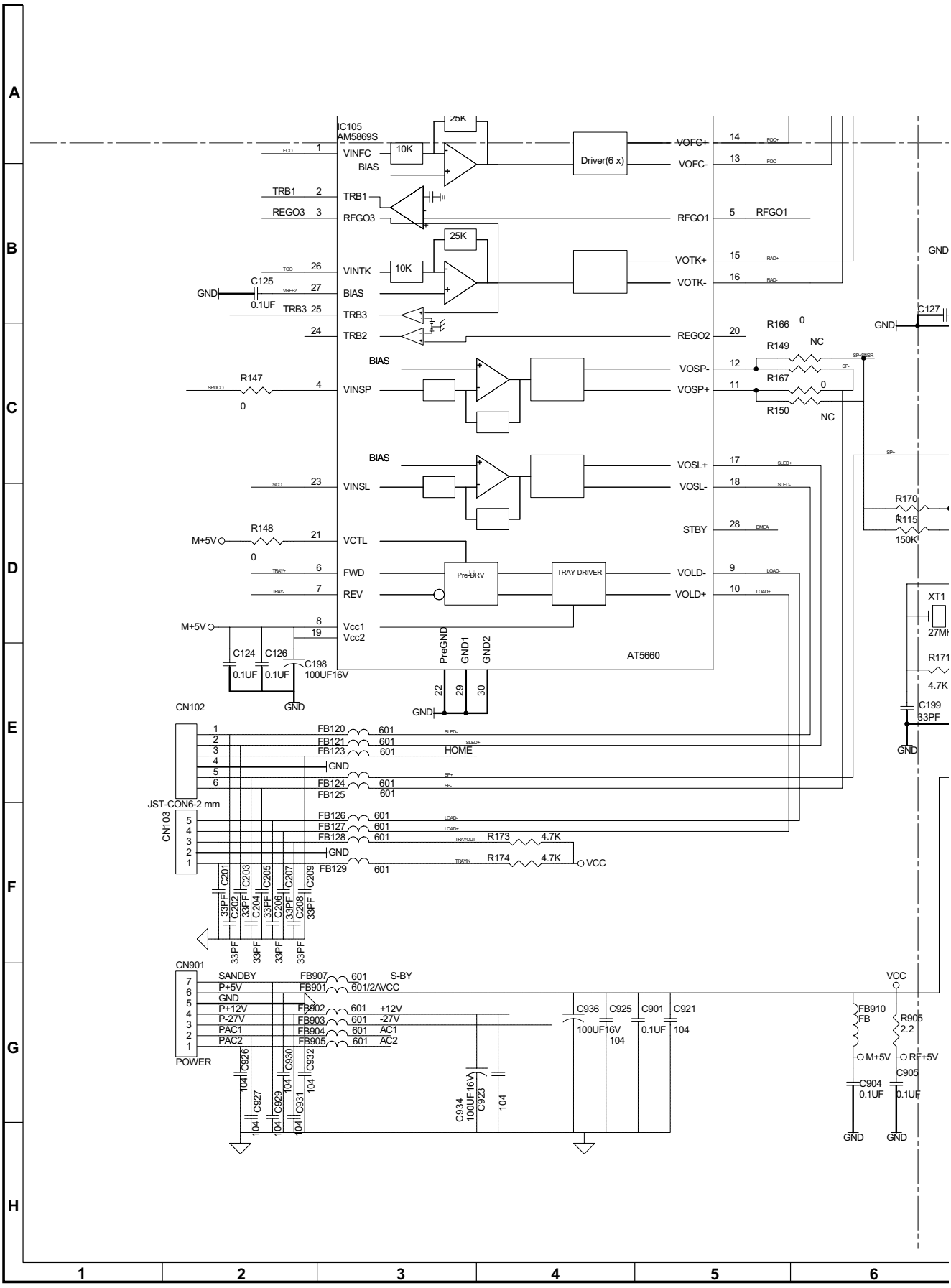
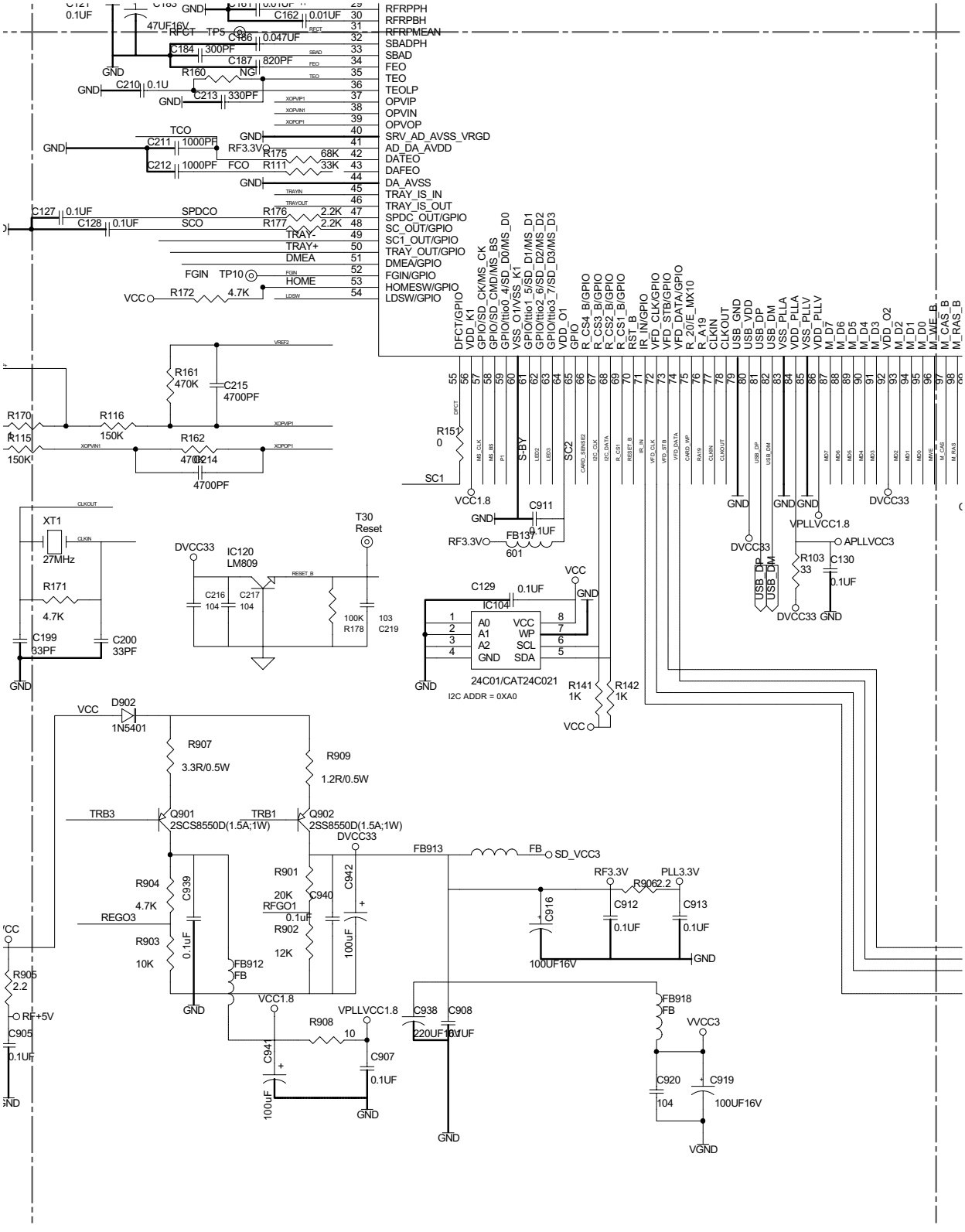


Figure 2: MAIN PCB SCHEMATIC (5/8)

MAIN PCB SCHEMATIC



7	8	9	10	11	12

Figure 2: MAIN PCB SCHEMATIC (6/8)

MAIN PCB SCHEMATIC

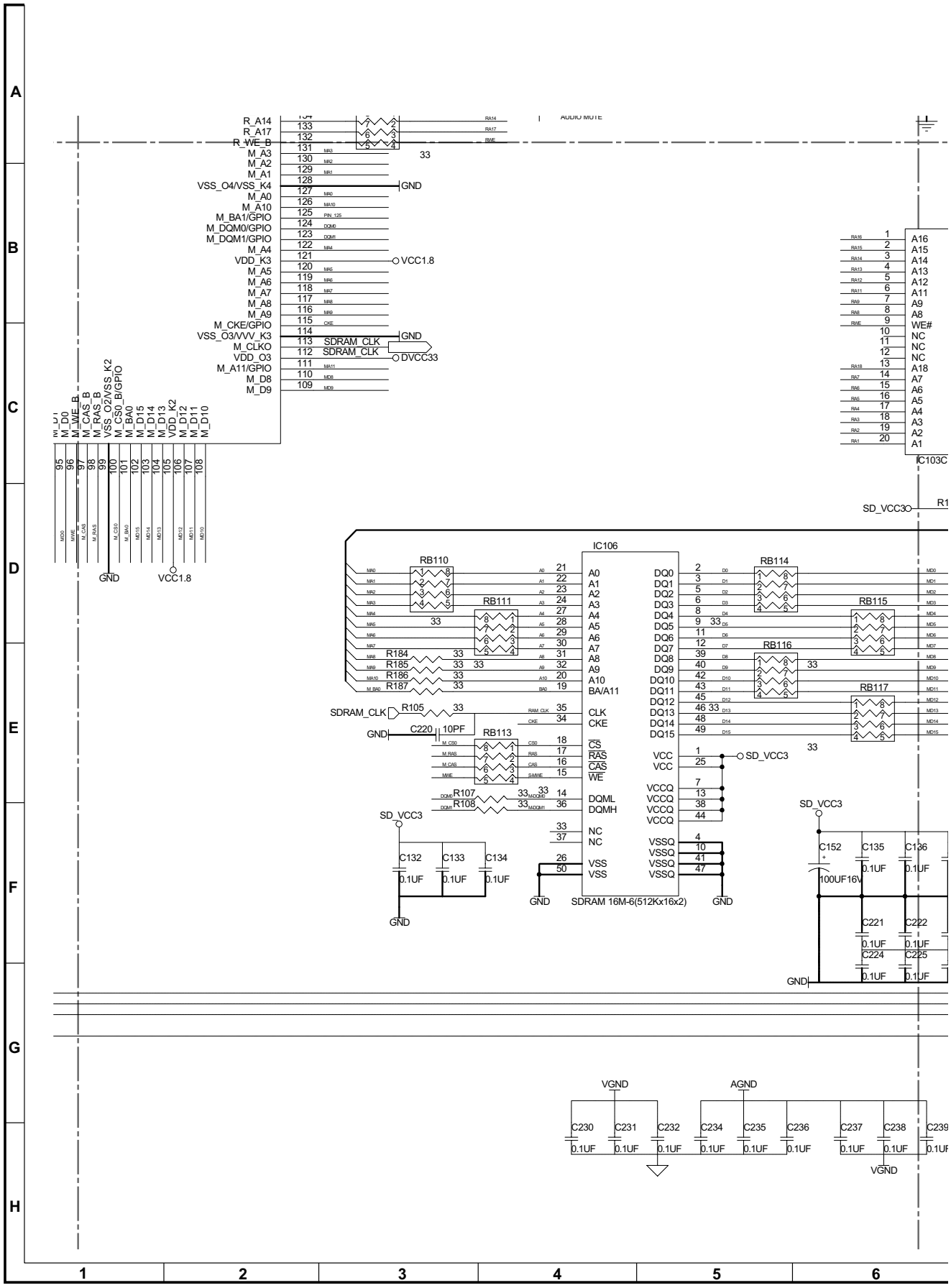


Figure 2: MAIN PCB SCHEMATIC (7/8)

MAIN PCB SCHEMATIC

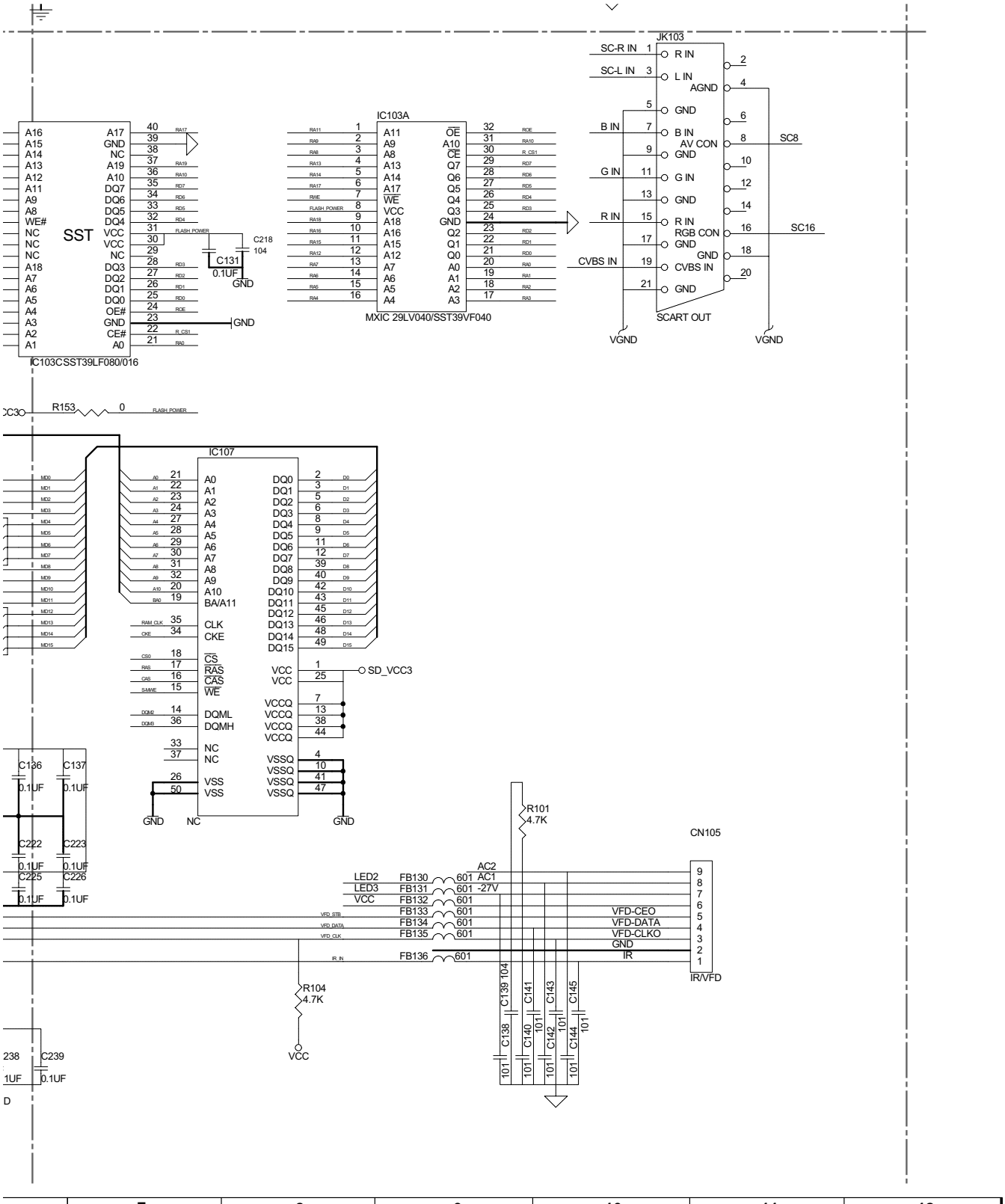


Figure 2: MAIN PCB SCHEMATIC (8/8)

POWER PCB SCHEMATIC

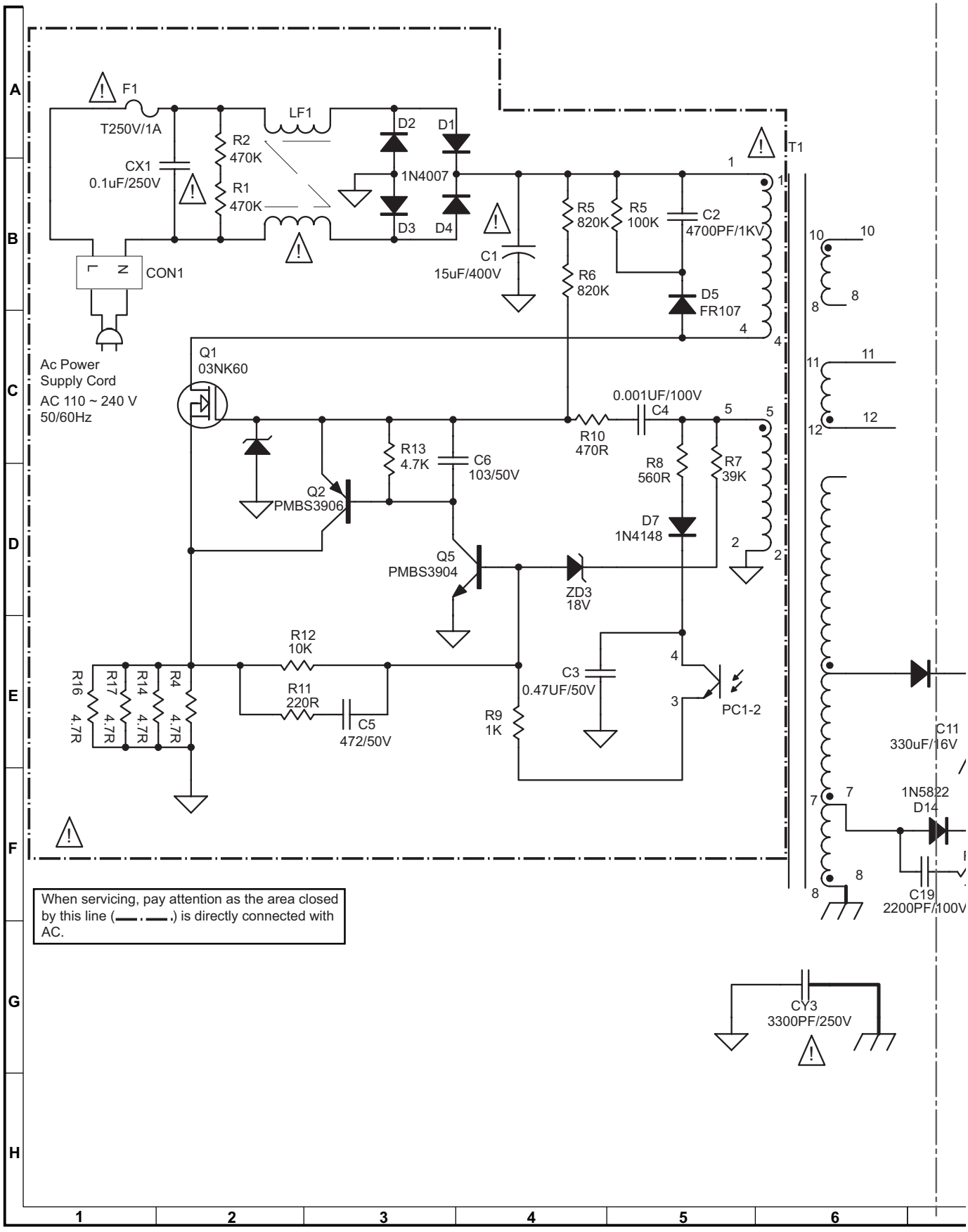


Figure 3: POWER PCB SCHEMATIC (1/2)

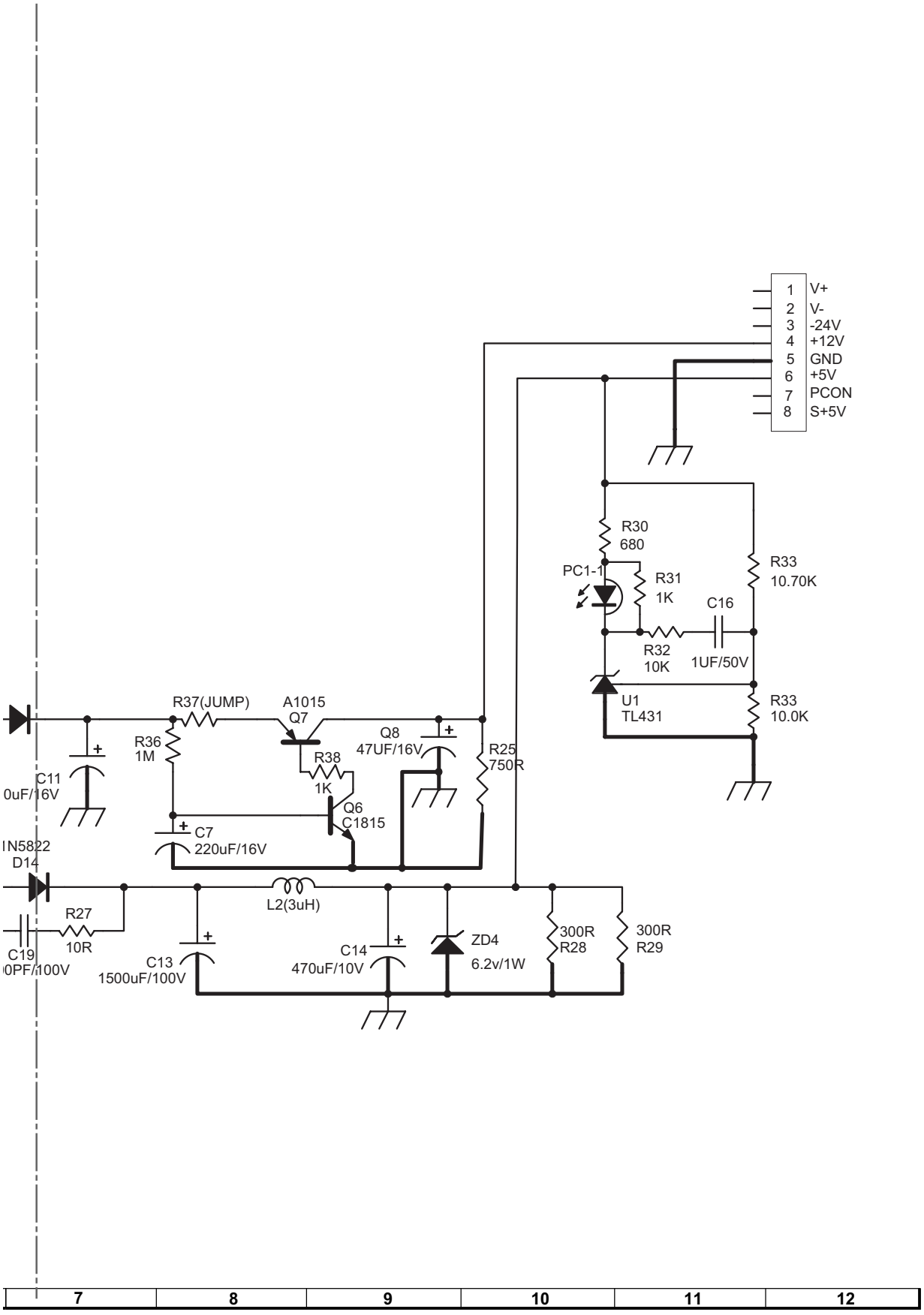


Figure 3: POWER PCB SCHEMATIC (2/2)

IC'S PIN VOLTAGE CHART

IC101(SPCA8202D)

Ref.number / Mode	1	2	3	4	5	6	7	8	9	10	11	12
play	0.94	0.95	0.42	2.09	0.95	2.1	2.1	2.1	2.1	2.2	2.2	2.2
stop	0.94	0.95	0.44	2.09	0.95	2.1	2.1	2.1	2.1	2.2	2.2	2.2
	13	14	15	16	17	18	19	20	21	22	23	24
play	2.47	1.5	1.5	2.45	1.7	0	0	3.3	2.46	0.19	0.19	3.3
stop	2.47	1.5	1.5	2.45	1.7	0	0	3.3	2.46	0.19	0.19	3.3
	25	26	27	28	29	30	31	32	33	34	35	36
play	2.1	1.67	1.7	1.53	1.95	1.95	1.64	2.02	1.98	1.75	1.86	1.86
stop	2.1	1.68	1.7	1.53	1.95	1.95	1.64	2.02	1.98	1.75	1.86	1.86
	37	38	39	40	41	42	43	44	45	46	47	48
play	2.65	2.7	1.55	0	3.3	1.83	1.7	0	0	5	1.52	1.77
stop	2.65	2.7	1.55	0	3.3	1.83	1.7	0	0	5	1.52	1.77
	49	50	51	52	53	54	55	56	57	58	59	60
play	0	0	3.3	2.1	0	0	0	1.8	2.9	2.83	2.92	0
stop	0	0	3.3	2.1	0	0	0	1.8	2.9	2.83	2.92	0
	61	62	63	64	65	66	67	68	69	70	71	72
play	3.3	3.3	3.3	3.3	2.86	3.3	3.3	3.3	2.8	3.3	5	5
stop	3.3	3.3	3.3	3.3	2.86	3.3	3.3	3.3	2.8	3.3	5	5
	73	74	75	76	77	78	79	80	81	82	83	84
play	5	5	0	0	1.7	1.65	1.65	3.3	1.5	1.53	0	3.26
stop	5	5	0	0	1.7	1.65	1.65	3.3	1.5	1.53	0	3.26
	85	86	87	88	89	90	91	92	93	94	95	96
play	0	1.78	1.66	1	1.3	1.3	1.37	3.3	1.5	1.5	1.58	3.2
stop	0	1.78	0.7	0	0.4	0.3	0.1	3.3	0.4	0.4	0.1	2.9
	97	98	99	100	101	102	103	104	105	106	107	108
play	3.2	3.2	0	2.6	1.38	1.5	1.67	1.8	1.4	0	1.48	1.4
stop	3.1	3	0	1.1	0.5	0.3	0.5	1.8	0.4	0	0.5	0.5
	109	110	111	112	113	114	115	116	117	118	119	120
play	1.04	1.08	0	3.3	1.87	0	0.7	0	0	0	0	0.08
stop	0.29	0.27	0	3.3	1.84	0	1.64	0	0	0.16	0.25	0.13
	121	122	123	124	125	126	127	128	129	130	131	132
play	1.8	0	2.82	2.83	0	0	0	0	0	0	0	3.3
stop	1.8	0.18	2.08	2.09	0	0	0	0.11	0.06	0	0.15	3.3
	133	134	135	136	137	138	139	140	141	142	143	144
play	0.45	1.9	2.3	3.3	2.4	1.5	1.5	2.1	3.3	3.3	1.8	0
stop	0.6	2	0.6	3.3	2.39	0.7	2.34	2.7	3.3	3.3	1.8	0
	145	146	147	148	149	150	151	152	153	154	155	156
play	1.7	0.4	1.6	0.7	0.6	0	1.45	0.9	1.34	0.27	0.2	3.01
stop	1.3	0.4	1.27	0.8	0.53	0	1.54	0.8	1.26	0.08	0.08	3.25
	157	158	159	160	161	162	163	164	165	166	167	168
play	0.26	1.51	2.3	1.7	0	1.26	0.7	1.6	1.58	1.23	3.3	0
stop	0.08	0.9	1.37	0.25	0	0.6	3.2	0.4	1.58	1.58	3.3	0

	169	170	171	172	173	174	175	176	177	178	179	180
play	0.3	2.7	0	1.25	3.3	1.26	0	2.88	1.67	0	1.66	1.67
stop	0.9	2.27	0	0	0	0	0	2.88	1.67	0	1.66	1.67
	181	182	183	184	185	186	187	188	189	190	191	192
play	0.62	3.3	0.6	0.6	0.59	1.21	0.52	3.29	0	3.29	0	1.07
stop	0.62	3.3	0	0.6	0.59	1.21	0.52	3.29	0	3.29	0	1.08
	193	194	195	196	197	198	199	200	201	202	203	204
play	1.17	3.29	1.2	0	3.26	0.03	2.64	1.67	1.67	1.67	1.68	1.46
stop	1.17	3.29	1.2	0	3.26	0.3	2.64	1.67	1.67	1.67	1.68	1.46
	205	206	207	208	209	210	211	212	213	214	215	216
play	0	1.68	1.65	1.65	3.26	3.3	0.4	1.61	1.79	0.89	2.38	2.38
stop	0	1.68	1.65	1.65	3.26	3.3	0.4	1.61	1.8	0.89	2.38	2.38

IC105(AM5869)

Ref.number / Mode	1	2	3	4	5	6	7	8	9	10	11	12
play	1.66	3.44	1.25	1.44	1.26	0	0	5	0	0	1.25	3.53
stop	1.66	3.44	1.25	1.44	1.26	0	0	5	0	0	1.4	3.4
	13	14	15	16	17	18	19	20	21	22	23	24
play	2.33	2.52	2.45	2.38	2.58	2.18	5	2.35	5	0	1.74	4.86
stop	2.33	2.52	2.54	2.25	2.6	2.2	5	2.3	5	0	1.74	4.86
	25	26	27	28								
play	2.8	1.67	1.67	3.3								
stop	2.8	1.69	1.69	3.3								

IC103A(MX29LV040TC-707)

Ref.number / Mode	1	2	3	4	5	6	7	8	9	10	11	12
play	0.9~1.6	2.7	0.6	0.4	1.5~2.5	0.2~0.9	3.28	3.28	0	0.15~0.3	3	0.15
stop	2.9	0.3	0.7	0.6	3	0	3.28	3.28	0	0.58	2.8	0
	13	14	15	16	17	18	19	20	21	22	23	24
play	1	1.8~3.21	1.5~2.4	0.2~1.7	0	3.2	0	0	1.7	1.3	1.9	0
stop	0.98	0.68	0.68	2.3	0	3.23	0	0	0.8	1.8	1.8	0
	25	26	27	28	29	30	31	32				
play	1.3	1.4	1.9	1.6	1.6	1.3	0	3.28				
stop	0.8	0.8	1.8	1.5	1.4	0.6	3.2	3.2				

IC106(1X16Y3VTW-7)

Ref.number / Mode	1	2	3	4	5	6	7	8	9	10	11	12
play	3.27	1.2	1.3	0	1.1	1.3	3.27	1.2	1.12	0	1.3	1.6
stop	3.27	0.23	0.23	0	0.25	0.23	3.27	0.21	0.18	0	0.18	0.26
	13	14	15	16	17	18	19	20	21	22	23	24
play	3.27	2.9	3.2	3.1	3.1	2.6	0	0	0	0	0	0
stop	3.27	2.55	2.96	2.98	2.86	1.45	0.3	0	0	0	0	0.16
	25	26	27	28	29	30	31	32	33	34	35	36
play	3.27	0	0	0	0	0	0	0	0	0.7	1.65	2.9

stop	3.27	0	0.14	0.1	0.1	0	0	0	0	1.25	1.6	2.55
	37	38	39	40	41	42	43	44	45	46	47	48
play	0	3.27	1.3	1.39	0	1.4	1.4	3.27	1.39	1.25	0	1.49
stop	0	3.27	0.24	0.22	0	0.22	0.22	3.27	0.19	0.18	0	0.16
	49	50										
play	1.13	0										
stop	0.18	0										

IC402(AZ4558)

Ref.number / Mode								
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**
play	0	0	0	-11.53	0	0	0	11.82
stop	0	0	0	-11.53	0	0	0	11.82

IC104(24C02)

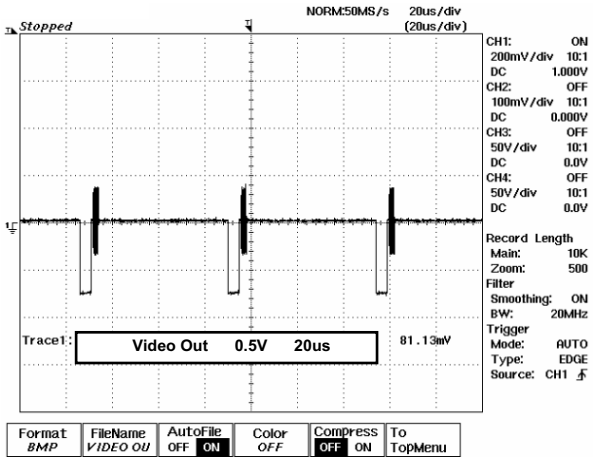
Ref.number / Mode								
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**
play	0	0	0	0	3.3	3.33	0	4.88
stop	0	0	0	0	3.3	3.33	0	4.88

IC120(STM809)

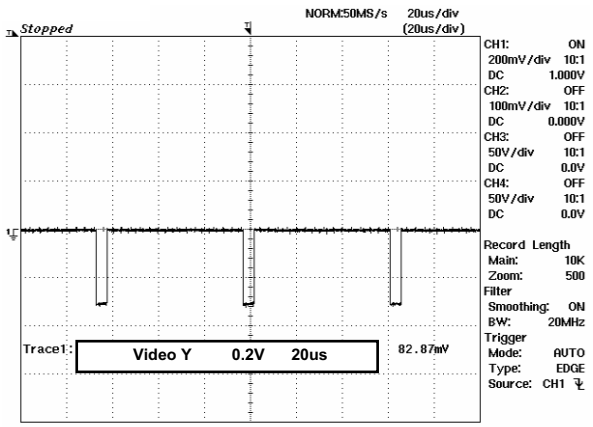
Ref.number / Mode			
	1	**2**	**3**
play	3.28	0	3.26
stop	3.28	0	3.26

WAVEFORMS

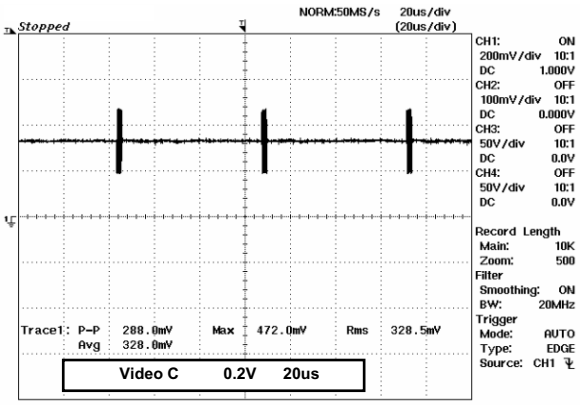
WF1 **VIDEO OUT**



WF2 **S-VIDEO OUT - Y Output**



WF3 **S-VIDEO OUT - C Output**

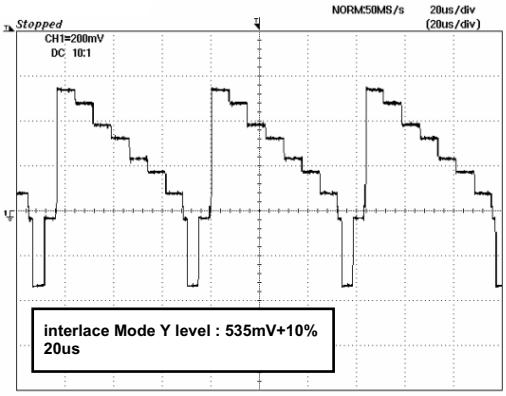


Note :

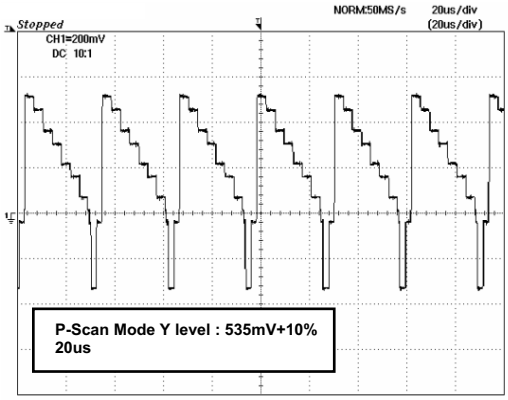
WF1 ~ WF3 : Power On (Stop) Mode.

WF4 ~ WF7 : DVD Test Disc TDV-540A. (Color Bar 75%)

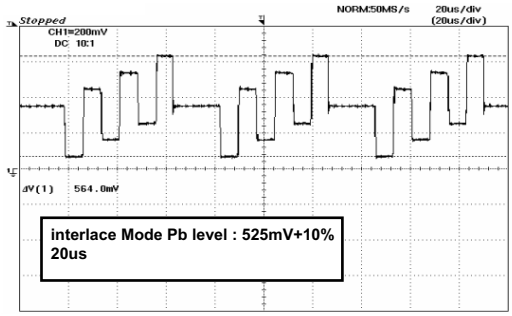
WF4 **COMPONENT VIDEO OUT - Y Output (Interlace mode)**



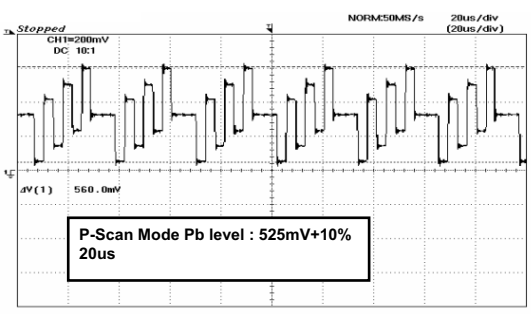
WF5 **COMPONENT VIDEO OUT - Y Output (P-Scan mode)**



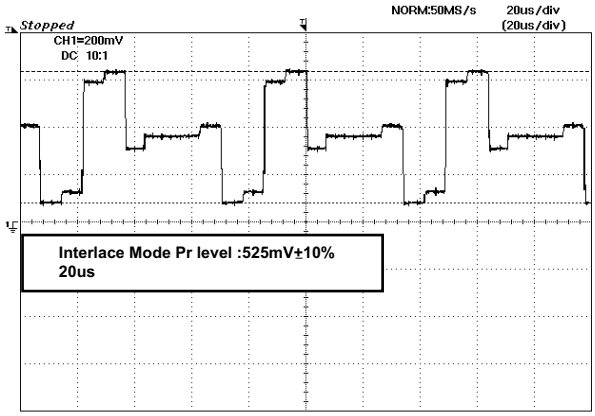
WF6 **COMPONENT VIDEO OUT - Pb/Cb Output (Interlace mode)**



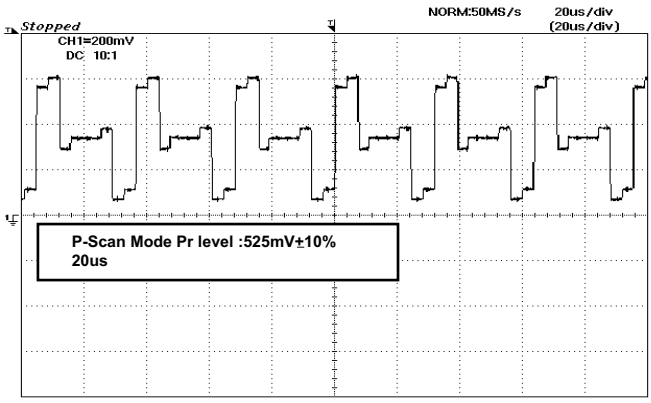
WF7 **COMPONENT VIDEO OUT - Pb/Cb Output (P-Scan mode)**



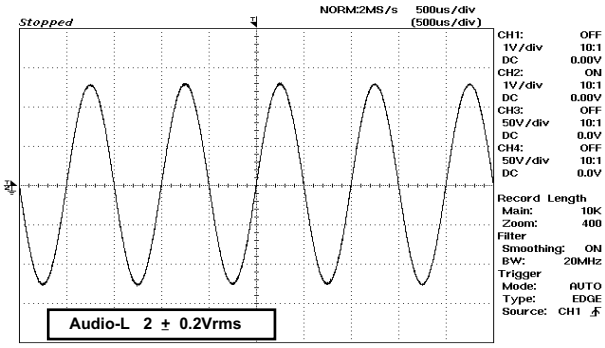
WF8 **COMPONENT VIDEO OUT - Pr/Cr Output (Interlace mode)**



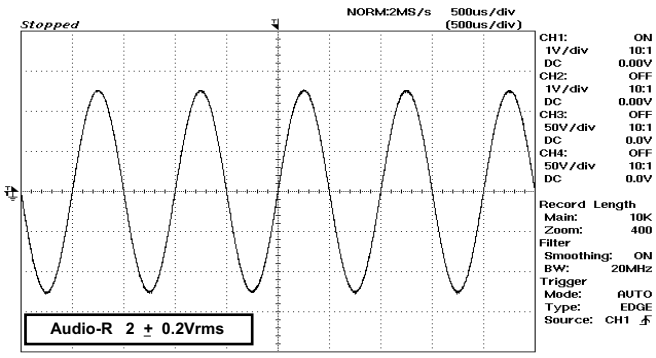
WF9 **COMPONENT VIDEO OUT - Pr/Cr Output (P-Scan mode)**



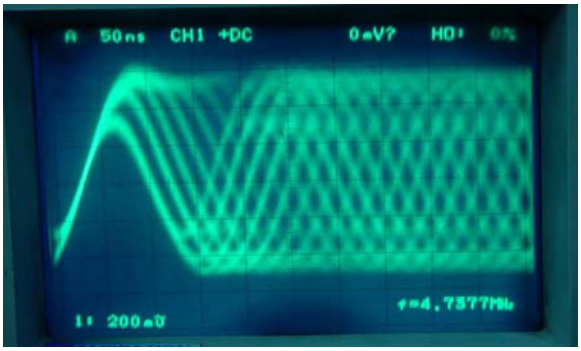
WF10 **Audio Left**



WF11 **Audio Right**



WF12 **Rf Signal**



Note :

WF8 ~ WF9 : DVD Test Disc TDV-540A. (Color Bar 75%)

WF10 ~ WF11 : DVD/CD Test Disc 1KHz, 0dB.

WF12 : Normal Disc.

-MEMO-

OVERALL WIRING CONNECTION

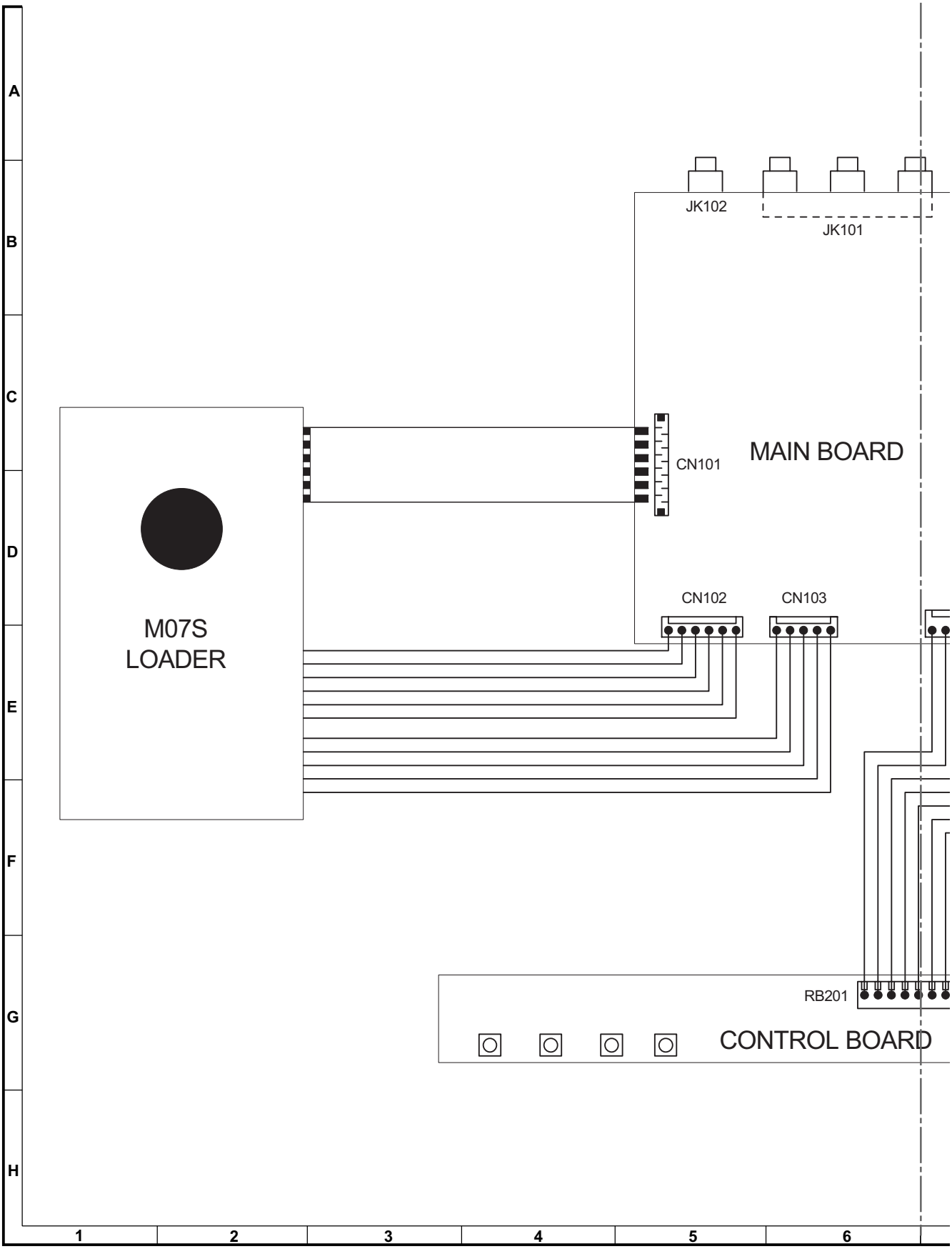


Figure 1: OVERALL WIRING CONNECTION (1/2)

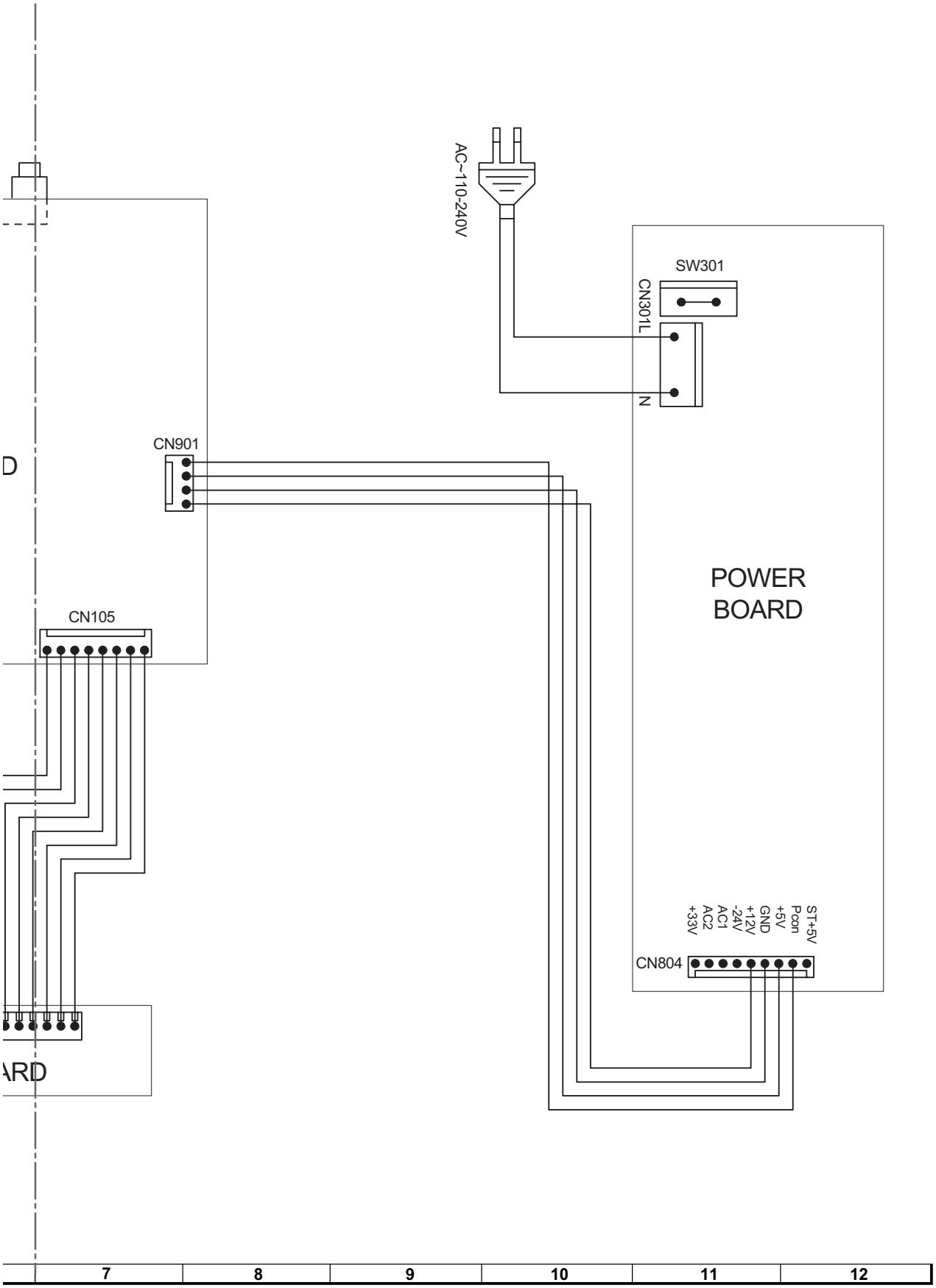


Figure 1: OVERALL WIRING CONNECTION (2/2)

WIRING DIAGRAM

WIRING SIDE OF POWER PCB (TOP VIEW)

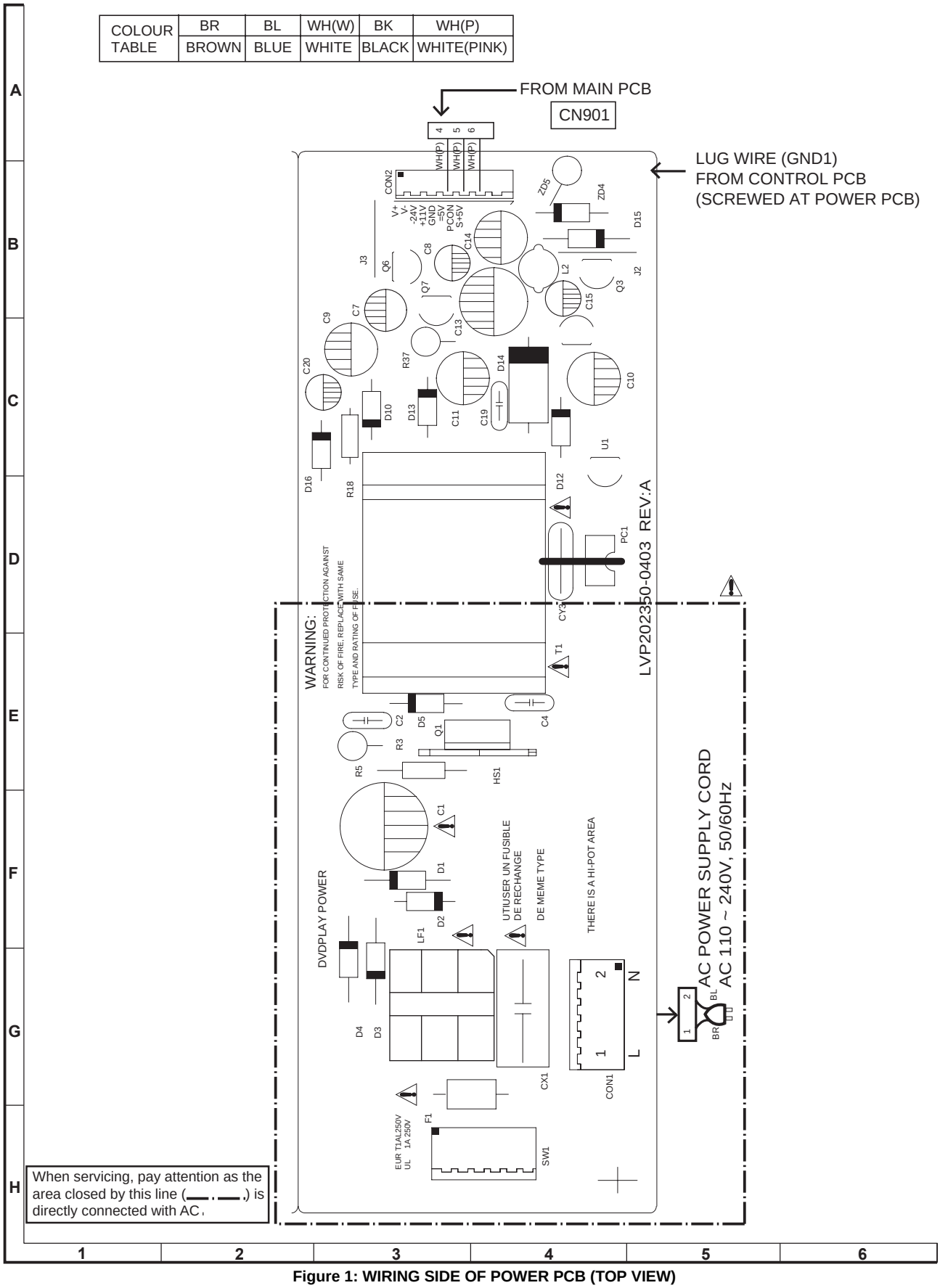


Figure 1: WIRING SIDE OF POWER PCB (TOP VIEW)

WIRING SIDE OF POWER PCB (BOTTOM VIEW)

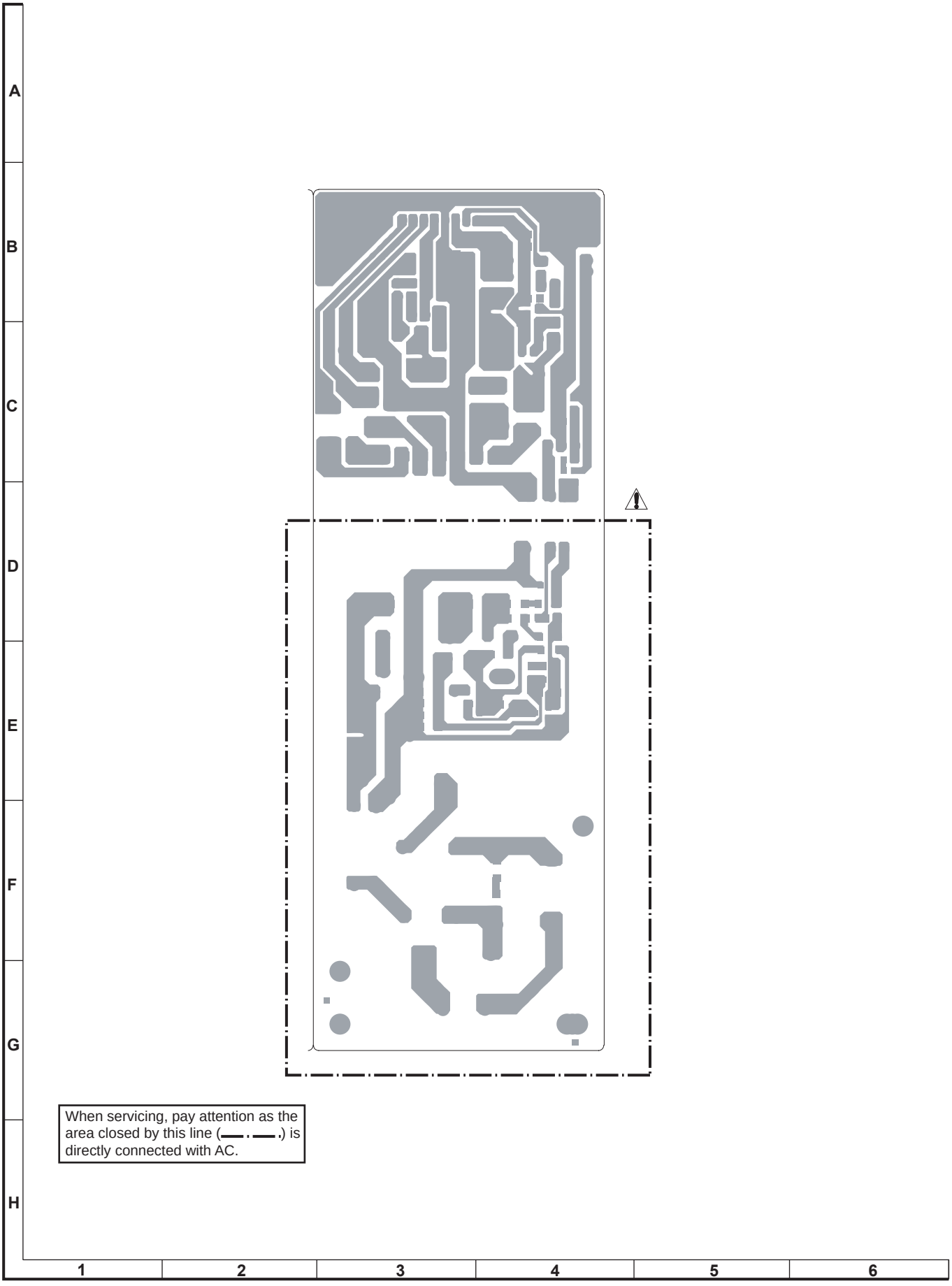


Figure 1: WIRING SIDE OF POWER PCB (BOTTOM VIEW)

WIRING SIDE OF MAIN PCB (TOP VIEW)

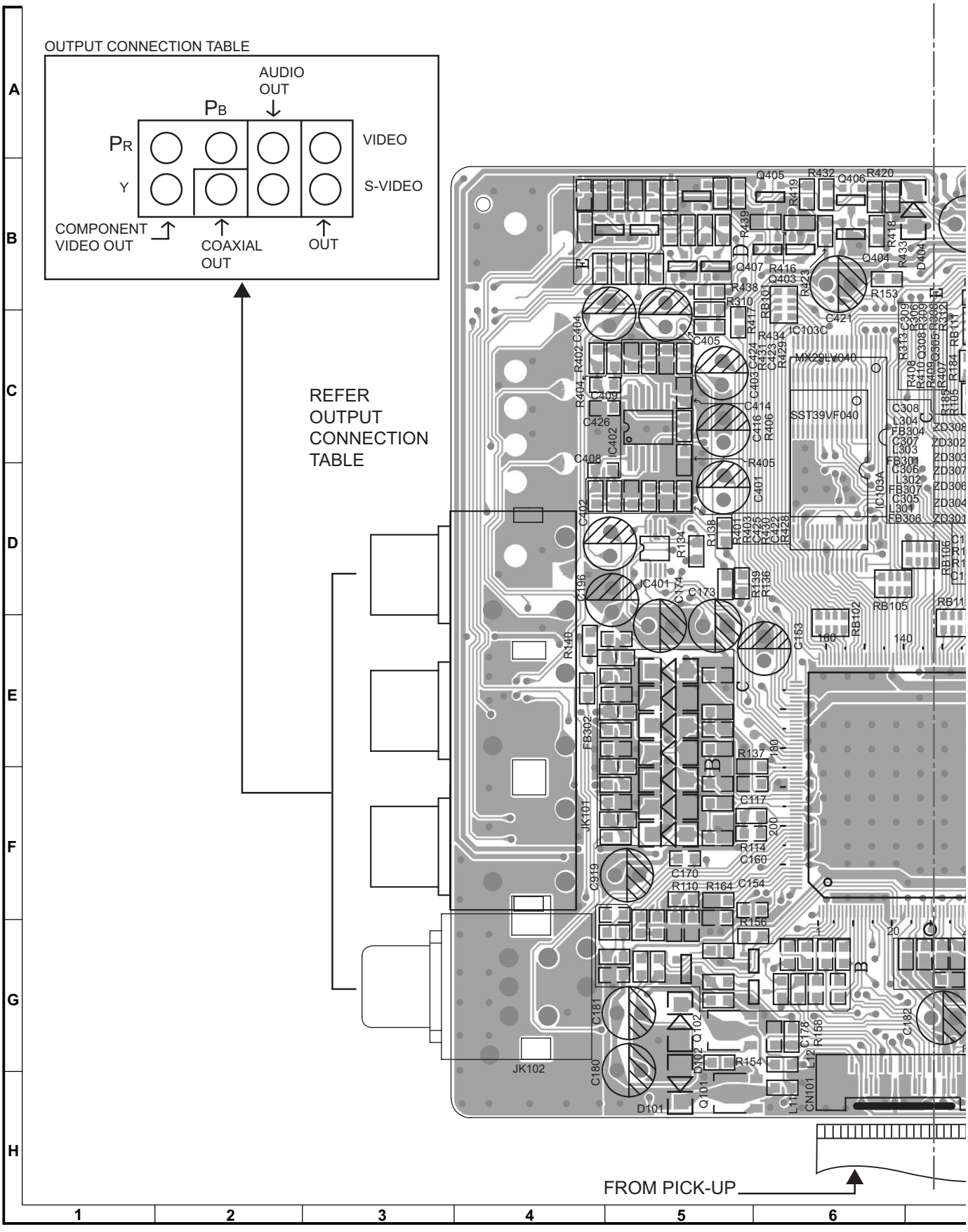
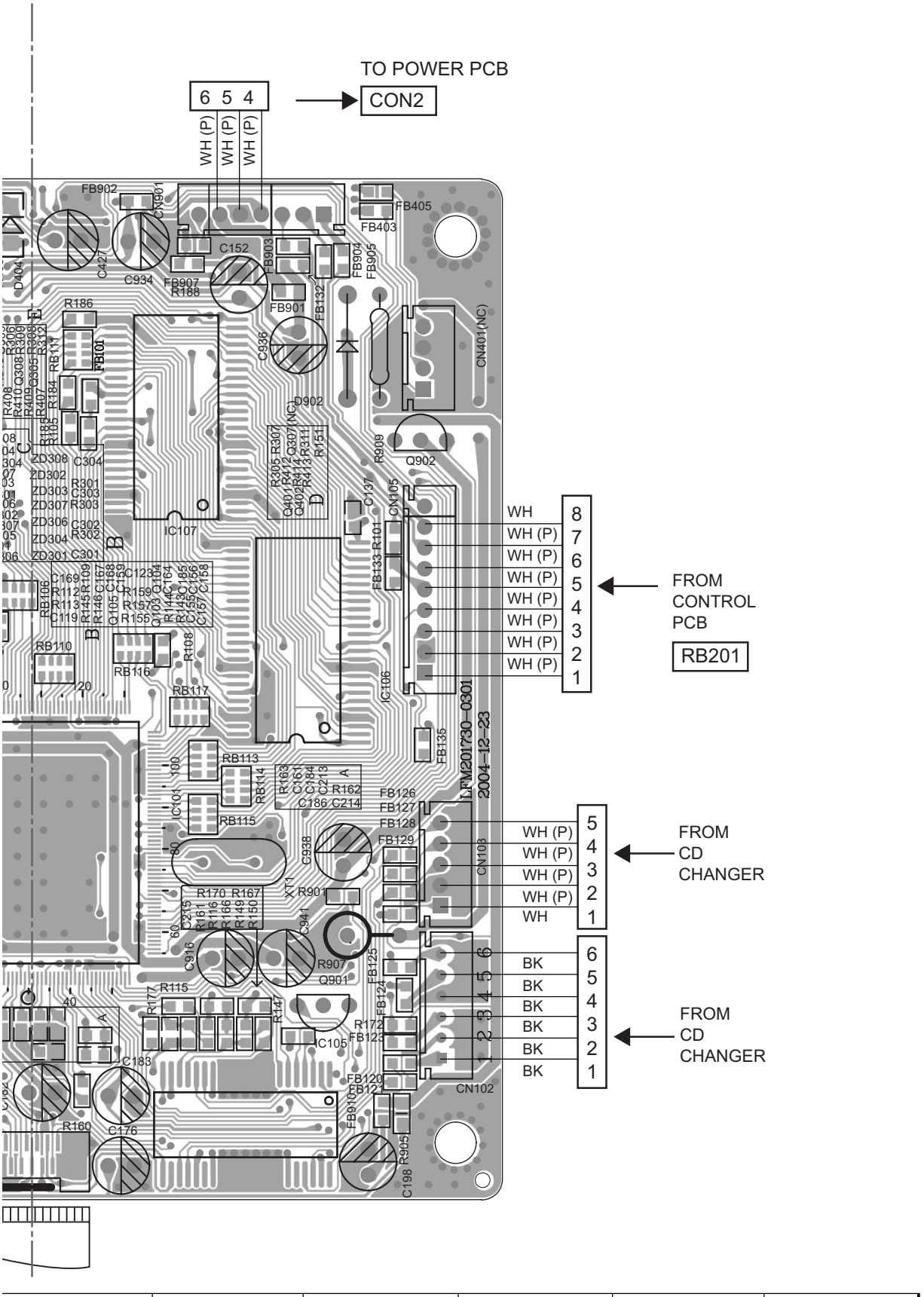


Figure 2: WIRING SIDE OF MAIN PCB (TOP VIEW) 1/2

WIRING SIDE OF MAIN PCB (TOP VIEW)



7	8	9	10	11	12

Figure 2: WIRING SIDE OF MAIN PCB (TOP VIEW) 2/2

WIRING SIDE OF MAIN PCB (BOTTOM VIEW)

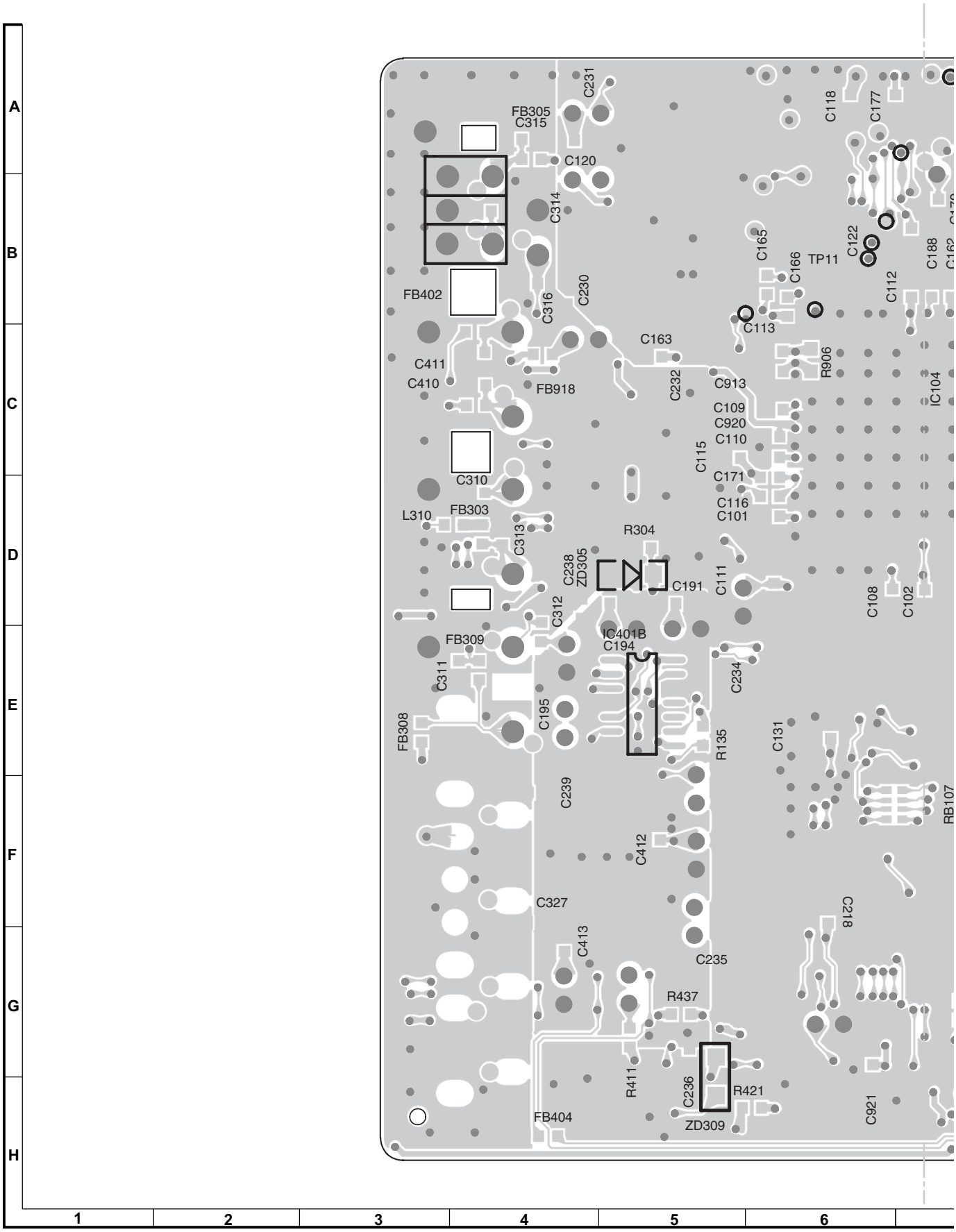
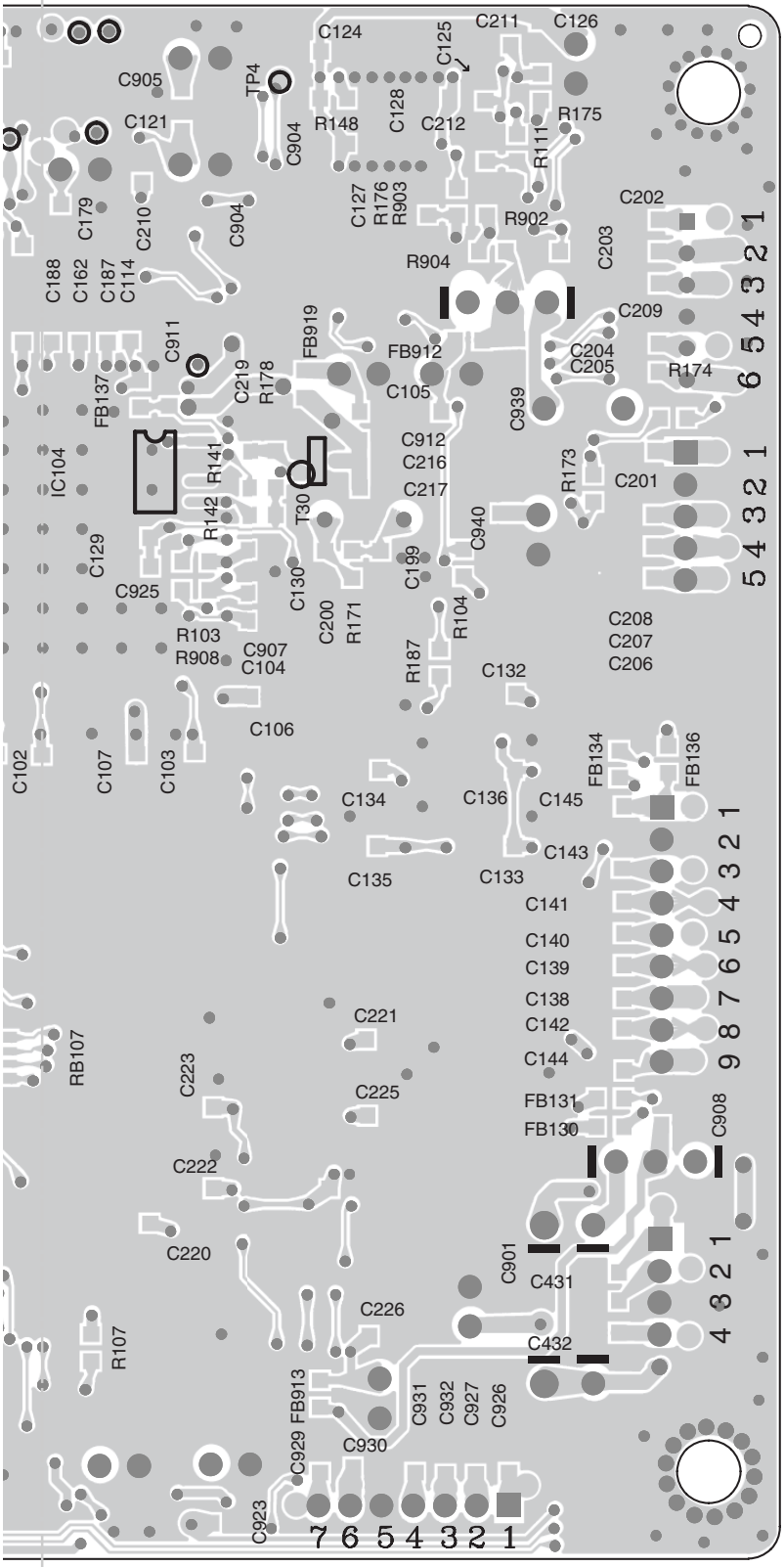


Figure 2: WIRING SIDE OF MAIN PCB (BOTTOM VIEW) 1/2

WIRING SIDE OF MAIN PCB (BOTTOM VIEW)



7 | 8 | 9 | 10 | 11 | 12

Figure 2: WIRING SIDE OF MAIN PCB (BOTTOM VIEW) 2/2

WIRING SIDE OF CONTROL PCB (TOP VIEW)

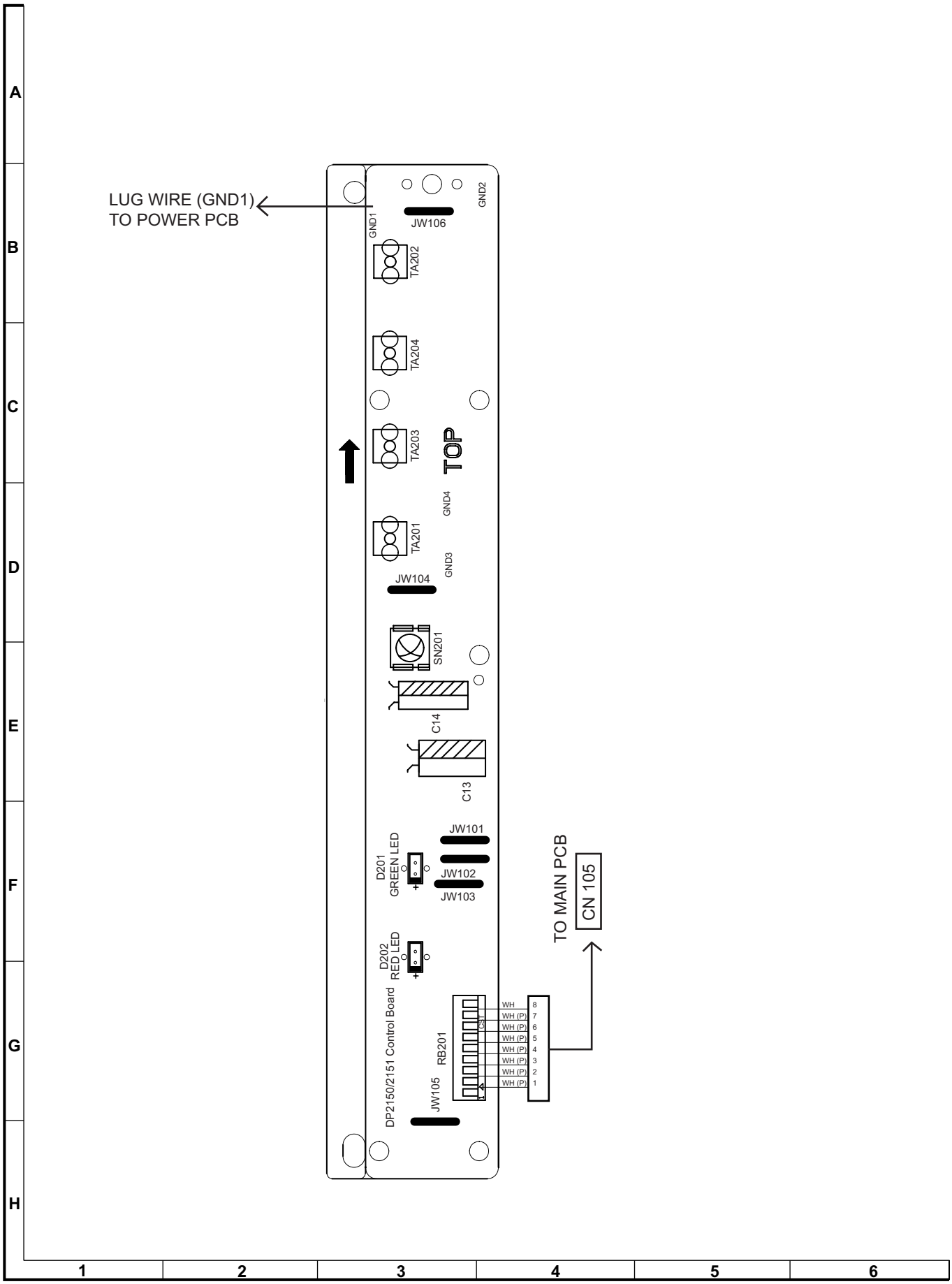


Figure 3: WIRING SIDE OF CONTROL PCB (TOP VIEW)

WIRING SIDE OF CONTROL PCB (BOTTOM VIEW)

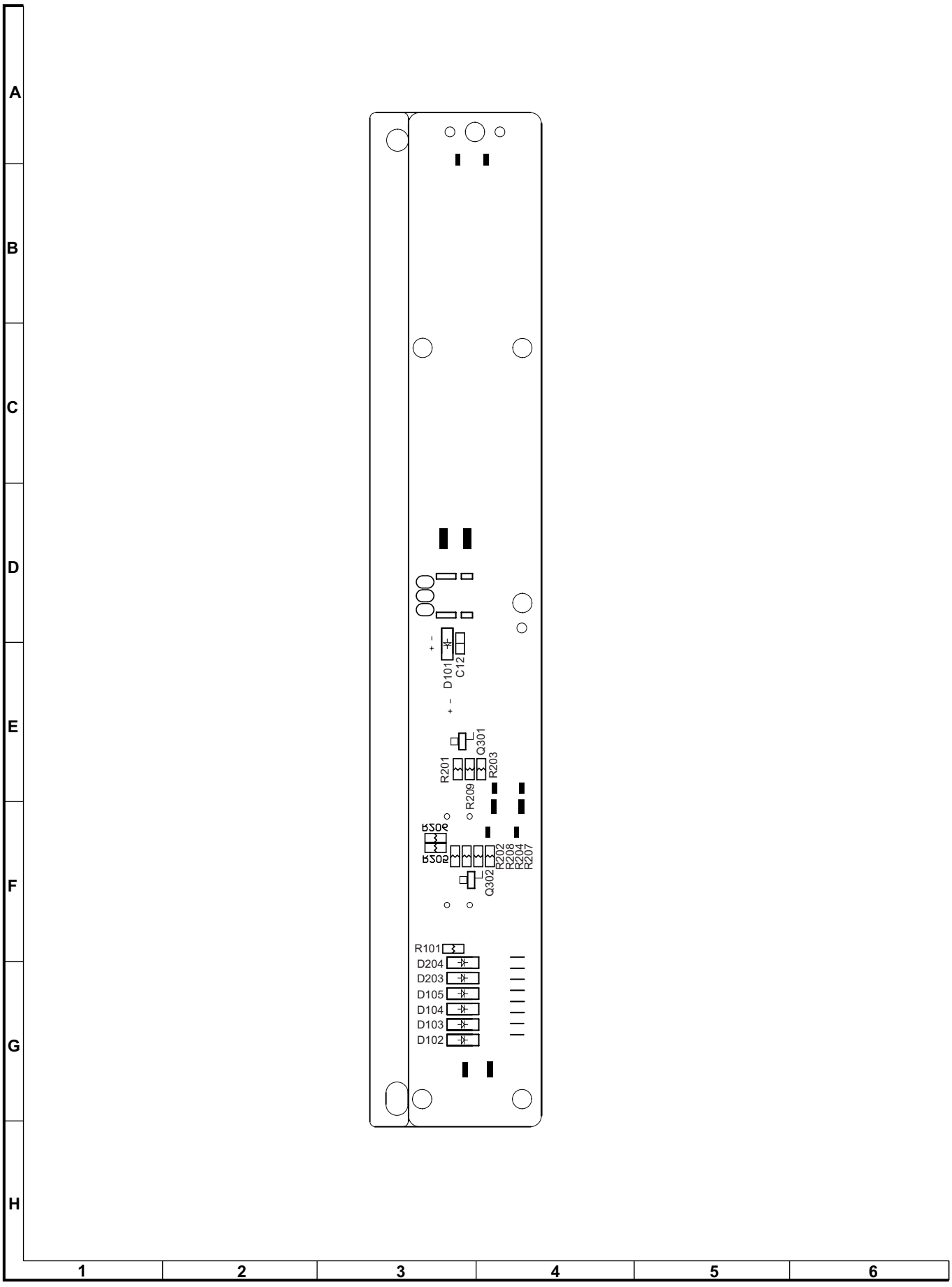


Figure 3: WIRING SIDE OF CONTROL PCB (BOTTOM VIEW) 1/2

WIRING SIDE OF CONTROL PCB (BOTTOM VIEW)

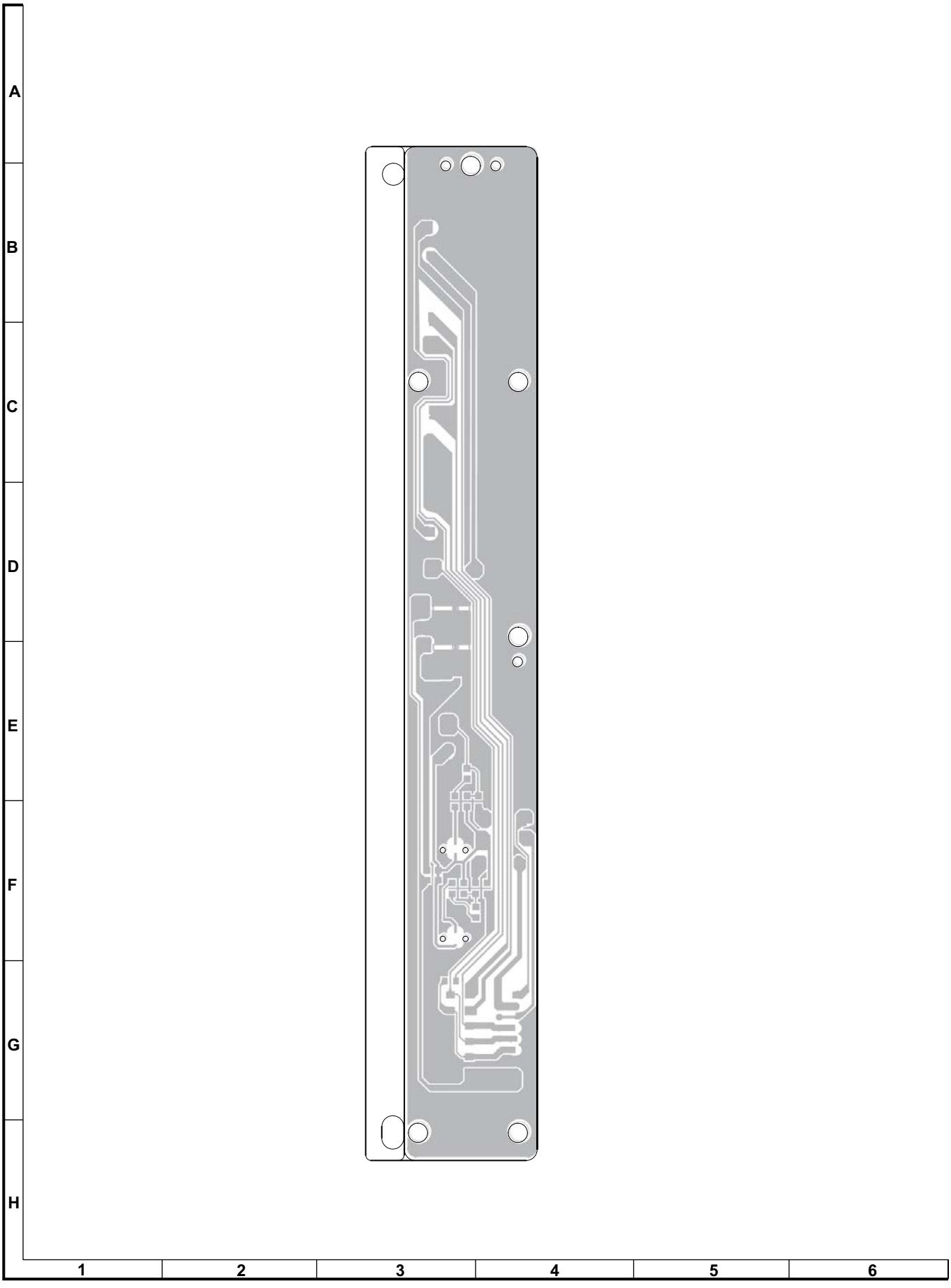


Figure 3: WIRING SIDE OF POWER PCB (BOTTOM VIEW) 2/2

NOTES ON SCHEMATIC DIAGRAM

- Resistor:
  To differentiate the units of resistors, such symbol as K and M are used; the symbol K means 1000 ohm and the symbol M means 1000 kohm and the resistor without any symbol is ohm-type resistor. Besides, the one with “Fusible” is a fuse type.

- Capacitor:
  To indicate the unit of capacitor, a symbol P is used: this symbol P means pico-fared and the unit of the capacitor without such a symbol is microfared. As to electrolytic capacitor, the expression “capacitance/withstand voltage” is used.

  (CH), (TH), (RH), (UJ): Temperature compensation

  (ML): Mylar type

  (P.P.): Polypropylene type

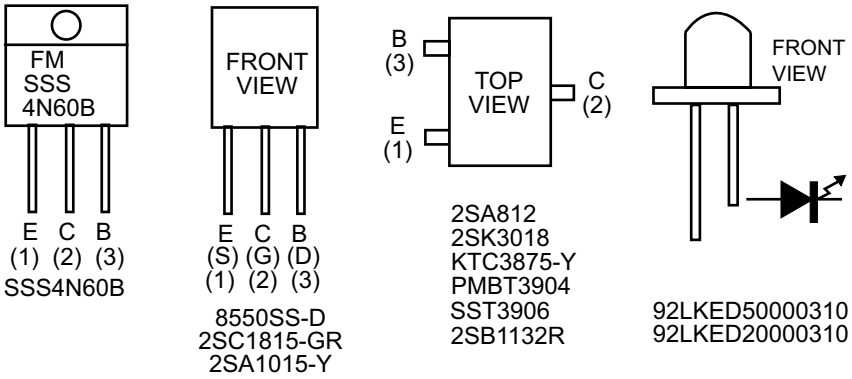
- Schematic diagram and Wiring Side of P.W. Board for this model are subject to change for improvement without prior notice.

- The indicated voltage in each section is the one measured by Digital Multimeter between such a section and the chassis with no signal given.

- Parts marked with “⚠ (⊏二二二⊐) are are important for maintaining the safety of the set. Be sure to replace these parts with specified ones for maintaining the safety and performance of the set.

REF. No.	DESCRIPTION	POSITION
TA201	OPEN/CLOSE	ON-OFF
TA202	ON/STANDBY	ON-OFF
TA203	PLAY/PAUSE	ON-OFF
TA204	STOP	ON-OFF

TYPES OF TRANSISTOR AND LED



TROUBLESHOOTING

If you experience any of the following difficulties while using the system, use this troubleshooting guide to help you remedy the problem. Should any problem persist, consult your nearest service center.

No power

- Is the power cord firmly plugged into the power outlet?
- One of the safety mechanisms may be operating. In this event, unplug the player from the power outlet briefly and then plug it in again.

No picture

- Check that the system is connected securely.
- The video connecting cord is damaged. Replace it with a new one.
- Make sure you connect the system to video input connector on the TV.
- Make sure you turn on the TV.
- Make sure you select the video input on the TV so that you can view the pictures from this system.

The picture noise appears

- Clear the disc.
- If video from this system has to go through your VCR to get to your TV, the copy-protection applied to some DVD programs could affect picture quality. If you still experience problems after checking your connections, please try connecting your DVD system directly to your TV's S-Video input, if your TV is equipped with this input.

The aspect ratio of the screen cannot be changed even though you set TV DISPLAY in the SETUP menu when you play a wide picture.

- The aspect rate is fixed on your DVD disc.
- If you connect the system with the S-Video cable, connect directly to the TV. Otherwise, you may not change the aspect rate.
- Depending on the TV, you may not change the aspect rate.

There is no sound or only a very low-level sound is heard.

- The system is in pause mode or in slow-motion play mode, or fast forward or fast reverse is performed. Press **▶/II** to return to normal play mode. Please also check audio cable connect securely or not.

Severe hum or noise is heard

- Check that the connecting cords are away from a transformer or motor.
- Move your TV away from the audio components.
- The plugs and jacks are dirty. Wipe them with a cloth slightly moistened with alcohol.
- Clear the disc.

The sound loses stereo effect when you play CD

- Set the Audio mode to "STEREO" in the SETUP menu.
- Make sure you connect the system appropriately.

The surround effect is difficult to hear when you are playing a Dolby Digital sound track

- Depending on the DVD disc, the output signal may not be the entire 5.1 channel but monaural or stereo even if the sound track is recorded in Dolby Digital format.

The language for the subtitles cannot be changed when you play a DVD

- Multilingual subtitles are not recorded on the DVD.
- Changing the language for the subtitles is prohibited on the DVD.

The subtitles cannot be turned off when you play a DVD

- Depending on the DVD, you may not be able to turn the subtitles off.

The angles cannot be changed when you play a DVD

- Multi-angles are not recorded on the DVD.
- Change the angles when the angle mark appears on the TV screen.
- Changing the angles is prohibited on the DVD.

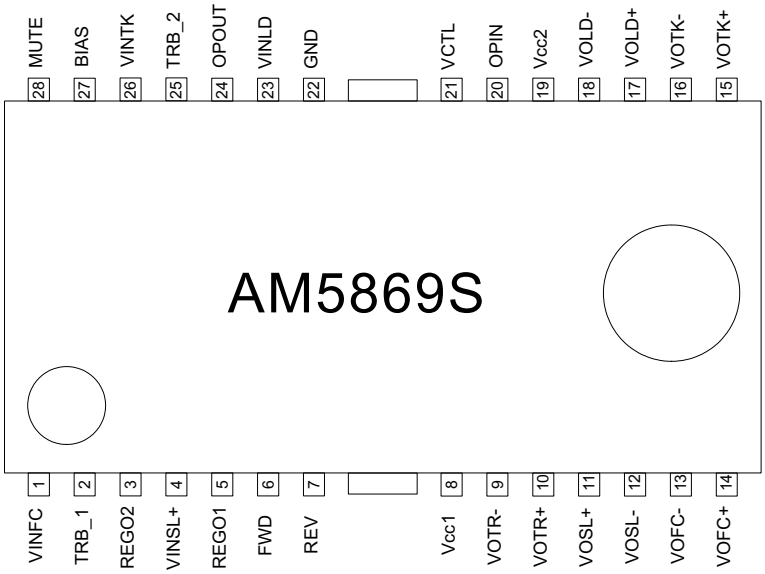
The system does not operate properly

- Static electricity, etc., may affect the system's operation. Press the **STANDBY** button to turn off, then press again to turn on.

IC PIN FUNCTION DESCRIPTIONS

IC105 5 Channel BTL Driver for DVD PLayer

Pin Configuration:



Pin Description:

PIN(S)	NAME	DESCRIPTION
PIN No	**Pin Name**	**Function**
1	VINFC	Input for focus driver
2	TRB_1	connect to external transistor base
3	REGO2	Regulator voltage output, connect to external transistor collector
4	VINSL+	Input for the sled driver
5	REGO1	Regulator voltage output, connect to external transistor collector
6	FWD	Tray driver forward input
7	REV	Tray driver reverse input
8	Vcc1	Vcc for pre-drive block and power block of sled and tray
9	VOTR-	Tray driver output(-)
10	VOTR+	Tray driver output(+)
11	VOSL+	Sled driver output(+)
12	VOSL-	Sled driver output(-)
13	VOFC-	Focus driver output(-)
14	VOFC+	Focus driver output(+)
15	VOTK+	Tracking driver output(+)
16	VOTK-	Tracking driver output(-)
17	VOLD+	Spindle driver output(+)
18	VOLD-	Spindle driver output(-)
19	Vcc2	Vcc for power block of spindle, tracking and focus
20	OPIN	Comparator input
21	VCTL	Speed control input of tray driver
22	GND	Ground
23	VINLD	Input for spindle driver
24	OPOUT	Comparator output
25	TRB_2	Connect to external transistor base
26	VINTK	Input for tracking driver
27	BIAS	Input for reference voltage
28	MUTE	Input for mute control

NOTES) Symbol of +and- (output of drivers) means polarity to input pin.

(For example, if voltage of pin1 is high, pin14 is high.)

IC105 5 Channel BTL Driver for DVD PLayer

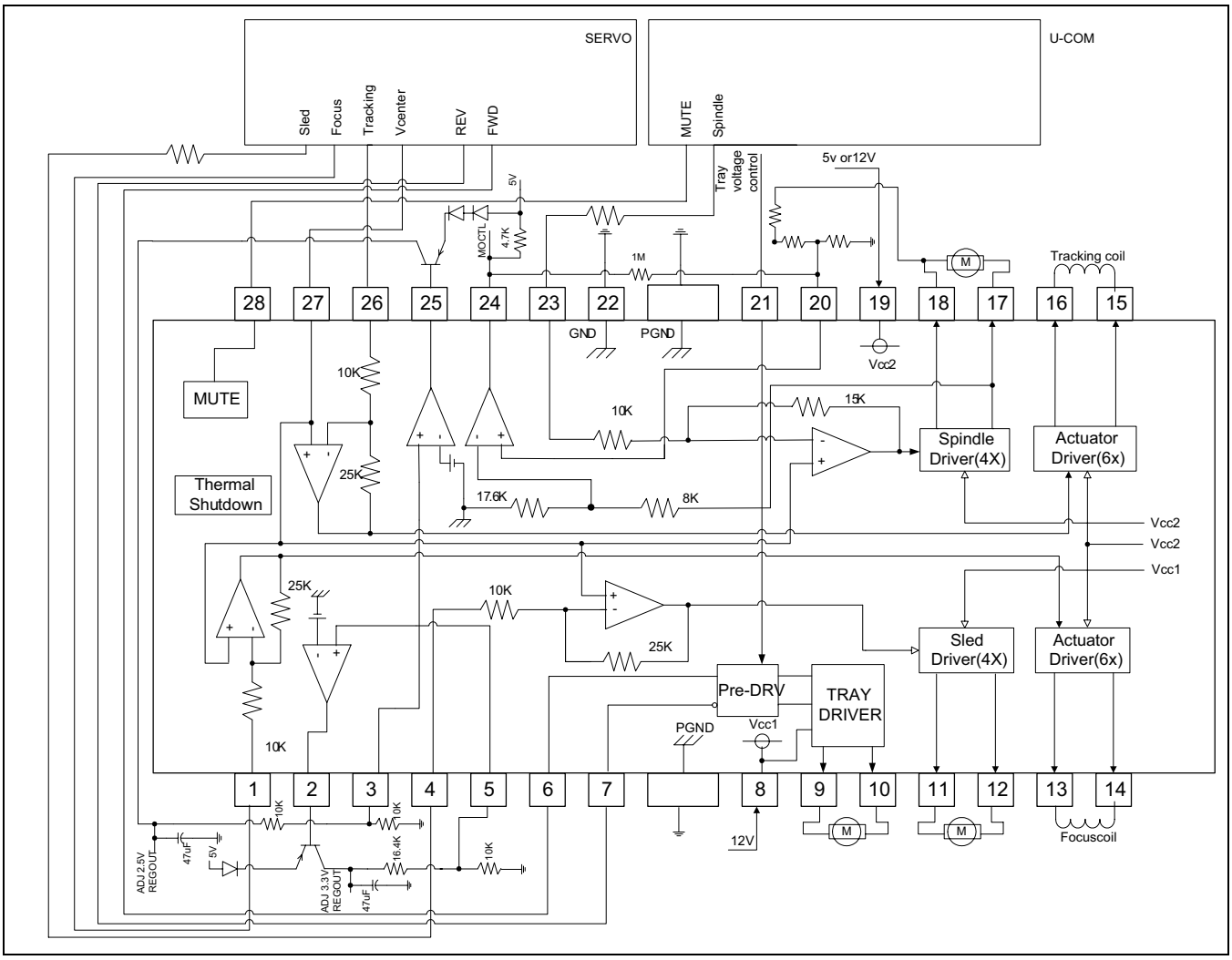
Tray driver logic input:

FWD(pin6)	REV(pin7)	VOTR+(pin10)	VOTR-(pin9)	Function
L	L	OPEN	OPEN	Open mode
L	H	L	H	Reverse mode
H	L	H	L	Forward mode
H	H	L	L	Brake mode

Input circuit of pin6 and pin7 is designed to avoid simultaneous activation of upper and lower output tr.; however, in order to improve reliability, apply motor forward/reverse input once through open mode.
We recommend time period for open longer than 10msec.

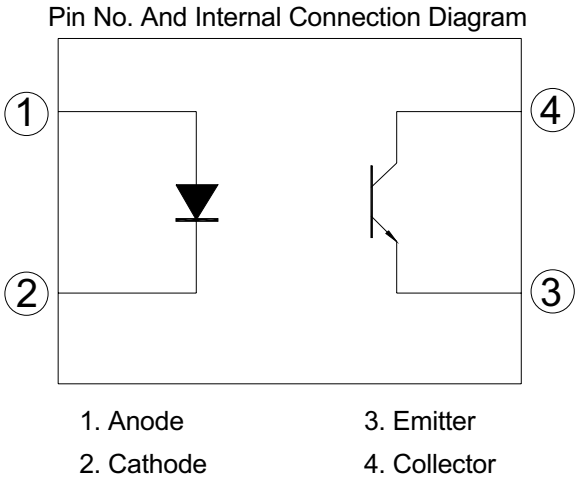
"H" side Output voltage on output voltage (VOL+, VOL-) varies depending on output control terminal for tray (pin21). "H" side output voltage is set three times (9.2dB Typ)VTCL(pin21). And, "L" side output voltage is equal to output saturation voltage.

Application circuit:



PC1, Optical Sensor (EL-817)

Pin Configuration:



U1, Voltage Regulator (TL431)

Pin Configuration:

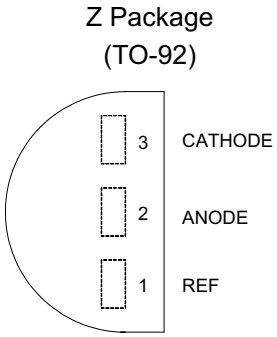


Figure 1: PIN CONFIGURATION OF TL431

Functional Block Diagram:

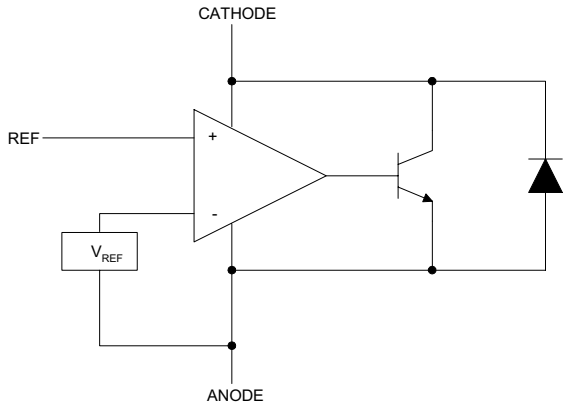
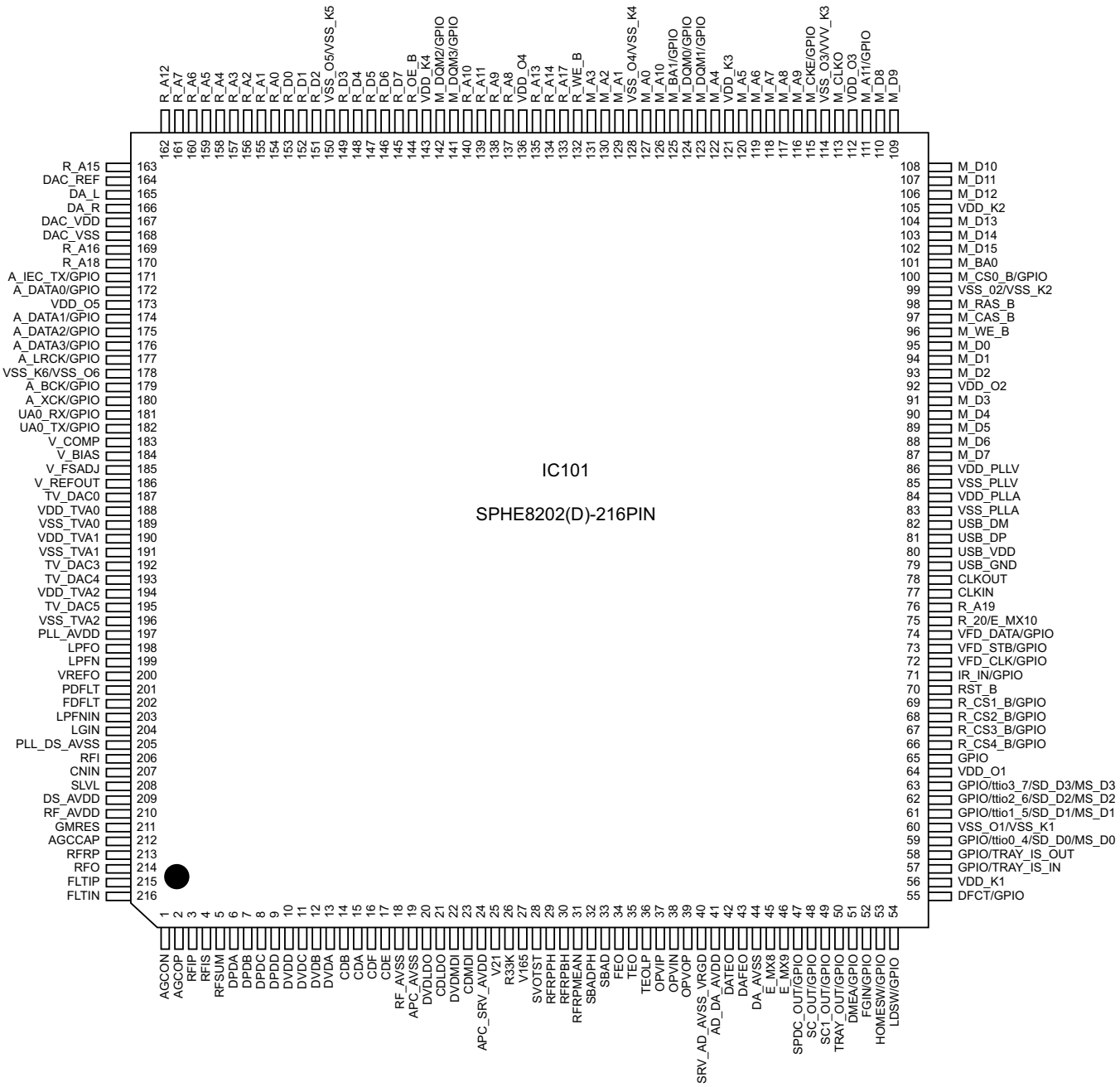


Figure 2: FUNCTIONAL BLOCK DIAGRAM OF TL431

IC101, DVD SINGLE CHIP MPEG A/V PROCESSOR

Signal Description:



Signal Description:

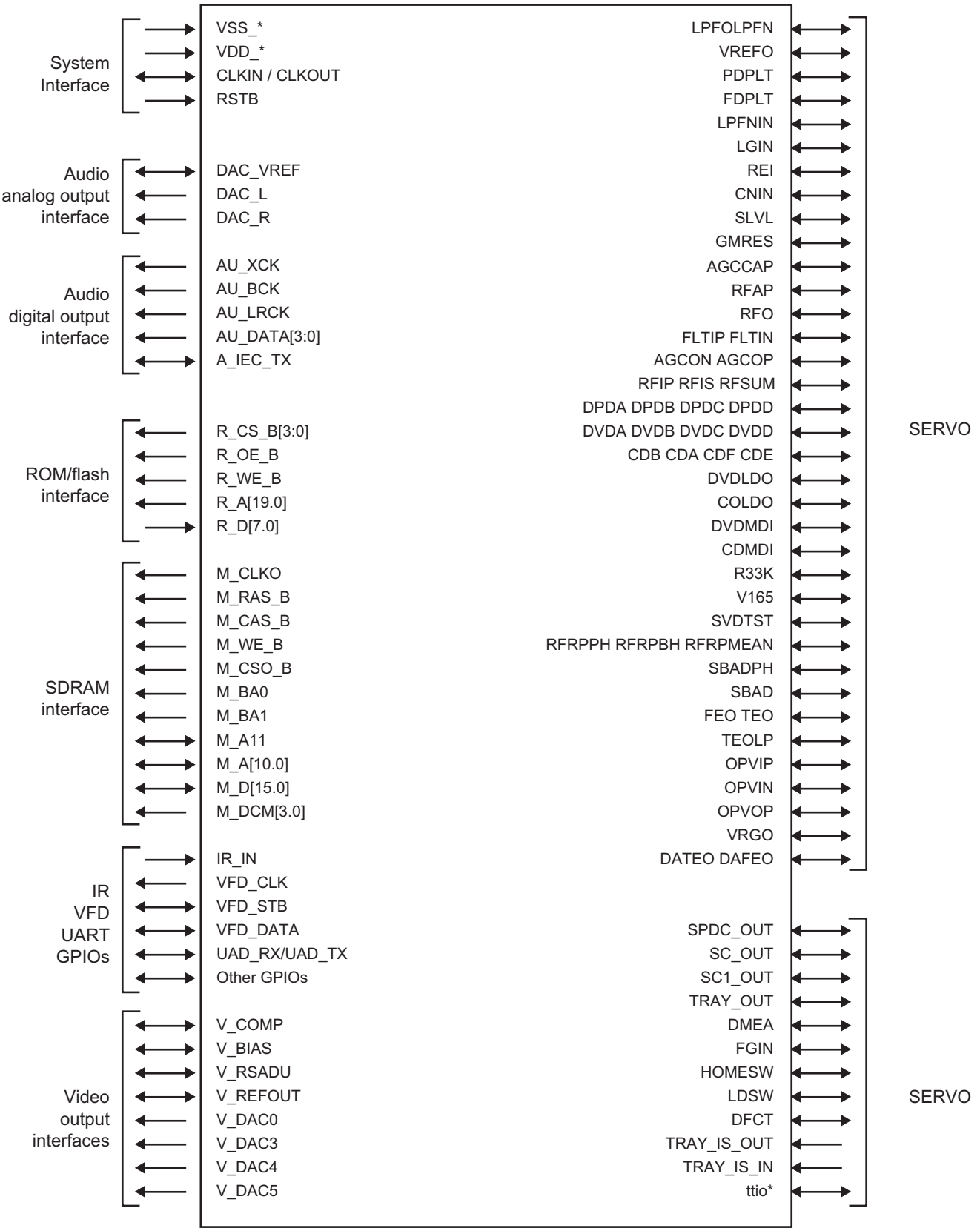


Table 1 8202D Pin Description

Name	Pin No	I/O	Definition
AGCON	1	O	Differential AGC output #N
AGCOP	2	O	Differential AGC output #P
RFIP	3	I	Differential RF input #P
RFIS	4	I	Single-ended RF equalizer input.
RFSUM	5	O	RF summing amplified output.
DPDA	6	I	AC coupled RF inputs for the DPD #A, from the main beam photo detector.
DPDB	7	I	AC coupled RF inputs for the DPD #B, from the main beam photo detector.
DPDC	8	I	AC coupled RF inputs for the DPD #C, from the main beam photo detector.
DPDD	9	I	AC coupled RF inputs for the DPD #D, from the main beam photo detector.
DVDD	10	I	DVD RF inputs #A, from the main beam photo detector.
DVDC	11	I	DVD RF inputs #B, from the main beam photo detector.
DVDB	12	I	DVD RF inputs #C, from the main beam photo detector.
DVDA	13	I	DVD RF inputs #D, from the main beam photo detector.
CDB	14	I	CD RF inputs #B, from the main beam photo detector.
CDA	15	I	CD RF inputs #A, from the main beam photo detector.
CDF	16	I	CD tracking error inputs #F, from the sub beam photo detector.
CDE	17	I	CD tracking error inputs #E, from the sub beam photo detector.
RF_AVSS	18	S	Servo RF ground.
APC_AVSS	19	S	Servo APC ground.
DVDLDO	20	O	DVD APC output.
CDLDO	21	O	CD APC output.
DVDMDI	22	I	DVD APC input from monitor photo diode.
CDMDI	23	I	CD APC input from monitor photo diode.
APC_SRV_AVDD	24	S	Servo A PC and analog 3.3V power (216pin only)
V21	25	-	Reference DV bias voltage.
R33K	26	-	External reference resistor input.
V165	27	-	Reference DV bias voltage.
SVOTST	28	O	RF peak hold external capacitor.
RFRPPH	29	O	RFRP peak hold signal output.
RFRPBH	30	O	RFRP bottom hold signal output.
RFRRMEAN	31	O	RFRP mean signal output.
SBADPH	32	O	Sub-beam adds peak hold signal output.
SBAD	33	O	Sub-beam adds signal output.
FEO	34	O	Focus error signal output.
TEO	35	O	Tracking error signal output.
TEOLP	36	A	
OPVIP	37	I	Op-amp 1 positive input.
OPVIN	38	I	Op-amp 1 negative input.
OPVOP	39	O	Op-amp output.
SRV_AD_VRGD_AVSS	40	S	Servo/ADC analog ground
AD_DA_AVDD	41	S	Servo/ADC/DAC 3.3V power
DATEO	42	A	
DAFEO	43	A	
DA_AVSS	44	S	Servo DAC ground

<table>
<tr><th>Symbol</th><th>Pin NO</th><th>I/O</th><th>Description</th></tr>
<tr><td>E_MX8</td><td>45</td><td>I/O</td><td>GPIO[70]
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[5:4]=2'b01</td><td>UA1_RXD</td><td>I</td></tr>
<tr><td>sft_cfg7[5:4]=2'b11</td><td>656_DATA[0]</td><td>O</td></tr>
<tr><td>sft_cfg1[11:9]=3'b110</td><td>RISC_INT1_11</td><td>I</td></tr>
<tr><td>sft_cfg7[11]=1'b0,
sft_cfg0[11]=1'b1,
fm_qpio_len[3:0]>8</td><td>FM_GPICB[12]</td><td>I/O</td></tr>
<tr><td>sft_cfg0[11]=1'b0,
fm_qpio_len[3:0]4'b1100</td><td>FM_GPICB[29]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[5]=1'b1</td><td>TV_EXT_DATA_Cr[7]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[70](default)</td><td>I/O</td></tr>
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<tr><td>E_MX9</td><td>46</td><td>I/O</td><td>GPIO[71]
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<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[5:4]=2'b01</td><td>UA1_TXD</td><td>O</td></tr>
<tr><td>sft_cfg7[5:4]=2'b11</td><td>656_DATA[1]</td><td>O</td></tr>
<tr><td>sft_cfg1[11:9]=3'b110</td><td>RISC_INT1_12</td><td>I</td></tr>
<tr><td>sft_cfg7[11]=1'b0,
sft_cfg0[11]=1'b1,
fm_qpio_len[3:0]>8</td><td>FM_GPIOB[13]</td><td>I/O</td></tr>
<tr><td>sft_cfg0[11]=1'b 0,
fm_qpio_len[3:0]4'b1100</td><td>FM_GPICB[30]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[5]=1'b1</td><td>TV_EXT_DATA_Cr[6]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[71](default)</td><td>I/O</td></tr>
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<tr><td>SPDC_OUT/GPIO</td><td>47</td><td>I/O</td><td>Servo SPDC_OUT
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_RESET_B</td><td>O</td></tr>
<tr><td>sft_cfg4[0]=1'b1</td><td>SPDC_OUT (default)</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_PDF</td><td>I</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>OTP_TEST_ADDR[0]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[0]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>SC_OUT/GPIO</td><td>48</td><td>I/O</td><td>Servo SC_OUT
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_DIOR_B</td><td>O</td></tr>
<tr><td>sft_cfg4[1]=1'b1</td><td>SC_OUT (default)</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_PDE</td><td>I</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>OTP_TEST_ADDR[1]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[1]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>SC1_OUT/GPIO</td><td>49</td><td>I/O</td><td>Servo SC1_OUT
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_DIOW_B</td><td>O</td></tr>
<tr><td>sft_cfg4[2]=1'b1</td><td>SC_OUT (default)</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_PDD</td><td>I</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>OTP_TEST_ADDR[2]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[2]</td><td>I/O</td></tr>
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<table>
<tr><th>Symbol</th><th>Pin NO</th><th>I/O</th><th>Description</th></tr>
<tr><td>TRAY_OUT/GPIO</td><td>50</td><td>I/O</td><td>Servo TRAY_OUT
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_IORDY</td><td>I</td></tr>
<tr><td>sft_cfg4[3]=1'b1</td><td>TRAY_OUT (default)</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_PDC</td><td>I</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>OTP_TEST_ADDR[3]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[3]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>DMEA_OUT/GPIO</td><td>51</td><td>I/O</td><td>Servo DMEA
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_DMACK</td><td>O</td></tr>
<tr><td>sft_cfg4[4]=1'b1</td><td>DMEA_OUT (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_PDB</td><td>I</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>OTP_TEST_ADDR[4]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[4]</td><td>I/O</td></tr>
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<tr><td>FGIN/GPIO</td><td>52</td><td>I/O</td><td>Servo FGIN
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_DMARO</td><td>I</td></tr>
<tr><td>sft_cfg4[5]=1'b1</td><td>FGIN_OUT (default)</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_PDA</td><td>I</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>OTP_TEST_PGM</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[5]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>HOMESW/GPIO</td><td>53</td><td>IO</td><td>Servo HOMESW
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[3:2]=2'b10</td><td>UA0_RXD</td><td>I</td></tr>
<tr><td>sft_cfg1[8:6]=3'b010</td><td>R_CSALL_B</td><td>O</td></tr>
<tr><td>sft_cfg7[7:6]=2'b11</td><td>PCMCIA_IOW_B</td><td>O</td></tr>
<tr><td>sft_cfg8[1]=1'b1</td><td>DSP_FL0</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[9]</td><td>I</td></tr>
<tr><td>sft_cfg7[14:13]=2'b01</td><td>EXT_CLK48</td><td>I</td></tr>
<tr><td>sft_cfg6[4]=1'b1</td><td>DELAY_CHAIN1</td><td>O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>OTP_TEST_DATA</td><td>O</td></tr>
<tr><td>(other)</td><td>GPIO[6] (default)</td><td>I/O</td></tr>
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<tr><td>LDSW/GPIO</td><td>54</td><td>IO</td><td>Servo LDSW
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[3:2]=2'b10</td><td>UA0_TXD</td><td>I</td></tr>
<tr><td>sft_cfg1[8:6]=3'b10</td><td>UA1_RXD</td><td>O</td></tr>
<tr><td>sft_cfg7[7:6]=2'b11</td><td>PCMCIA_IOR_B</td><td>O</td></tr>
<tr><td>sft_cfg8[2]=1'b1</td><td>DSP_FL1</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[8]</td><td>I</td></tr>
<tr><td>sft_cfg7[15:14]=2'b11</td><td>CLK27_OUT</td><td>I</td></tr>
<tr><td>sft_cfg7[14:13]=2'b10</td><td>EXT_CLK48</td><td>O</td></tr>
<tr><td>sft_cfg6[4]=1'b1</td><td>DELAY_CHAIN2</td><td>O</td></tr>
<tr><td>(other)</td><td>GPIO[7] (default)</td><td>I/O</td></tr>
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<table>
<tr><th>Symbol</th><th>Pin NO</th><th>I/O</th><th>Description</th></tr>
<tr><td>DFCT/GPIO</td><td>55</td><td>IO</td><td>Servo DFCT
<table>
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<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_INTRQ</td><td>I</td></tr>
<tr><td>sft_cfg4[6]=1'b1</td><td>DFCT (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>DAC_DATA_F[7]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[8]</td><td>I/O</td></tr>
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<tr><td>VDD_K1</td><td>56</td><td>S</td><td>Kernel logic power supply #1</td></tr>
<tr><td>GPIO/TRAY_IS_IN</td><td>57</td><td>IO</td><td>GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_ADR[1]</td><td>O</td></tr>
<tr><td>sft_cfg8[3]=1'b1</td><td>DSP_FL2</td><td>O</td></tr>
<tr><td>fm_qpio_len[3:0]>0</td><td>FM_GPIOB[0]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[6]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[9] (default)</td><td>I/O</td></tr>
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<tr><td>GPIO/TRAY_IS_OUT</td><td>58</td><td>IO</td><td>GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_ADR[2]</td><td>O</td></tr>
<tr><td>sft_cfg8[4]=1'b1</td><td>DSP_FL2</td><td>O</td></tr>
<tr><td>fm_qpio_len[3:0]>0</td><td>FM_GPIOB[0]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[5]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[10] (default)</td><td>I/O</td></tr>
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<tr><td>GPIO/ttio0_4</td><td>59</td><td>IO</td><td>GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_ADR[0]</td><td>O</td></tr>
<tr><td>sft_cfg4[9]=1'b1</td><td>ttio4/ttio0</td><td>I/O</td></tr>
<tr><td>sft_cfg1[11:9]=3'b001</td><td>RISC_INT1_11</td><td>I</td></tr>
<tr><td>sft_cfg3[11:10]=2'b01</td><td>ADC_BCK, digital audio input interface bit clock</td><td>I/O</td></tr>
<tr><td>fm_qpio_len[3:0]>0</td><td>FM_GPIOB[2]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[4]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[11] (default)</td><td>I/O</td></tr>
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<tr><td>VSS_O1/VSS_K1</td><td>60</td><td>S</td><td>Kernel logic power shared ground supply #1</td></tr>
<tr><td>GPIO/ttio1_5</td><td>61</td><td>IO</td><td>GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_CS1</td><td>O</td></tr>
<tr><td>sft_cfg4[9]=1'b1</td><td>ttio5/ttio1</td><td>I/O</td></tr>
<tr><td>sft_cfg4[15:13]=3'b001</td><td>HSYNC_PC</td><td>O</td></tr>
<tr><td>sft_cfg1[11:9]=3'b001</td><td>RISC_INT1_12</td><td>I</td></tr>
<tr><td>sft_cfg3[11:10]=2'b01</td><td>ADC_LRCK, digital audio input interface L/R strobe</td><td>I/O</td></tr>
<tr><td>fm_qpio_len[3:0]>0</td><td>FM_GPIOB[3]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[3]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[12] (default)</td><td>I/O</td></tr>
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<tr><th>Symbol</th><th>Pin NO</th><th>I/O</th><th>Description</th></tr>
<tr><td>GPIO/ttio2_6</td><td>62</td><td>IO</td><td>GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[11:10]=2'b01,2'b10</td><td>AT_CS0</td><td>O</td></tr>
<tr><td>sft_cfg4[9]=1'b1</td><td>ttio6/ttio2</td><td>I/O</td></tr>
<tr><td>sft_cfg4[15:13]=3'b001</td><td>VSYNC_PC</td><td>O</td></tr>
<tr><td>sft_cfg3[15:14]=2'b01</td><td>ISA_IOCHRDY</td><td>I</td></tr>
<tr><td>sft_cfg1[11:9]=3'b001</td><td>RISC_INT1_13</td><td>I</td></tr>
<tr><td>sft_cfg3[11:10]=2'b01</td><td>ADC_DATA, digital audio input interface data</td><td>I</td></tr>
<tr><td>fm_qpio_len[3:0]>1</td><td>FM_GPIOB[4]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[2]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[13] (default)</td><td>I/O</td></tr>
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<tr><td>GPIO/ttio3_7</td><td>63</td><td>IO</td><td>GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg4[9]=1'b1</td><td>ttio7/ttio3</td><td>I/O</td></tr>
<tr><td>sft_cfg2[6:8]=2'b11</td><td>PCMCIA_WAIT_B</td><td>I</td></tr>
<tr><td>sft_cfg1[11:8]=4'b0001</td><td>EXT_CLK27</td><td>I</td></tr>
<tr><td>sft_cfg1[11:9]=3'b001</td><td>RISC_INT1_14</td><td>I</td></tr>
<tr><td>fm_qpio_len[3:0]>1</td><td>FM_GPIOB[5]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[1]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[14] (default)</td><td>I/O</td></tr>
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<tr><td>VDD_O1</td><td>64</td><td>S</td><td>I/O power supply #1</td></tr>
<tr><td>GPIO</td><td>65</td><td>IO</td><td>GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[5:4]=2'b10</td><td>UA1_TXD</td><td>O</td></tr>
<tr><td>sft_cfg[8:6]=3'b001</td><td>R_CSALL_B</td><td>O</td></tr>
<tr><td>syscik_sel[4]</td><td>EXT_SYSCLK</td><td>I</td></tr>
<tr><td>sft_cfg7[11:8]=4'b0010</td><td>EXT_CLK27</td><td>I</td></tr>
<tr><td>fm_qpio_len[3:0]>3</td><td>FM_GPIOB[6]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_F[0]</td><td>I</td></tr>
<tr><td>sft_cfg7[13:12]=2'b11</td><td>CLK54_OUT</td><td>O</td></tr>
<tr><td>sft_cfg9[14:13]=2'b11</td><td>EXT_CLK48</td><td>I</td></tr>
<tr><td>sft_cfg6[4]=1'b1</td><td>DELAY_CHAIN3</td><td>O</td></tr>
<tr><td>(other)</td><td>GPIO[15] (default)</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>R_CS4_B/GPIO</td><td>66</td><td>IO</td><td>ROM / SRAM / flash chip select #4 or GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg1[3]=1'b1</td><td>R_CS4_B (default)</td><td>O</td></tr>
<tr><td>sft_cfg1[7]=1'b1 & fm_qpio_len[3:0]=10,11,12</td><td>FM_GPIOB[20]</td><td>I/O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_E[9]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[16]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>R_CS3_B/GPIO</td><td>67</td><td>IO</td><td>ROM / SRAM / flash chip select #3 or GPIO
<table>
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<tr><td>sft_cfg1[2]=1'b1</td><td>R_CS3_B (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_E[8]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[17]</td><td>I/O</td></tr>
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<table>
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<tr><td>R_CS2_B/GPIO</td><td>68</td><td>IO</td><td>ROM / SRAM / flash chip select #2 or GPIO
<table>
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<tr><td>sft_cfg1[1]=1'b1</td><td>R_CS2_B (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_E[7]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[18]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>R_CS1_B/GPIO</td><td>69</td><td>IO</td><td>ROM / SRAM / flash chip select #2 or GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg1[0]=1'b1</td><td>R_CS1_B (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_E[6]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[19]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>RST_B</td><td>70</td><td>I</td><td>System reset (active low reset)</td></tr>
<tr><td>IR_IN/GPIO</td><td>71</td><td>I</td><td>GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_GPIO[20]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[20]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>VFD_CLK/GPIO</td><td>72</td><td>IO</td><td>GPIO[24] for VFD_CLK</td></tr>
<tr><td>VFD_STB/GPIO</td><td>73</td><td>IO</td><td>GPIO[22] for VFD_STB
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_E[5]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[22] (default)</td><td>I/O</td></tr>
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<tr><td>VFD_DATA/GPIO</td><td>74</td><td>IO</td><td>GPIO[23] for VFD_DATA
<table>
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<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_E[4]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[23] (default)</td><td>I/O</td></tr>
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<tr><td>R_A20</td><td>75</td><td>I/O</td><td>ROM / SDRAM / flash address bus bit [20] (216 pin package)</td></tr>
<tr><td>R_A19 (E_VK11)</td><td>76</td><td>I/O</td><td>ROM / SDRAM / flash address bus bit [19]</td></tr>
<tr><td>CLKIN</td><td>77</td><td>I</td><td>Clock input / crystal in (XTALI)</td></tr>
<tr><td>CLKOUT</td><td>78</td><td>O</td><td>Clock input / crystal in (XTALO</td></tr>
<tr><td>RESERVED_N</td><td>79</td><td>A</td><td>Reserved</td></tr>
<tr><td>RESERVED_P</td><td>80</td><td>A</td><td>Reserved</td></tr>
<tr><td>RESERVED</td><td>81</td><td>A</td><td>Reserved</td></tr>
<tr><td>RESERVED</td><td>82</td><td>A</td><td>Reserved</td></tr>
<tr><td>VSS_PLLA</td><td>83</td><td>S</td><td>Ground pin for audio PLL</td></tr>
<tr><td>VSS_PLLA</td><td>84</td><td>S</td><td>3.3V power supply pin for audio PLL</td></tr>
<tr><td>VSS_PLLV</td><td>85</td><td>S</td><td>Ground pin for system PLL and audio PLL</td></tr>
<tr><td>VDD_PLLV</td><td>86</td><td>S</td><td>1.8V power supply pin for system PLL</td></tr>
<tr><td>M_DD[7]</td><td>87</td><td>I/O</td><td>SDRAM data bus [7]</td></tr>
<tr><td>M_DD[6]</td><td>88</td><td>I/O</td><td>SDRAM data bus [6]</td></tr>
<tr><td>M_DD[5]</td><td>89</td><td>I/O</td><td>SDRAM data bus [5]</td></tr>
<tr><td>M_DD[4]</td><td>90</td><td>I/O</td><td>SDRAM data bus [4]</td></tr>
<tr><td>M_DD[3]</td><td>91</td><td>I/O</td><td>SDRAM data bus [3]</td></tr>
<tr><td>VDD_02</td><td>92</td><td>S</td><td>I/O power supply #2</td></tr>
<tr><td>M_DD[2]</td><td>93</td><td>I/O</td><td>SDRAM data bus [2]</td></tr>
<tr><td>M_DD[1]</td><td>94</td><td>I/O</td><td>SDRAM data bus [1]</td></tr>
<tr><td>M_DD[0]</td><td>95</td><td>I/O</td><td>SDRAM data bus [0]</td></tr>
<tr><td>M_WE_B</td><td>96</td><td>I/O</td><td>SDRAM write enable / row precharge</td></tr>
<tr><td>M_CAS_B</td><td>97</td><td>I/O</td><td>SDRAM column address strobe (CASB)</td></tr>
<tr><td>M_RAS_B</td><td>98</td><td>I/O</td><td>SDRAM column address strobe (RASB)</td></tr>
<tr><td>VSS_O2/VSS_K2</td><td>99</td><td>S</td><td>Kernel logic / I/O power shared ground supply #2</td></tr>
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<table>
<tr><th>Symbol</th><th>Pin NO</th><th>I/O</th><th>Description</th></tr>
<tr><td>M_CS0_B/GPIO</td><td>100</td><td>IO</td><td>SDRAM chip select 0, or GPIO[24]
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg0[0]=1'b1</td><td>SDRAM chip select(default)</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_D[2]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[24]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>M_BAO</td><td>101</td><td>IO</td><td>SDRAM bank select address_[0]_</td></tr>
<tr><td>M_DD[15]</td><td>102</td><td>IO</td><td>SDRAM data bus [15]</td></tr>
<tr><td>M_DD[14]</td><td>103</td><td>IO</td><td>SDRAM data bus [14]</td></tr>
<tr><td>M_DD[13]</td><td>104</td><td>IO</td><td>SDRAM data bus [13]</td></tr>
<tr><td>VDD_K2</td><td>105</td><td>S</td><td>Kernel logic power supply#2</td></tr>
<tr><td>M_DD[12]</td><td>106</td><td>IO</td><td>SDRAM data bus [12]</td></tr>
<tr><td>M_DD[11]</td><td>107</td><td>IO</td><td>SDRAM data bus [11]</td></tr>
<tr><td>M_DD[10]</td><td>108</td><td>IO</td><td>SDRAM data bus [10]</td></tr>
<tr><td>M_DD[9]</td><td>109</td><td>IO</td><td>SDRAM data bus [9]</td></tr>
<tr><td>M_DD[8]</td><td>110</td><td>IO</td><td>SDRAM data bus [8]</td></tr>
<tr><td>M__A[11]/GPIO</td><td>111</td><td>IO</td><td>SDRAM address bus [11] or GPIO [25]
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg1[4]=1'b1</td><td>DRAM address bus M_A[11] (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_C[2]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[25]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>VDD_03</td><td>112</td><td>S</td><td>I/O power supply #3</td></tr>
<tr><td>M_CLKO</td><td>113</td><td>O</td><td>SDRAM clock output</td></tr>
<tr><td>VSS_03/VSS_K3</td><td>114</td><td>S</td><td>Kernel logic/I/O power shared ground supply #3</td></tr>
<tr><td>M_CKE/GPIO</td><td>115</td><td>IO</td><td>SDRAM clock enable, or gpio[26]
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg0[1]=1'b1</td><td>DRAM clock enable (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_C[1]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[26]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>M_A[9]</td><td>116</td><td>lO</td><td>SDRAM address bus [9]</td></tr>
<tr><td>M_A[8]</td><td>117</td><td>lO</td><td>SDRAM address bus [8]</td></tr>
<tr><td>M_A[7]</td><td>118</td><td>lO</td><td>SDRAM address bus [7]</td></tr>
<tr><td>M_A[6]</td><td>119</td><td>I/O</td><td>SDRAM address bus [6]</td></tr>
<tr><td>M_A[5]</td><td>120</td><td>I/O</td><td>SDRAM address bus [5]</td></tr>
<tr><td>VDD_K3</td><td>121</td><td>S</td><td>Kernel logic power supply #3</td></tr>
<tr><td>M_A[4]</td><td>122</td><td>I/O</td><td>SDRAM address bus [4]</td></tr>
<tr><td>M_DQM1/GPIO</td><td>123</td><td>I/O</td><td>SDRAM data input/output mask for M_DD[15:8] or GPIOA[27]</td></tr>
<tr><td>M_DQM0/GPIO</td><td>124</td><td>I/O</td><td>SDRAM data input/output mask for M_DD[7:0] or GPIOA[28]
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg0[2]</td><td rowspan="2">SDRAM data input/output mask for M_DD[7,8] (default)</td><td>I/O</td></tr>
<tr><td></td><td></td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO _D_R[5]</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_B[3]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[28]</td><td>I/O</td></tr>
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<tr><td>M_BA1/GPIO</td><td>125</td><td>I/O</td><td>SDRAM bank select address [1] or GPIOA[29]
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg0[6]=1'b1</td><td>SDRAM bank select address [1] (default)</td><td>I/O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO _D_R[6]</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_DATA_B[2]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[29]</td><td>I/O</td></tr>
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<table>
<tr><th>Symbol</th><th>Pin NO</th><th>I/O</th><th>Description</th></tr>
<tr><td>M_A[10]</td><td>126</td><td>O</td><td>SDRAM address bus [10]</td></tr>
<tr><td>M_A[0]</td><td>127</td><td>O</td><td>SDRAM address bus [0]</td></tr>
<tr><td>VSS_04/VSS_K4</td><td>128</td><td>S</td><td>Kernel logic I/O power started around supply #4</td></tr>
<tr><td>M_A[1]</td><td>129</td><td>O</td><td>SDRAM address bus [1]</td></tr>
<tr><td>M_A[2]</td><td>130</td><td>O</td><td>SDRAM address bus [2]</td></tr>
<tr><td>M_A[3]</td><td>131</td><td>O</td><td>SDRAM address bus [3]</td></tr>
<tr><td>R_WE_B</td><td>132</td><td>I/O</td><td>ROM / SRAM / flash write strobe</td></tr>
<tr><td>R_A17</td><td>133</td><td>I/O</td><td>ROM / SRAM / flash address bus bit [17]</td></tr>
<tr><td>R_A14</td><td>134</td><td>I/O</td><td>ROM / SRAM / flash address bus bit [14]</td></tr>
<tr><td>R_A13</td><td>135</td><td>I/O</td><td>ROM / SRAM / flash address bus bit [13]</td></tr>
<tr><td>VDD_04</td><td>136</td><td>S</td><td>I/O power supply #4</td></tr>
<tr><td>R_A8</td><td>137</td><td>O</td><td>ROM / SRAM / flash address bus bit [8]</td></tr>
<tr><td>R_A9</td><td>138</td><td>O</td><td>ROM / SRAM / flash address bus bit [9]</td></tr>
<tr><td>R_A11</td><td>139</td><td>I/O</td><td>ROM / SRAM / flash address bus bit [11]</td></tr>
<tr><td>R_A10</td><td>140</td><td>O</td><td>ROM / SRAM / flash address bus bit [10]</td></tr>
<tr><td>M_DQM3/GPIO</td><td>141</td><td>I/O</td><td>SDRAM data input/output mask for M_DD[31:24] or GPIOA[38]
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg0[5]=1'b1</td><td>SDRAM data input/output mask for M_DD[31:24] (defalut)</td><td>O</td></tr>
<tr><td>sft_cfg2[3:2]=2'b11</td><td>UAC_RXO</td><td>I</td></tr>
<tr><td>sft_cfg1[8:6]=3'b011</td><td>R_CSAUb_B</td><td>O</td></tr>
<tr><td>sft_cfg3[13:12]=2'b10</td><td>TV_HSYNC</td><td>I/O</td></tr>
<tr><td>sft_cfg4[15:13]=3'b010</td><td>TV_HSYNC_PC</td><td>O</td></tr>
<tr><td>sft_cfg7[7:6]=2'b01</td><td>PCMCIA_LOW_B</td><td>O</td></tr>
<tr><td>sft_cfg0[13:12]=2'b01</td><td>TV_LOD_G[2]</td><td>O</td></tr>
<tr><td>sft_cfg7[1]=1'b0</td><td rowspan="3">FM_GPIO8[19]</td><td rowspan="3">I/O</td></tr>
<tr><td>sft_cfg0[11]=1'b0</td></tr>
<tr><td>fm_gpio_lan[13:0]>9</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_D_L[5]</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_OPA[1]</td><td>I</td></tr>
<tr><td>sft_cfg8[10]=1'b1</td><td>OGT_BIST_FAIL</td><td>O</td></tr>
<tr><td>(other)</td><td>OPIO[38]</td><td>I/O</td></tr>
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<tr><td>M_DQM2/GPIO</td><td>142</td><td>I/O</td><td>SDRAM data input/output mask for M_DD[31:24] or GPIOA[38]
<table>
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<tr><td>sft_cfg0[4]=1'b1</td><td>SDRAM data input/output mask for M_DD[23:16] (default)</td><td>O</td></tr>
<tr><td>sft_cfg2[8:2]=2'b11</td><td>UA0_TXD</td><td>O</td></tr>
<tr><td>sft_cfg3[13:12]=2'b10</td><td>TV_VSYNC</td><td>I/O</td></tr>
<tr><td>sft_cfg4[15:13]=3'b010</td><td>TV_VSYNC_PC</td><td>O</td></tr>
<tr><td>sft_cfg7[7:6]=2'b01</td><td>PCMCIA_IOR_B</td><td>O</td></tr>
<tr><td>sft_cfg0[13:12]=2'b01</td><td>TV_LCD_G[3]</td><td>O</td></tr>
<tr><td>sft_cfg7[1]=1'b0,</td><td rowspan="3">FM_GPIOB[18]</td><td rowspan="3">I/O</td></tr>
<tr><td>sft_cfgO[11]=1'b0,</td></tr>
<tr><td>fm_gpio_len[3:0]>9</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_D_L[6]</td><td>O</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_OPA[2]</td><td>I</td></tr>
<tr><td>sft_cfg8[10]=1'b1</td><td>BUF_CTRL_BIST_FAIL</td><td>O</td></tr>
<tr><td>(other)</td><td>GPIO[39]</td><td>I/O</td></tr>
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<tr><td>VDD_K4</td><td>143</td><td>S</td><td>Kernel logic power supply #4</td></tr>
<tr><td>R_OE_B</td><td>144</td><td>I/O</td><td>ROM / SRAM / flash output enable</td></tr>
<tr><td>R_D7</td><td>145</td><td>I/O</td><td>ROM / SRAM / flash data bus bit [7]</td></tr>
<tr><td>R_D6</td><td>146</td><td>I/O</td><td>ROM / SRAM / flash data bus bit [6]</td></tr>
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<tr><td>R_D5</td><td>147</td><td>I/O</td><td>ROM / SRAM / flash data bus bit [5]</td></tr>
<tr><td>R_D4</td><td>148</td><td>I/O</td><td>ROM / SRAM / flash data bus bit [4]</td></tr>
<tr><td>R_D3</td><td>149</td><td>I/O</td><td>ROM / SRAM / flash data bus bit [3]</td></tr>
<tr><td>VSS_O5/VSS_K5</td><td>150</td><td>S</td><td>Kernel logic / I/O power shared ground supply #5</td></tr>
<tr><td>R_D2</td><td>151</td><td>I/O</td><td>ROM / SRAM / flash data bus bit [2]</td></tr>
<tr><td>R_D1</td><td>152</td><td>I/O</td><td>ROM / SRAM / flash data bus bit [1]</td></tr>
<tr><td>R_D0</td><td>153</td><td>I/O</td><td>ROM / SRAM / flash data bus bit [0]</td></tr>
<tr><td>R_A0</td><td>154</td><td>O</td><td>ROM / SRAM / flash address bus bit [0]</td></tr>
<tr><td>R_A1</td><td>155</td><td>O</td><td>ROM / SRAM / flash address bus bit [1]</td></tr>
<tr><td>R_A2</td><td>156</td><td>O</td><td>ROM / SRAM / flash address bus bit [2]</td></tr>
<tr><td>R_A3</td><td>157</td><td>O</td><td>ROM / SRAM / flash address bus bit [3]</td></tr>
<tr><td>R_A4</td><td>158</td><td>O</td><td>ROM / SRAM / flash address bus bit [4]</td></tr>
<tr><td>R_A5</td><td>159</td><td>O</td><td>ROM / SRAM / flash address bus bit [5]</td></tr>
<tr><td>R_A6</td><td>160</td><td>O</td><td>ROM / SRAM / flash address bus bit [6]</td></tr>
<tr><td>R_A7</td><td>161</td><td>O</td><td>ROM / SRAM / flash address bus bit [7]</td></tr>
<tr><td>R_A12</td><td>162</td><td>I/O</td><td>ROM / SRAM / flash address bus bit [12]</td></tr>
<tr><td>R_A15</td><td>163</td><td>I/O</td><td>ROM / SRAM / flash address bus bit [15]</td></tr>
<tr><td>DAC_VREF</td><td>164</td><td>A</td><td>Audio DAC reference voltage, connect a 0.10F to ground</td></tr>
<tr><td>DAC_L</td><td>165</td><td>A</td><td>Audio DAC left-channel output</td></tr>
<tr><td>DAC_R</td><td>166</td><td>A</td><td>Audio DAC right-channel output</td></tr>
<tr><td>DAC_VDD</td><td>167</td><td>S</td><td>3.3V power supply for on-chip audio DAC</td></tr>
<tr><td>DAC_VSS</td><td>168</td><td>S</td><td>Ground pin for on-chip audio DAC</td></tr>
<tr><td>R_A16</td><td>169</td><td>I/O</td><td>ROM / SRAM / flash address bus bit [16]</td></tr>
<tr><td>R_A18</td><td>170</td><td>I/O</td><td>ROM / SRAM / flash address bus bit [18]</td></tr>
<tr><td>A_IEC_TX/GPIO</td><td>171</td><td>I/O</td><td>IEC 958 transmit data
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg3[8]=1'b1</td><td>A_IEC_TX (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_C[0]</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_OPF[0]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[52]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>A_DATA[0]/GPIO</td><td>172</td><td>I/O</td><td>Serial audio data output for channel 1/0 or GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg3[1]=1'b1</td><td>A_DATA[0] (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_C[1]</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_OPF[1]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[53]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>VDD_O5</td><td>173</td><td>S</td><td>I/O power supply #5</td></tr>
<tr><td>A_DATA[1]/GPIO</td><td>174</td><td>I/O</td><td>Serial audio data output for channel 3/2 or GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg3[2]=1'b1</td><td>A_DATA[1] (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_C[2]</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_OPF[2]</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[54]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>A_DATA[2]/GPIO</td><td>175</td><td>I/O</td><td>Serial audio data output for channel 5/4 or GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg3[3]=1'b1</td><td>A_DATA[2] (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_PWAD</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_PDALL</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[55]</td><td>I/O</td></tr>
</table></td></tr>
</table>

<table>
<tr><th>Symbol</th><th>Pin NO</th><th>I/O</th><th>Description</th></tr>
<tr><td>A_DATA[3]/GPIO</td><td>176</td><td>I/O</td><td>Serial audio data output for channel 7/6 or GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg3[4]=1'b1</td><td>A_DATA[3] (default)</td><td>O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_SPGA</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAG_TEST</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[56]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>A_LRCK/GPIO</td><td>177</td><td>I/O</td><td>PCM data output DR strobe
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg3[6]=1'b1</td><td>A_LRCK (default)</td><td>I/O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_MODE1</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_UD</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[57]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>VSS_O6/VSS_K6</td><td>178</td><td>S</td><td>Kernel logic I/O power shard ground supply #6</td></tr>
<tr><td>A_BCK/GPIO</td><td>179</td><td>I/O</td><td>PCM bit clock
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg3[0]=1'b1</td><td>A_BCK (default)</td><td>I/O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_MODE1_1</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_BGPD</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[58]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>A_BCK/GPIO</td><td>180</td><td>I/O</td><td>Audio over-sampling clock
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg3[9]=1'b1</td><td>A_XCK (default)</td><td>I/O</td></tr>
<tr><td>sft_cfg8[8]=1'b1</td><td>ADC_MONO_MODE2</td><td>I</td></tr>
<tr><td>sft_cfg8[9]=1'b1</td><td>DAC_CLK</td><td>I</td></tr>
<tr><td>(other)</td><td>GPIO[59]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>UA0_RX/GPIO</td><td>181</td><td>I/O</td><td>UART #0 data receive or GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[3:2]=2'b01</td><td>UART0_RX (default)</td><td>I</td></tr>
<tr><td>sft_cfg3[13:12]=2'b01</td><td>TV_HSYNC</td><td>I/O</td></tr>
<tr><td>sft_cfg4[15:13]=3'b011</td><td>HSYNC_PC</td><td>O</td></tr>
<tr><td>(other)</td><td>GPIO[60]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>UA0_TX/GPIO</td><td>182</td><td>I/O</td><td>UART #0 data transmit or GPIO
<table>
<tr><th>Priority selection</th><th>Function</th><th>Dir</th></tr>
<tr><td>sft_cfg2[3:2]=2'b01</td><td>UART0_TX (default)</td><td>O</td></tr>
<tr><td>sft_cfg3[13:12]=2'b01</td><td>TV_HSYNC</td><td>I/O</td></tr>
<tr><td>sft_cfg4[15:13]=3'b011</td><td>HSYNC_PC</td><td>O</td></tr>
<tr><td>(other)</td><td>GPIO[61]</td><td>I/O</td></tr>
</table></td></tr>
<tr><td>V_COMP</td><td>183</td><td>A</td><td>(VDAC CBU) Compensation pin. Connect a 0.1pF ceramic capacitor to bypass this pin to VSSA. The lead length must be kept as short as possible to avoid noise.</td></tr>
<tr><td>V_BIAS</td><td>184</td><td></td><td>(VDAC CBL) Bias voltage. Connect a 0.1pF ceramic capacitor to bypass this pin to VSSA. The lead length must be kept as short as possible to avoid noise.</td></tr>
<tr><td>V_FSADJ</td><td>185</td><td>A</td><td>Full-Scale adjustment control pin. The full-scale current of D/A resistor (Pset) between this pin and ground.</td></tr>
<tr><td>V_REFOUT</td><td>186</td><td>A</td><td>(VDAC VREF/bandgap output) Voltage reference output. It generates typical 1.2V voltage reference and may be used to drive V_REFIN pin directly.</td></tr>
</table>

Symbol	Pin NO	I/O	Description
V_DAC[0]	187	A	Video DAC output #0. This is a high-impedance current source output. These outputs can drive a 37.5Ω load directly.
VDD_TVA0	188	S	TV_DAC power supply #0
VSS_TVA0	189	S	TV_DAC ground pin #6
VDD_TVA1	190	S	TV_DAC power supply #1
VSS_TVA1	191	S	TV_DAC ground pin #1
V_DAC[3]	192	A	Video DAC output #3. This is a high-impedance current source output. These outputs can drive a 37.5Ω load directly.
V_DAC[4]	193	A	Video DAC output #4. This is a high-impedance current source output. These outputs can drive a 37.5Ω load directly.
VDD_TVA2	194	S	TV_DAC power supply #2
V_DAC[5]	195	A	Video DAC output #5. This is a high-impedance current source output. These outputs can drive a 37.5Ω load directly.
VSS_TVA2	196	S	TV_DAC ground pin #2
PLL_AVDD	197	S	Servo PLL 3.3V power.
LPFO	198	A	NC pin
LPFN	199	A	NC pin
VREFO	200	A	
PDFLT	201	A	
FDFLT	202	A	
LPFNIN	203	A	
LGIN	204	A	
PLL_DS_AVSS	205	S	Servo PLL/Data-slicer ground.
RFI	206	A	
CNIN	207	A	
SLVL	208	A	
DS_AVDD	209	S	Servo Data slicer 3.3V power.
RF_AVDD	210	S	Servo RF 3.3V power.
GMRES	211	A	External reference resistor input.
AGCCAP	212	A	External AGC capacitor connected to ground.
RFRP	213	O	RFRP signal output.
RFO	214	O	RF signal output.
FLTIP	215	I	Differential RF equalizer input #P.
FLTIN	216	I	Differential RF equalizer input #N.

IC104, EEPROM 2K BIT 2-WIRE SERIAL

DESCRIPTION:

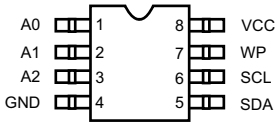
The Turbo IC 24CO1/24CO2 is a serial 1K/2K EEPROM fabricated with Turbo's proprietary, high reliability, high performance CMOS technology. It's 1K/2K of memory is configured as 16/32 pages with each page containing 8 bytes. This device offers significant advantages in low power applications.

The Turbo IC 24C01/24C02 uses the PC addressing protocol and 2-wire serial interface which includes a bidirectional serial data bus syncronized by a clock. It offers a flexible byte write and a faster 8-byte page write.

The Turbo IC 24C01/24C02 is assembled in either a 8-pin PDIP or 8-pin SOIC package. Pin #1 is the A0 device address input for the device. Pin#2 is the A1 device adrress input for the device. Pin #3 is the A2 device address input for the device, such that a total of eight 24C01/24C02 devices can be connected on a single bus. Pin #4 is the ground (Vss). Pin #5 is the serial data (SDA) pin used for bidirectional transfer of data. Pin#6 is the serial clock (SCL) input pin. Pin #7 is the write protect (WP) pin used to be protect hardware data. Pin#8 is the power supply (Vcc) pin.

All data is serially transmitted in bytes (8 bits) on the SDA bus. To access the Turbo IC24C01/24C02 (slave) for a raad or write operation, the controller (master) issues a start condition by pulling SDA from high to low while SCL is high. The master then issues the device address byte which consists of 1010(A2)(A1)(A0)($R\overline{W}$). The most significant bits(1010) are a device type code signifying an EEPROM device. A0,A1,and A2 are the device address select bits which has to match the A0,A1,and A2 pin inputs on the device. The B[7] bit (or B[6] bit in the 24C01) is the most significant bit of the memory address. The read/write bit determines whether to do a read or write operation. After each byte is transmitted , the receiver has to provide an acknowledge by pulling the SDA bus low on the ninth clock cycle. The acknowledge is a handshake signal to the transmitter indicating a successful data transmission.

Pin Description:



8 PIN SOIC

PIN DESCRIPTION:

DEVICE ADDRESS (A0 & A1 & A2).

A0, A1, and A2 are device address inputs that enables a total of sight 24C02 devices to connect on a simple bus, if the address input pin is left unconnected, it is interpreted as zero.

WRITE PROTECT(WP)

When the write protect input is connected to Vcc, the entire memory array is protected against write operations. For normal write operations, the write protect pin should be grounded, When the pin is left unconnected. WP is interpreted as zero.

SERIAL DATA(SDA)

SDA is a bidirectional pin used to transfer data in and out of the Turbo IC24C01/24C02. The pin is an open-drain output. A pullup resistor must be connected from SDA to Vcc.

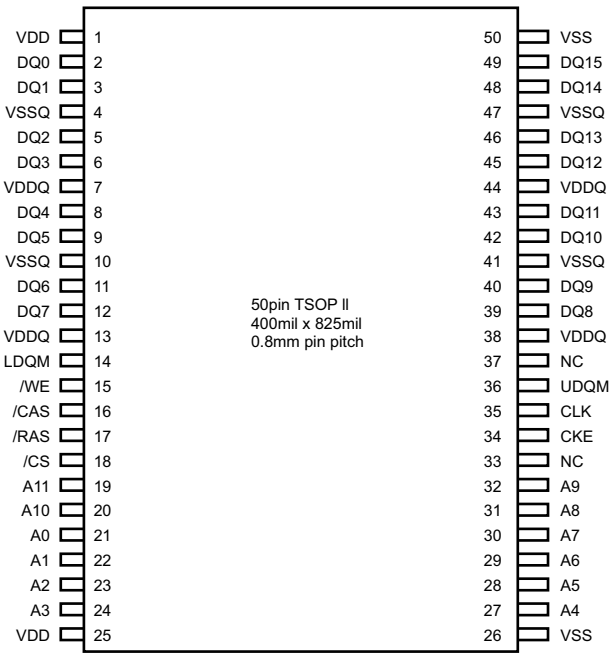
SERIAL CLOCK(SCL)

The SCL input synchronizes the data or the SDA bus. It is used in conjunction with SDA to define the start and stop conditions. It is also used in conjunction with SDA to transfer data to and from the Turbo IC 24C01/24C02.

DV-SL700W

IC106, CMOS DRAM 143MHZ 16MB

Pin Configuration:

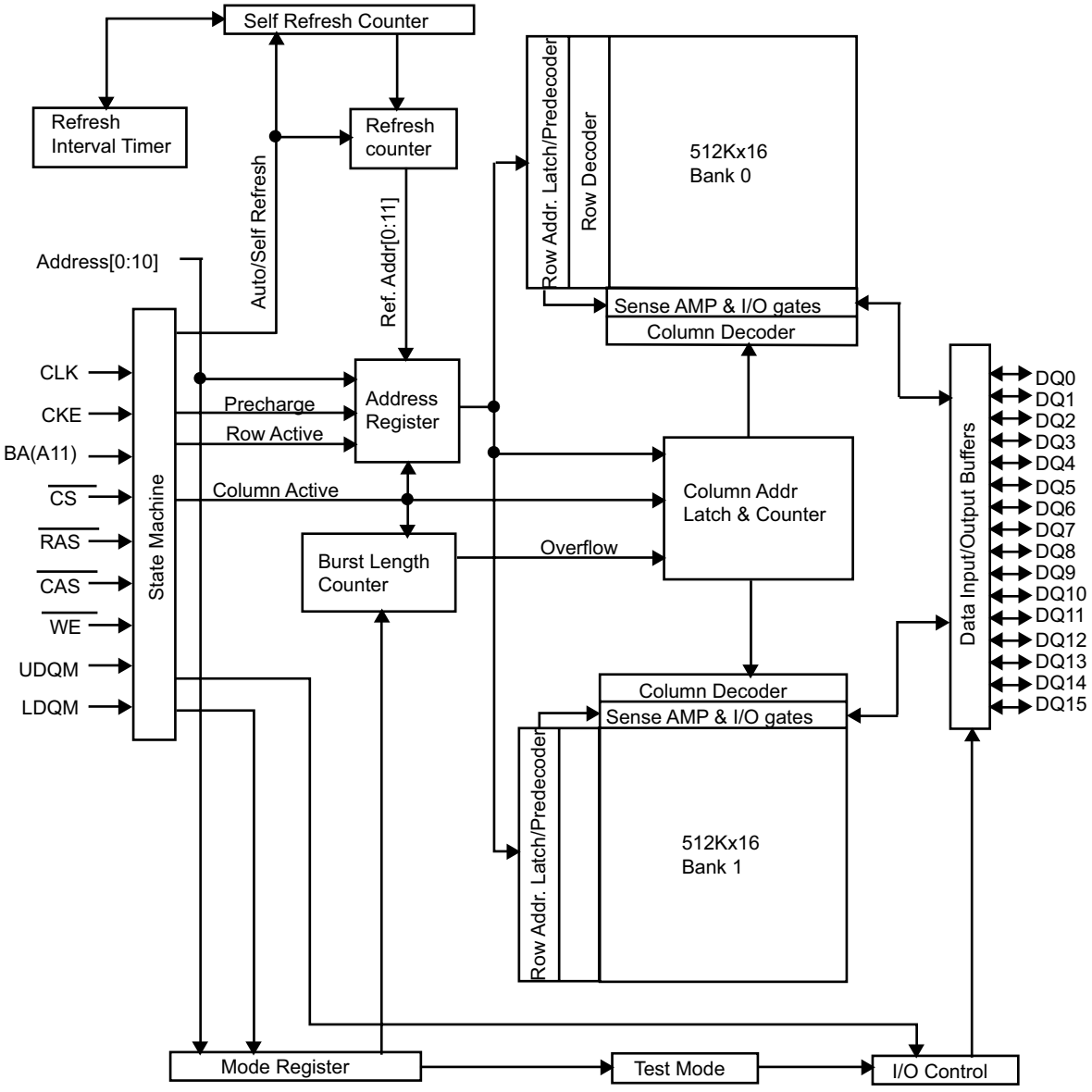


Pin Description:

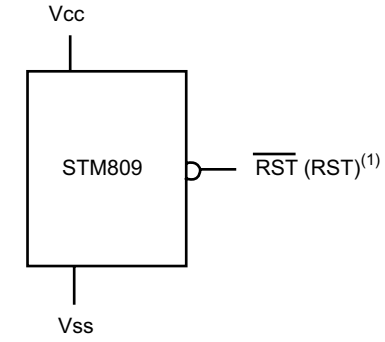
PIN	PIN NAME	DESCRIPTION
CLK	Clock	The system clock input. All other inputs are referenced to the SDRAM on the rising edge of CLK.
CKE	Clock Enable	Controls internal clock signal and when deactivated, the SDRAM will be one of the stales among power down, suspend or self refresh.
$\overline{CS}$	Chip Select	Command input enable or mask except CLK, CKE and DQM.
BA	Bank Address	Select either one of banks during both $\overline{RAS}$ and $\overline{CAS}$ activity.
A0 ~ A10	Address	Row Address: RAD ~ RA10, Coiumn Address: CA0 ~ CA7 Auto-precharge flag: A10
$\overline{RAS}$, $\overline{CAS}$, $\overline{WE}$	Row Address Strobe, Column Address Strobe, Write Enable	$\overline{RAS}$, $\overline{CAS}$ and $\overline{WE}$ define the operation. Refer function truth table for details.
LDQM, UDQM	Date Input/Output Mask	DQM control output buffer in read mode and mask input data in write mode.
DQ0 ~ DQ15	Date Input/Output	Multiplexed data input/output pin.
VDD/VSS	Power Supply/Ground	Power supply for internal circuit and input buffer
VDDQ/VSSQ	Data Output Power/Ground	Power supply for DQ
NC	No Connection	No connection

IC106, CMOS DRAM 143MHZ 16MB

Functional Block Diagram:



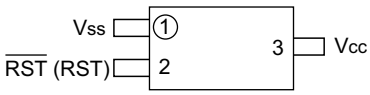
IC120, ST 3.0V RESET CIRCUIT



Logic Diagram (STM809)

Vss	Ground
$\overline{RST}$	Active-Low RESET Output
RST(1)	Active-High RESET Output
Vcc	Supply Voltage
$\overline{MR}$(2)	Manual Reset Input

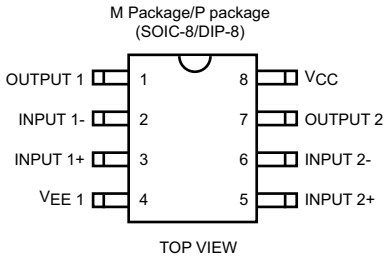
Signal Names



SOT23-3 Connections

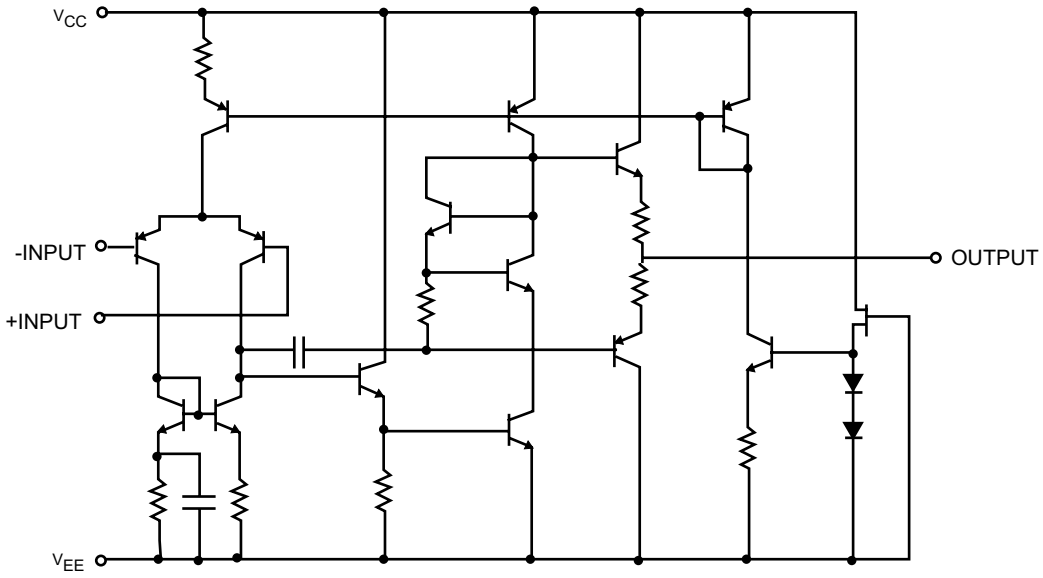
IC402, DUAL LOW NOISE OPERATIONAL AMP

Pin Configuration:



Pin Configuration of AZ4558A

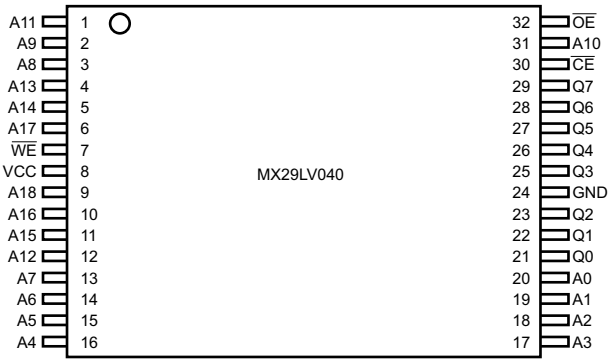
Functional Block Diagram:



Representative Schematic Diagram of AZ4558A (Each Amplifier)

IC103A, CMOS 3V 4M FLASH MEMORY

Pin Configurations:



Sector Structure

Sector	A18	A17	A16	Address Range
SA0	0	0	0	00000h-0FFFFh
SA1	0	0	1	10000h-1FFFFh
SA2	0	1	0	20000h-2FFFFh
SA3	0	1	1	30000h-3FFFFh
SA4	1	0	0	40000h-4FFFFh
SA5	1	0	1	50000h-5FFFFh
SA6	1	1	0	60000h-6FFFFh
SA7	1	1	1	70000h-7FFFFh

Note: All sectors are 64 Kbytes in side.

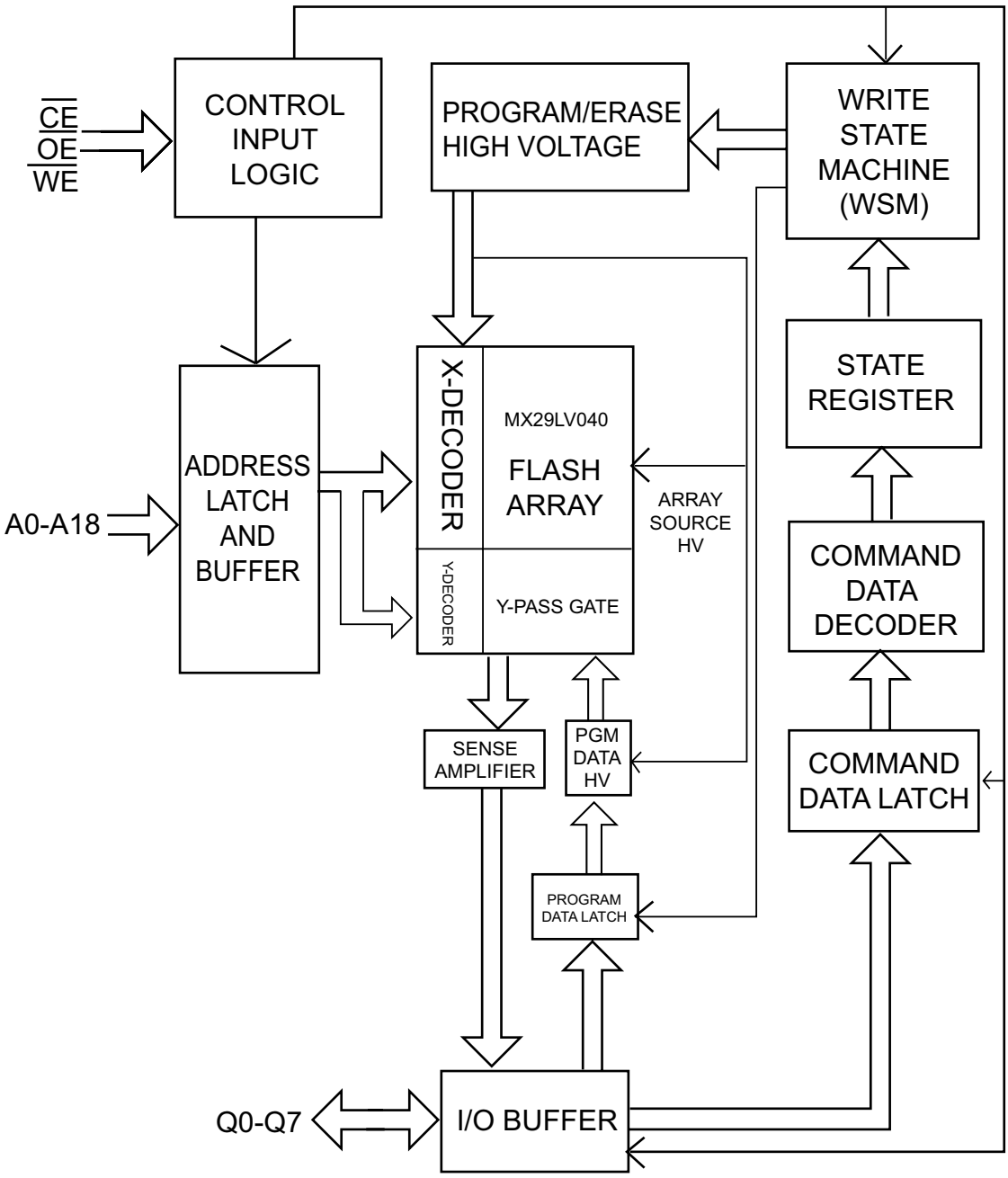
PIN DESCRIPTION

SYMBOL	**PIN NAME**
A0~A18	Address Input
Q0~Q7	Data Input/Output
$\overline{CE}$	Chip Enable Input
$\overline{WE}$	Write Enable Input
$\overline{OE}$	Output Enable Input
GND	Ground Pin
VCC	+3.0V single power supply

DV-SL700W

IC103A, CMOS 3V 4M FLASH MEMORY

Block Diagram:



SHARP PARTS GUIDE

DVD PLAYER

MODEL DV-SL700W

CONTENTS

Parts marked with "⚠" are important for maintaining the safety of the set. Be sure to replace these parts with specified ones for maintaining the safety and performance of the set.

SHARP CORPORATION

NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
[1] INTEGRATED CIRCUITS					
IC101	92LRCI00820206	BE			IC 216PIN SPHE8202D LQFP SUNPLUS DVD SINGLE CHIP MPEG A / V PROC
IC104	92LRCI24000206	AG			IC 8P AF24BC02-SI SOIC APLUS EEPROM 2K BIT 2-WIRE SERIAL
IC105	92LRCI00586901	AP			IC 28PIN AM5869S HSOP AMTEK 5-CH BTL LF DRIVER FOR DVD PLAYER
IC106	92LRCI16161001	AW			IC 50P HY57V161610ET-7 TSOPII HYNIX 143MHz 2BANKSx512KX16BIT
IC120	92LRCI00080912	AG			GP IC 3PIN STM809SWX6F SOT23-3 ST 3.0V RESET CIRCUIT
IC103A	92LRCI29004000	AV			IC 32P MX29LV040CTC-70G TSOP MXIC CMOS 3V 4M FLASH MEMORY
IC402	92LRCI00455806	AG			GP IC 8P AZ4558AM-E1 SOIC-8 AAC DUAL LOW NOISE OPER. AMP
PC1	92LRHO00081702	AG			GP OPTICAL SENSOR 4P CTR:200~400 EVERLIGHT:EL-817 (C)
U1	92LRHI00431001	AE			GP IC 3PIN TL431 TO-92 CHANG JIANG VOLTAGE REGULATOR
[2] TRANSISTORS					
Q1	92LRHM00406001	AL			MOSFET SSS4N60B TO-220F N-CH FAIRCHILD 4A 600V
Q2	92LRCP00390601	AD			XISTR NPN PMBT3904 SOT23 PHILIPS
Q5	92LRCN00390405	AC			XISTR NPN PMBT3904 SOT23 PHILIPS NPN SWITCHING TRANSISTOR
Q6	92LRAN20181501	AC			GP XISTR NPN 2SC1815-GR TOSHIBA
Q7	92LRAP00101501	AC			GP XISTR PNP A1015 Y TO-92 J.CHANGJIANG
Q101,Q102	92LRCP20113203	AE			GP XISTR PNP 2SB1132RT100 ROHM HFE:180-390 MEDIUM POWER
Q103,Q104	92LRCM20301802	AC			GP MOSFET 2SK3018 30V/0.1A SOT23 GR N-CH
Q105	92LRCN00390405	AC			XISTR NPN PMBT3904 SOT23 PHILIPS NPN SWITCHING TRANSISTOR
Q301,Q302	92LRCP20073301	AC			GP XISTR PNP 2SA812 HFE:200~400 SMT TYPE (2SA733)
Q305	92LRCN00390405	AC			XISTR NPN PMBT3904 SOT23 PHILIPS NPN SWITCHING TRANSISTOR
Q401,Q402	92LRCN00390405	AC			XISTR NPN PMBT3904 SOT23 PHILIPS NPN SWITCHING TRANSISTOR
Q403~Q407	92LRCP00390601	AC			XISTR PNP SST3906 SOT ROHM GENERAL PURPOSE
Q901,Q902	92LRAP00855001	AC			XISTR PNP 8550SS-D TO-92 CJ GP HFE:160~300 1W OUTPUT AM
[3] DIODES					
D1~ D4	92LRAD11400710	AC			GP DIODE 1N4007 AI
D5	92LRAD10010710	AC			GP DIODE FR107 1A 1000V
D7	92LRCD11414811	AB			GP DIODE CHIP BAV16W / IN4148 (SKYWELL) SOT-123
D13	92LRAD10010710	AC			GP DIODE FR107 1A 1000V
D14	92LRHD11582211	AF			GP RECTIFIER IN5822 3A 45V W/KINK SCHOTTKY BARRIER
D101	92LRCD11414811 (CONTROL PCB)	AB			GP DIODE CHIP BAV16W / IN4148 (SKYWELL) SOT-123
D101	92LRCD11414811 (MAIN PCB)	AB			GP DIODE CHIP BAV16W / IN4148 (SKYWELL) SOT-123
D102	92LRCD11414811 (CONTROL PCB)	AB			GP DIODE CHIP BAV16W / IN4148 (SKYWELL) SOT-123
D102	92LRCD11414811 (MAIN PCB)	AB			GP DIODE CHIP BAV16W / IN4148 (SKYWELL) SOT-123
D103~D105	92LRCD11414811	AB			GP DIODE CHIP BAV16W / IN4148 (SKYWELL) SOT-123
D201	92LKED50000310	AC			GP LED 3 DIA GREEN ROUND
D202	92LKED20000310	AC			GP LED 3 DIA RED ROUND
D203,D204	92LRCD11414811	AB			GP DIODE CHIP BAV16W / IN4148 (SKYWELL) SOT-123
D301~D304	92LRAD11400710	AC			GP DIODE IN4007 AI
D305,D306	92LRAD10010710	AC			GP DIODE FR107 1A 1000V
D308	92LRAD10010410	AC			GP DIODE HER104 1A 300V 50nS AI
D311	92LRHD11582211	AF			GP RECTIFIER IN5822 3A 45V W / KINKSCHOTTKY BARRIER
D404	92LRCD11414811	AB			GP DIODE CHIP BAV16W / IN4148 (SKYWELL) SOT-123
D902	92LRAD11400110	AB			GP DIODE IN4007 AI
ZD1,ZD3	92LRCZ00501810	AC			CHIP ZENER 18V 5% 0.5W (J3) MOTOROLA MMSZ5248BTP1
ZD4	92LRAZ01000620	AC			GP DIODE ZNR 6.2V 5% 1W DO-41 IN4735A
ZD301~ZD308	92LRCZ00500710	AC			GP CHIP ZENER 6.8V 5% 0.5W (E5) SOD-123 MOTOROLA MMSZ5235BT1
[4] FILTER					
⚠ LF1	92LJFT00100701	AK			LINE FILTER 20mH ET-20 EF001
[5] VIBRATORS/CRYSTAL					
XT1	92LJQC15810060	AG			GP CRYSTAL 27MHz HC49 / US + /-15ppm DONG YUAN SHORT DP3080
[6] TRANSFORMER					
⚠ T1	92LTSA10000815	AE			SW TRANSFORMER EEL-22 15W OUT / 5 ET001
[7] COILS					
FB123	92LSCB00603060	AB			FERRITE 600 OHM 0603 SMT TYPE 20%
FB125~FB137	92LSCB00603060	AB			FERRITE 600 OHM 0603 SMT TYPE 20%
FB301~FB307	92LSCB00603010	AC			GP CHIP BEAD 100 OHM AT 100MHz SMD 0603 GTH DP301
FB309	92LSCB00603010	AC			GP CHIP BEAD 100 OHM AT 100MHz SMD 0603 GTH DP301
FB401,FB402	92LSCB00603060	AB			FERRITE 600 OHM 0603 SMT TYPE 20%
FB901	92LSCB00603112	AC			GP CHIP BEAD 600 OHM 25% AT 100MHz 2A 0603 MICROGATE:MGLB1608-600T-L
FB902	92LSCB00603060	AB			FERRITE 600 OHM 0603 SMT TYPE 20%
FB904,FB905	92LSCB00603060	AB			FERRITE 600 OHM 0603 SMT TYPE 20%
FB907,FB910	92LSCB00603060	AB			FERRITE 600 OHM 0603 SMT TYPE 20%
FB912,FB913	92LSCB00603110	AB			GP CHIP BEAD 600 OHM 25% AT 100MHz IDC:200 EROCORE:CB160808T-601Y
FB918,FB919	92LSCB00603110	AB			GP CHIP BEAD 600 OHM 25% AT 100MHz IDC:200 EROCORE:CB160808T-601Y
L2	92LSIN00360000	AD			GP INDUC 20uH 10% D0.45x27.5Ts
L11,L12	92LSCN02360000	AC			GP CHIP INDUC 10uH 10% SMD 0805 SGMI2012K100KT
L301~L303	92LSCN02350090	AC			GP INDUCTOR 2.4uH 5% SMD 0805 VIDEO SW
L304	92LSCN02350190	AC			CHOKE 1 uH 5% 0805 SMT TYPE
L310	92LSCN12260090	AC			GP CHIP INDUC 1.8uH 10% SMD 0603 IDC:200 EROCORE:CB160808T-601Y
[8] ARRAY PARTS					
RB101,RB102	92LQCP01508133	AC			GP CHIP ARRAY 4x33 OHM 1 / 10 W 5% 8 PIN CHIP

	NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
	[8] ARRAY PARTS					
	RB105~RB107	92LQCP01508133	AC			GP CHIP ARRAY 4x33 OHM 1 / 10 W 5% 8 PIN CHIP
	RB110,RB111	92LQCP01508133	AC			GP CHIP ARRAY 4x33 OHM 1 / 10 W 5% 8 PIN CHIP
	RB113~RB117	92LQCP01508133	AC			GP CHIP ARRAY 4x33 OHM 1 / 10 W 5% 8 PIN CHIP
	[9] CAPACITORS					
⚠	C1	92LPLE0995K200	AG			GP COND ELECT 15 uF 400V 20% 105C SIZE=D10 X H21 X P5 MM
	C2	92LPVD2354Q020	AD			GP COND DISC 4700 pF 1KV 10% Y5P
	C3	92LPYL43957047	AC			GP CHIP CAP 0.47 uF 50V 20% 0805 Y5V
	C4	92LPRM0373A020	AC			GP COND MYLAR 0.001 uF 100V 5% AI
	C5	92LPYL45647020	AB			GP CHIP CAP 0.0047 uF 50V 10% 0805 TYPE X7R
	C6	92LPYL45647030	AC			GP CHIP CAP 0.01 uF 50V 10% 0805 TYPE Y5V
	C7	92LPRE03954022	AC			GP COND ELECT 220 uF 16V 20% 85°C
	C8	92LPRE09954047	AH			GP COND ELECT 47 uF 16V 20% 105°C MINI SIZE DTA150 / SANYO
	C11	92LPVE29954010	AD			GP COND ELECT 330 uF 16V 20% 105°C LOW ESR D8 X L12MM
	C12	92LPYL43957040	AB			GP CHIP CAP 0.1 uF 50V 20% 0805 TYPE Y5V
	C13	92LPVE03953020	AF			GP COND ELECT 2200 uF 10V 20% 85°C CUT 3.5MM (USE IN POWER PCB)
	C13	92LPRE03953033	AC			GP COND ELECT 330 uF 10V 20% 85°C (USE IN CONTROL PCB)
	C14	92LPRE09954010	AC			GP COND ELECT 100 uF 16V 20% 105°C (USE IN CONTROL PCB)
	C14	92LPRE03953047	AC			GP COND ELECT 470 uF 10V 20% 85°C (USE IN POWER PCB)
	C16	92LPYL43967050	AC			GP CHIP CAP 1 uF 50V +80-20% 0805 TYPE Y5V
	C19	92LPVM0373A020	AC			GP COND MYLAR 0.0022 uF 100V 5%
	C101~C137	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C138	92LPXL45647010	AA			GP CHIP CAP 100 pF 50V 10% 0603 TYPE X7R
	C140,C141	92LPXL41037001	AB			GP CHIP CAP 10 pF 50V 5% 0603 TYPE NPO
	C142	92LPXL45647010	AA			GP CHIP CAP 100 pF 50V 10% 0603 TYPE X7R
	C143	92LPXL41037001	AB			GP CHIP CAP 10 pF 50V 5% 0603 TYPE NPO
	C144,C145	92LPXL45647010	AA			GP CHIP CAP 100 pF 50V 10% 0603 TYPE X7R
	C152	92LPME03954010	AC			GP COND ELECT 100 uF 16V 20% Size=5x11mm AI 85°C P=2.5mm
	C153	92LPME03954000	AC			GP COND ELECT 10 uF 16V 20% 85°C P=2.5mm AI
	C154~C158	92LPXL45647081	AA			GP CHIP CAP 680 pF 50V 10% 0603 TYPE X7R
	C159~C162	92LPXL45647030	AB			GP CHIP CAP 0.01 uF 50V 10% 0603 TYPE X7R
	C163~C166	92LPXL43954050	AB			GP CHIP CAP 1 uF 16V 20% 0603 TYPE Y5V
	C167	92LPXL45647083	AB			GP CHIP CAP 0.068 uF 50V 10% 0603 TYPE X7R
	C168	92LPXL45647010	AA			GP CHIP CAP 100 pF 50V 10% 0603 TYPE X7R
	C169,C170	92LPXL41037047	AB			GP CHIP CAP 47 pF 50V 5% 0603 TYPE NPO
	C176	92LPME83954010	AC			GP COND ELECT 100 uF 16V 20% AI H=5mm P=2.5mm
	C177~C179	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C180,C181	92LPME03954047	AC			GP COND ELECT 47 uF 16V 20% SIZE=5x11mm AI 85°C P=2.5mm
	C182,C183	92LPME89954000	AC			GP COND ELECT 47 uF 16V 20% 105°C AI Type D=5mm H<=5mm P=2.5mm DP3
	C184	92LPXL45647031	AA			GP CHIP CAP 300 pF 50V ±10% 0603 TYPE X7R
	C186	92LPXL45647047	AC			GP CHIP CAP 0.047 uF 50V 10% 0603 TYPE X7R
	C187	92LPXL45647021	AA			GP CHIP CAP 820 pF 50V 10% 0603 TYPE X7R
	C188	92LPXL45645040	AC			GP CHIP CAP 0.22 uF 25V 10% 0603 TYPE X7R
	C198	92LPME03954010	AC			GP COND ELECT 100 uF 16V 20% Size=5x11mm AI 85°C P=2.5mm
	C199~C209	92LPXL41037000	AA			GP CHIP CAP 33 pF 50V 5% 0603 TYPE NPO
	C210	92LPXL45647040	AC			GP CHIP CAP 0.001 uF 50V 10% 0603 TYPE X7R
	C211,C212	92LPXL45647020	AA			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C213	92LPXL45647033	AA			GP CHIP CAP 330 pF 50V 10% 0603 TYPE X7R
	C214,C215	92LPXL45647072	AA			GP CHIP CAP 0.0047 uF 50V 10% 0603 TYPE X7R
	C216~C218	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C219	92LPXL45647030	AB			GP CHIP CAP 0.01 uF 50V 10% 0603 TYPE X7R
	C221~C226	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C230~C232	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C234~C239	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C301	92LPXL45647071	AA			GP CHIP CAP 470 pF 50V 10% 0603 TYPE X7R
	C302,C303	92LPXL45647071	AA			GP CHIP CAP 470 pF 50V 10% 0603 TYPE X7R
	C304	92LPXL45647001	AA			GP CHIP CAP 200 pF 50V 10% 0603 TYPE X7R
	C305,C306	92LPXL45647071	AA			GP CHIP CAP 470 pF 50V 10% 0603 TYPE X7R
	C307	92LPXL45647071	AA			GP CHIP CAP 470 pF 50V 10% 0603 TYPE X7R
	C308	92LPXL45647001	AA			GP CHIP CAP 200 pF 50V 10% 0603 TYPE X7R
	C309	92LPXL41037050	AA			GP CHIP CAP 15 pF 50V 5% 0603 TYPE NPO
	C310,C311	92LPXL41037050	AA			GP CHIP CAP 15 pF 50V 5% 0603 TYPE NPO
	C312~C315	92LPXL41037050	AA			GP CHIP CAP 15 pF 50V 5% 0603 TYPE NPO
	C316	92LPXL41037050	AA			GP CHIP CAP 15 pF 50V 5% 0603 TYPE NPO
	C401~C403	92LPME03954000	AC			GP COND ELECT 10 uF 16V 20% 85°C P=2.5mm AI
	C404	92LPME03954010	AC			GP COND ELECT 100 uF 16V 20% Size=5x11mm AI 85C P=2.5mm
	C405	92LPME03954000	AC			GP COND ELECT 10 uF 16V 20% 85°C P=2.5mm AI
	C408,C409	92LPXL45647018	AA			GP CHIP CAP 180 pF 50V 10% 0603 TYPE X7R
	C410,C411	92LPXL45647010	AA			GP CHIP CAP 100 pF 50V 10% 0603 TYPE X7R
	C412~C414	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C416	92LPME03954010	AC			GP COND ELECT 100 uF 16V 20% Size=5x11mm AI 85C P=2.5mm
	C421	92LPME03954022	AC			GP COND ELECT 220 uF 16V 20% 85°C
	C422~C425	92LPXL45647056	AA			GP CHIP CAP 0.0056 uF 50V 10% 0603 TYPE X7R
	C426,C901	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C427	92LPME03954022	AC			GP COND ELECT 220 uF 16V 20% 85°C
	C904,C905	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C907,C908	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C911~C913	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C916,C919	92LPME03954010	AC			GP COND ELECT 100 uF 16V 20% Size=5x11mm AI 85C P=2.5mm
	C920,C921	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C923	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C925~C927	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C929~C932	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
	C925~C927	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R

NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
[9] CAPACITORS					
C929,C932	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
C934	92LPME03954010	AC			GP COND ELECT 100 uF 16V 20% Size=5x11mm AI 85°C P=2.5mm
C936	92LPME03953010	AC			GP COND ELECT 100 uF 10V 20% 85°C P=2.5mm Size 5x11mm
C938	92LPME03954022	AC			GP COND ELECT 220 uF 16V 20% 85°C
C940	92LPXL45647040	AC			GP CHIP CAP 0.1 uF 50V 10% 0603 TYPE X7R
C941	92LPME03953010	AC			GP COND ELECT 100 uF 10V 20% 85°C P=2.5mm Size 5x11mm
⚠ CX1	92LPVX2024F042	AE			GP COND SAFETY 0.1 uF 250V + / -10% X2 P=15MM CUT 3.5MM
⚠ CY3	92LPVY1705F120	AF			GP COND SAFETY 0.0033 uF Y1 250V 20% X1 400V WD16E332MACT4KV (PAN)
[10] RESISTORS					
FB101	92LQCF01503000	AA			GP CHIP RES. 0 OHM 1 / 10W 5% TF 0603 SMT TYPE
Q7	92LQCF08502000	AA			GP CHIP RES. 0 OHM JUMP WIRE 0805 SMT TYPE
R1,R2	92LQCF04501040	AA			GP CHIP RES. 470K OHM 1 / 4W 5% CF 1206 SMT TYPE
R3	92LQHF10500040	AC			GP RES.100K OHM 1W 5% 0805 SMT TYPE (USE IN CONTROL PCB)
R4	92LQCF0450109A	AA			GP CHIP RES. 4.7 OHM 1 / 4W 5% 1206 SMT TYPE
R5	92LQCF04500024	AA			GP CHIP RES. 820K OHM 1 / 4W 5% CF
R6	92LQCF04501082	AA			GP CHIP RES. 820K OHM 1 / 4W 5% 1206 SMT TYPE
R7	92LQCF04501093	AA			GP CHIP RES. 39K OHM 1 / 4W 5% CF 1206 SMT TYPE
R8	92LQCF0450156A	AA			GP CHIP RES. 560 OHM 1 / 4W 5% 1206 SMT TYPE
R9	92LQCF0850202A	AA			GP CHIP RES. 1K OHM 1 / 8W 5% 0805 SMT TYPE
R10	92LQCF04501010	AA			GP CHIP RES. 470 OHM 1 / 4W 5% CF 1206 SMT TYPE
R11	92LQCF08502022	AA			GP CHIP RES. 220 OHM 1 / 8W 5% 0805 SMT TYPE
R12	92LQCF08502030	AA			GP CHIP RES. 10K OHM 1 / 8W 5% 0805 SMT TYPE
R13	92LQCF0450102A	AA			GP CHIP RES. 4.7K OHM 1 / 4W 5% CF 1206 SMT TYPE
R14	92LQCF0450109A	AA			GP CHIP RES. 4.7 OHM 1 / 4W 5% 1206 SMT TYPE
R16,R17	92LQCF0450109A	AA			GP CHIP RES. 4.7 OHM 1 / 4W 5% 1206 SMT TYPE
R25	92LQCF0450175A	AA			GP CHIP RES. 750 OHM 1 / 4W 5% 1206 SMT TYPE
R27	92LQCF045010AA	AA			GP CHIP RES. 10 OHM 1 / 4W 5% CF 1206 SMT TYPE
R28,R29	92LQCF04501310	AA			GP CHIP RES. 300 OHM 1 / 4W 5% CF 1206 SMT TYPE
R30	92LQCF08502068	AA			GP CHIP RES. 680 OHM 1 / 8W 5% 0805 SMT TYPE
R31	92LQCF0850202A	AA			GP CHIP RES. 1K OHM 1 / 8W 5% 0805 SMT TYPE
R32	92LQCF08502030	AA			GP CHIP RES. 10K OHM 1 / 8W 5% 0805 SMT TYPE
R33	92LQCF08102072	AA			GP CHIP RES. 10.7K OHM 1 / 8W 1% 0805 SMT TYPE
R34	92LQCF08502030	AA			GP CHIP RES. 10K OHM 1 / 8W 5% 0805 SMT TYPE
R36	92LQCF08502050	AA			GP CHIP RES. 1M OHM 1 / 8W 5% 0805 SMT TYPE
R38	92LQCF0850202A	AA			GP CHIP RES. 1K OHM 1 / 8W 5% 0805 SMT TYPE
R101	92LQCF08502000	AA			GP CHIP RES. 0 OHM JUMP WIRE 0805 SMT TYPE (USE IN CONTROL PCB)
R101	92LQCF01503047	AA			GP CHIP RES. 4.7K OHM 1 / 10W 5% 0603 SMT TYPE (USE IN MAIN PCB)
R103	92LQCF015030Y0	AA			GP CHIP RES. 33 OHM 1 / 10W 5% 0603 SMT TYPE
R104	92LQCF01503047	AA			GP CHIP RES. 4.7K OHM 1 / 10W 5% 0603 SMT TYPE
R105	92LQCF015030Y0	AA			GP CHIP RES. 33 OHM 1 / 10W 5% 0603 SMT TYPE
R107,R108	92LQCF015030Y0	AA			GP CHIP RES. 33 OHM 1 / 10W 5% 0603 SMT TYPE
R109	92LQCF01503015	AA			GP CHIP RES. 1.5K OHM 1 / 10W 5% 0603 SMT TYPE
R110	92LQCF0150303A	AA			GP CHIP RES. 20K OHM 1 / 10W 5% 0603 SMT TYPE
R111	92LQCF01503073	AA			GP CHIP RES. 47K OHM 1 / 10W 5% 0603 SMT TYPE
R112,R113	92LQCF01503051	AA			GP CHIP RES. 5.1K OHM 1 / 10W 5% 0603 SMT TYPE
R114~R116	92LQCF01503054	AA			GP CHIP RES. 150K OHM 1 / 10W 5% 0603 SMT TYPE
R134	92LQCF01503047	AA			GP CHIP RES. 4.7K OHM 1 / 10W 5% 0603 SMT TYPE
R137	92LQCF01503091	AA			GP CHIP RES. 390 OHM 1 / 10W 5% 0603 SMT TYPE
R138,R139	92LQCF01503000	AA			GP CHIP RES. 0 OHM 1 / 10W 5% TF 0603 SMT TYPE
R140	92LQCF01503001	AA			GP CHIP RES. 10 OHM 1 / 10W 5% 0603 SMT TYPE
R141,R142	92LQCF01503002	AA			GP CHIP RES. 1K OHM 1 / 10W 5% TF 0603 SMT TYPE
R143~R148	92LQCF01503000	AA			GP CHIP RES. 0 OHM 1 / 10W 5% TF 0603 SMT TYPE
R151,R153	92LQCF01503000	AA			GP CHIP RES. 0 OHM 1 / 10W 5% TF 0603 SMT TYPE
R154,R155	92LQCF01503519	AA			GP CHIP RES. 5.1 OHM 1 / 10W 5% 0603 SMT TYPE
R156,R157	92LQCF01503040	AA			GP CHIP RES. 100K OHM 1 / 10W 5% 0603 SMT TYPE
R158,R159	92LQCF01503071	AA			GP CHIP RES. 470 OHM 1 / 10W 5% 0603 SMT TYPE
R161,R162	92LQCF01503074	AA			GP CHIP RES. 470K OHM 1 / 10W 5% 0603 SMT TYPE
R163	92LQCF01103002	AA			GP CHIP RES. 33K OHM 1 / 10W 1% 0603 SMT TYPE
R164	92LQCF01103001	AA			GP CHIP RES. 1.2K OHM 1 / 10W 1% 0603 SMT TYPE
R166,R167	92LQCF01503000	AA			GP CHIP RES. 0 OHM 1 / 10W 5% TF 0603 SMT TYPE
R170	92LQCF0150309A	AA			GP CHIP RES. 1 OHM 1 / 10W 5% 0603 SMT TYPE
R171~R174	92LQCF01503047	AA			GP CHIP RES. 4.7K OHM 1 / 10W 5% 0603 SMT TYPE
R175	92LQCF0150308A	AA			GP CHIP RES. 68K OHM 1 / 10W 5% 0603 SMT TYPE
R176,R177	92LQCF015030Z2	AA			GP CHIP RES. 2.2K OHM 1 / 10W 5% 0603 SMT TYPE
R178	92LQCF01503040	AA			GP CHIP RES. 100K OHM 1 / 10W 5% 0603 SMT TYPE
R184~R187	92LQCF015030Y0	AA			GP CHIP RES. 33 OHM 1 / 10W 5% 0603 SMT TYPE
R188	92LQCF01503047	AA			GP CHIP RES. 4.7K OHM 1 / 10W 5% 0603 SMT TYPE
R201	92LQCF08502033	AA			GP CHIP RES. 330 OHM 1 / 8W 5% 0805 SMT TYPE
R202	92LQCF08502018	AA			GP CHIP RES. 1.8K OHM 1 / 8W 5% 0805 SMT TYPE
R203, R204	92LQCF0850202A	AA			GP CHIP RES. 1K OHM 1 / 8W 5% 0805 SMT TYPE
R205	92LQCF08502030	AA			GP CHIP RES. 10K OHM 1 / 8W 5% 0805 SMT TYPE
R206	92LQCF08502010	AA			GP CHIP RES. 10 OHM 1 / 8W 5% 0805 SMT TYPE
R207	92LQCF08502030	AA			GP CHIP RES. 10K OHM 1 / 8W 5% 0805 SMT TYPE
R208,R209	92LQCF08502047	AA			GP CHIP RES. 4.7K OHM 1 / 8W 5% 0805 SMT TYPE
R301	92LQCF015030Z7	AA			GP CHIP RES. 75 OHM 1 / 10W 5% 0805 SMT TYPE
R302~R304	92LQCF015030Z7	AA			GP CHIP RES. 75 OHM 1 / 10W 5% 0603 SMT TYPE
R305,R306	92LQCF01503010	AA			GP CHIP RES. 100 OHM 1 / 10W 5% 0603 SMT TYPE
R307	92LQCF01503003	AA			GP CHIP RES. 10K OHM 1 / 10W 5% 0603 SMT TYPE
R308	92LQCF01503002	AA			GP CHIP RES. 1K OHM 1 / 10W 5% 0603 SMT TYPE
R309	92LQCF01503021	AA			GP CHIP RES. 200 OHM 1 / 10W 5% 0603 SMT TYPE
R401,R402	92LQCF01503023	AA			GP CHIP RES. 12K OHM 1 / 10W 5% 0603 SMT TYPE
R403,R404	92LQCF01503042	AA			GP CHIP RES. 2.4K OHM 1 / 10W 5% 0603 SMT TYPE
R405,R406	92LQCF01503003	AA			GP CHIP RES. 10K OHM 1 / 10W 5% 0603 SMT TYPE
R407,R408	92LQCF01503002	AA			GP CHIP RES. 1K OHM 1 / 10W 5% 0603 SMT TYPE

NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
[10] RESISTORS					
R410~R413	92LQCF01503002	AA			GP CHIP RES. 1K OHM 1 / 10W 5% 0603 SMT TYPE
R416	92LQCF0150302A	AA			GP CHIP RES. 2K OHM 1 / 10W 5% 0603 SMT TYPE
R417~R421	92LQCF01503002	AA			GP CHIP RES. 1K OHM 1 / 10W 5% 0603 SMT TYPE
R423	92LQCF01503003	AA			GP CHIP RES. 10K OHM 1 / 10W 5% 0603 SMT TYPE
R428~R429	92LQCF01503015	AA			GP CHIP RES. 1.5K OHM 1 / 10W 5% 0603 SMT TYPE
R430~R431	92LQCF01503032	AA			GP CHIP RES. 3K OHM 1 / 10W 5% 0603 SMT TYPE
R432	92LQCF01503068	AA			GP CHIP RES. 6.8K OHM 1 / 10W 5% 0603 SMT TYPE
R433	92LQCF01503073	AA			GP CHIP RES. 47K OHM 1 / 10W 5% 0603 SMT TYPE
R434	92LQCF01503001	AA			GP CHIP RES. 10 OHM 1 / 10W 5% 0603 SMT TYPE
R437,R438	92LQCF01503071	AA			GP CHIP RES. 470 OHM 1 / 10W 5% 0603 SMT TYPE
R439	92LQCF01503032	AA			GP CHIP RES. 3K OHM 1 / 10W 5% 0603 SMT TYPE
R901	92LQCF0150303A	AA			GP CHIP RES. 20K OHM 1 / 10W 5% 0603 SMT TYPE
R902	92LQCF01503023	AA			GP CHIP RES. 12K OHM 1 / 10W 5% 0603 SMT TYPE
R903	92LQCF01503003	AA			GP CHIP RES. 10K OHM 1 / 10W 5% 0603 SMT TYPE
R904	92LQCF01503047	AA			GP CHIP RES. 4.7K OHM 1 / 10W 5% 0603 SMT TYPE
R905	92LQCF08502090	AA			GP CHIP RES. 2.2 OHM 1 / 8W 5% 0805 SMT TYPE
R906	92LQCF01503229	AA			GP CHIP RES. 2.2 OHM 1 / 10W 5% 0603 SMT TYPE
R907	92LQAF02500090	AB			GP CHIP RES. 3.3 OHM 1 / 2W 5% CF
R908	92LQCF01503001	AA			GP CHIP RES. 10 OHM 1 / 10W 5% 0603 SMT TYPE
[11] OTHER CIRCUITARY PARTS					
CON1	92LCCN39602Z14	AD			GP CONNECTOR 4P PITCH=3.96mmREMOVE PIN 2 & 3 H TYPE
CN102	92LCCN20000006	AC			GP CONNECTOR 6P PITCH=2.0mmB6B-PH-K TOP BASE
CN103	92LCCN20000005	AC			GP CONNECTOR 5P PITCH=2.0mmB5B-PH-K TOP BASE
CN105	92LCCN20000008	AD			GP CONNECTOR 8P PITCH=2.0mmB8B-PH-K TOP BASE
CN901	92LCCN20000003	AC			GP CONNECTOR 3P PITCH=2.0mmB3B-PH-K TOP BASE
D15	92LXJP22000003	AC			WIRE BARE 22 GA TINNED
⚠ F1	92LKHT02010030	AG			GP FUSE CERAMIC ICP 1A 250V TIME-LAG 4X10.5MM SMKO / VDE / CCC / UL
GND1	92LCSL20703500	AE			GP SOLDER LUG OD7.0xID3.5xT0.3mm NICKEL W / 5820-220AA
J3	92LXJP22000003	AC			WIRE BARE 22 GA TINNED
JK101	92LCJR00630120	AK			GP RCA JACK 6P RD-BU-WT / GN-OR-RD RCA-613D-027 W / GND
JK102	92LCJC01000430	AG			GP RCA + DIN JK 1RCA + 4P DIN YEL W / GND RCA -125
JW101~JW106	92LXJP22000003	AC			WIRE BARE 22 GA TINNED
R37,R909	92LXJP22000003	AC			WIRE BARE 22 GA TINNED
RB201	92LVFL80124516	AK			GP CON / WIRE 8P 160mm 2468#26 RAINBOW A=2.0mm B=2012V DR4012 QU
SN201	92LRHO26381001	AL			GP IRT RECEIVER IRM-2638AS10F4 37.9KHz EVERLIGHT
SW1	92LXJP22000003	AC			WIRE BARE 22 GA TINNED
TA201~TA204	92LMAW06000100	AC			GP AI TACT SW SKHVBE3520 ALPS 5.0mm 160 GF
⚠ AC CORD	92LVPE00327911	AS			GP P. CORD 2P 1726 BK EUR (TIE X STEEL) 100W KE21 B3.96VH 00204921BW.CDR 0.50 mm
⚠ AC CORD	92LVPE41A25310	BC			GP L. CORD 3 PIN 1750 mm BLK BS FUSE 3A 250V W/PVC BUSH, VOLEX PLUG : MP5
CON/WIRE	92LVFC24011040	AG			GP FFC CABLE 24P 240mm UL20696 PITCH=0.5mm DR620
CON2	92LVFL30124500	AH			GP CON / WIRE 3P 160mm 2468#26 RAINBOW A= 2mm B=2012V
CON/WIRE	92LVFL50124400	AH			GP CON FLAT CABLE 5P 120mm 2468#26 RAINBOW A ,B=2mm
CON/WIRE	92LVWA20000214	AH			GP CON / WIRE 6P 100mm 1007#28 A ,B=2mm BLACK
[12] PCB ASSEMBLY (Non Replacement items)					
	92LAPE20173516	CB			MAIN PCB ASS'Y
	92LAPE10515055	AZ			CONTROL PCB ASS'Y
	92LAPE20235015	BH			POWER PCB ASS'Y
[13] CABINET PARTS					
1	92LGSE20016911	AZ			TOP COVER
2	92LGSE20016811	AY			BOTTOM CAB
3	92LBPF10603104	AY			FRONT PANEL
4	92LBPD10110101	AM			DVD DOOR
5	92LBRF40002601	AC			FOOT
6	92LGSE10054901	AD			BRACKET
7	92LBPK10209901	AK			FUNCTION KEY
8	92LBPN10024305	AT			LENS
9	92LABN04R34301	BY			DVD MECHANISM ASSY
10	92LDSS10002201	AC			SPACER
11	92LBFP12106001	AE			TOP COVER SHEET
12	92LBFP42103601	AF			BOTTOM CAB SHEET
S1	92LHST4E308460	AB			SCREW M3.0xP0.5x6mm
S2	92LHSF05308460	AB			SCREW M3.0xP0.5x6mm FLAT HEAD
S3	92LHSP05005480	AB			SCREW T3.0xP1.06x8mm
S4	92LHSW05308480	AB			SCREW M3.0xP0.5x8mm
	92LYLB20800202	AC			CPA LABEL (S'PORE SAFETY LABEL, PSM)
	92LYLB42000201	AD			POP LABEL (L50 x W12mm)
	92LYLB11100725	AC			SPEC LABEL (L65 x H27mm)
	92LYLB41110202	AC			SPEC LABEL (THAI-700W/L70 x 50mm)
	92LYLB42408101	AC			TISI LABEL-Silver (20mm x 20mm)
	92LYLB41110301	AC			HONG KONG LABEL (With Rating/ 45mm x 12mm)
	92LYLB40804301	AF			SIRIM LABEL
[14] ACCESSORIES/PACKING PARTS					
	92LICC10032405	AZ			Carton
	92LICG10008003	BA			Packing Case
	92LITF43108001	AG			Polyfoam Right
	92LITF43008001	AG			Polyfoam Left
	92LWIR23200102	AZ			Remote Control
	92LVRC30300114	AN			RCA Cable
	92LDHA40100101	AE			Handle
	92LFBY00141503	AH			Battery

NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
[14] ACCESSORIES/PACKING PARTS					
	92LYLB42000201	AD			Pop Label (Indonesia)
	92LYWC40013601	AM			Warranty Card (Indonesia)
	92LYOM10034301	AL			Operation Manual
	92LYOM40013101	AQ			Operation Manual (Thailand)
	92LYOM40013601	AN			Operation Manual (Hong Kong)
	92LYOM40013501	AN			Operation Manual (Middle East)
	92LYLB20700102	AB			Bar Code Label (EAN : 4 9740 1999 0804)
	92LYLB12401814	AC			Contract Label (Colour - White Base)
	92LYLB42100501	AB			Production Date Label (43mm X 8mm)
	92LYLB40500201	AC			Serial No Label (55mm X 11mm)
	92LYLB12408801	AB			TISI Label (23mm X 23mm)
	92LICG10008003	BA			P-Case (DV-SL700W)
	92LYLB42408501	AB			Hong Kong Safety Label (55mm X 15mm)
	92LYLB41602601	AB			G-Card Reminding Label (100mm X 50mm)
	92LYLB10900911	AC			Case Mark Label (35mm X 50mm)
	92LICC10032405	AZ			Outer Carton (6 Sets / Carton)
	92LYLB42408801	AB			Made in Malaysia Label (69mm X 10mm)
	92LYLB12101105	AB			Production Date Label (54mm X 10mm)
	92LYMC41004201	AF			G-Card (Hong Kong)
	92LYMC41004301	AM			G-Card (Indonesia)

EXPLODED VIEW

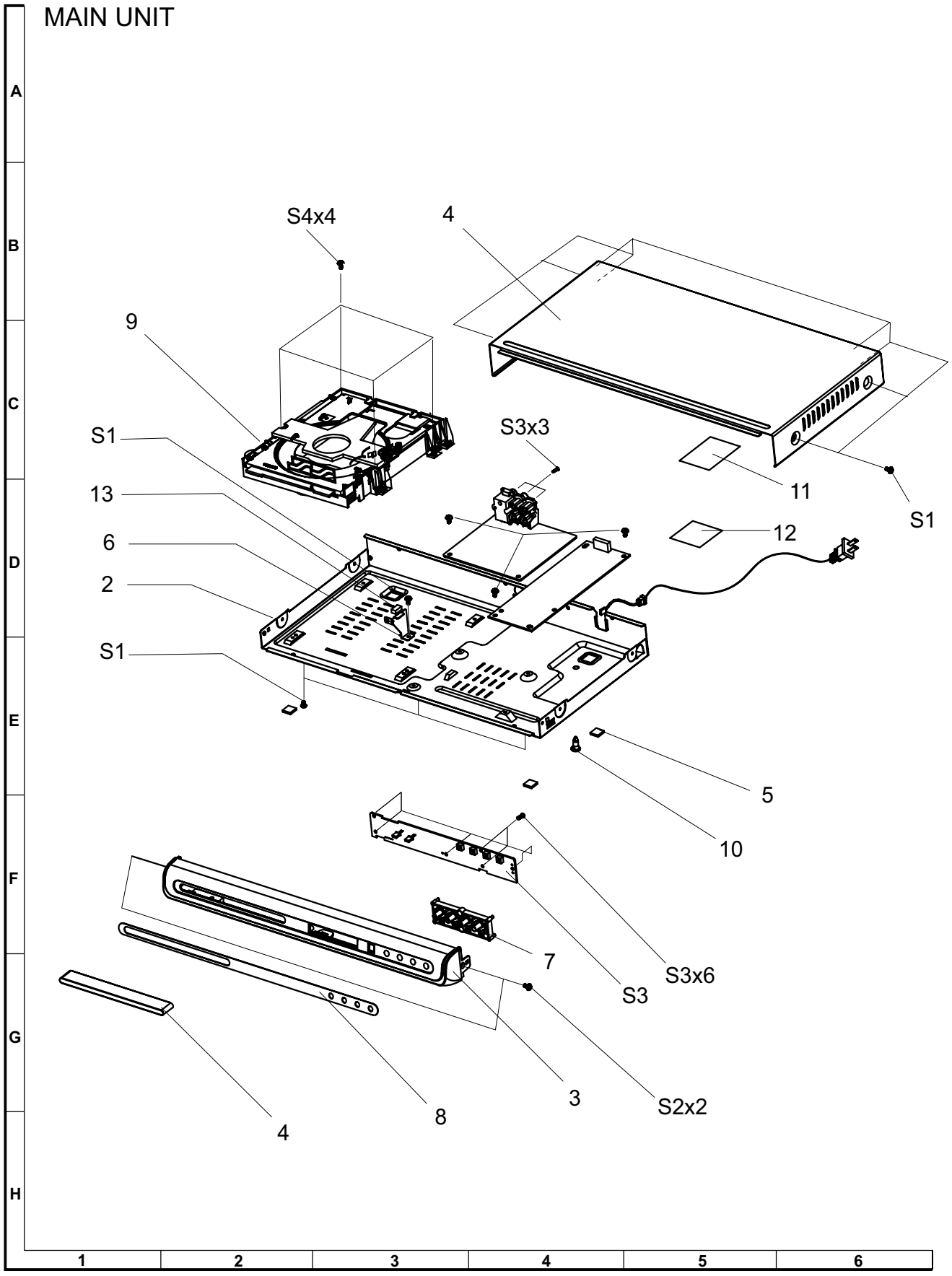


Figure1 : MAIN BOX PARTS

-**MEMO**-

-**MEMO**-

SHARP

SHARP CORPORATION
Sharp-Roxy Corporation
Sungai Petani, Kedah,
Malaysia

Printed in Malaysia
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