

DJ-190

Service Manual

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SPECIFICATIONS

	TX	RX
Frequency Coverage		
DJ-190T (u.s. Amateur version)	144.000 ~ 147.995MHz	135.000 ~ 173.995MHz
DJ-190E (European Amateur version)	144.000 ~ 145.995MHz	144.000 ~ 145.995MHz
DJ-190TA1 (commercial version VHFL)	135.000 ~ 155.000MHz	135.000 ~ 173.995MHz
DJ-190TA2 (commercial version VHFH)	150.000 ~ 173.995MHz	135.000 ~ 173.995MHz
Channel Step:	5, 10, 12.5, 15, 20, 25, 30kHzsteps	
Memory Channels:	40 Channels	
Antenna Impedance:	50ohm unbalanced	
Frequency Stability:	+/-5 ppm	
Microphone Input Impedance:	2kohm nominal.	
Signal Type:	F3E (FM)	
Offset Range:	0 ~ 99.995MHz	
Deviation:	15kHz max.	
TX Output (supply voltage):	1.5W (4.8V) / 3.5W (7.2V) / 5W (9.6 ~ 13.8V)	
RX Sensitivity:	12dB SINAD better than - 16dBu	
RX Selectivity:	-6dB/ +/- 12kHz	
I.F.:	(1st) 21.25MHz / (2nd) 450kHz	
Power Supply Requirements:	4.8 ~ 13.8V DC (4.8V DC standard)	
Current Consumption	Transmitting: Approx. 1.2 Amp. in High Power	
at 13.8V DC:	Setting	
	Receiving: Squelched Approx. 24mA (BS on)	
Operating Temperature:	-10 ~ +60°C, 14 ~ 140°F	
Dimensions:	57(W) x 151(H) x 27(D) mm	
(with EBP-37N without projections)	2 1/4(W) x 6(H) x 1 1/16(D) inches	
Weight:	Approx. 300g	
Subaudible Tones (CTCSS) :	Encoder installed (50 tones)	

CIRCUIT DESCRIPTION

- 1) Receiver System The receiver system is a double superheterodyne system with a 21.7 MHz first IF and a 450 kHz second IF.
1. Front End The received signal at any frequency in the 130.00- to 173.995-MHz range is passed through the low-pass filter (L102, L103, L104, C113, C107, C116, and C114) and tuning circuit (L112 and D107), and amplified by the RF amplifier (Q107). The signal from Q107 is then passed through the tuning circuit (L109, L110, L111, and varicaps D104, D105 and D106) and converted into 21.7 MHz by the mixer (Q106). The tuning circuit, which consists of L112, L109, varicaps D107 and D104, L110 L111, varicaps D105 and D106, is controlled by the tracking voltage from the CPU so that it is optimized for the reception frequency. The local signal from the VCO is passed through the buffer (Q108), and supplied to the source of the mixer (Q106). The radio uses the lower side of the superheterodyne system.
2. IF Circuit The mixer mixes the received signal with the local signal to obtain the sum of and difference between them. The crystal filter (XF101 , XF102) selects 21.7 MHz frequency from the results and eliminates the signals of the unwanted frequencies. The first IF amplifier (Q105) then amplifies the signal of the selected frequency.
3. Demodulator Circuit After the signal is amplified by the first IF amplifier (Q105), it is input to pin 16 of the demodulator IC (IC104). The second local signal of 21.25 MHz (shared with PLL IC reference oscillation), which is oscillated by the internal oscillation circuit in IC102 and crystal (X101), is input through pin 1 of IC104. Then, these two signals are mixed by the internal mixer in IC104 and the result is converted into the second IF signal with a frequency of 450 kHz. The second IF signal is output from pin 3 of IC104 to the ceramic filter (FL101), where the unwanted frequency band of that signal is eliminated, and the resulting signal is sent back to the IC104 through pins 5 and 7.
- The second IF signal input via pin 7 is demodulated by the internal limiter amplifier and quadrature detection circuit in IC104, and output as an audio signal through pin 9.
4. Audio Circuit The audio signal from pin 9 of IC104 is compensated to the audio frequency characteristics in the de-emphasis circuit (R162, R161, C172, C173) and amplified by the AF amplifier (Q109). The signal is then input to pin 2 of the electronic volume (IC103) for volume adjustment, and output from pin 1. The adjusted signal is sent to the audio power amplifier (IC105) through pin 2 to drive the speaker.
5. Squelch Circuit Part of the audio signal from pin 9 of IC104 is amplified by the noise filter amplifier consisting of R176, R186, R177, C179, C183, C191, and C194, and the internal noise amplifier in IC104. The desired noise of the signal is output through pin 11 of IC104, to be further amplified by the noise amplifier (Q115). The amplified noise signal is rectified by voltage doublers D109 and input to pin 4 of CPU (IC5).

2) Transmitter System	
1. Modulator Circuit	The audio signal is converted to an electric signal in either the internal or external microphone, and input to the microphone amplifier (IC6). IC6 consists of two operational amplifiers; one amplifier (pins 1, 2, and 3) is composed of pre-emphasis and IDC circuits and the other (pins 5, 6, and 7) is composed of a splatter filter. The maximum frequency deviation is determined to its optimal value by switch circuits consisting of Q9 and Q10 and input to the cathode of the varicap of the VCO, to change the electric capacity in the oscillation circuit. This produces the frequency modulation.
2. Power Amplifier Circuit	The transmitted signal is oscillated by the VCO, amplified by the pre-drive amplifier (Q102) and drive amplifier (Q101), and input to the power module (IC101). The signal is then amplified by the power module (IC101) and led to the antenna switch (D101) and low-pass filter (L102, L103, L104, C113, C107, C116, and C114), where unwanted high harmonic waves are reduced as needed, and the resulting signal is supplied to the antenna.
3. APC Circuit	Part of the transmission power from the low-pass filter is detected by D103, converted to DC, and then amplified by a differential amplifier. The output voltage controls the bias voltage from pin 2 of the power module (IC101) to maintain the transmission power constant.
3) PLL Synthesizer Circuit	
1. PLL	The dividing ratio is obtained by sending data from the CPU (IC5) to pin 2 and sending clock pulses to pin 3 of the PLL IC (IC102). The oscillated signal from the VCO is amplified by the buffer (Q117) and input to pin 6 of IC102. Each programmable divider in IC102 divides the frequency of the input signal by N according to the frequency data, to generate a comparison frequency of 5 or 6.25 kHz.
2. Reference Frequency Circuit	The reference frequency appropriate for the channel steps is obtained by dividing the 21.25 MHz reference oscillation (X101) by 4250 or 3400, according to the data from the CPU (IC5). When the resulting frequency is 5 kHz, channel steps of 5, 10, 15, 20, 25 and 30 kHz are used. When it is 6.25 kHz, the 12.5 kHz channel step is used.
3. Phase Comparator Circuit	The PLL (IC102) uses the reference frequency, 5 or 6.25 kHz. The phase comparator in the IC102 compares the phase of the frequency from the VCO with that of the comparison frequency, 5 or 6.25 kHz, which is obtained by the internal divider in IC102
4. PLL Loop Fitter Circuit	If a phase difference is found in the phase comparison between the reference frequency and VCO output frequency, the charge pump output (pin 8) of IC102 generates a pulse signal, which is converted to DC voltage by the PLL loop filter and input to the varicap of the VCO unit for oscillation frequency control.

5. VCO Circuit

A Colpitts oscillation circuit driven by Q301 directly oscillates the desired frequency. The frequency control voltage determined in the CPU (IC5) and PLL circuit is input to the varicaps (D301 and D304). This changes the oscillation frequency, which is amplified by the VCO buffer (Q302) and output from the VCO unit.

Note

The oscillation frequency is determined by turning Q301 ON and OFF.

Displayed frequencies	Q301
TX: 130.00 - 139.995 MHz RX: 130.00 - 161.695 MHz	OFF
TX: 140.00 - 173.995 MHz RX: 161.70 - 173.995 MHz	ON

4) CPU and Peripheral Circuits The CPU turns ON the LCD via segment and common terminals with 1/3 the duty and 1/3 the bias, at the frame frequency is 85Hz.

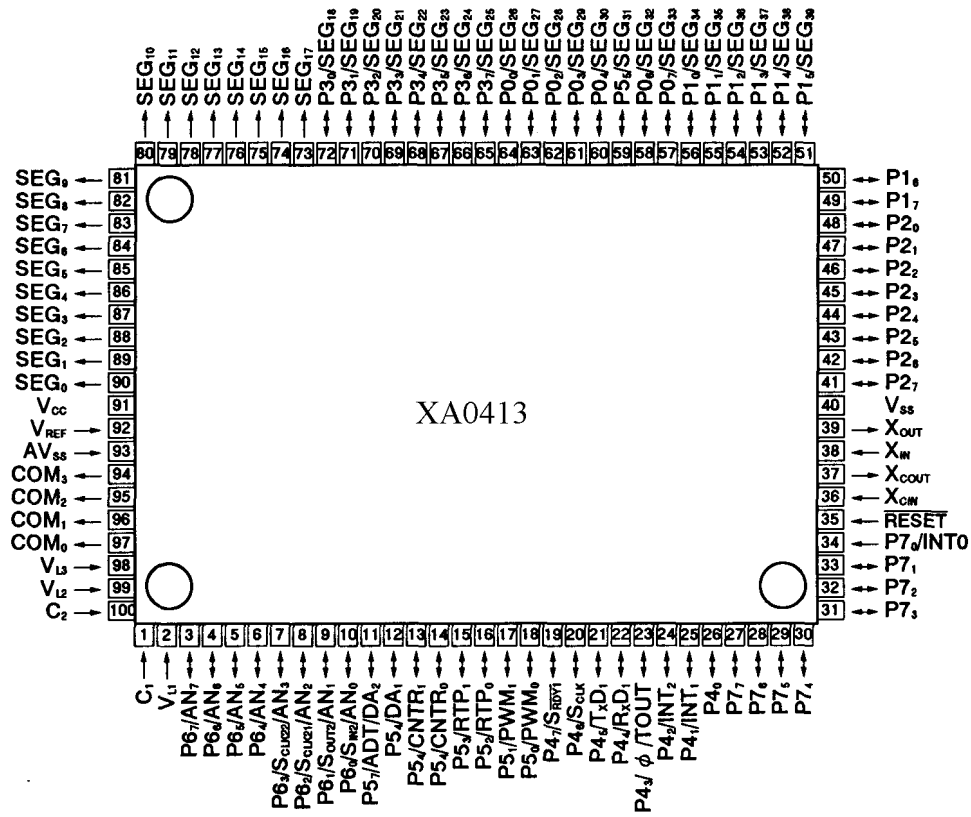
2. Display Lamp Circuit When the LAMP key is pressed, "H" is output from pin 45 of CPU (IC5) to the bases of Q1 then turn ON and the LEDs (D1, D3) Bright.

3. Reset and Backup Circuits When the power from the DC jack or external battery increases from 0 V to 2.5 or more, "H" level reset signal is output from the reset IC (IC2) to pin 35 of the CPU (IC5), causing the CPU to reset. The reset signal, however, waits at C6 and R98, and does not enter the CPU until the CPU clock (X1) has stabilized. When the external power drops to 3.2 V or below, the output signal from the backup IC (IC3), which has been input to pin 34 of the CPU, changes from "H" to "L" level. The CPU will then be in the backup state.

4. S(Signal)Meter Circuit The DC potential of pin 13 of IC104 is input to pin 3 of the CPU (IC5), converted from an analog to a digital signal, and displayed as the S-meter signal on the LCD.

5. Tone Encoder The CPU (IC5) is equipped with an internal tone encoder. The tone signal (67.0 to 254.1 Hz) is output from pin 11 of the CPU to the varicap of the VCO for modulation.

5) CPU Terminal Functions: M38267M8L (XA413)



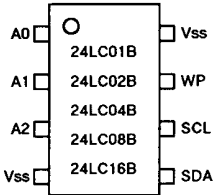
No.	Pin Name	Signal	I/O	Logic	Description
1	C1	C1	-	-	-
2	VL1	VL1	I	A/D	LCD powersupply
3	P67/AN7	SMT	I	A/D	S-meter input
4	P66/AN6	SQL	I	A/D	Noise level input for squelch
5	P65/AN5	BAT	I	A/D	Low battery detection input
6	P64/AN4	BP5	I	A/D	Band plan5
7	P63/CLK22/AN3	BP4	I		Band plan4
8	P62/CLK21/AN2	UL	I	Activehigh	PLL unlock signal input
9	P61/SOUT2/AN1	BP1,2	I	A/D	Band plans 1 and 2
10	P60/SIN2/ANO	MOM	I	Active low	Monitor key input
11	P57/ADT/DA2	CTOUT	O	D/A	CTCSS tone output
12	P56/AD1	DTOUT	O	D/A	
13	P55/CNTR1	TSQD	I	Active low	CTCSS tone detection input
14	P54/CNTR0	BEP	O	Pulse	Beep tone output/Band plan 3
15	P53/RTPI	STB2	I/O	Active low/pulse	CTCSS unit detection/Strobe signal to CTCSS unit
16	P52/RTPO	MUTE	I/O	Activehigh	Microphone mute
17	P51/PWM1	CLK	O	Pulse	Serial clock output for PLL, CTCSS
18	P50/PWM0	DATA	O	Pulse	Serial data output for PLL CTCSS
19	P47/SRDY1	ACK	I/O	Pulse	Band plan 6
20	P46/SCLK1	STB1	O	Pulse	Strobe for PLL IC
21	P45/TXD1	UTX	O	Pulse	UART data transmission output
22	P44/RXD1	URX	I	Pulse	UART data reception input
23	P43/D/TOUT	TBST	O	Pulse	Tone burst (1750Hz) output (European version)
24	P42/INT2	RE2	I	Active low	
25	P41/INT1	RE1	I	Active low	Rotary encoder Input
26	P40	PTT	I	Activehigh	PTT input
27	P77	DSL	O	Active low	
28	P76	STD	I/O	Activehigh	Deviation adjustment during transmission
29	P75	DSD	I	Pulse	Deviation adjustment during transmission
30	P74	T3C	O	Active low	TX power ON/OFF output
31	P73	P3C	O	Active low	PLL power ON/OFF output
32	P72	AFP	O	Active low	AFAMP power ON/OFF output
33	P71	R3C	O	Active low	RX power ON/OFF output
34	P70/INT0	BU	I	Active low	Backup signal detection input
35	RESET	RST	I	Active low	Resetinput
36	XCIN	XCIN	-	-	-
37	XCOUNT	XCOUT	-	-	-
38	XIN	XIN	-	-	Main clock input
39	XOUT	XOUT	-	-	Main clock output
40	VSS	GND	-	-	CPU ground
41	P27	PSW	I	Active low	Power switch input
42	P26	SCL	O	Pulse	Serial clock for EEPROM
43	P25	C3C	O	Activehigh	C3 power ON/OFF output
44	P24	SDA	O	Pulse	Serial data for EEPROM
45	P23	LMP	O	Activehigh	Lamp ON/OFF
46	P22	T/KEY	I	Active low	Tone burst/LPTT input
47	P21	K00	I/O	-	Band plan BP7 input
48	P20	K01	O	-	Key matrix output
49	P17	K02	O	-	
50	P16	K03	O	-	

No.	Pin Name	Signal	I/O	Logic	Description
51	P15/SEG39	F/KEY	I	Active low	Function key input
52	P14/SEG38	K10	I	-	
53	P13/SEG37	K11	I	-	
54	P12/SEG36	K12	I	-	
55	P11/SEG35	K13	I	-	
56	P11/SEG34	K14	I	-	Key matrix input
57	P07/SEG33	SFT	O	-	VCO frequency range change
58	P06/SEG32	SD	O	Active low	Signal detection output
59	P05/SEG31	AFC	O	Active high	AF tone control output
60	P04/SEG30	DA4	O	-	
61	P03/SEG29	DA3	O	-	
62	P02/SEG28	DA2	O	-	
63	P01/SEG27	DA1	O	-	
64	P00/SEG26	DA0	O	-	
65	P37/SEG25	S25	O	-	
66	P36/SEG24	S24	O	-	
67	P35/SEG23	S23	O	-	
68	P34/SEG22	S22	O	-	
69	P33/SEG21	S21	O	-	
70	P32/SEG20	S20	O	-	
71	P31/SEG19	S19	O	-	
72	P30/SEG18	S18	O	-	
73	SEG17	S17	O	-	
74	SEG16	S16	O	-	
75	SEG15	S15	O	-	
76	SEG14	S14	O	-	
77	SEG13	S13	O	-	
78	SEG12	S12	O	-	
79	SEG11	S11	O	-	LCD segment signal
80	SEG10	S10	O	-	
81	SEG9	S9	O	-	
82	SEG8	S8	O	-	
83	SEG7	S7	O	-	
84	SEG6	S6	O	-	
85	SEG5	S5	O	-	
86	SEG4	S4	O	-	
87	SEG3	S3	O	-	
88	SEG2	S2	O	-	
89	SEG1	S1	O	-	
90	SEG0	SO	O	-	
91	VCC	VDD	-	-	CPU power terminal
92	VREF	VREF	-	-	AD converter power supply
93	AVSS	AVSS	-	-	AD converter ground
94	COM3	COM3	-	-	-
95	COM2	COM2	O	-	LCD COM2 output
96	COM1	COM1	O	-	LCD COM1 output
97	COM0	COM0	O	-	LCD COM0 output
98	VL3	VL3	I	-	LCD power supply
99	VL2	VL2	I	-	LCD power supply
100	C2	I	-	-	

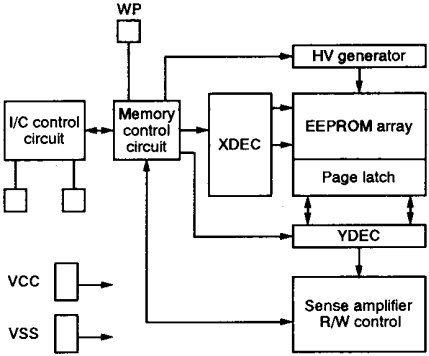
SEMICONDUCTOR DATA

1) 24LC16BT-I/SN (XA0351) EEPROM

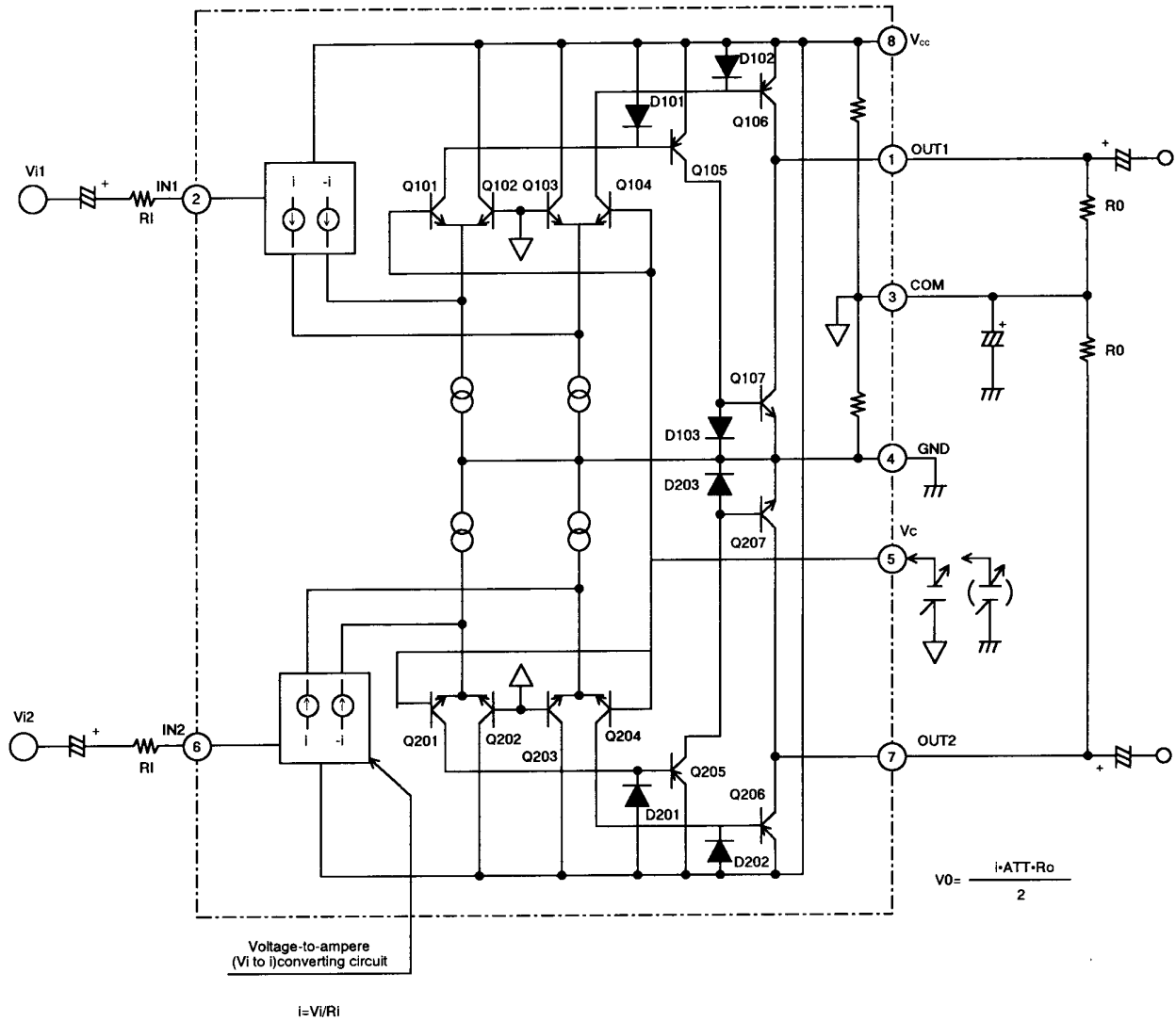
Pin Assignment



Block Diagram



2) M5222FP-600C (XA0385) Electronic Volume

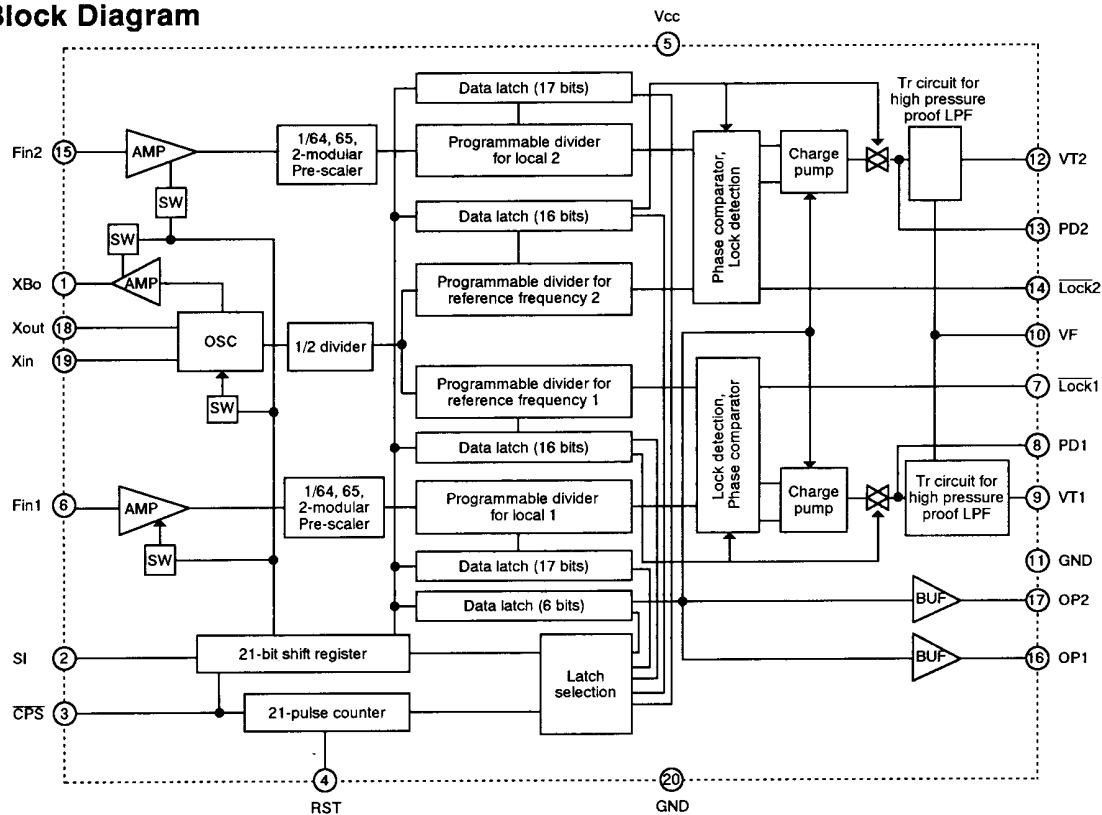


3) M64076GP (XA0352) PLL

Pin Assignment

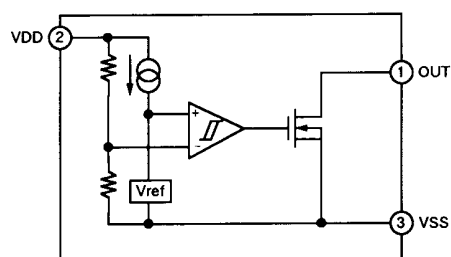
XBo	1	20	GND
SI	2	19	Xin
CPS	3	18	Xout
RST	4	17	OP2
Vcc	5	16	OP1
Fin1	6	15	Fin2
Lock1	7	14	Lock2
PD1	8	13	PD2
VT1	9	12	VT2
VF	10	11	GND

Block Diagram



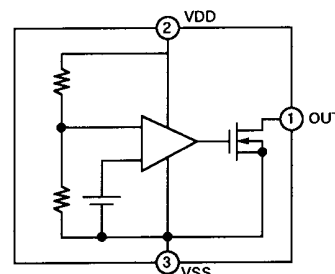
4) RH5VL25AA-T1 (XA0309) C-MOS Voltage Detector

Block Diagram



5) RH5VA32AA-T1 (XA0198) C-MOS Voltage Detector

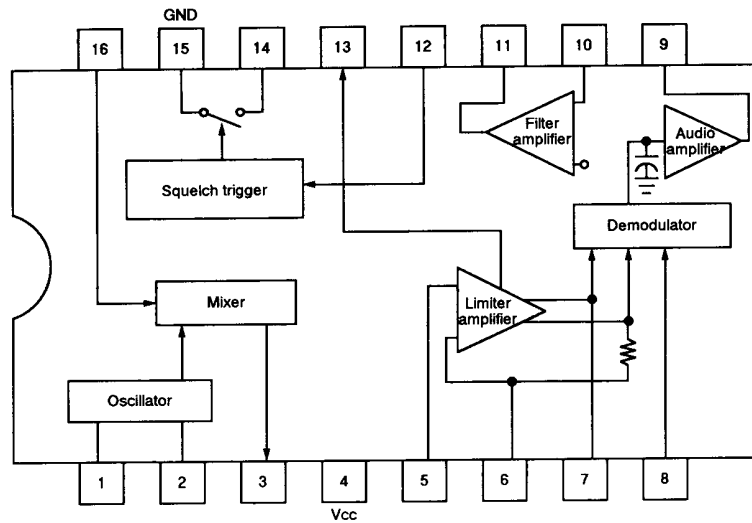
Block Diagram



6) MC3372VM-EL (XA0343)

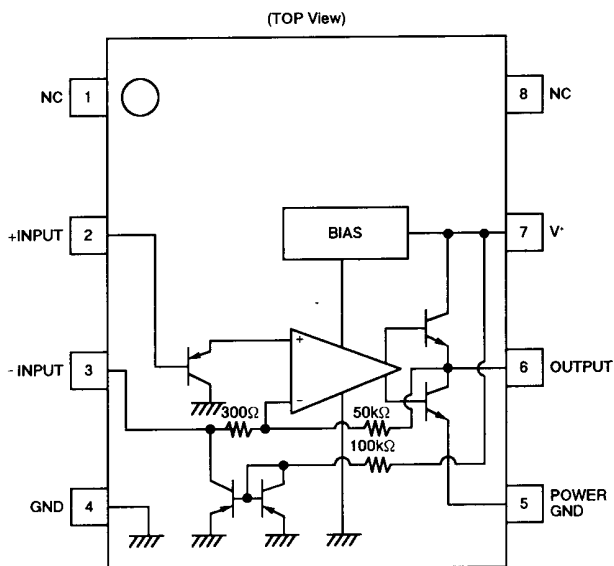
Narrow Band FM IF IC

Block Diagram



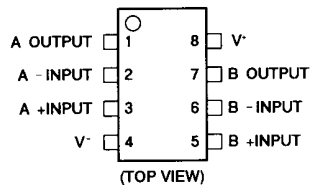
7) NJM2070M T1 (XA0210)

Audio Power Amplifier

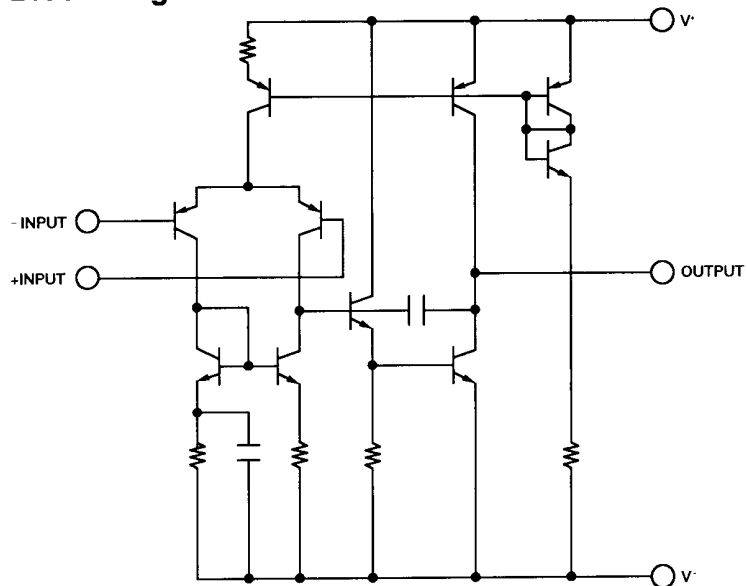


8) NJM2100M T1 (XA0209) Operational Amplifier

Pin Assignment



Block Diagram



9) Transistor, Diode, and LED Outline Drawings

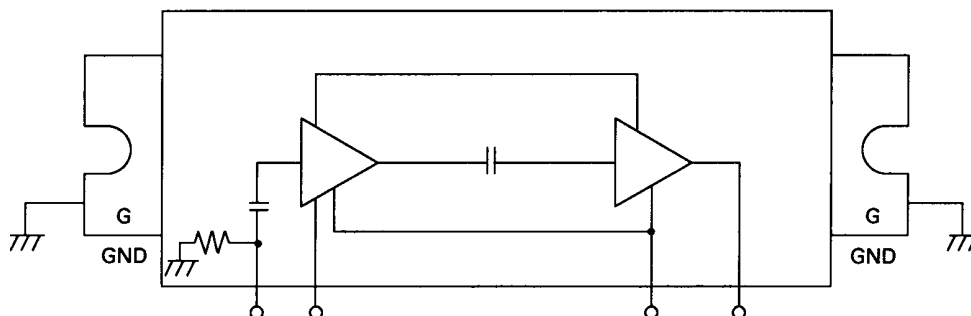
Top View

DA204U T106 XD0130	FMA7XT 148 XU0027	MA716 TW XD0118	MA741WA TX XD0251	MA742 TX XD0250
UN211H TX XU0040	UN2214 TX XU0038	UN9111 TX XU0062	XP1501 TX XU0172	

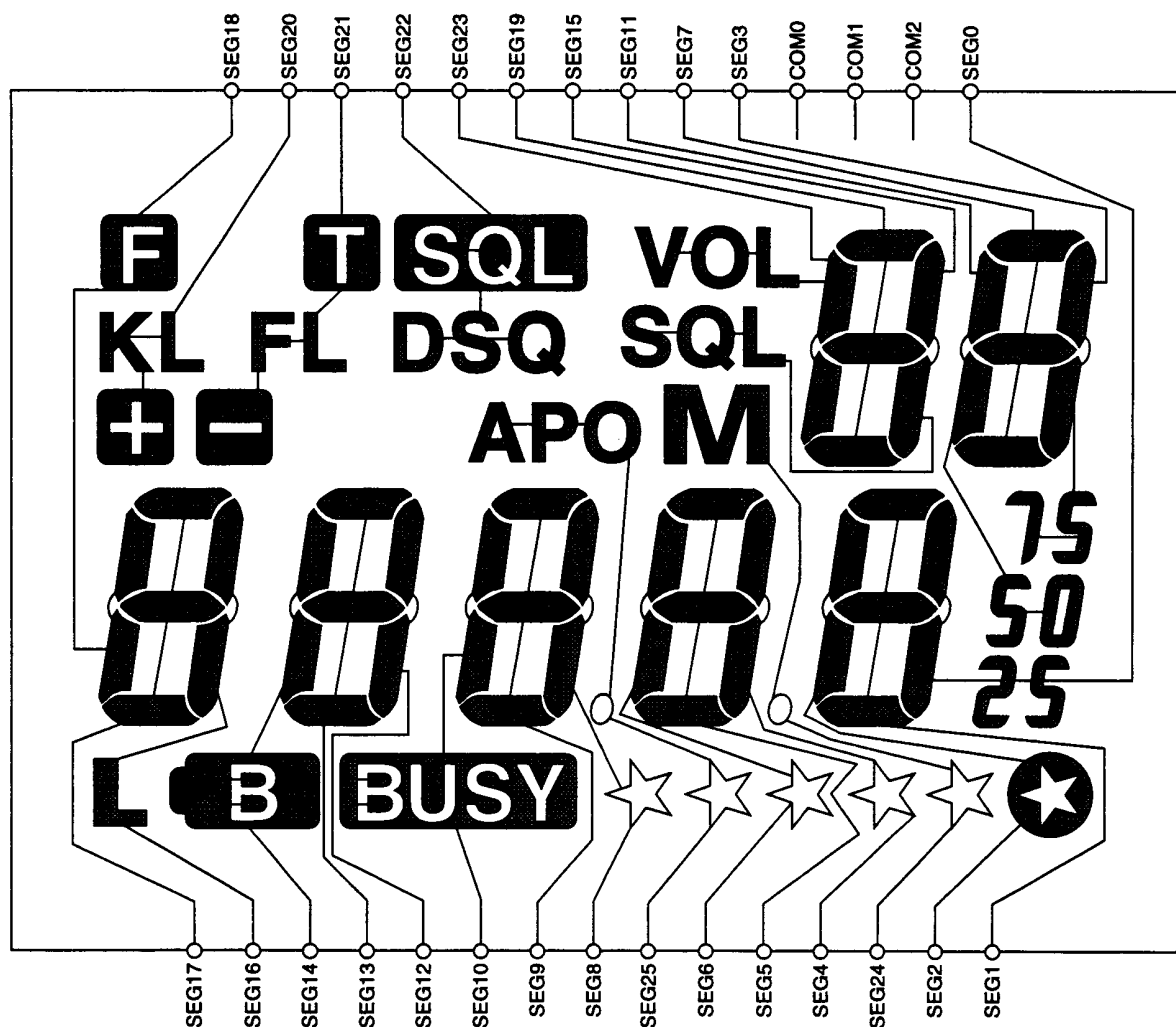
10) P. A. Module (IC101)

TA1 : XA0439
TA2 : XA0421

T : XA0381

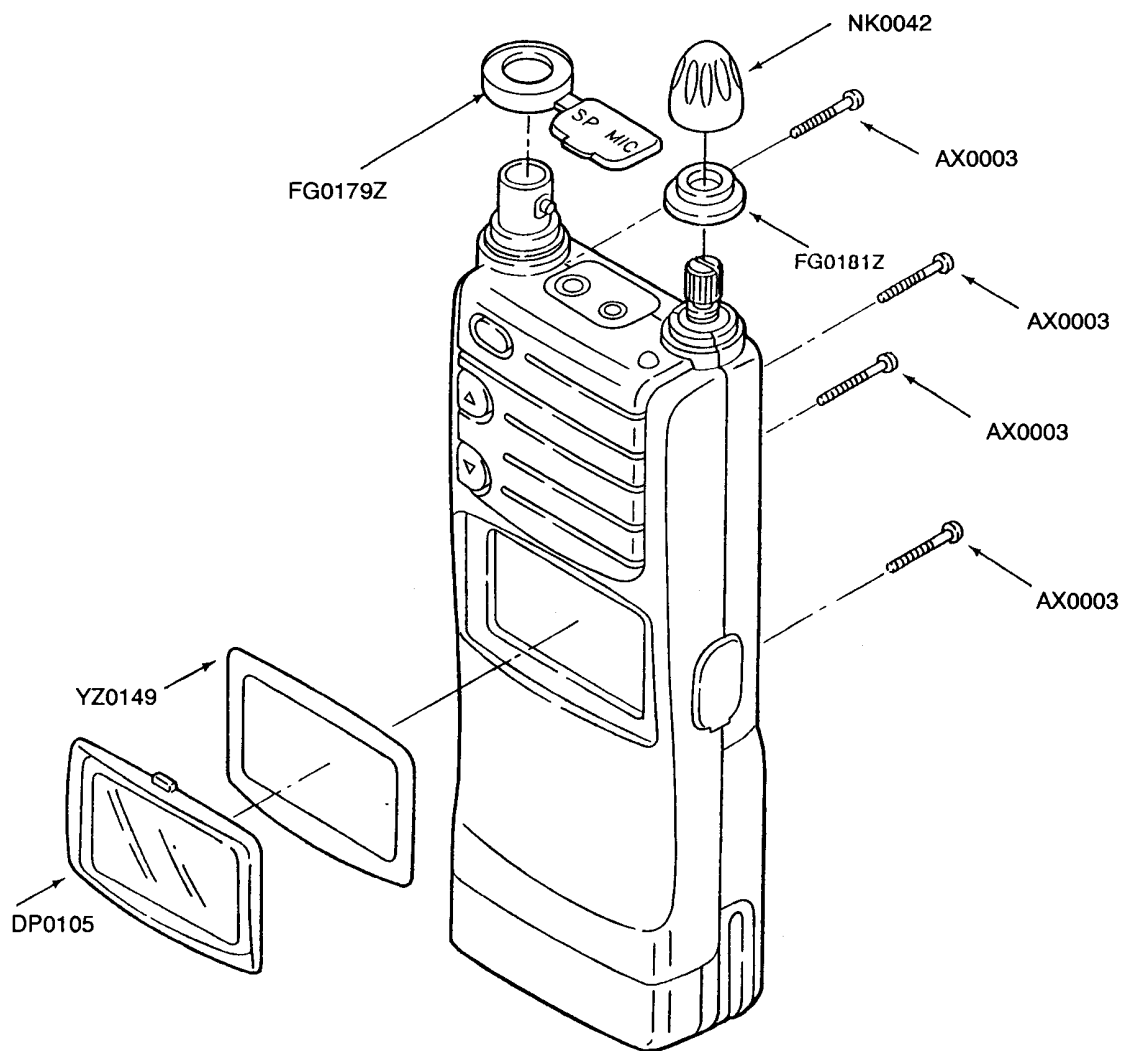


11) LCD Connection

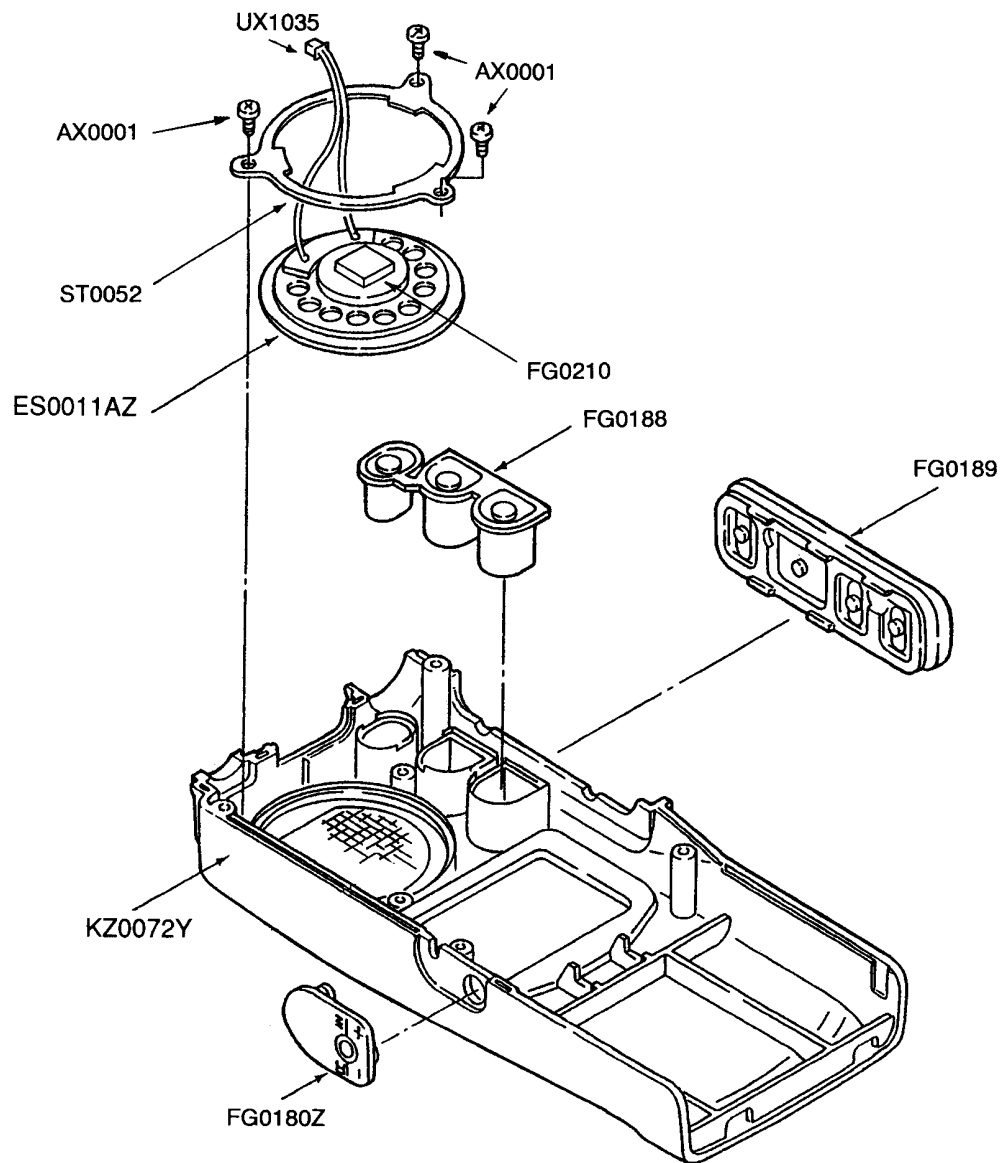


EXPLODED VIEW

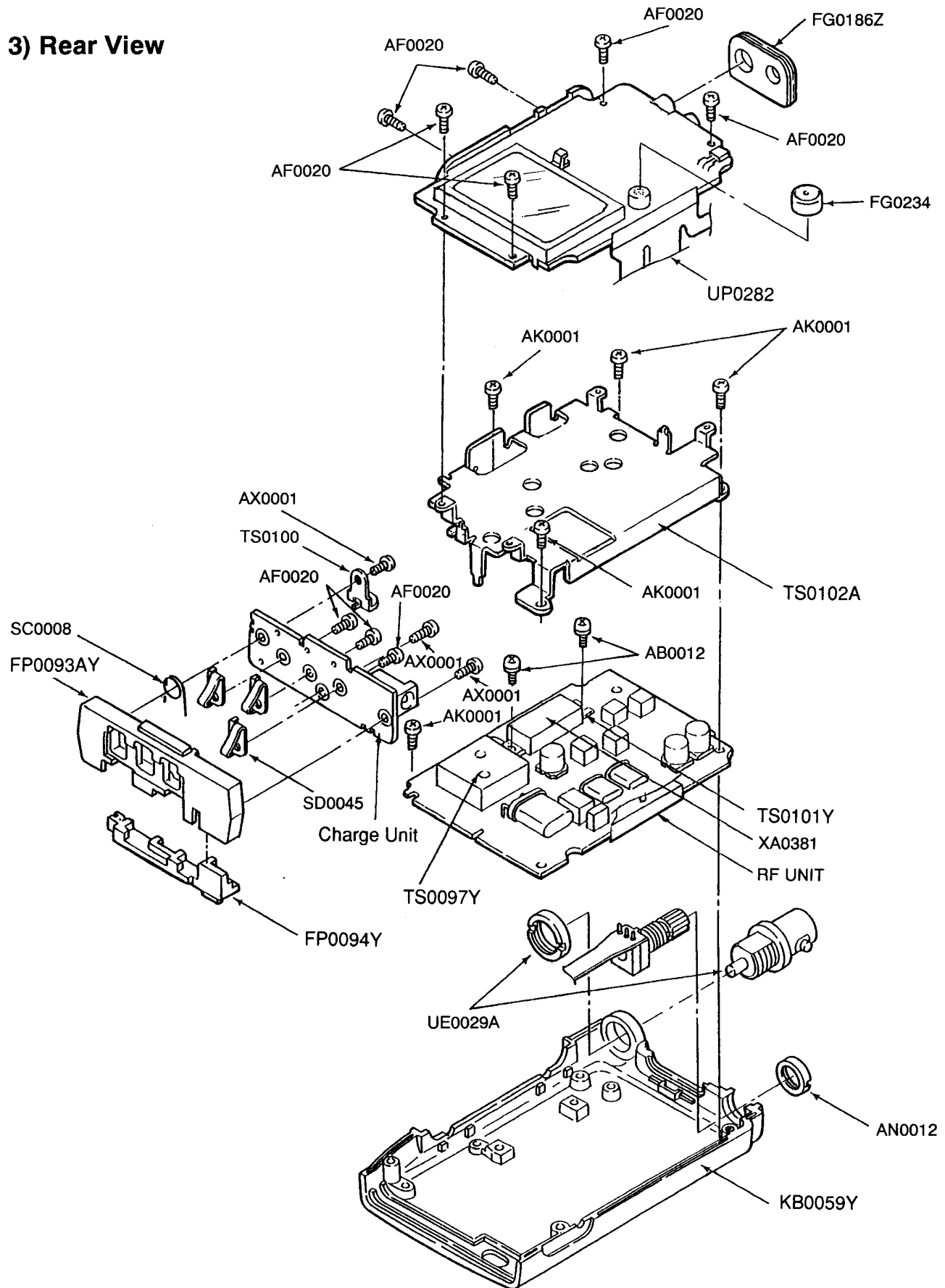
1) Front View 1



2) Front View 2



3) Rear View



PARTS LIST

Ref.No	PartsNo.	Description	Parts Name	Ver.
C1	CU3035	Chip C	C1608BIH102KTA	
C2	CU3035	Chip C	C1608BIH102KTA	
C3	CS0206	Chip Tantal	TMCMD0G107MTR	
C4	CU3017	Chip C	C1608CHI1H330T-AS	
C5	CU3017	Chip C	C1608CHI1H330T-AS	
C6	CS0208	Chip Tantal	TMCMA01475MTR	
C7	CU3035	Chip C	C1608BIH102KTA	
C8	CU3035	Chip C	C1608BIH102KTA	
C9	CS0206	Chip Tantal	TMCMD0G107MTR	
C10	CS0373	Chip Tantal	TMCMD1C476MTR	
C11	CS0206	Chip Tantal	TMCMD0G107MTR	
C12	CU3059	Chip C	C1608F1E104ZTA	
C16	CS0057	Chip Tantal	TMCSA0J225MTR	
C18	CS0049	Chip Tantal	TMCSA1C105MTR	
C19	CU3021	Chip C	C1608CHI1H680JTA	
C20	CU3035	Chip C	C1608BIH102KTA	
C21	CU3056	Chip C	C1608F1E473ZTA	
C22	CU3035	Chip C	C1608BIH102KTA	
C23	CU3035	Chip C	C1608BIH102KTA	
C24	CU3051	Chip C	C1608BIH1E223KTA	
C25	CU3051	Chip C	C1608BIH1E223KTA	
C26	CU3027	Chip C	C1608CHI1H221JTA	
C27	CU3035	Chip C	C1608BIH102KTA	
C29	CU3027	Chip C	C1608CHI1H221JTA	
C30	CU3059	Chip C	C1608F1E104ZTA	
C31	CS0063	Chip Tantal	TMCSA1V104MTR	
C32	CU3035	Chip C	C1608BIH102KTA	
C33	CU3035	Chip C	C1608BIH102KTA	
C35	CU3035	Chip C	C1608BIH102KTA	
C36	CU3035	Chip C	C1608BIH102KTA	
C37	CU3026	Chip C	C1608CHI1H181JT-AS	
C38	CS0049	Chip Tantal	TMCSA1C105MTR	
C39	CU3059	Chip C	C1608F1E104ZTA	
C40	CU3059	Chip C	C1608F1E104ZTA	
C41	CU3059	Chip C	C1608F1E104ZTA	
C43	CS0063	Chip Tantal	TMCSA1V104MTR	
C44	CU3047	Chip C	C1608BIH103KTA	
C45	CU3035	Chip C	C1608BIH102KTA	
C47	CU3059	Chip C	C1608F1E104ZTA	
C48	CU3059	Chip C	C1608F1E104ZTA	
CN3	UP0282		DJG5 IF-RF	
CN4	UE0144		TE1208P128G02	
CN7	UE0267		AXN420C330P	
D1	XL0045	LED	PG1101F-TR	
D3	XL0045	LED	PG1101F-TR	
D7	XD0291	Diode	MA729-TX	
D9	XD0291	Diode	MA729-TX	
D10	XL0046	LED	VRPG4607K	
D11	XD0250	Diode	MA742 TX	
IC1	XA0351	IC	24LC16BT-1/SN	
IC2	XA0309	IC	RH5V1.25AA-T1	
IC3	XA0198	IC	RH5V132AA-T1	
IC4	XA0383	IC	S-81235SG-Q1-T1	
Ref.No	PartsNo.	Description	Parts Name	Ver.
IC5	XA0402	IC	M38267M8L-101FP	
IC6	XA0209	IC	NJM12100M T1	
J1	MAC12GG	Wire	#30A11L-025-H1	
JK1	UJ0019	Connector	HSJ1493-01-010	
JK2	UJ0022	Connector	HSJ1102-01-540	
L1	QC0003	Coil	MLF3216A1R0K-T	
L2	QC0003	Coil	MLF3216A1R0K-T	
L4	QC0003	Coil	MLF3216A1R0K-T	
L5	QC0003	Coil	MLF3216A1R0K-T	
L6	QC0442	Coil	MLF1608A1R0K-T	
L7	QC0442	Coil	MLF1608A1R0K-T	
L8	QC0442	Coil	MLF1608A1R0K-T	
L9	QC0442	Coil	MLF1608A1R0K-T	
L10	QC0442	Coil	MLF1608A1R0K-T	
LCD1	EL0030	LCD	LCD XH618	
MIC1	EY0012	Mic	EN-123T	
Q1	XL0064	Transistor	UN5210 TX	
Q3	XU0040	Transistor	UN211H TX	
Q5	XL0040	Transistor	UN211H TX	
Q7	XU0014	Transistor	DTCL44EKA T146	
Q9	XL0064	Transistor	UN5210 TX	
Q10	XU0064	Transistor	UN5210 TX	
Q11	XT0095	Transistor	2SC4081 T106R	
Q12	XU0064	Transistor	UN5210 TX	
Q13	XL0064	Transistor	UN5210 TX	
R1	RK3028	Chip R	ERJ3GSYJ151V	
R3	RK3028	Chip R	ERJ3GSYJ151V	
R4	RK3050	Chip R	ERJ3GSYJ103V	
R5	RK3038	Chip R	ERJ3GSYJ102V	
R6	RK3058	Chip R	ERJ3GSYJ473V	
R7	RK3038	Chip R	ERJ3GSYJ102V	
R8	RK3046	Chip R	ERJ3GSYJ472V	
R9	RK3058	Chip R	ERJ3GSYJ473V	
R10	RK3058	Chip R	ERJ3GSYJ473V	
R11	RA0003	Chip R	NNR4E0AJ102E	
R12	RK3038	Chip R	ERJ3GSYJ102V	
R13	RK3038	Chip R	ERJ3GSYJ102V	
R14	RK3038	Chip R	ERJ3GSYJ102V	
R15	RK3024	Chip R	ERJ3GSYJ680V	
R16	RK3024	Chip R	ERJ3GSYJ680V	
R18	RK3036	Chip R	ERJ3GSYJ681V	
R19	RK3074	Chip R	ERJ3GSYJ105V	
R20	RK3038	Chip R	ERJ3GSYJ102V	
R21	AXN420C330P		AXN420C330P	
R22	RK3074	Chip R	ERJ3GSYJ105V	
R23	RK3043	Chip R	ERJ3GSYJ272V	
R24	RK3038	Chip R	ERJ3GSYJ102V	
R26	RK3038	Chip R	ERJ3GSYJ102V	
R28	RK3058	Chip R	ERJ3GSYJ473V	
R30	RA0003	Chip R	NNR14E0AJ102E	
R31	RK3055	Chip R	ERJ3GSYJ273V	
R32	RK3058	Chip R	ERJ3GSYJ473V	
R33	RK3058	Chip R	ERJ3GSYJ473V	
R34	RK3058	Chip R	ERJ3GSYJ473V	
Ref.No	PartsNo.	Description	Parts Name	Ver.
R35	RK3058	Chip R	ERJ3GSYJ473V	
R36	RK1018	Chip R	ERJ8GEYJ101V	
R37	RK3038	Chip R	ERJ3GSYJ102V	
R38	RK3041	Chip R	ERJ3GSYJ182V	
R39	RK3038	Chip R	ERJ3GSYJ102V	
R40	RK3068	Chip R	ERJ3GSYJ334V	
R41	RK3065	Chip R	ERJ3GSYJ184V	
R42	RK3061	Chip R	ERJ3GSYJ823V	
R43	RK3058	Chip R	ERJ3GSYJ473V	
R44	RK3052	Chip R	ERJ3GSYJ223V	
R47	RK3052	Chip R	ERJ3GSYJ153V	
R48	RK3062	Chip R	ERJ3GSYJ104V	
R49	RK3048	Chip R	ERJ3GSYJ682V	
R52	RK3041	Chip R	ERJ3GSYJ182V	
R53	RK3046	Chip R	ERJ3GSYJ472V	
R54	RK3062	Chip R	ERJ3GSYJ104V	
R55	RK3050	Chip R	ERJ3GSYJ103V	
R56	RK3066	Chip R	ERJ3GSYJ224y	
R57	RK3039	Chip R	ERJ3GSYJ122V	
R58	RK3069	Chip R	ERJ3GSYJ394V	
R59	RK3051	Chip R	ERJ3GSYJ123V	
R60	RK3038	Chip R	ERJ3GSYJ102V	
R61	RK3054	Chip R	ERJ3GSYJ223V	
R62	RK3065	Chip R	ERJ3GSYJ184V	
R63	RK3056	Chip R	ERJ3GSYJ333V	
R64	RK3058	Chip R	ERJ3GSYJ473V	
R65	RK3058	Chip R	ERJ3GSYJ473V	
R66	RK3055	Chip R	ERJ3GSYJ273V	
R67	RK3046	Chip R	ERJ3GSYJ472V	
R68	RK3061	Chip R	ERJ3GSYJ823V	
R69	RK3050	Chip R	ERJ3GSYJ103V	
R70	RK3062	Chip R	ERJ3GSYJ104V	
R71	RK3034	Chip R	ERJ3GSYJ471V	
R72	RK3056	Chip R	ERJ3GSYJ333V	
R73	RK3051	Chip R	ERJ3GSYJ123V	
R75	RK3058	Chip R	ERJ3GSYJ473V	
R76	RK3038	Chip R	ERJ3GSYJ102V	
R78	RK3038	Chip R	ERJ3GSYJ102V	
R79	RK3001	Chip R	ERJ3GSYJ0R00V	E
R80	RK3046	Chip R	ERJ3GSYJ472V	
R82	RK3058	Chip R	ERJ3GSYJ473V	
R83	RK3058	Chip R	ERJ3GSYJ473V	
R84	RK3038	Chip R	ERJ3GSYJ102V	E
R86	RK3058	Chip R	ERJ3GSYJ473V	
R87	RK3046	Chip R	ERJ3GSYJ472V	
R89	RK3038	Chip R	ERJ3GSYJ102V	
R92	RK3001	Chip R	ERJ3GSYJ0R00V	
R93	RK3042	Chip R	ERJ3GSYJ222V	
R94	RK3030	Chip R	ERJ3GSYJ221V	
R95	RK3030	Chip R	ERJ3GSYJ221V	
R96	RK3038	Chip R	ERJ3GSYJ102V	
R97	RK3038	Chip R	ERJ3GSYJ102V	
R98	RK3070	Chip R	ERJ3GSYJ474V	
R99	RK3001	Chip R	ERJ3GSYJ0R00V	E
Ref.No.	PartsNo.	Description	Parts Name	Ver.
C701	CU3015	Chip C	C1608CHI1H220JTA	
C702	CU3015	Chip C	C1608CHI1H220JTA	
C703	CU3023	Chip C	C1688CHI1H101JTA	
C705	CS0237	Chip Tanta	TMCMA1A475MTR	
C709	CS0049	Chip Tanta	TMCSA1C105MTR	
C710	CU3059	Chip C	C1608F1E104ZTA	
C711	CS0236	Chip Tanta	TMCMA01685MTR	
C712	CU3035	Chip C	C1608BIH102KTA	
C713	CU3019	Chip C	C1608CHI1H470JTA	
C714	CS0049	Chip Tanta	TMCSA1C105MTR	
C715	CS0049	Chip Tanta	TMCSA1C105MTR	
CN701	UE0274		AXN320C038P	
UP0295A			EJ28U PCB	
R701	RK3048	Chip R	ERJ3GSYJ682V	
R702	RK3089	Chip R	ERJ3GSYJ912V	
R703	RK3066	Chip R	ERJ3GSYJ224V	
R704	RK3074	Chip R	ERJ3GSYJ105V	
R705	RK3051	Chip R	ERJ3GSYJ123V	
R707	RK3067	Chip R	ERJ3GSYJ274V	
R710	RK3047	Chip R	ERJ3GSYJ562V	
R715	RK3060	Chip R	ERJ3GSYJ683V	
R716	RK3054	Chip R	ERJ3GSYJ223V	
R717	RK3055	Chip R	ERJ3GSYJ273V	
R718	RK3062	Chip R	ERJ3GSYJ104V	
IC701	XA0239	IC	AK2341	
	XQ0077	Crystal	38C 3.686400MHZ	
	TZ0069		EJ28U	
	HK0396A		Package	
	HP0029		Protect.Bag	

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Ref.No.	Parts No.	Description	Parts Name	Ver.
C101	CU3035	Chip C	C1608CH1H102KTA	
C102	CU3035	Chip C	C1608CH1H102KTA	
C103	CU3035	Chip C	C1608CH1H102KTA	
C104	CU3035	Chip C	C1608CH1H102KTA	
C105	CU3035	Chip C	C1608CH1H102KTA	
C106	CU3035	Chip C	C1608CH1H102KTA	
C107	CU3010	Chip C	C1608CH1H090CTA	TE.TATAH
C107	CU3007	Chip C	C1608CH1H060CTA	TA2
C108	CU3007	Chip C	C1608CH1H060CTA	TA2
C110	CU3017	Chip C	C1608CH1H330JTA	
C112	CU3011	Chip C	C1608CH1H100CTA	
C113	CU3017	Chip C	C1608CH1H330JTA	TE.TATAH
C114	CU3013	Chip C	C1608CH1H150JTA	TA2
C114	CU3019	Chip C	C1608CH1H470JTA	TE.TATAH
C114	CU3013	Chip C	C1608CH1H150JTA	TA2
C115	CU3013	Chip C	C1608CH1H150JTA	
C116	CU3019	Chip C	C1608CH1H470JTA	TE.TATAH
C116	CU3012	Chip C	C1608CH1H120JTA	TA2
C117	CS0049	Chip Tantal	TMCSA1C105MTR	
C118	CU3035	Chip C	C1608CH1H102KTA	
C119	CU3035	Chip C	C1608CH1H102KTA	
C121	CU3004	Chip C	C1608CH1H030CTA	TE.TATAH
C121	CU3003	Chip C	C1608CH1H020CTA	TA2
C122	CU3004	Chip C	C1608CH1H030CTA	TE.TATAH
C123	CU3015	Chip C	C1608CH1H220JTA	
C124	CU3035	Chip C	C1608CH1H102KTA	
C125	CU3002	Chip C	C1608CH1H010CTA	
C126	CU3002	Chip C	C1608CH1H010CTA	
C127	CS0049	Chip Tantal	TMCSA1C105MTR	
C128	CU3035	Chip C	C1608CH1H102KTA	
C129	CU3035	Chip C	C1608CH1H102KTA	
C130	CS0220	Chip Tantal	TMCMA1C225MTR	
C131	CU3051	Chip C	C1608CH1E223KTA	
C132	CU3047	Chip C	C1608CH1H103XTA	
C133	CU3047	Chip C	C1608CH1H103KTA	
C134	CU3035	Chip C	C1608CH1H102KTA	
C135	CU3009	Chip C	C1608CH1H080CTA	
C136	CU3047	Chip C	C1608CH1H103KTA	
C137	CS0220	Chip Tantal	TMCMA1C225MTR	
C141	CU3035	Chip C	C1608CH1H102KTA	
C142	CU3002	Chip C	C1608CH1H020CTA	TE.TATAH
C143	CU3003	Chip C	C1608CH1H020CTA	
C144	CU3003	Chip C	C1608CH1H020CTA	
C146	CU3007	Chip C	C1608CH1H060CTA	
C148	CU3006	Chip C	C1608CH1H050CTA	
C149	CU3011	Chip C	C1608CH1H100CTA	
C150	CU3011	Chip C	C1608CH1H100CTA	
C151	CU3004	Chip C	C1608CH1H030CTA	

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Ref.No.	Parts No.	Description	Parts Name	Ver.
C204	CU3059	Chip C	C1608JF1E104ZTA	
C205	CE0373	Electrolytic	16XV 100UV	
C206	CS0366	Chip Tantal	TMCMA0G106MTR	
C215	CU3035	Chip C	C1608CH1H102KTA	
C216	CU3035	Chip C	C1608CH1H102KTA	
C217	CU3019	Chip C	C1608CH1H470JTA	
C218	CU3035	Chip C	C1608CH1H102KTA	
C219	CS0366	Chip Tantal	TMCMA0G106MTR	
C220	CS0063	Chip Tantal	TMCSA1V104MTR	
C224	CU3035	Chip C	C1608CH1H102KTA	
C225	CU3035	Chip C	C1608CH1H102KTA	
C226	CS0049	Chip Tantal	TMCMA1C105MTR	
C228	CS0377	Chip Tantal	TMCMB0G476MTR	
C229	CS0237	Chip Tantal	TMCMA1A475MTR	
C230	CS0366	Chip Tantal	TMCMA0G106MTR	
D101	XD0066	Diode	RLS135 TE 11	
D102	XD0066	Diode	RLS135 TE 11	
D103	XD0251	Diode	MA741WA-TX	
D104	XD0299	Diode	MA304-TX	
D105	XD0299	Diode	MA304-TX	
D106	XD0299	Diode	MA304-TX	
D107	XD0299	Diode	MA304-TX	
D108	XD0129	Diode	ISS318 TT11	
D109	XD0118	Diode	MA716 TX	
D113	XD0130	Diode	DA204U T106	
F1101	XC0018	Filter	CFWM450E	
JK101	RD0108		JPV01R-01	
IC101	XA0381	IC	S-AV28	TE.TATAH
IC102	XA0421	IC	PF0311	TA2
IC103	XA0385	IC	MC3372VX-EL	
IC104	XA0343	IC	MC3372VX-EL	
IC105	XA0210	IC	NJK2070XT1	
L101	QC0016	Coil	MLF3216A2R2K-T	
L102	QKA65A	Coil	XRL5 3.5T 0.4	
L103	QKA65A	Coil	MRL15 3.5T 0.4	
L104	QKA65A	Coil	MRL15 3.5T 0.4	
L105	QC0430	Coil	MLF1608DR10K-T	
L106	QC0430	Coil	MLF1608DR10K-T	
L107	QKA75A	Coil	QKA75A	
L108	QC0090	Coil	MLF3216A4R7K-T	
L109	QA0071	Coil	LQA0071	
L110	QA0071	Coil	LQA0071	
L111	QA0071	Coil	LQA0071	
L112	QA0071	Coil	LQA0071	
L113	QC0009	Coil	MLF3216DR10K-T	
L114	QC0430	Coil	MLF1608DR10K-T	
Q101	XT0119	Transistor	2SC3356-T1BR24	
Q102	XT0119	Transistor	2SC3356-T1BR24	
Q103	XU0172	Transistor	XP1501-TX	

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Ref.No.	Parts No.	Description	Parts Name	Ver.
Q105	XT0096	Transistor	2SC4099 T106N	
Q106	XE0020	FET	2SK3601GE TL	TE.TA.
Q106	XE0009	FET	2SK302GR	
Q107	XT0137	Transistor	2SC5065-Q(TB85L)	
Q108	XT0096	Transistor	2SC4099 T106N	
Q109	XT0095	Transistor	2SC4081 T106R	
Q110	XT0088	Transistor	2SA1213V TE12R	
Q111	XT0088	Transistor	2SA1213V TE12R	
Q112	XU0027	Transistor	FM47AT148	
Q113	XU0172	Transistor	XP1501-TX	
Q114	XT0088	Transistor	2SA1213V TE12R	
Q115	XT0095	Transistor	2SC4081 T106R	
Q116	XU0172	Transistor	XP1501-TX	
Q117	XT0137	Transistor	2SC5065-Q(TB85L)	
Q118	XU0125	Transistor	DTA144EUAT106	
Q119	XU0038	Transistor	UN2214 TX	
Q120	XU0062	Transistor	UN9111 TX	
R101	RK3028	Chip R	ERJ6G5YJ151V	
R102	RK3026	Chip R	ERJ6G5YJ101V	
R103	RK3026	Chip R	ERJ6G5YJ101V	
R104	RK3034	Chip R	ERJ6G5YJ471V	
R105	RK3046	Chip R	ERJ6G5YJ472V	
R106	RK3050	Chip R	ERJ6G5YJ103V	
R107	RK3046	Chip R	ERJ6G5YJ472V	
R108	RK3046	Chip R	ERJ6G5YJ472V	
R110	RK3026	Chip R	ERJ6G5YJ101V	
R111	RK3026	Chip R	ERJ6G5YJ101V	
R113	RK3050	Chip R	ERJ6G5YJ103V	TE.TATAH
R113	RK3051	Chip R	ERJ6G5YJ123V	TA2
R114	RK3050	Chip R	ERJ6G5YJ103V	
R115	RK3026	Chip R	ERJ6G5YJ101V	
R116	RK3050	Chip R	ERJ6G5YJ103V	
R117	RK3034	Chip R	ERJ6G5YJ471V	
R118	RK3050	Chip R	ERJ6G5YJ103V	TE.TATAH
R118	RK3051	Chip R	ERJ6G5YJ123V	TA2
R119	RK3038	Chip R	ERJ6G5YJ102V	
R121	RK3050	Chip R	ERJ6G5YJ103V	
R122	RK3030	Chip R	ERJ6G5YJ221V	
R123	RK3026	Chip R	ERJ6G5YJ101V	
R124	RK3022	Chip R	ERJ6G5YJ470V	
R126	RK3050	Chip R	ERJ6G5YJ103V	
R128	RK3052	Chip R	ERJ6G5YJ153V	
R130	RK3050	Chip R	ERJ6G5YJ103V	
R131	RK3038	Chip R	ERJ6G5YJ102V	
R133	RK3053	Chip R	ERJ6G5YJ183V	
R135	RK3066	Chip R	ERJ6G5YJ224V	
R137	RK3047	Chip R	ERJ6G5YJ562V	
R138	RK3038	Chip R	ERJ6G5YJ102V	
R140	RK3052	Chip R	ERJ6G5YJ153V	
R142	RK3030	Chip R	ERJ6G5YJ221V	
R143	RK3042	Chip R	ERJ6G5YJ222V	

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ADJUSTMENT

1) Required Test Equipment

The following items are required to adjust radio parameters:

1. Regulated power supply	Supply voltage: 5 ~ 14 VDC Current : 3 A or more
2. Digital multi meter	Voltage range : FS = Approx. 20 V Current: 10A or more Input resistance : High impedance
3. Oscilloscope	Measurable frequency : Audio frequency
4. Audio dummy load	Impedance: 8 ohm Dissipation: 1 W or more Jack: 3.5 mm D
5. SSG	Output frequency: 200 MHz or more Output level : -20 dB/0.1 a V ~ 120dB/1V Modulation : AM/FM
6. Spectrum Analyzer	Measuring range : Up to 2 GHz or more
7. Power meter	Measurable frequency: Up to 200 MHz Impedance : 50 ohm unbalanced Measuring range : 0.1W ~ 10 W
8. Audio voltmeter	Measurable frequency : Up to 100 kHz Sensitivity : 1 mV to 10 V
9. Audio generator	Output frequency : 67 Hz to 10 kHz Output impedance : 600 ohm , unbalanced
10. Distortion meter /SINAD meter	Measurable frequency : 1 kHz Input level : Up to 40 dB Distortion level : 1 % - 100%
11. Frequency counter	Measurable frequency : Up to 200 MHz Measurable stability : Approx. +/-0. 1 ppm
12. Linear detector	Measurable frequency : Up to 200 MHz Characteristics: Flat CN: 60 dB or more

Note

- * Standard modulation: 1 kHz +/-3.5 kHz/DEV
- * Reference sensitivity: 12dB SINAD
- * Specified audio output level : 200 mW at 8 ohm
- * Standard audio output level : 50 mW at 8 ohm
- * Use an RF cable (3D2W: 1 m) for test equipment.
- * Attach a fuse to the RF test equipment.
- * All SSG outputs are indicated by EMF.
- * Supply voltage for the transceiver: 13.8 VDC

2) Adjustment Mode

The DJ - 190 does not require a serviceperson to manipulate the components on the printed - circuit board, except the trimmer and coil when adjusting frequency. Most of the adjustments for the transceiver are made by using the keys on it while the unit is in the adjustment mode. Because the adjustment mode temporarily uses the channels, frequency must be set on each channel before adjustments can be made. For instructions on how to program the channels, see the "DJ - 190 INSTRUCTION MANUAL" which came with the product. In consideration of the radio environment, the frequency on each channel must be near the value (+/- 1 MHz) listed in the table below. To enter the adjustment mode, turn the power off, hold down both the UP and DOWN keys, and press the POWER key. "chEc" appears on the LCD for about two seconds, and "C" appears indicating the unit is in the adjustment mode.

Channel frequencies used in the adjustment mode

Channel	Channel function	Frequency
1	Reference frequency adjustment	145 MHz
2	High power adjustment	* 145 MHz
3	Low power adjustment	* 145 MHz
4	Minimum frequency sensitivity adjustment	136 MHz
5	Medium frequency sensitivity adjustment	145 MHz
6	Maximum frequency sensitivity adjustment	173 MHz
7	S-meter (1) adjustment	* 145 MHz
8	S-meter (FULL) adjustment	* 145 MHz
9	Deviation	* 145 MHz
12	Tone 67 Hz test	* 145 MHz
13	Tone 88.5 Hz test	* 145 MHz
14	Tone 250.3 Hz test	* 145 MHz
15	Tone burst test	* 145 MHz
16	Aging (Not required to use)	145 MHz
20	VCO frequency shift change (Do not change).	-

* 162MHz for TA2 Version

Caution

■ Do not press the **UP** or **DOWN** key while channel 20 is selected in the adjustment mode. Otherwise, the VCO switch frequency will change, causing a malfunction.

Reference Frequency Adjustment

1. In the adjustment mode, select channel 1 by rotating the main tuning dial.
2. Press the **(PTT)** key to start transmission.
3. Rotate TC101 on the RF circuit board until the value on the frequency counter matches the one displayed on the LCD.
4. On 145.05MHz measure TP near the VCO and adjust L301 to obtain $1.1V \pm 0.1V$ (If the second decimal point is flashing, the PLL is unlocked).

High Power Adjustment

1. In the adjustment mode, select channel 2 by rotating the main tuning dial.
2. Hold down the **(F)** key and press the **(H/L)** key to enter the high power mode ("L" at the lower-left of the display disappears).
3. Hold down the **(PTT)** key to start transmission.
4. While watching the reading of the TX power meter, set the output power to the value closest to 5 W by using the **(UP)** or **(DOWN)** keys.
5. When the **(PTT)** key is released, the output power at that time will be stored as the high power setting.

Low Power Adjustment

1. In the adjustment mode, select channel 3 by rotating the main tuning dial.
2. Hold down the **(F)** key and press the **(H/L)** key to enter the low power mode ("L" appears at the lower-left of the display).
3. Hold down the **(PTT)** key to start transmission.
4. While watching the reading of the TX power meter, set the output power to the value closest to 0.8 W by using the **(UP)** or **(DOWN)** keys.
5. When the **(PTT)** key is released, the output power at that time will be stored as the low power setting.

Minimum Frequency Sensitivity Adjustment

See "Note on Adjusting the Sensitivity" later in this section.

1. In the adjustment mode, select channel 4 by rotating the main tuning dial.
2. Using the **(UP)** or **(DOWN)** key, set the minimum frequency sensitivity.

Medium Frequency Sensitivity Adjustment

See "Note on Adjusting the Sensitivity" later in this section.

1. In the adjustment mode, select channel 5 by rotating the main tuning dial.
2. Using the **(UP)** or **(DOWN)** key, set the medium frequency sensitivity.

Maximum Frequency Sensitivity Adjustment

See "Note on Adjusting the Sensitivity" later in this section.

1. In the adjustment mode, select channel 6 by rotating the main tuning dial.
2. Using the **(UP)** or **(DOWN)** key, set the maximum frequency sensitivity.

S-meter (1) Adjustment

1. In the adjustment mode, select channel 7 by rotating the main tuning dial. The S-meter will show a single star (★).
2. Enter "0" dB μ (EMF) with the transceiver tester.
3. Press the **DOWN** key. The transceiver beeps indicating the new setting has been stored successfully.

S-meter (FULL) Adjustment

1. In the adjustment mode, select channel 8 by rotating the main tuning dial. The S-meter will show all six stars (★ ★ ★ ★ ★ ★).
2. Enter "+20" dB μ (EMF) with the transceiver tester.
3. Press the **DOWN** key. The transceiver beeps indicating the new setting has been stored successfully.

Deviation

1. In the adjustment mode, select channel 9 by rotating the main tuning dial.
2. Input a 50 mVrms, 1 KMz signal with your transceiver tester through the external microphone jack.
3. With the tester, put the transceiver in the transmission mode.
4. Using the **UP** or **DOWN** key, set the deviation to the value closest to 4.5kHz. The deviation has three levels namely 0 to 2 which is displayed in the upper right corner of the LCD.

Tone 67 Hz Test

This function is only for checking the tone encoder, not adjusting it.

1. In the adjustment mode, select channel 12 by rotating the main tuning dial.
2. Press the **PTT** key. A 67 Hz tone is automatically sent.
3. Check the deviation with the transceiver tester.

Tone 88.5 Hz Test

1. In the adjustment mode, select channel 13 by rotating the main tuning dial.
2. Press the **PTT** key. An 88.5 Hz tone is automatically sent.
3. Check the deviation with the transceiver tester.

Tone 250.3 Hz Test

1. In the adjustment mode, select channel 14 by rotating the main tuning dial.
2. Press the **(PTT)** key. A 250.3 Hz tone is automatically sent.
3. Check the deviation with the transceiver tester.

Tone Burst Test

This function is only for checking the tone burst, not adjusting it.

1. In the adjustment mode, select channel 15 by rotating the main tuning dial.
2. Press the **(PTT)** key. A 1750 Hz tone burst is automatically sent.
3. Check the deviation with the transceiver tester.

Aging

Perform this aging test only when necessary.

1. In the adjustment mode, select channel 16 by rotating the main tuning dial. The transceiver automatically repeats transmission for a minute and reception for another minute.

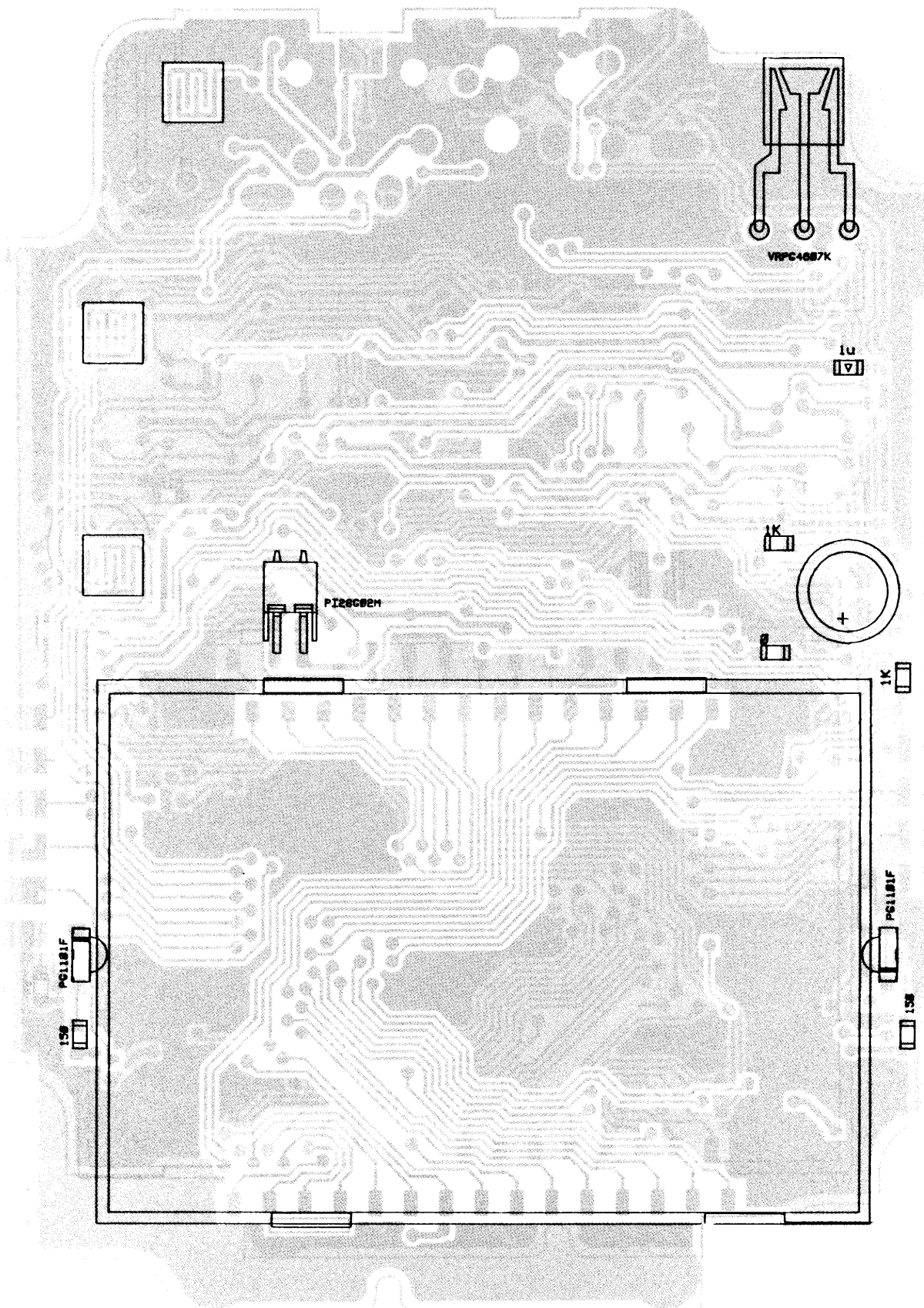
Note on Adjusting Sensitivity

Sensitivity is adjusted by applying the optimum voltage from the CPU to the varicap of the tuning circuit. The coil manipulation for L109, L110, L111, and L112 is not required. If any of the coils is accidentally rotated, return it to the default position as described below, before adjusting the sensitivity.

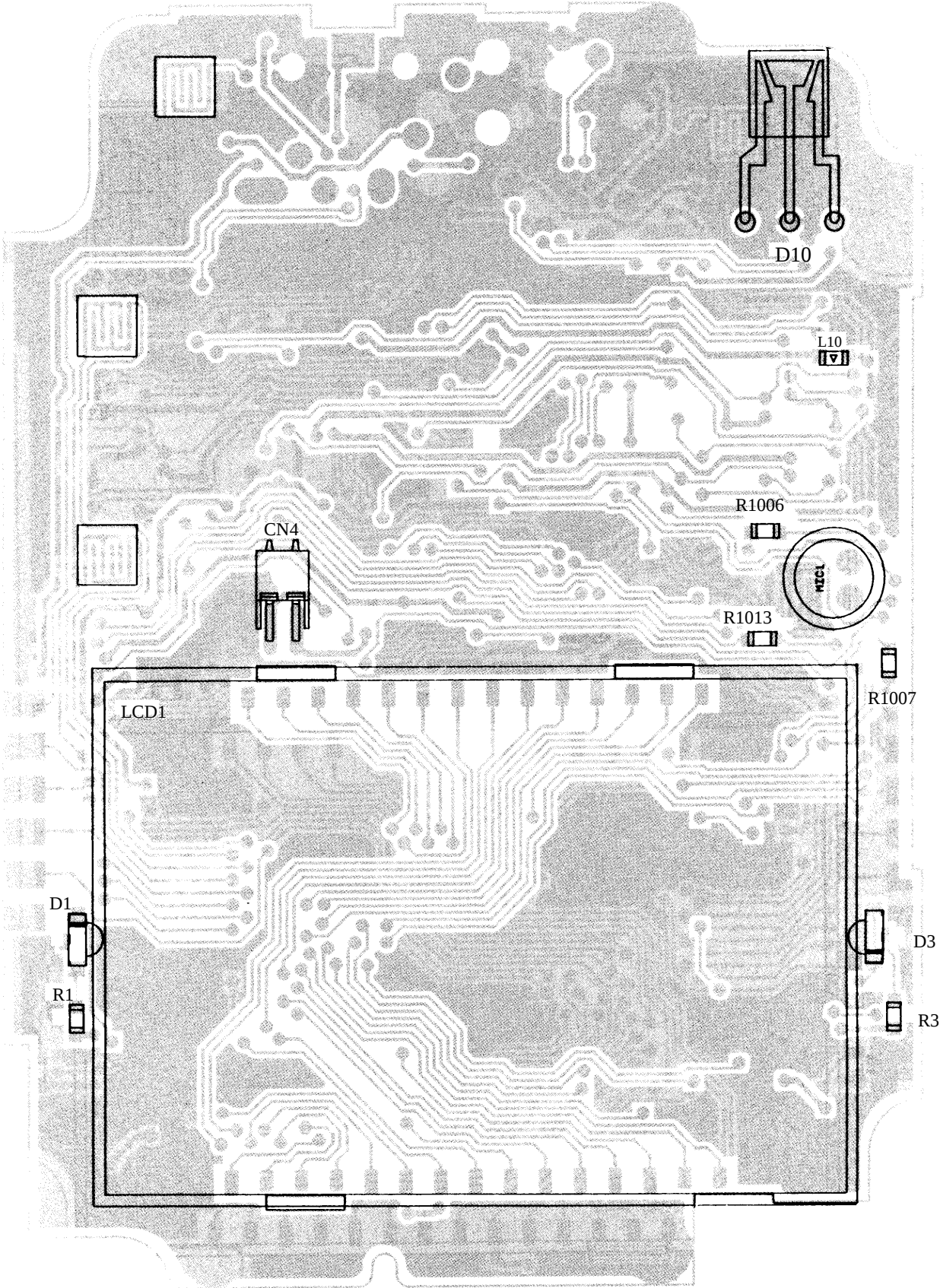
1. Program any frequency within 145MHz +/-1MHz on memory channel 5.
2. Holding down both the **(UP)** and **(DOWN)** key, press the POWER switch to turn the power ON. "chEc" will appear on the LCD for two seconds, and "C" appears.
3. Select channel 5 by rotating the main tuning dial.
4. Using the **(UP)** or **(DOWN)** keys, set the adjustment data to "7F" ("7F" appears in the channel number area on the LCD).
5. Turn the power OFF.
6. Holding down both the **(UP)** and **(DOWN)** key, turn the power ON. When the "C" no longer appears, the transceiver is in the normal status.
7. Set the reception frequency to 145 MHz +/-1MHz. Rotate the coil to maximize the sensitivity.

PC BOARD VIEW

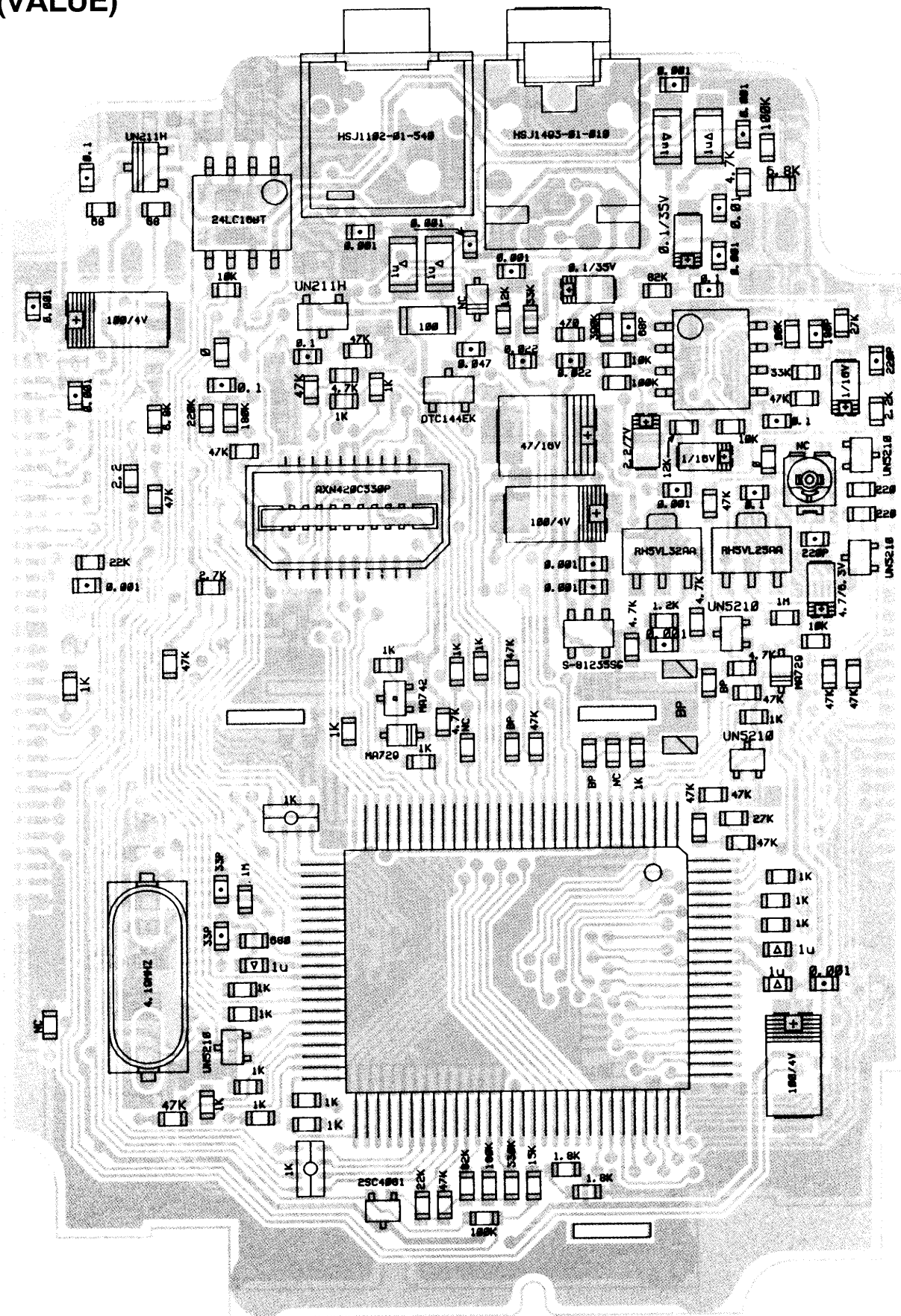
CPU Unit Side A (VALUE)



CPU Unit Side A (REFERENCE)

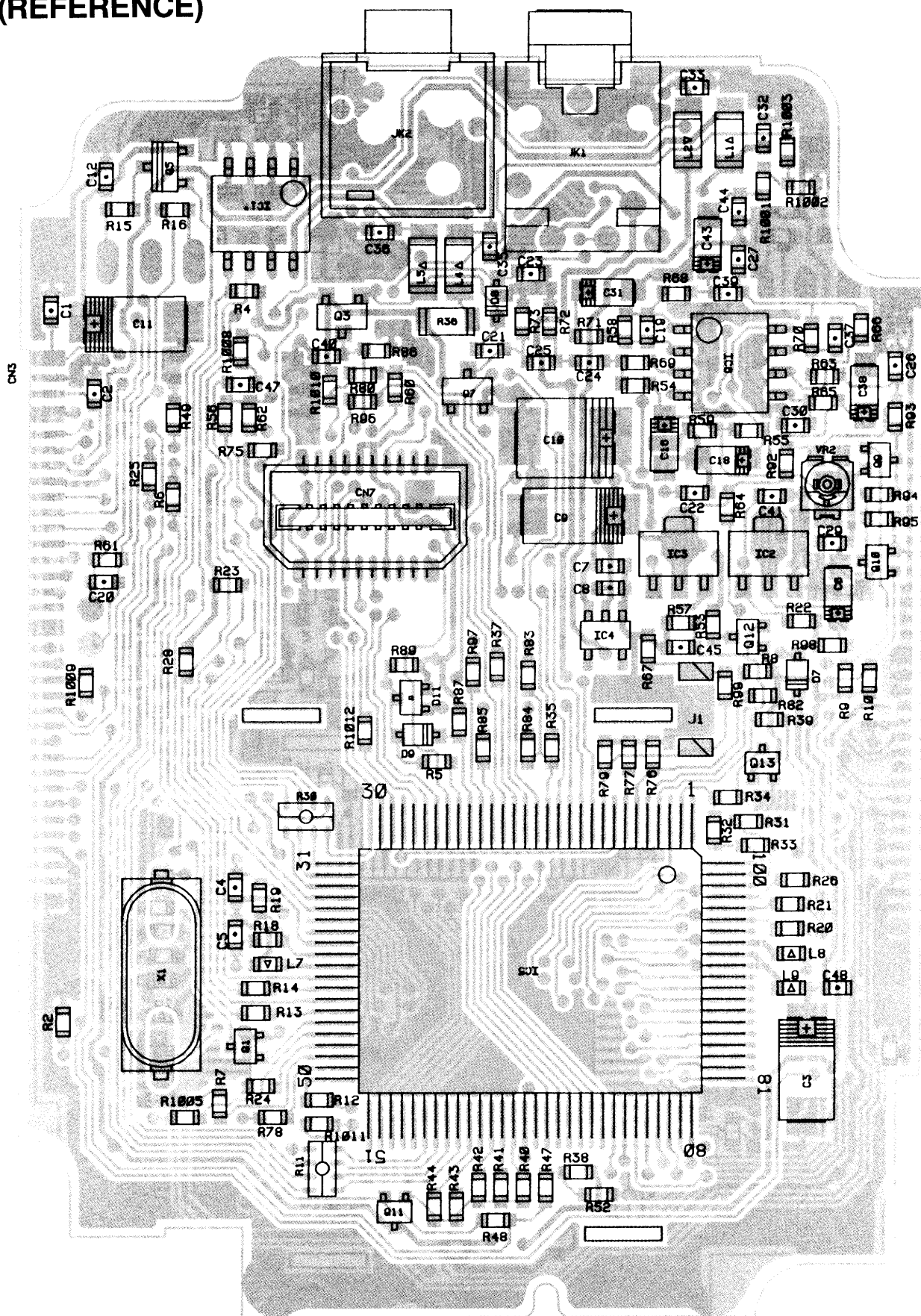


val

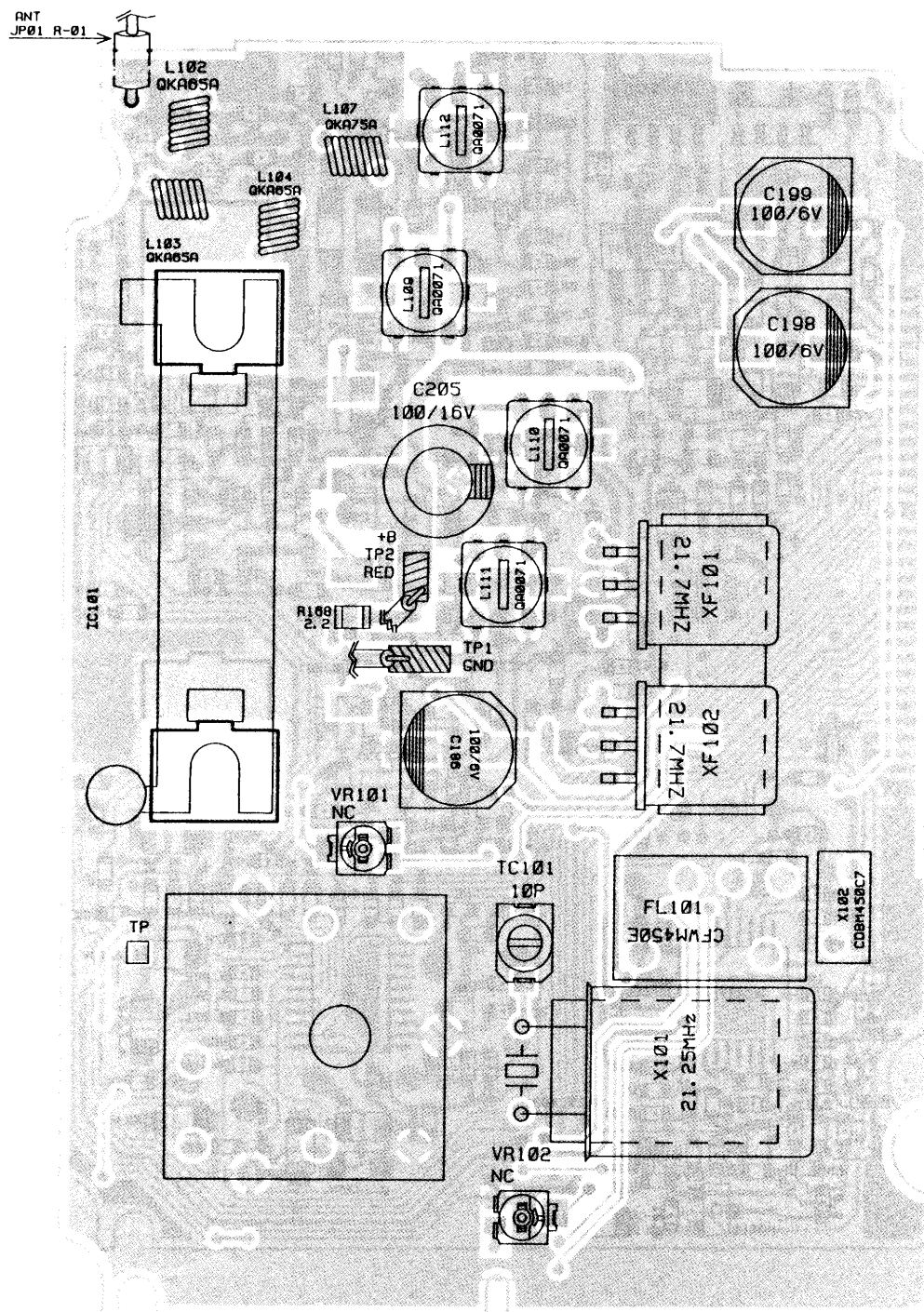


CPU Unit Side B (REFERENCE)

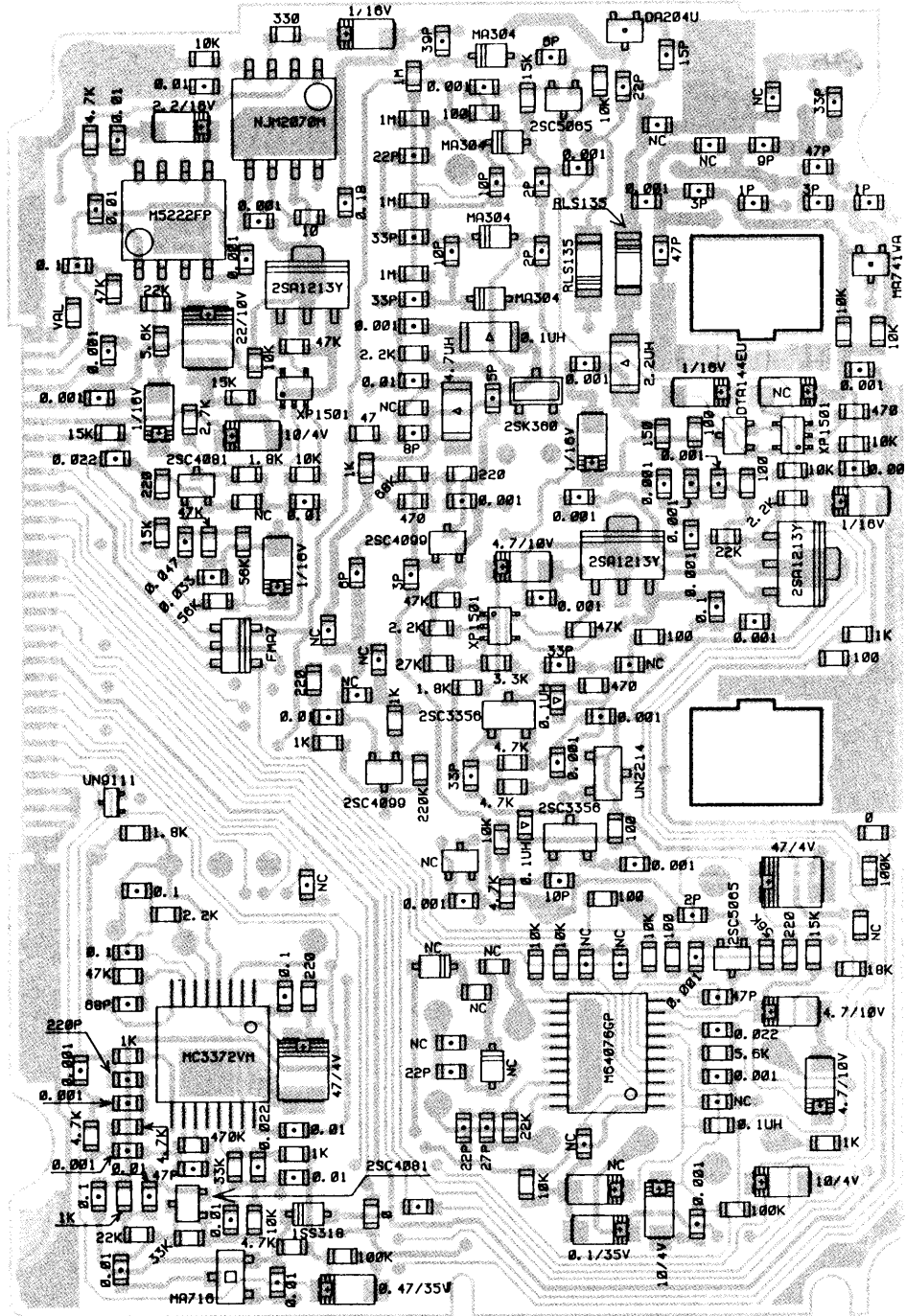
* If IC1 is replaced, clone data must be transferred to the repaired radio from a brand new radio. For clone procedures, see page 24 of the Instruction Manual.



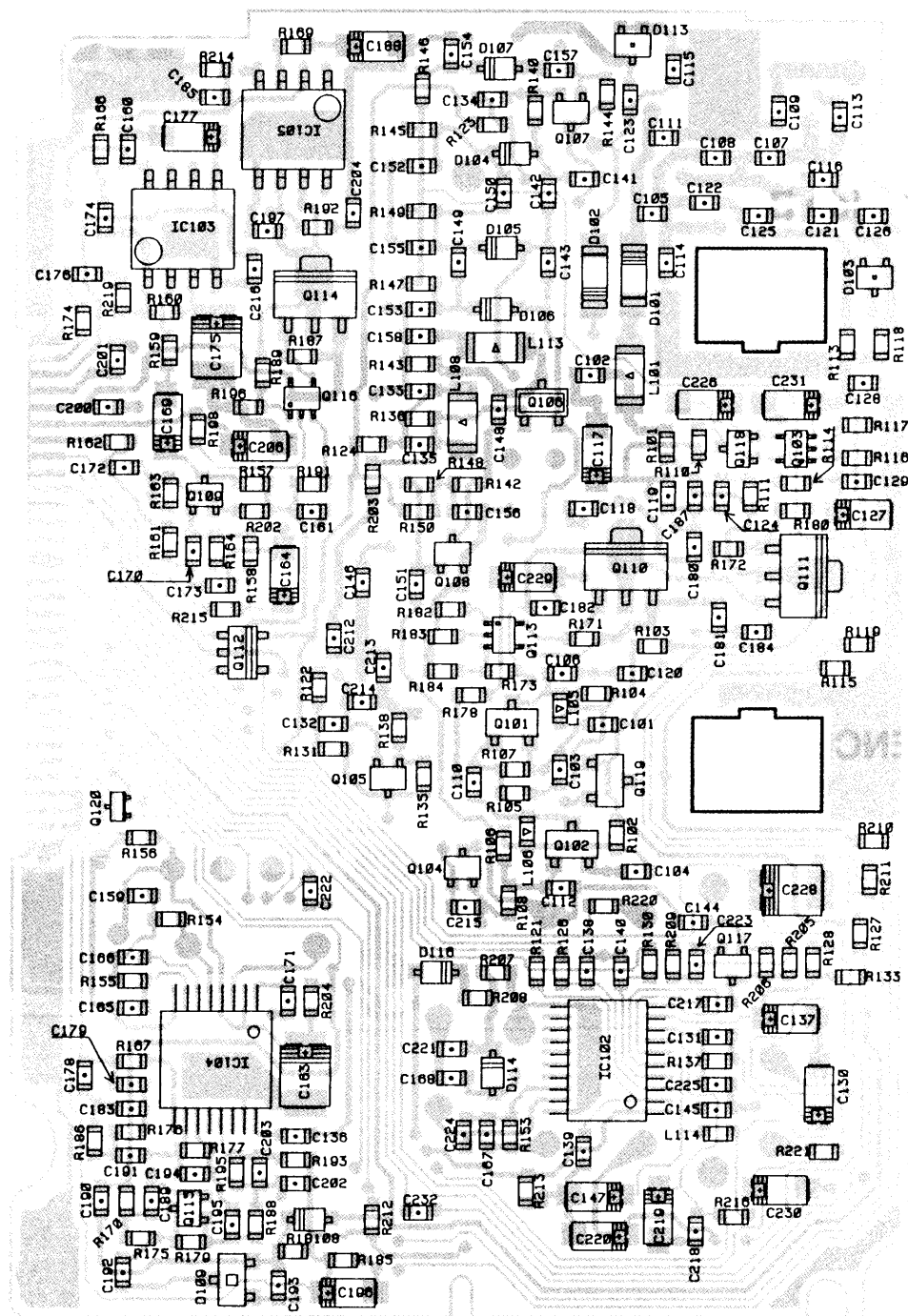
RF Unit Side A (VALUE/REFERENCE)



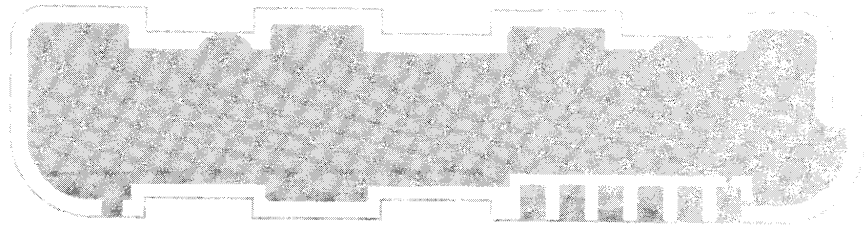
(VALUE)



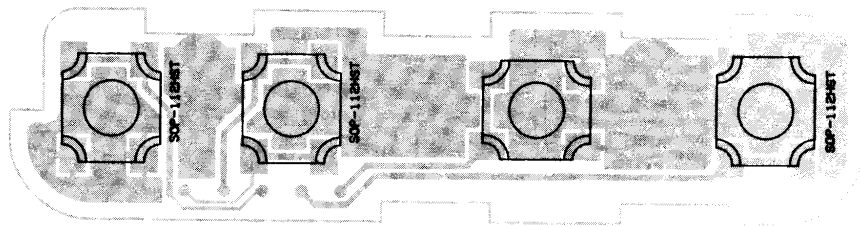
RF Unit Side B (REFERENCE)



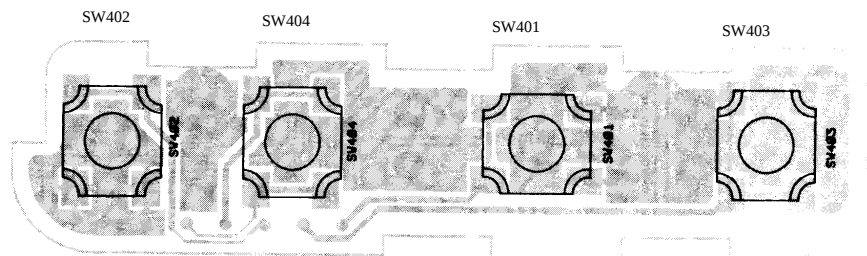
**PTT Unit Side A
(VALUE/REFERENCE)**



**PTT Unit Side B
(VALUE)**

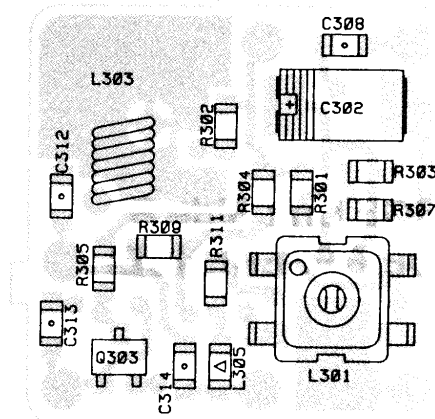
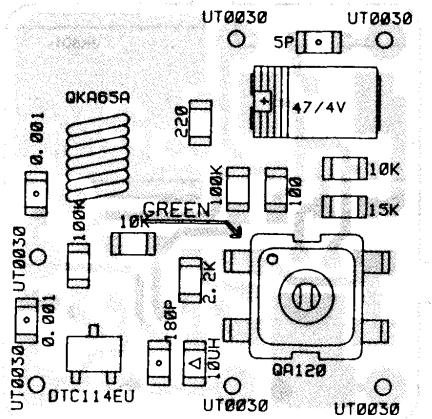


(REFERENCE)



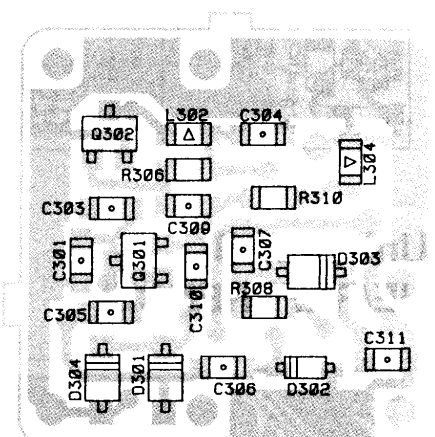
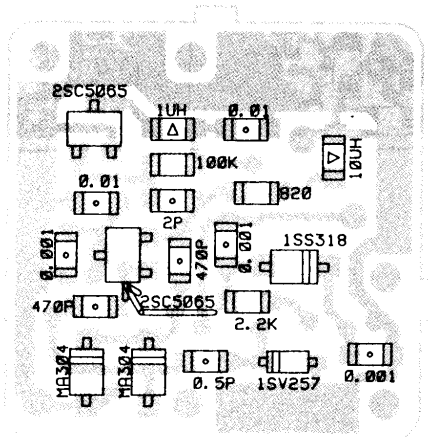
VCO Unit Side A (VALUE)

(REFERENCE)



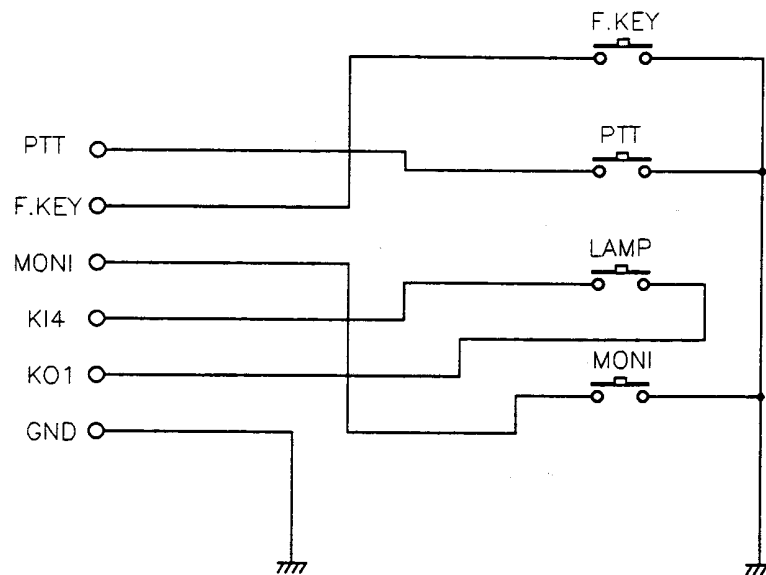
**VCO Unit Side B
(VALUE)**

(REFERENCE)

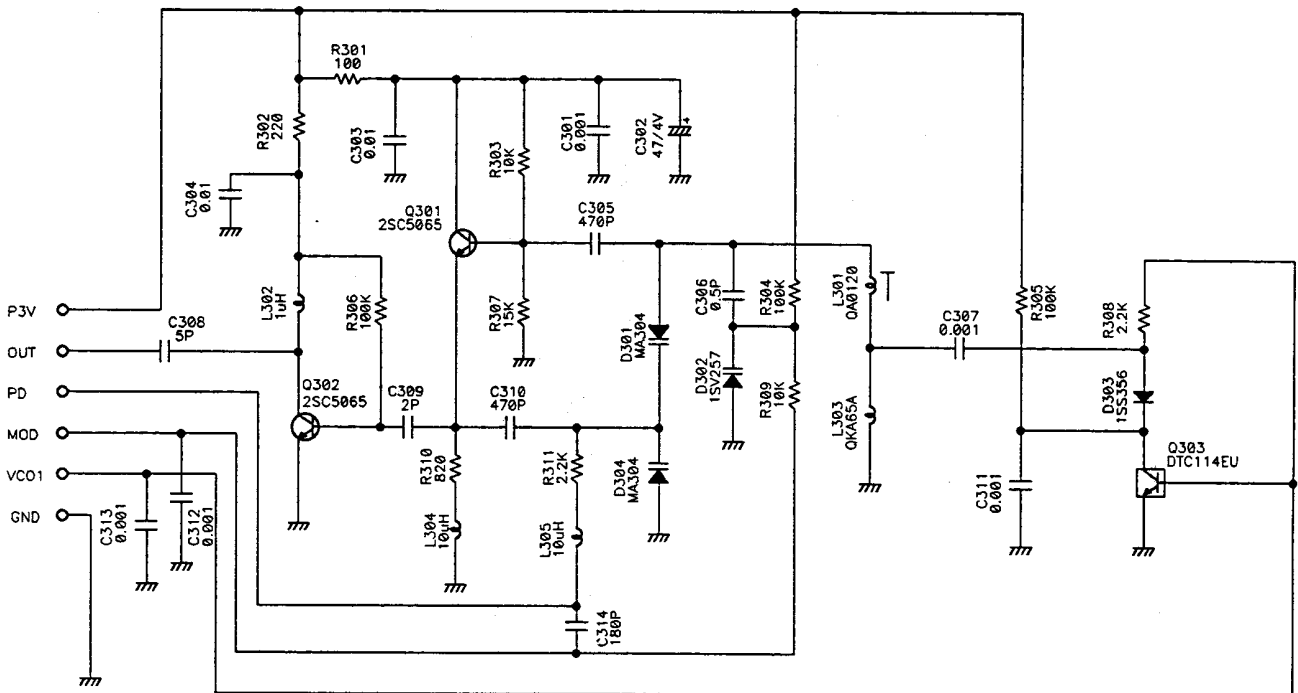


CIRCUIT DIAGRAM

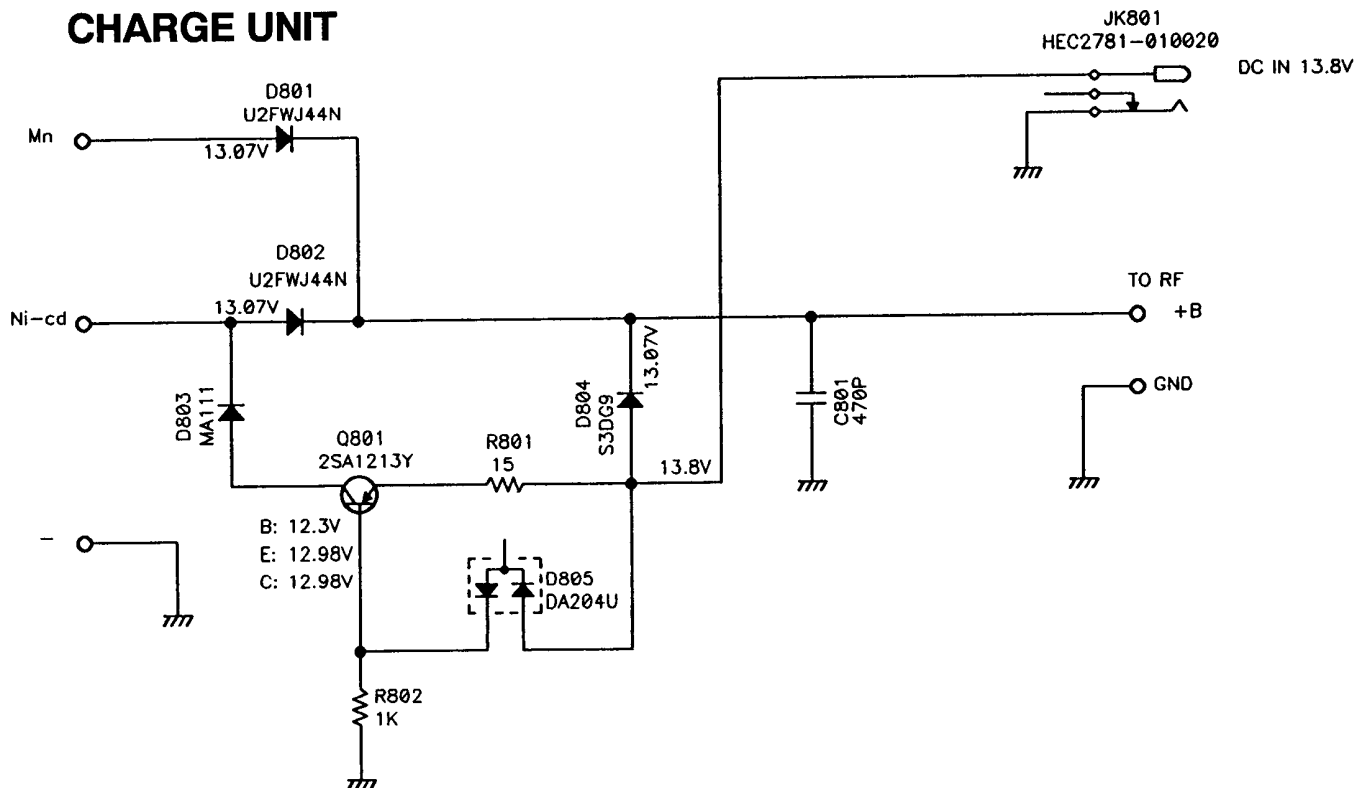
PTT UNIT



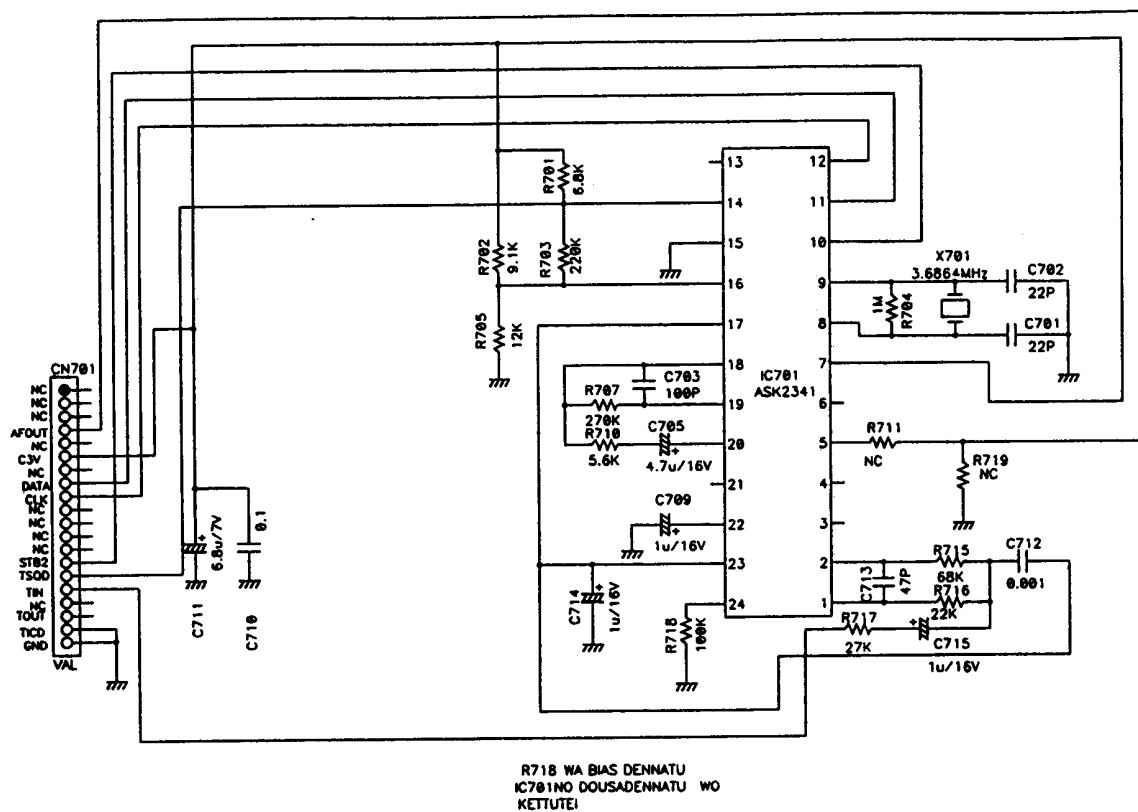
VCO UNIT



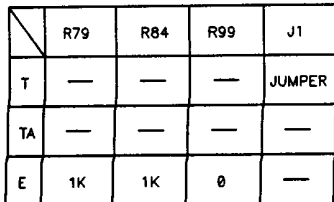
CHARGE UNIT

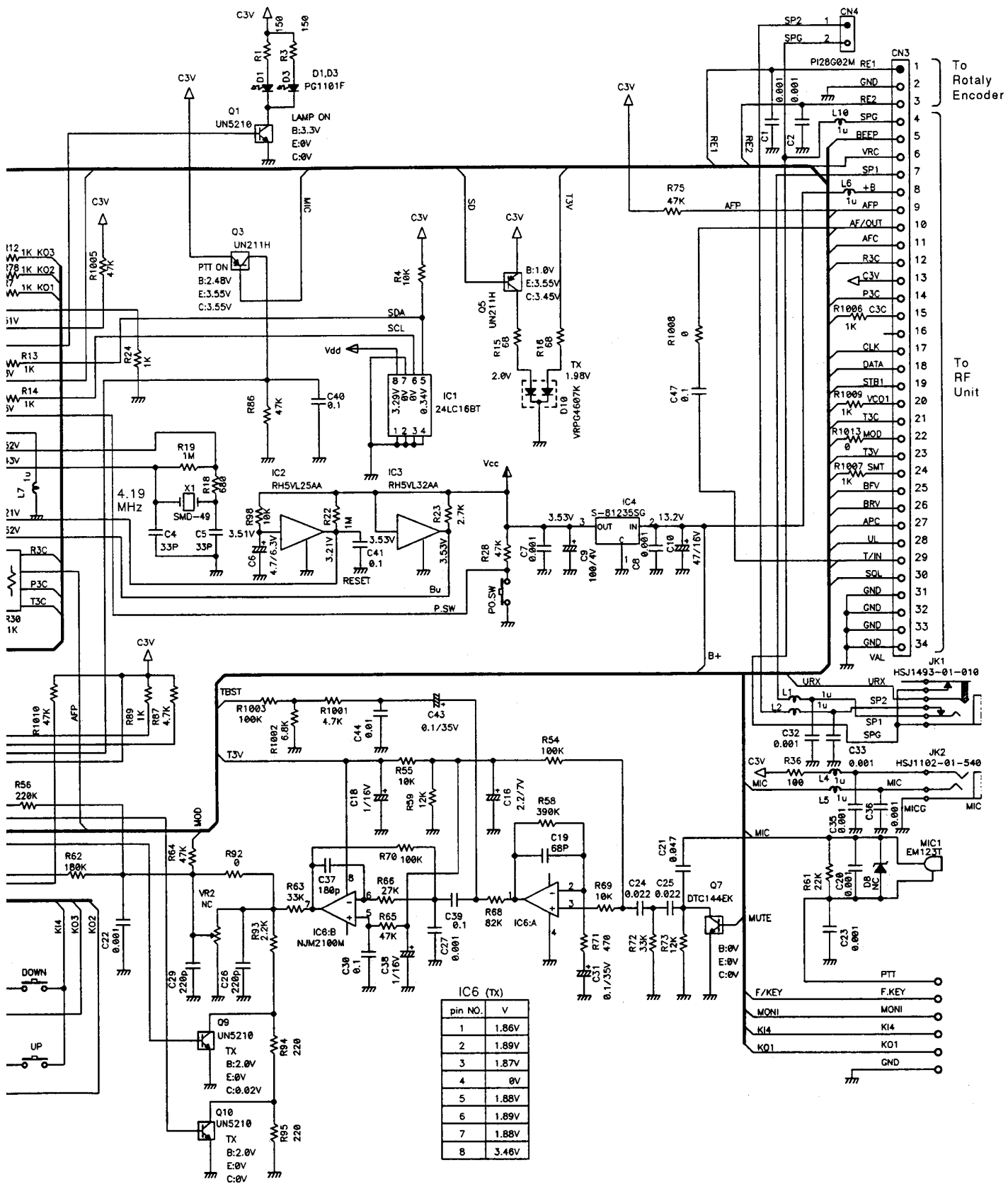


TSQ UNIT



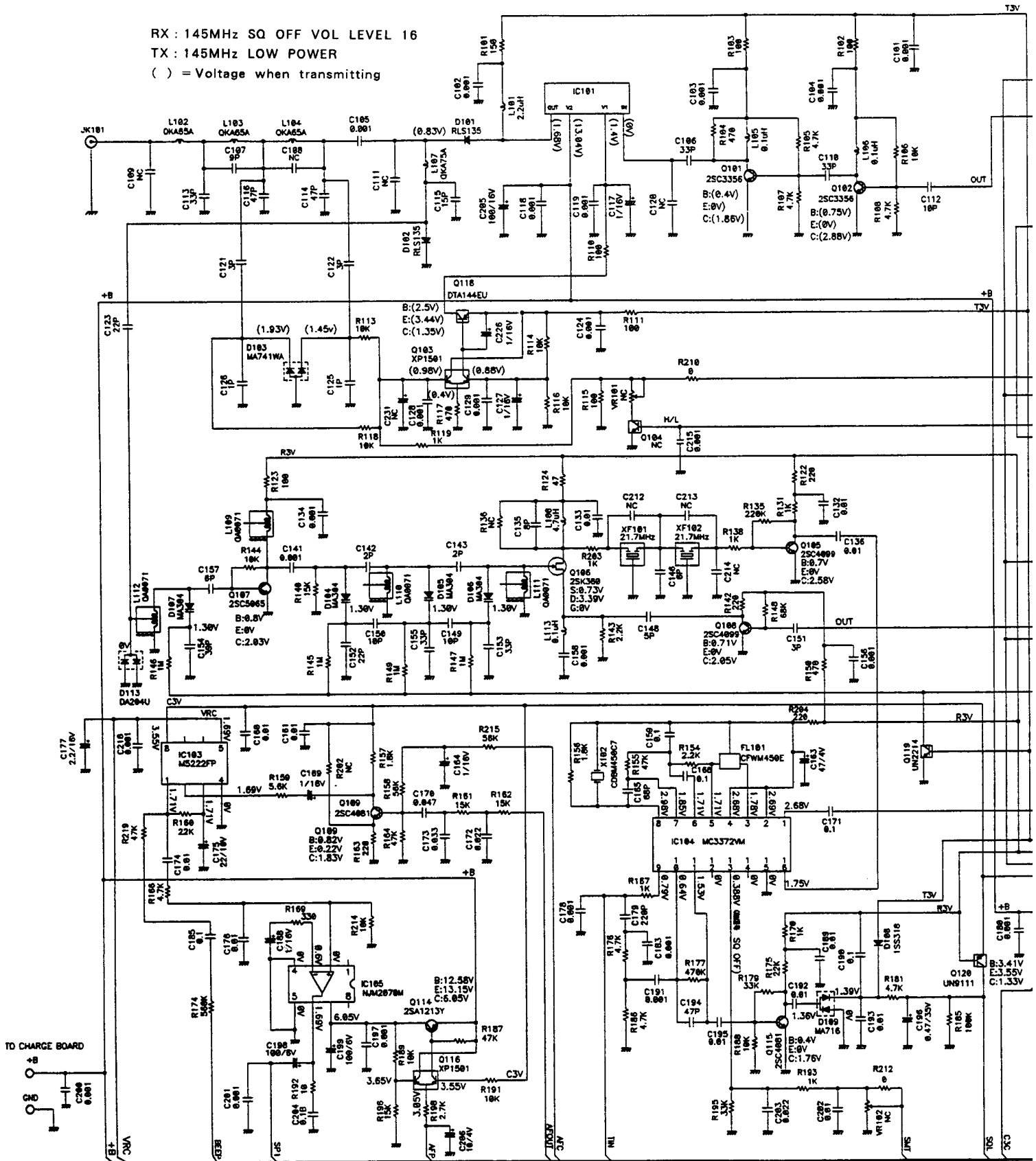
CPU UNIT





RF UNIT

RX : 145MHz SQ OFF VOL LEVEL 16
TX : 145MHz LOW POWER
() = Voltage when transmitting



LOCK DIAGRAM

The diagram illustrates the internal components and signal flow of a radio receiver. Key components include:

- Antenna and Initial Stages:** APC DET (D103 MA741WA), APC AMP (Q103 XP1501), ANT SW (D101102 RLS135), BPF (D104105 MA304), RF AMP (Q107 2SC5065), MIXER (Q106), BPF (D104105 MA304), IF AMP (Q105 2SC4099), 2ND IF MIXER (IC104 MC3372), AF SW (Q5 2S144), AF AMP (Q108 2SC4081), VR (Q103 MS223P), AF POWER AMP (IC105 NA4870), and SPEAKER.
- Frequency Synthesis and PLL:** PLL DATA line, PLL (IC102 M64076GP), VCO (Q301 2SC5585), VCO BUFFER (Q302 2SC5585), BUFFER (Q117 2SC4099), BUFFER (Q108 2SC4081), TX/RX SW (Q303 DTC114EU), and LOOP FILTER.
- Control and Interface:** ROTARY ENCODER, TONE DECODER (IC1 24LC16BT), EEPROM (IC2 RH5V125AA), RESET, OPTIN, TONE, MIC MUTE (Q7 UN2214), MIC (IC5 NA12100), DEV CTRL (Q910 UN5210), CPU (ICS M39267), KEY INPUT (Q5 UN211), CPU CLOCK (4.19MHz), BATTERY (B+), GROUND (GND), and DISPLAY (LCD).
- LEDs and Lamps:** TX LED (Q111 2SA1213), RX LED (Q5 UN211), LCD LAMP SW (Q1 UN5210), and LCD LAMP.

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