

DOT MATRIX LIQUID CRYSTAL DISPLAY MODULE

COMPANY NAME:

USER 'MANUAL

LMG-S12K64-BIG5 Serial

LCD Module Description: _	
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PROPO	SED BY	APPROVED
Design	Approved	
Tel.886 Fax:886	2-20092512 2-20002510	

SDEC TECHNOLOGY CORP.

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LCM SAMPLE APPROVAL

(液晶顯示模組樣品確認書)

	PART A: <u>FILLED BY SDEC TECH</u> (由 SDEC 填為 COMBANY NAME(安全点錄):									
	1) COMPANY NAME(客戶名稱):									
2) !	2) SDEC ITEM NO. (產品型號):									
3)	CUSTOMER ITEM NO. (客戶產品型號):									
4)]	LCM Function (LCM 內容):									
A	LCD TYPE (LCD 種類):□ TN, □ HTN, □ S			•						
	□ NEGATIVE/反向,									
B	VIEWING AREA (視角方向): ☐ 3H, ☐ 6H,		<u> </u>							
C	POLARIZER COLOR (偏光板顏色): ☐ GRAY/☐ BLUE									
D	BACKLIGHT COLOR (背光顏色): ☐ YELLOV	/								
	□ RED/紅光, □ BLUE/藍光, □	GRE	EN/翠綠	光, 🗌 WHITE/白光						
E	TEMPERATURE (溫度):□ NORMAL/常溫, □	WID	E/廣溫							
\mathbf{F}	PCB RESISTOR (PCB 板電阻): R8(KΩ), R1	/R2/R4	/R5(1K)	Ω), R3(K Ω),						
Г			,	0						
	$\mathbf{R7}(0\Omega),\mathbf{R10}(0\Omega)$,	ΚΩ)						
G	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A	KΩ	,	ΚΩ)						
G SA	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A MPLE DELIVERY DATE (出樣日期):	KΩ	,	ΚΩ)						
G SA	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A	KΩ	,	ΚΩ)						
G SA 2 · F	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A MPLE DELIVERY DATE (出樣日期):	KΩ	?),R11(
G SA 2 · F CHEC	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A MPLE DELIVERY DATE (出樣日期): PART B: FILLED BY CUSTOMER (請客戶填寫)	ΚΩ	?),R11(
G SA 2 · H CHEC	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A MPLE DELIVERY DATE (出樣日期): PART B: FILLED BY CUSTOMER (請客戶填寫) CK LIST ITEMS (檢查項目):	Κ Ω	N G							
G SA 2 · F CHE 1).LC 2).PO	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A MPLE DELIVERY DATE (出樣日期): PART B: FILLED BY CUSTOMER (請客戶填寫) CK LIST ITEMS (檢查項目): M SIZE AND THICKNESS:(LCM 尺寸及厚度):	ο K	N G							
G SA 2 · F CHEC 1).LC 2).PO 3).EL	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A MPLE DELIVERY DATE (出樣日期): PART B: FILLED BY CUSTOMER (請客戶填寫) CK LIST ITEMS (檢查項目): M SIZE AND THICKNESS:(LCM 尺寸及厚度): LARIZER COLOR: (偏光板色澤):	ο K	N G							
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G SA 2 · F CHE 1).LC 2).PO 3).EL 4).VII 5).BA	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A MPLE DELIVERY DATE (出樣日期): PART B:FILLED BY CUSTOMER (請客戶填寫) CK LIST ITEMS (檢查項目): M SIZE AND THICKNESS:(LCM 尺寸及厚度): LARIZER COLOR: (偏光板色澤): ECTRO CHARACTERISTIC: (電氣特性): EWING AREA (視角範圍):	ο K	N G							
G SA 2 · F CHEC 1).LC 2).PO 3).EL 4).VII 5).BA 6).TE	R7(0Ω), R10(CONTROL IC (控制 IC): ST7920-0A MPLE DELIVERY DATE (出樣日期): PART B:FILLED BY CUSTOMER (請客戶填寫) CK LIST ITEMS (檢查項目): M SIZE AND THICKNESS:(LCM 尺寸及厚度): LARIZER COLOR: (偏光板色澤): ECTRO CHARACTERISTIC: (電氣特性): EWING AREA (視角範圍): CKLIGHT ILLIMINATION (背光亮度):	ο K	N G							
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0.1	SDEC		LCD	M	ODUL	E	NUM	BERI	NG	SYST	EM			
LM	С	_	S	T	C	2	Е	16	D	L	Y	Y	_	
[1]	[2]		[3]	[4]	[5]	[6]	[7]	[8]	[9]	[10]	[11]	[12]		[13]
	_													
LM	G		S	S		12	A	64	U	Е	G	W	_	
Г17	[2]		[3]	[4]	[5]	[6]	[7]	[8]	[9]	[10]	Г111	[12]		Г137

[1]			[[4]	[3] [0]	[[/]	[o]	[2]				
0.2 S	SDEC LO	CD	MO	DULE	VAR	IATION	LIS	ST			
NO	ITEM					DESCI	RIPTIC	ON			
[1]	LCD MODULI	Е	LIQUI	D CRYSTAI	L DISP	LAY MODU	JLE				
[2]	LCM MODUL	Е	D DIO	GITS MODE	EL C	CHARACT	ER MO	DDEL G GRAPHIC MODEL			
[3]	LCM OF FUNCT	ΓΙΟΝ	В	BIG SIZI	E TYP	PΕ	S	SMALL/NORMAL TYPE			
[4]	LCD MODEL		T	TN LC	D		Н	HTN LCD			
[4]	LCD MODEL		S	STN LO	CD	T	F	FSTN LCD			
[5]	IC PACKING	1	PA	CKAGE TYP	ЕС	CHIP(OR C	OG)TYI	PE T TAB TYPE			
		[6]	CHAR	ACTER MO	DEL:	NUMBER	GRAP.	HIC MODEL: WIDE SIDE OF			
	STANDARD	[0]	OF LIN	NES			DOTS				
[A]	TYPE LCM	[7]	FROM	A TO Z							
	TITELEN	[8]	CHAR	ACTER MO	DEL:N	UMBER	GRAP.	HIC MODEL:HIGH SIDE OF			
		[o]	OF CH	ARACTER			DOTS				
	STANDARD	[6]	DIGIT	DIGITS MODEL: YEAR+MONTH+NUMBER							
[B]	OR CUSTOM	[7]	CHARACTER MODEL: CHARACTER+LINES+NUMBER								
	DESIGNED		GRAPHIC MODEL: WIDE SIDE DOTS+HIGH SIDE DOTS+NUMBER								
	TYPE LCM	[8]									
[9]	VIEWING AN	GLE	R 3	O'CLOCK	D 6	O'CLOCK	L 9	O'CLOCK U 12 O'CLOCK			
			R	WITHOUT	Г ВАС	CKLIGHT	Е	EL (TRANSFLECTIVE)			
[10]	BACKLIGHT			ARRAY L	ED						
			L	(TRANSF)	LECTIV	/E)	F	FL (TRANSMISSIVE)			
	DOL ADVEST		G	GRAY			Y	YELLOW GREEN			
[11]	POLARIZER COLOR			NEGATIV	E ST	N TYPE	: BL	UE			
	COLOR		N	N NEGATIVE FSTN TYPE: BLACK							
					ь го	11 1 111E					
	BACKLIGHT		В	BLUE			G	GREEN			
[12]	COLOR		O	ORANGE			R	RED			
				WHITE			Y	YELLOW GREEN			
[13]	VERSION		01	ENGLISH	-JAPAN	NESE	02	ENGLISH-EURPEAN			
[13]	VERSION		03	ENGLISH	-RUSSI	AN	Е	EDGE LED BACKLIGHT			

**All of our STN Panel belong "WIDE TEMPERATURE"



CONTENTS

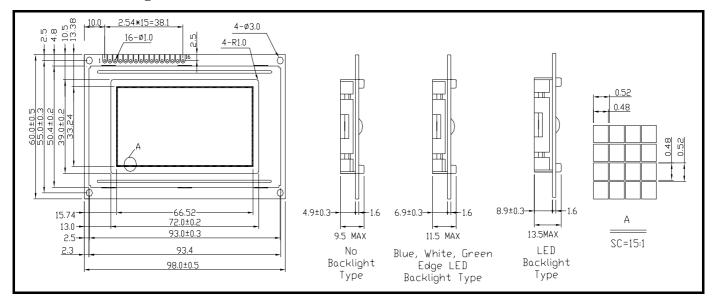
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1. Mechanical Specification

ITEM	STANDA	RD VALUE		UNIT		
NUMBER OF CHARACTERS	8 CHARACTERS X 4 LINES (128*64Dots)					
CHARACTER FORMAT	16 X	16 DOTS				
MODULE DIMENSION NO BACKLIGHT	98.0 (W) X 60	98.0 (W) X 60.0 (H) X 9.5 (T)				
MODULE DIMENSION EDGE LED BACKLIGHT		mm				
MODULE DIMENSION ARRAY LED BACKLIGHT	98.0 (W) X 60	mm				
MODULE DIMENSION EDGE LED BACKLIGHT (BLUE)	98.0 (W) X 60	mm				
VIEWING DISPLAY AREA	72.0 (W)	mm				
ACTIVE DISPLAY AREA	66.52 (W)	mm				
DOT SIZE	0.48 (W) X 0.48 (H)					
DOT PITCH	0.52 (W) X 0.52 (H)					
 ARRAY LED BACKLIGHT COLOR 	YELLOW GREEN OR ORANGE OR RED					
BACKLIGHT INPUT	DC +4.2V	V	150		mA	
BACKLIGHT HALF-LIFT TIME			HR.			
● EDGE LED BACKLIGHT COLOR	BLUF					
BACKLIGHT INPUT	DC+3.2V	V	78		mA	
BACKLIGHT HALF-LIFT TIME	3,000 (AVOID LIGHTING CONTINUOUSLY)					

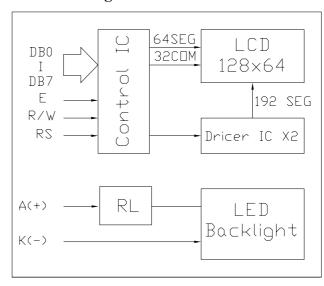
2. Mechanical Diagram



3. Interface Pin Connections

NO	SYMBOL	LEVEL	FUNCTION
1	VSS		GND (0V)
2	VDD		DC +5V
3	N.C		
4	RS	H/L	Register select
5	R/W	H/L	Read/Write
6	Е	H,H→L	Enable signal
7	DB0	H/L	Data Bit 0
8	DB1	H/L	Data Bit 1
9	DB2	H/L	Data Bit 2
10	DB3	H/L	Data Bit 3
11	DB4	H/L	Data Bit 4
12	DB5	H/L	Data Bit 5
13	DB6	H/L	Data Bit 6
14	DB7	H/L	Data Bit 7
15	A(+)	DC+5V	LED Backlight +
16	K(-)	0V	LED Backlight -

4. Block Diagram





5. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYPE	MAX.	UNIT
INPUT VOLAGE	VI	VSS		VDD	V
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS		5.0	6.5	V
SUPPLY VOLTAGE FOR LCD	V_{LCD}			6.5	V
STN FSTN WIDE TEMPERATURE RANGE	OPTERATING	-20~+70	STORAGE	-30~+80	$^{\circ}$ C
STATIC ELECTRICITY	Be sure that you ar	e grounded when	handing LCM.		

6. Electrical Characteristics

ITEM	SYN	CONDITION	MIN.	TYPE	MAX.	UNIT
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS		4.5	5.0	5.5	V
SUPPLY VOLTAGE FOR LCD	V_{LCD}	Ta= 25°C	5.6	5.8	6.0	V
INPUT HIGH VOLTAGE	VIH		0.7VDD		VDD	V
INPUT LOW VOLTAGE	VIL		0		0.1VDD	V
SUPPLY CURRENT (LOGIC)	IDD	VDD=+5V			4.0	mA

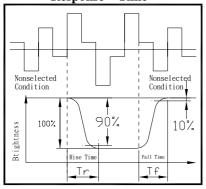
7. Optical Characteristics

Ta at 25°C

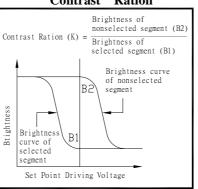
ITEM	SYM	CONDITION	MIN.	TYPE	MAX.	UNIT
VIEW ANGLE (TOP/BOTTOM)	$\theta 1/\theta 2$	CR≥5		45/35		deg.
VIEW ANGLE (LEFT/RIGHT)	φ1/φ2	CR≥5		35/35		deg.
CONTRAST RATIO	CR	-		5		
RESPONSE TIME (RISE)	TON/Tr	-		170		mS
RESPONSE TIME (DECAY)	TOFF/Tf			220		mS

8. Optical Definitions

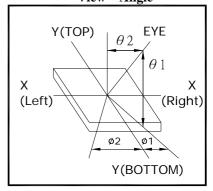
Response Time



Contrast Ration



View Angle



9. Display Address

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Line 1	80	Ή	81H		82H		83H		84H		85	H	86	Н	87	'H
Line 2	90)H	91	Н	92	Н	93	Н	94	Н	95	Н	96	Н	97	Ή
Line 3	88	3H	89	Ή	8A	Н	8E	BH	8C	CH	81	Н	8E	EH	8F	H
Line 4	98	3H	99	Ή	9 <i>A</i>	Н	9E	BH	90	H	91	Н	9E	EH	9F	H

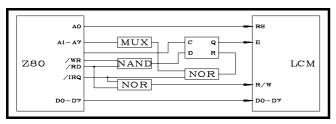
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
Line 1																
Line 2																
Line 3																
Line 4																

^{*}A Ram Bank is 16 bits (2 bytes)

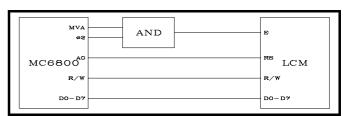


10. Interface to MPU

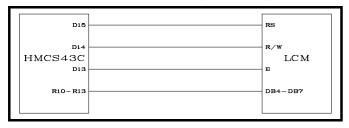
10.1 Interface to Z-80 CPU



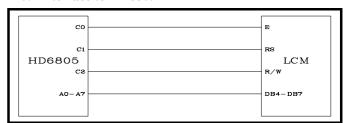
10.2 Interface to MC6800 CPU



10.3 Interface to 4-bit CPU (HMCS43C)



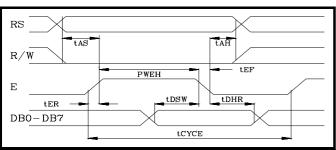
10.4 Interface to HD6805 MP



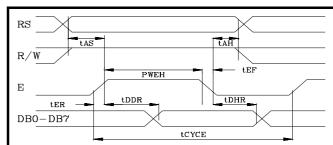
11. Timing Control

11.1 Write and Read Operation

Write Operation

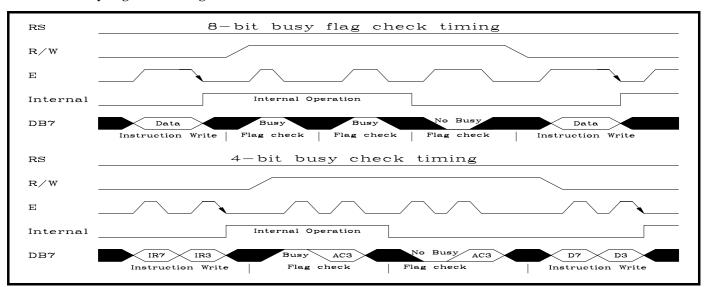


Read Operation



Item	Symbol	Limit (Min.)	Limit (Max.)	Unit
Enable Cycle Time	tCYCE	1200	-	ns
Enable Pules Width (High level)	PWEH	140	-	ns
Enable Rise/Fall Time	tER,tEF		25	ns
Address Set-Up Time (RS,R/W,E)	tAS	10		ns
Address Hole Time	tAH	20		ns
Data Set-Up Time	tDSW	40		ns
Data Delay Time	tDDR		100	ns
Data Hold Time	tDHR	20		ns

11.2 Busy flag check timing

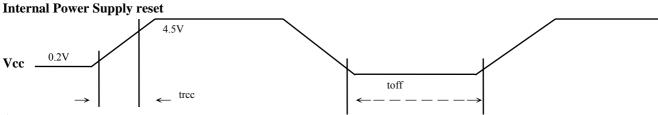


Note: IR7, IR3: Instruction 7th bit, 3rd bit; AC3: Address Counter 3rd bit.



12. Initialization of LCM

The LCM automatically initializes (reset) when power is turned on using the internal reset circuit. If the power supply conditions for correctly operating of the internal reset circuit are not met, initialization by instruction is required. Use the procedure is next page for initialization.



(Note 1) 10 ms \geq trcc \geq 0.1 ms, toff \geq 1 ms.

(Note 2) toff stipulates the time of power OFF for momentary power supply dip or when power supply cycles ON and OFF.

T .						•. /=		l	•. /= -			·	
Item	Symbol	st cond	iition	Lir	nit (M	un.)	Lin	nit (M	ax.)		Unit		
Power supply rise time	tree toff	+				0.1			10			ms	
Power supply off time	1011	1				1		<u> </u>			<u> </u>	ms	
8 bit Int	erface						4	bit In	terfac	ce			
Power	On							Powe	er On				
								,	,				
Wait time	> 40 ms						Wa	it tim	e > 40	ms			
								,	,				
Functio	on Set			Function Set									
RS R/W DB7 DB6 DB5		DB1	DB0	RS	R/W	DB7	DB6		DB4		DB2	DB1	DB0
0 0 0 0 1	1 X 0	X	X	0	0	0	0	1	0	X	X	X	X
				J									
Wait time	$> 100 \mu s$						Wa	it time	e > 100	lμs			
1	r								,				
Function	n Set						7	Functi	on Set	f			
	DB4 DB3 DB2	DB1	DB0	RS	R/W	DR7	DB6		DB4		DB2	DB1	DB0
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	1 X 0	X	X	0	0	0	0	1	0	X	X	X	X
J				0	0	X	0	X	X	X	X	X	X
Wait time	$>$ 37 μ s						•		,		•	J.	
	ν οι μισ						Wa	it time	e > 100	lμs			
Display ON/O	NFF Control								,	,			
RS R/W DB7 DB6 DB5 1		DR1	DB0			ī	Displa	v ON/	OFF (Contro	1		
0 0 0 0 0	0 1 D	C	В	RS	R/W		DB6					DB1	DB0
J	<u> </u>			0	0	0	0	0	0	X	X	X	X
Wait time	> 100 // s			0	0	1	D	С	В	X	X	X	X
vvaie sinie	ν 100 μ 5							,	,			i e e e e e e e e e e e e e e e e e e e	
Display	Cloor						Wa	it time	e > 100	lμs			
RS R/W DB7 DB6 DB5		DR1	DRO						ļ				
0 0 0 0 0	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	0	1				T	Display	v Clea	r			
J	<u> </u>			RS	R/W	DB7	DB6				DB2	DB1	DB0
Wait time	> 10ms			0	0	0	0	0	0	X	X	X	X
	7 101115			0	0	0	0	0	1	X	X	X	X
Entry Mo	ode Set							,	,				
RS R/W DB7 DB6 DB5		DB1	DB0				Wa	ait tim	e > 10	ms			
0 0 0 0 0	0 0 1	I/D	S					,	,				
<u> </u>	. ,						Eı	ntry N	Iode S	et			
Initializat	ion End			RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
				0	0	0	0	0	0	X	X	X	X
				0	0	0	1	I/D	S	X	X	X	X
								,	,				
							Ini	tializa	tion E	nd			



13. Instruction Set

Instruction Table: (RE=0: Basic Instruction)

Instruction				In	struct	ion C	ode				Description	Ex. Time
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	540KHz
Display Clear	0	0	0	0	0	0	0	0	0	1	Fill DDRAM with "20H" and set DDRAM address counter (AC) to "00H".	4.6ms
Return Home	0	0	0	0	0	0	0	0	1	X	Set DDRAM address counter (AC) to "00H", and put cursor to origin: the content of DDRAM are not changed.	4.6ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operation are performed during data rite/read. For normal operation. I/D=1: increment; 0: decrement; S=1: accompanies display shift when data is written, for normal operation, set to zero.	72 μ s
Display ON/OFF Control	0	0	0	0	0	0	1	D	С	В	D=1: Display ON. C=1: Cursor ON. B=1: Character Blink ON.	72 μ s
Cursor or Display shift	0	0	0	0	0	1	S/C	R/L	X	X	S/C=1: Display shift; 0:Cursor move. R/L=1: shift to right; 0: shift to left.	72 μ s
Function Set (Modify)	0	0	0	0	1	DL	X	0 RE	X	X	DL=1: Interface is 8 bits. 0: Interface is 4 bits. RE=0: Normal instruction .1: Extended instruction.	72 μ s
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address to address counter (AC).	72 μ s
Set DDRAM address	0	0	1	0	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	72 μ s
Read Busy Flag and AC	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Read busy flag (BF) for completion of internal operation, also Read out the value of address counter (AC)	
Write RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data to internal RAM. (DDRAM/CGRAM/GDRAM)	72 μ s
Read RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM. (DDRAM/CGRAM/GDRAM)	72 μ s

Instruction Table (RE=1: extended instruction)

Instruction				In	struct	ion C	ode				Description	Ex. Time
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	540KHz
Standby	0	0	0	0	0	0	0	0	0	1	Enter standby mode, any other instructions can terminate. COM132 are halted.	72 μ s
Scroll or RAM address Select	0	0	0	0	0	0	0	0	1	SR	SR=1: enable vertical scroll position. SR=0: enable CGRAM address (basic instruction).	72 μ s
Reverse (by line)	0	0	0	0	0	0	0	1	R1	R0	Select 1 out of 4 line (in DDRAM) and decide whether to reverse the display by toggling this instruction.	72 μ s
Extended Function Set	0	0	0	0	1	DL	X	1 RE	G		DL=1: 8-bit interface. 0: 4-bit interface. RE=1: Extended instruction.0: basic instruction set. G=1: Graphic display ON. 0: Graphic display OFF	72 μ s
Set Scroll address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	SR=1: AC5~AC0 the address of vertical scroll.	72 μ s
Set Graphic Display RAM address	0	0	1	0 0	0 AC5	0 AC4	AC3 AC3	AC2 AC2	AC1 AC1	AC0 AC0	Set GDRAM address to address counter (AC). Set the vertical address first and followed the horizontal address by consecutive writings. Vertical address rang: AC5AC0. Horizontal address rang: AC3AC0.	72 μ s



14. User Font Patterns (CG RAM Character) , Graph Display RAM Address User Font Patterns (CG RAM Character)

		ter Code M data)		CG	RAM	Add	ress					GRAN (High		ı						GRA (Low				
B15-B4	В3	B2 B1	В0	B5 B4	В3	B2	В1	В0	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
					0	0	0	0	0	1	0	0	0	0	0	1	0	0	1	0	0	0	0	0
					0	0	0	1	0	1	0	0	0	0	0	1	0	0	0	1	0	0	0	0
					0	0	1	0	1	1	1	1	0	0	0	1	0	0	0	1	0	0	0	0
					0	0	1	1	0	1	0	0	0	0	1	1	1	1	1	1	1	1	1	0
					0	1	0	0	0	1	0	0	0	1	0	1	0	0	0	0	1	0	0	0
					0	1	0	1	0	1	0	1	0	0	0	1	0	0	0	0	1	0	0	0
					0	1	1	0	0	1	0	1	0	0	0	1	1	1	1	1	1	1	1	0
0	X	00	X	00	0	1	1	1	1	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0
	21	00	11		1	0	0	0	1	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0
					1	0	0	1	1	0	1	0	0	0	0	1	1	1	1	1	1	1	1	0
					1	0	1	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0
					1	0	1	1	0	1	0	0	1	0	0	1	0	0	0	0	1	0	0	0
					1	1	0	0	1	1	1	1	1	0	0	1	1	1	1	1	1	1	1	0
					1	1	0	1	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0
					1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
					1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
					0	0	0	0	0	0	1	0	0	0	0	1	1	1	1	1	1	1	0	0
					0	0	0	1	0	0	1	0	0	0	0	1	0	1	0	1	0	1	0	0
					0	0	1	0	0	1	0	1	0	0	0	1	1	1	1	1	1	1	0	0
					0	0	1	1	1	1	1	1	1	0	0	0	0	0	1	0	0	0	0	0
					0	1	0	0	1	0	1	0	1	0	0	0	1	1	1	1	1	0	0	0
					0	1	0	1	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0
					0	1	1	0	0	0	1	0	0	0	0	1	1	1	1	1	1	1	0	0
0	X	01	X	01	0	1	1	1	1	1	1	1	1	0	0	0	1	0	0	0	1	0	0	0
					1	0	0	0	0	0	1	0	0	0	0	0	0	1	0	1	0	0	0	0
					1	0	0	1	0	0	1	0	0	0	0	1	1	1	1	1	1	1	0	0
					1	0	1	0	0	0	1	0	1	0	0	0	0	0	1	0	0	0	0	0
					1	0	1	1	1	0	1	0	1	0	0	0	1	1	1	1	1	0	0	0
					1	1	0	0	1	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0
					1	1	0	1	0	0	1	1	1	0	0	0	0	0	1	0	0	0	0	0
					1	1	1	0	1	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0
					1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Note:

- 1. DDRAM data (character code) bit1 and bit2 are identical with CGRAM address bit4 and bit5.
- 2. CGRAM address bit0 to bit3 specify total 16 rows. Row-16 is for cursor display. The data in Row-16 will be logically OR to the cursor.
- 3. CGRAM data for each address is 16 bits.
- 4. To select CGRAM font, the bit4 through bit15 of DDRAM data must be "0" while bit0 and bit3 are "don't care".

Graph Display RAM Address

GDRAM Vertical		GDRAM Horizo	ontal address (X)	
Address (Y)	0	1		15
0	D15 → D0	D15 → D0		D15 → D0
1	D15 → D0	D15 → D0		D15 → D0
2	D15 → D0	$D15 \rightarrow D0$		$D15 \rightarrow D0$
3	D15 → D0	D15 → D0		D15 → D0
:	:	:		:
60	D15 → D0	D15 → D0		D15 → D0
61	D15 → D0	D15 → D0		$D15 \rightarrow D0$
62	D15 → D0	D15 → D0		D15 → D0
63	D15 → D0	$D15 \rightarrow D0$		D15 → D0



15. Software Example

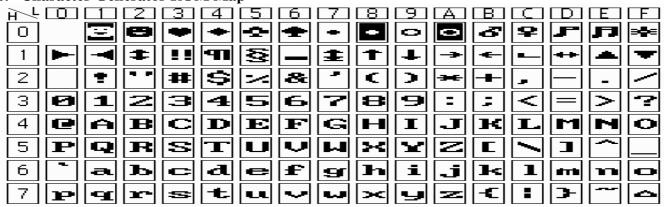
15.1 8-bit operation (8 bits 2 lines)

	_				_			1111	_	_		
Function				D 6						D 0	Display	Description
Power on delay	_			Ť			Ť	1	Ī	Ť		Initialization. No display appears.
Function set		0						0				Sets to 8-bit operation and selects 2-line display character font. (Note: number of display lines and character fonts cannot be change after this.)
Display OFF	0	0	0	0	0	0	1		0	0		Turn off display.
Display ON	0	0	0	0	0	0	1	1	1	0	_	Turn on display and cursor
Entry Mode Set	0	0	0	0	0	0	0	1	1	0	_	Set mode to increment the address by one and to shift the cursor to the right, at the time of write, to the DD/CG RAM Display is not shifted.
Write data to CG/DD RAM	1 1	0	1 1	0	1 1	1 0	0 1	1 1		0 1	雄_	Write "雄". Cursor incremented by one and shift to right.
Write data to CG/DD RAM		0		1	0		0 1	1		1	雄鐸_	Write "鐸".
Set DD RAM	0				0				0		雄鐸	Set RAM address so that the cursor is propositioned at the head of the second line.
Write data to CG/DD RAM				*							雄鐸 CR_	Write "C", and "R".
Cursor or display shift	0	0	0	0	0	1	0	0	X	X	<u>雄鐸</u> C <u>R</u>	Shift only the cursor position to the left.
Write data to CG/DD RAM				*							雄鐸 CO., LTD	Write "O., LTD.".
Entry Mode Set	0	0	0	0	0	0	0	1	1	1	雄鐸 CO., LTD	Set display mode shift at the time during writing operation.
Write data to CG/DD RAM	1	0	0	1	1	1	1	0	0	0	<u>鐸</u> ., LTD. x_	Write "x". Cursor incremented by one and shift to right. (The display move to left.)
Write data to CG/DD RAM				*								Write other characters.
Return Home	0	0	0	0	0	0	0	0	1	0	雄鐸 CO., LTD.	Return both display and cursor to the original position (Set address to zero).

15.2 4-bit operation (4-bit, 1 line)

	Function R R D D D Display Description													
Function	R	R	D	D	D	D	Display	Description						
	\mathbf{S}	\mathbf{W}	7	6	5	4		•						
power on delay								initialization. No display appears.						
Function set								Sets to 4-bit operation. In this case, operation is handled as 8-bits by						
	0	0	0	0	1	0		initialization, and only this instruction completes with one write.						
Function set	0	0	0	0	1	0		Sets 4-bit operation and selects 1-line display character font on and						
	0	0	0	0	0	0		resetting is needed. (number of display lines and character fonts cannot						
								be changed hence after).						
Display ON/OFF Control	0	0	0	0	0	0	_	Turn on display and cursor.						
	0	0	1	1	1	0	_							
Entry Mode Set	0	0	0	0	0	0		Set mode to incremented the address by one and to shift the cursor to the						
	0	0	0	1	1	0	_	right, at the time of write. to the DD/CG RAM display is not shifted.						
Write data to CG/DD RAM	1	0	1	0	1	1	雄_	Write "雄". Cursor incremented by one and shift to right.						
	1	0	0	1	1	0								
	1	0	1	0	1	0								
	1	0	1	1	1	1								
							same as 8	3-bit operation						

16. Character Generator ROM Map



The character codes in $02H\sim7FH$ with use half-width alpha numeric fonts. The 16x16 BIG-5 Fonts are stored in A140H \sim D75FH.



17. Reliability Condition

			TN	Гуре	STN/FS	TN Type
			Normal Temp.	Wide Temp.	Normal Temp.	Wide Temp.
Viewing	Horizontal (Φ1/Φ2	2)	±30°	±30°	<u>±</u> 40°	<u>±</u> 40°
Angle	Vertical $(\Theta 2/\Theta 1)$)	15° to 35°	15° to 35°	35° to 55°	35° to 55°
Opera	ating Temperature		0 to 50°C	-20 to 70°C	0 to 50°C	*-20 to 70°C
Stor	age Temperature		-10 to 60°C	-30 to 80°C	-10 to 60°C	*-30 to 80°C
High Ten	nperature (Power Off	<u>:</u>)	240 Hours	240 Hours	240 Hours	240 Hours
			@60°C	@80°C	@60°C	@80°C
Low Ten	nperature (Power Off		240 Hours	240 Hours	240 Hours	240 Hours
			@-10°C	@-30°C	@-10°C	@-30°C
High Ter	nperature (Power On)	240 Hours	240 Hours	240 Hours	240 Hours
			@50°C	@70°C	@50°C	@70°C
Low Ten	nperature (Power On)	240 Hours	240 Hours	240 Hours	240 Hours
			@0°C	@-20°C	@0°C	@-20°C
High T	emperature & High		40°C/90%RH	40°C/90%RH	40°C/90%RH	40°C/90%RH
Hum	idity (Power Off)		240 Hours	240 Hours	240 Hours	240 Hours
Thermal Sh	nock C	A	60min@0°C	60min@-20°C	60min@0°C	60min@-20°C
5 Cycle	B	В	5min@25°C	5min@25°C	5min@25°C	5min@25°C
		C	60min@50°C	60min@70°C	60min@50°C	60min@70°C
LCD I	Lift (25°C/45%RH)		50,000 Hours	50,000 Hours	50,000 Hours	50,000 Hours

*Wide temp. version may not available for some products, Please consult our sales engineer or representatives.

18. Functional Test & Inspection Criteria

18.1 Sample plan

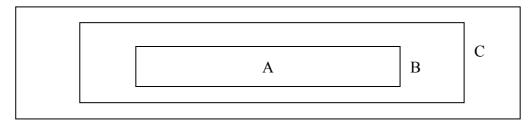
Sample plan according to MIL-STD-105D level 2, and acceptance/rejection criteria is.

Base on: Major defect: AQL 0.65 Minor defect: AQL 2.5

Inspection condition

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lus (20W) light intensity. All direction for inspecting the sample should be within 45° against perpendicular line.

18.3 Definition of Inspection Zone in LCD



Zone A: Character / Digit area

Zone B: Viewing area except Zone A (Zone A + Zone B = minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

18.4 Major Defect

All functional defects such as open (or missing segment), short, contrast differential, excess power consumption, smearing, leakage, etc. and overall outline dimension beyond the drawing. Are classified as major defects.



18.5 Inspection Parameters And Glass Pixel(偏光板和玻璃圖像檢驗)

NO	Polarizer(偏光板)			Criteria	
1	Black or White spots And Piercing (黑/白點和刺孔)	Dimer (面:		Acceptable number (可接受數量)	
		D<0).15	*	
		0.15≦[0.2	4	
		0.2≦D	≤ 0.25	2	
		D≦	0.3	0	
		D/面積=(Length/長	度+Width/寬度)/2 =>	* : Disregard(忽略)
2	Scratch (刮傷)	X(mm)	Y(mm)	Acceptable number (可接受數量)	
		*	0.04≧W	*	
		3.0≧L	0.06≧W	4	
		2.0≧L	$0.08 \ge W$	2	
		_	0.1≧W	0	
		X: Length	(長度)	Y:Width(寬度)	*: Disregard(忽略)
3	Air Bubbles (between glass & polarizer) 氣泡(玻璃跟偏光板之間)	Dimer (面:		Acceptable number (可接受數量)	
		D≦(0.15	*	
		0.15<	0 ≤0.25	2	
		0.25	< <u>D</u>	0	
		*: Disrega	ard (忽略		



		(1)Pixel shape (with Dent) /圖像凹度		
		-→←-0.152		
			●Less than 0.152 mm is no counted (小於 0.152mm 者不計)	
		(2)Pixel shape (with Projection)/圖像凹度		
		0.152		
			Should not be connected	
			next pixel	
			(點與點間不可先連接)	
4 Glass of Pixel (3)Deformation/變形				
	(玻璃的圖像)	-→←- X		
		Y	$(X+Y)/2 \le 0.15$ mm •Less than 0.1 mm is no counted $($ 小於 0.15 mm 者不計 $)$	
		(4) Deformation/變形		
		X Y	(X+Y)/2 ≤ 0.3mm •Less than 0.3 mm is no counted (小於 0.3mm 者不計)	



19. Test (測試條件) - Normal Temperature (常溫)

No change no display and in operation under the following text condition.

(在不改變原先顯示下進行以下測試操作)

Conditions : Unless otherwise specified, test will be conducted under the following condition.

Temperature : 20±5 °C Humidity : 40±5%RH

Tests will be not conducted under functioning state.

(條件:除非其他特殊情況,否則測試將以溫度:20±5°С,濕度:40±5%RH為主)

NO	Parameter	Conditions	Notes
1	High Temperature	$50^{\circ}\text{C} \pm 2^{\circ}\text{C}$, 96 hrs (operation state)	
	Operating	(96 小時,溫度 50°C±2°C電源開啟的操作情況下)	
2	Low Temperature	$0^{\circ}\text{C} \pm 2^{\circ}\text{C}$, 96 hrs (operation state)	
	Operating	(96 小時,溫度 0℃±2℃電源開啟的操作情況下)	
3	High Temperature	60°C ±2 °C , 96 hrs	
	Storage	(96 小時,溫度 60℃±2 ℃電源關閉靜態操作下)	
4	Low Temperature	-10°C ±2 °C , 96 hrs	
	Storage	(96 小時,溫度-10℃±2°C電源關閉靜態操作下)	
5	Damp Proof	40°C±2 °C , 85 ~ 90%RH , 96hr	
	Test	(96 小時,溫度:40℃±2 ℃,濕度:85~90%RH	
		電源關閉靜態操作下)	
6	6 Vibration Test Total fixed amplitude : 1.5 mm (完全固定輻射:1.5m		3
		Vibration Frequency : 10 ~ 55 Hz (震動頻率:10~55 Hz)	
		One cycle 60 seconds to 3 directions of X, Y, Z for	
		each 15 minutes (每一個循環 X,Y,Z軸方向各做 60 秒,連續	
		做 5 次,共計 15 分鐘)	
7	7 Shock Test To be measured after dropping from 60cm high on the conc		
		surface in packing state. (包裝材從 60 公分高的地方向地面落下)	
		Dropping method comer	
		F dropping (角落落下方式)	
		E A comer : once	
		c Edge dropping (側邊落下)	
		B, C, D edge: once	
		60cm	
		Face dropping (表面落下)	
		E, F, G face: once	
	1	<u>I</u>	l

Note 1: No dew condensation to be observed. (不要在"水氣凝結點"下觀察)

Note 2: The function test shall be conducted after 4 hours storage at the normal

Temperature and humidity after removed from the test chamber

(從實驗室移出後,放在一般常溫 (溫度:25℃,濕度:45%RH),

且四小時後通電流或電壓,看它是否能正常動作)

Note 3: Vibration test will be conducted to the product itself without putting it in a container. (在震動測試下,產品本身不需容器即能自行傳導)



20. Test (測試條件) - Wide Temperature (廣溫)

No change no display and in operation under the following text condition.

(在不改變原先顯示下進行以下測試操作)

Conditions : Unless otherwise specified, test will be conducted under the following condition.

Temperature : 20±5 °C Humidity : 40±5%RH

Tests will be not conducted under functioning state.

(條件:除非其他特殊情況,否則測試將以溫度:20±5°С,濕度:40±5%RH為主)

NO	Parameter	Conditions	Notes
1	High Temperature	70° C ±2 °C , 96 hrs (operation state)	
	Operating	(96 小時,溫度 70°C±2°C電源開啟的操作情況下)	
2	Low Temperature	-20°C±2 °C , 96 hrs (operation state)	
	Operating	(96 小時,溫度-20°C±2°C電源開啟的操作情況下)	
3	High Temperature	80°C±2 °C , 96 hrs	
	Storage	(96 小時,溫度 80°C±2°C電源關閉靜態操作下)	
4	Low Temperature	-30°C±2 °C , 96 hrs	
	Storage	(96 小時,溫度-30℃±2°C電源關閉靜態操作下)	1,2
5	Damp Proof	40°C±2 °C , 85 ~ 90%RH , 96hr	
	Test	(96 小時,溫度:40℃±2℃,濕度:85~90%RH	
		電源關閉靜態操作下)	
6	Vibration Test Total fixed amplitude : 1.5 mm (完全固定輻射:1.5 mm		3
		Vibration Frequency : 10 ~ 55 Hz (震動頻率:10~55 Hz)	
		One cycle 60 seconds to 3 directions of X, Y, Z for	
		each 15 minutes (每一個循環 X,Y,Z軸方向各做 60 秒,連續	
		做 5 次,共計 15 分鐘)	
7	Shock Test To be measured after dropping from 60cm high on the concre		
		surface in packing state. (包裝材從 60 公分高的地方向地面落下)	
		Dropping method comer	
		F dropping (角落落下方式)	
		E A comer : once	
		c Edge dropping (側邊落下)	
		B, C, D edge: once	
		60cm	
		Face dropping (表面落下)	
		E, F, G face: once	
			<u> </u>

Note 1: No dew condensation to be observed. (不要在"水氣凝結點"下觀察)

Note 2: The function test shall be conducted after 4 hours storage at the normal

Temperature and humidity after removed from the test chamber

(從實驗室移出後,放在一般常溫 (溫度:25°C,濕度:45%RH),

且四小時後通電流或電壓,看它是否能正常動作)

Note 3: Vibration test will be conducted to the product itself without putting it in a container. (在震動測試下,產品本身不需容器即能自行傳導)



21. Precautions Against Product Handling [產品使用注意事項]:

The following precautions will guide you in handling our product correctly.

[下列警戒引導正確地使用產品]

- 21.1 Care of the LCD module against static electricity discharge. [LCD 模組靜電注意事項]
 - 21.1.1 When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti static mats (made of rubber), to protect work tables against the hazards of electrical shock.

[操作模組時,避免操作者身體接地及任何造成靜電的設備同時使用,強烈建議(橡膠製) 抗靜電墊的使用,以免工作台面遭受到電氣干擾]

21.1.2 Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.

[緩慢小心地移除 LCD 模組上的保護膜,以防靜電產生]

21.1.3 Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

[避免穿著人造合成的工作服,建議棉質或是有傳導性的纖維質料]

- 21.2 Liquid crystal display devices (LCD devices) [液晶螢幕顯示器的組成]
 - 21.2.1 The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it. [偏光板是軟性原料製成,請勿刮傷]
 - 21.2.2 The LCD device panel used in the LCM is made of plate glass. Avoid any strong mechanical shock. Should the glass break handle it with care.

[模組使用的玻璃為平面玻璃,避免任何強烈的機械撞擊,且觸碰時請小心]

21.3 When the LCD module alone must be stored form long periods of time

[當 LCD 模組須長時間存放時]

- 21.3.1 Protect the modules from excessive external forces. [避免外力壓迫]
- 21.3.2 Protect the modules from high temperature and humidity. [避免處於高溫高濕下]
- 21.3.3 Keep the modules out of direct sunlight or direct exposure to ultraviolet rays. [遠離陽光曝曬或直接曝露在紫外線下]
- 21.4 Use the module with a power supply that is equipped with an overcurrent protector circuit, since the module is not provided with this protective feature.

[因為模組本身沒有防護,所以模組的供應器應配有過高電流的保護迴路]

21.5 Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.

[LCD 破裂液晶外漏時,切勿食下液晶;若手或衣服接觸到液晶,請立刻用肥皂清洗]

21.6 Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.

[當金屬框並沒焊接於 PCB 板上時,無法保證使用金屬框是具有傳導性,請連絡我們商討適當方式傳導]

- 21.7 For models which use CCFL [CCFL 的模組]:
 - 21.7.1 High voltage of 1000V or greater is applied to the CCFL cable connector area. [CCFL 排線連接器用於 1000V 以上的高電壓]
 - 21.7.2 Protect CCFL cables from rubbing against the unit and thus causing the wire jacket to become worn. [CCFL 排線必須有保護 CCFL 與模組磨擦,以防 CCFL 外殼受到損害]
 - 21.7.3 The use of CCFLs for extended periods of time at low temperatures will significantly shorten their service life. [長時間低溫使用 CCFL 會明顯縮減其使用壽命]



- 21.8 For models which use touch panels [觸控式面板模組]:
 - 21.8.1 Do not stack up modules since they can be damaged by components on neighboring modules. [勿堆疊模組以防損壞]
 - 21.8.2 Do not place heavy objects on top of the product. This could cause glass breakage. [勿將重物放置在產品上,會導致玻璃破損]
- 21.9 For models which use COG & TAB [COG 及 TAB 模組]:
 - 21.9.1 The mechanical strength of the product is low since the IC chip is faces out unprotected from the rear. Be sure to protect the rear of the IC chip from external forces.

 [由於 IC 晶片表面無防護,所以抗壓力有限,須加強保護以防外力]
 - 21.9.2 Given the fact that the rear of the IC chip is left exposed, in order to protect the unit from electrical damage, avoid installation configurations in which the rear of the IC chip runs the risk of making any electrical contact.

[勿暴露 IC 晶片以防電氣干擾,且避免安裝 IC 時有任何電子接觸]

- 21.10 Models which use flexible cable, heat seal, or TAB [加有軟排線、熱封條或 TAB 的模組]:
 - 21.10.1 In order to maintain reliability, do not touch or hold by the connector area. [以維持產品信賴度,請勿觸碰或握住連接器]
 - 21.10.2 Avoid any bending, pulling, or other excessive force, which can result in broken connections. [避免彎曲、拉扯或過度力量,會造成連接器損壞]
- 21.11 In case of acrylic plate is attached to front side of LCD panel, cloudiness (very small cracks) can occur on acrylic plate, being influenced by some components generated from polarizer film. Please check and evaluate those acrylic materials carefully before use.

 [貼在 LCD 玻璃前面的壓克力板若有模糊情況(微小裂縫),即會影響偏光板;使用前請仔細確 認壓克力材質]
- 21.12 In case of buffer material such as cushion/gasket is assembled into LCD module, it may have an adverse effect on connecting parts (LCD panel-TCP/ HEAT SEAL/ FPC, PCB-TCP/HEAT SEAL/FPC, TCP-HEAT SEAL, TCP-FPC, HEAT SEAL-FPC) depending on its materials. Please check and evaluate these materials carefully before use.

[緩衝原料像是減震墊/襯墊,或許會對連接器(LCD panel-TCP/ HEAT SEAL/ FPC, PCB-TCP/HEAT SEAL/FPC, TCP-HEAT SEAL, TCP-FPC, HEAT SEAL-FPC)造成反效果,使用前請仔細確認材料]

22. Warranty [保證]:

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

[此產品的製造是依照客戶的規格,被使用於客戶的一般電子產品上,保證產品製作根據出貨的規格,若產品的使用不是在一般電子設備,而組裝於下列產品上則無法受理(如醫療產品、核心電源控制設備、航空設備、防火及保全系統,或任何相關儀器會直接影響人類生命等),若模組使用於上述的儀器,則需商討各別產品責任義務的協定]



- 22.1 We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
 - [不受理因強大外力衝擊造成產品的缺陷]
- 22.2 We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
 - [不受理產品出貨後,因額外加工(包含拆裝及重新封包)造成的缺陷]
- 22.3 We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product, has passed your company's acceptance inspection procedures.
 - [不受理通過貴公司檢驗流程後,由於靜電造成產品的缺陷]
- 22.4 We cannot accept responsibility for intellectual property of a third party, which may arise through the application of our product to your assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.
 - [不受理因在客戶產品生產線端所產生的第三人智慧財產權責任,除非與我司生產製造方法有直接關係的問題]
- 22.5 When the product is in CCFL models, CCFL service life and brightness will vary according to the performance of the inverter used, leaks, etc. We cannot accept responsibility for product performance, reliability, or defect, which may arise.
 - [產品是 CCFL 模組時, CCFL 的壽命及亮度將取決於連接器的性能、漏電量等;無法受理因 CCFL 造成產品性能的缺陷]
- 22.6 SDEC will not be held responsible for any quality guarantee issue for defect products longer than 1(one) year from SDEC production which ever comes later.
 - [出廠超過一年的瑕疵品,任何品質擔保則不受理]