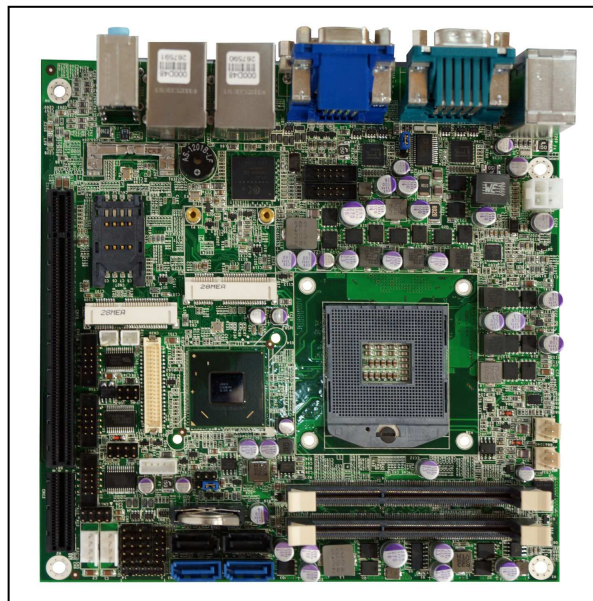


User Manual

MB-8300

Mini-ITX support Intel® 3rd generation Core™ i7/i5/i3/Celeron Mobile processor , Intel® QM77 chipset, DDR3 up to 8GB, 2 x Intel® Giga LAN, Onboard HDMI, DVI, VGA & LVDS, 4 x SATA, 4 x USB3.0, 4 x COM, GPIO, PCI-Express X16 & X1 slots, 2 x Mini-PCIE sockets, DC 8V ~ 32V input



Ver.	Release Date	Update
1.0	2013.04.12	Release
1.1	2013.04.17	Add GPIO pin define / Page 24
1.2	2013.07.10	Limit DC out current / Page 25
		Limit PCIe add-on card TDP / Page 29
		Add RS422/485 Pin define / Page 33

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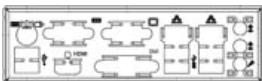
For more information or other AEWIN products, please visit our website <http://www.aewin.com.tw>.

For technical supports, please send your inquiry to sales@aewin.com.tw

Packing list

Before use this product, please make sure that the following materials have been shipped.

- ▶ 1 x MB-8300 board
- ▶ 1 x SATA cable, L/ 200mm (p/n: 46L-SATA11-00)
- ▶ 1 x 12V/5V SATA power cable , L/ 150mm (p/n: 46L-IPOW41-00)
- ▶ 1 x I/O plate (TBD) (p/n: TBD)
- ▶ 1 x CD Utility (p/n:)

p/n: 46L-SATA11-00	p/n: 46L-IPOW41-00	p/n: TBD
		

Model Name	Description
MB-8300A	Mini-ITX with Intel® QM77 chipset, 2 GLAN, DDR3 up to 8GB, HDMI, DVI, VGA, LVDS, SATA, COM, PCIe X16 slot
MB-8300B	Mini-ITX with Intel® QM77 chipset, 2 GLAN, DDR3 up to 8GB, HDMI, DVI, VGA, LVDS, SATA, COM, PCIe X16 slot & 4-ch video inputs
MB-8300C	Mini-ITX with Intel® QM77 chipset, 2 GLAN, DDR3 up to 8GB, HDMI, DVI, VGA, LVDS, SATA, COM, PCIe X16 and PCIe X1 slots

* If any of those items are missing or damaged, please contact with sales representative or distributor

Optional Accessory

Photo	Model Name	
	P/N:	49L-F00055-00
	CPU cooling Fan for socket 988B mobile processor	
	P/N:	RE-S01
	PCI-express riser card support 1 x PCIe X16 & 1 x PCI slots <u>Note:</u> Work with MB-8300C model only	
	P/N:	46L-ICOM38-00
	Dual D-Sub 9-pin COM port card, L/ 250mm, with bracket	
	P/N:	46L-IUSB07-AA
	Dual USB cable, L/ 250mm, with bracket	
	P/N:	46L-IPOW102-00
	P4 4-pin to DC Jack lockable power cable, L/ 200mm	

Safety Information

- To prevent electrical shock hazard, disconnect the power cable from the electrical outlet before relocating the system.
- When adding or removing devices to or from the system, ensure that the power cables for the devices are unplugged before the signal cables are connected. If possible, disconnect all power cables from the existing system before you add a device.
- Before connecting or removing signal cables from the motherboard, ensure that all power cables are unplugged.
- Seek professional assistance before using an adapter or extension cord. These devices could interrupt the grounding circuit.
- Make sure that your power supply is set to the correct voltage in your area.
- If you are not sure about the voltage of the electrical outlet you are using, contact your local power company.
- If the power supply is broken, do not try to fix it by yourself. Contact a qualified service technician or your retailer.

Operation Safety

- Before installing the motherboard and adding devices on it, carefully read all the manuals that came with the package.
- Before using the product, make sure all cables are correctly connected and the power cables are not damaged. If you detect any damage, contact your dealer immediately.
- To avoid short circuits, keep paper clips, screws, and staples away from connectors, slots, sockets and circuitry.
- Avoid dust, humidity, and temperature extremes. Do not place the product in any area where it may become wet.
- Place the product on a stable surface.
- If you encounter technical problems with the product, contact a qualified service technician or your retailer.

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1.1 Introduction

MB-8300, a Mini-ITX board supporting Intel® Ivy Bridge / Sandy Bridge Mobile CPU with integrated Intel® QM77 Express chipset. Onboard display includes HDMI, DVI-D, VGA and 24-bit dual channel LVDS and two DDR3 SO-DIMM supports a maximum of 16GB DDR3 1600 of system memory.

MB-8300 delivers flexible expansion slots allowing customers to install one PCI-Express X16 card and two Mini-PCIe cards. It is built with a SIM card holder capable of installing a SIM card with a Full-size Mini-PCIe 3G module for wireless connection. For half-size Mini-PCIe socket it also supports mSATA SSD as storage device (Optional). Furthermore it comes with a PCI-Express X1 slot that can be paired with AEWIN's riser card for an additional 1 ~ 3 PCI slots for various application.

MB-8300 is designed with a video/audio encode chip that supports 4-channel inputs under D1 resolution at real-time. AEWIN will provide software development kit allowing easy application development.

On the I/O front the MB-8300 provides plenty of connectivity. 2 x Intel® i211AT GbE LAN controller, 1 x RS232/422/485 & 3 x RS232, 4 x USB3.0 + 6 x USB2.0, HD Audio, PS/2 Keyboard/Mouse, 4 x SATA with RAID 0/1/5/10 and a LPC pin-header supporting AEWIN's TPM module for the added information protection. The MB-8300 accepts a wide range 8V ~ 32V DC input suitable for a variety of applications in digital signage, POS, kiosks, and factory automation.

About AEWIN

AEWIN offers reliable and solid products which are produced under Management System Standards: ISO9001-2000 Certificate. The certificate keeps us focused on our quality objectives of management and environmental production. Its willingness to customize standard products for meet unique customer needs makes AEWIN different. All ODM projects are welcome. Years of experiences enables AEWIN to fulfill the customer's vision, by delivering products to exact specifications. AEWIN R&D team is proud of its strong engineering background. R&D professionals account for 25% of the AEWIN workforce. We focus on developing new products for both emerging and established markets

For more information about OEM/ODM, please contact us :

Email: sales@aewin.com.tw TEL: +886-2-8692-6677

1.2 Specification of board

System	
From Factor	Mini-ITX motherboard
CPU	Intel® Core™ i7/i5/i3 Processor Mobile processor, Ivy Bridge, socket 988B. Intel® Core™ i7-3610QE, 2.3 GHz, Qual-Core/45W Intel® Core™ i5-3610ME, 2.7 GHz, Dual-Core/35W Intel® Core™ i5-2510E , 2.5 GHz, Dual-Core/35W Intel® Core™ i3-3120ME , 2.4 GHz, Dual-Core/35W Intel® Celeron® B810 , 1.6 GHz, Dual-Core/35W
Chipset	Intel QM77 Express chipset
Memory	2 x 204-pin DDR3 1333/1600 MHz / SODIMM up to 16GB, w/o ECC support
BIOS	AMI SPI BIOS
SSD	Half-size Mini-PCle socket support mSATA SSD
Watchdog timer	255 levels, 1 ~ 255 sec
Expansion	1 x PCI-Express X16 slot 1 x PCI-Express X1 (work with riser card for PCIe X1 or PCI 32-bit/33 MHz expansion) 1 x Full-size Mini-PCle socket w/ USB, PCIe signal 1 x Half-size Mini-PCle socket w/ USB, PCIe or SATA signal (Default is SATA signal) Note: Need to inform us to modify hardware before ship if want to Half-size Mini-PCle support PCIe signal
Board Size	170mm x 170mm
Operating Temp.	0°C~60°C (32°F~140°F)
Storage Temp.	.-20°C~80°C (-4°F~176°F)
Operating Hum.	10%~90% (non-condensing)

Display	
Chipset	Intel® processor integrated
Display interface	1 x external HDMI 1.4a
	1 x external VGA
	1 x external DVI-D
	1 x internal 24-bit Dual Channel LVDS

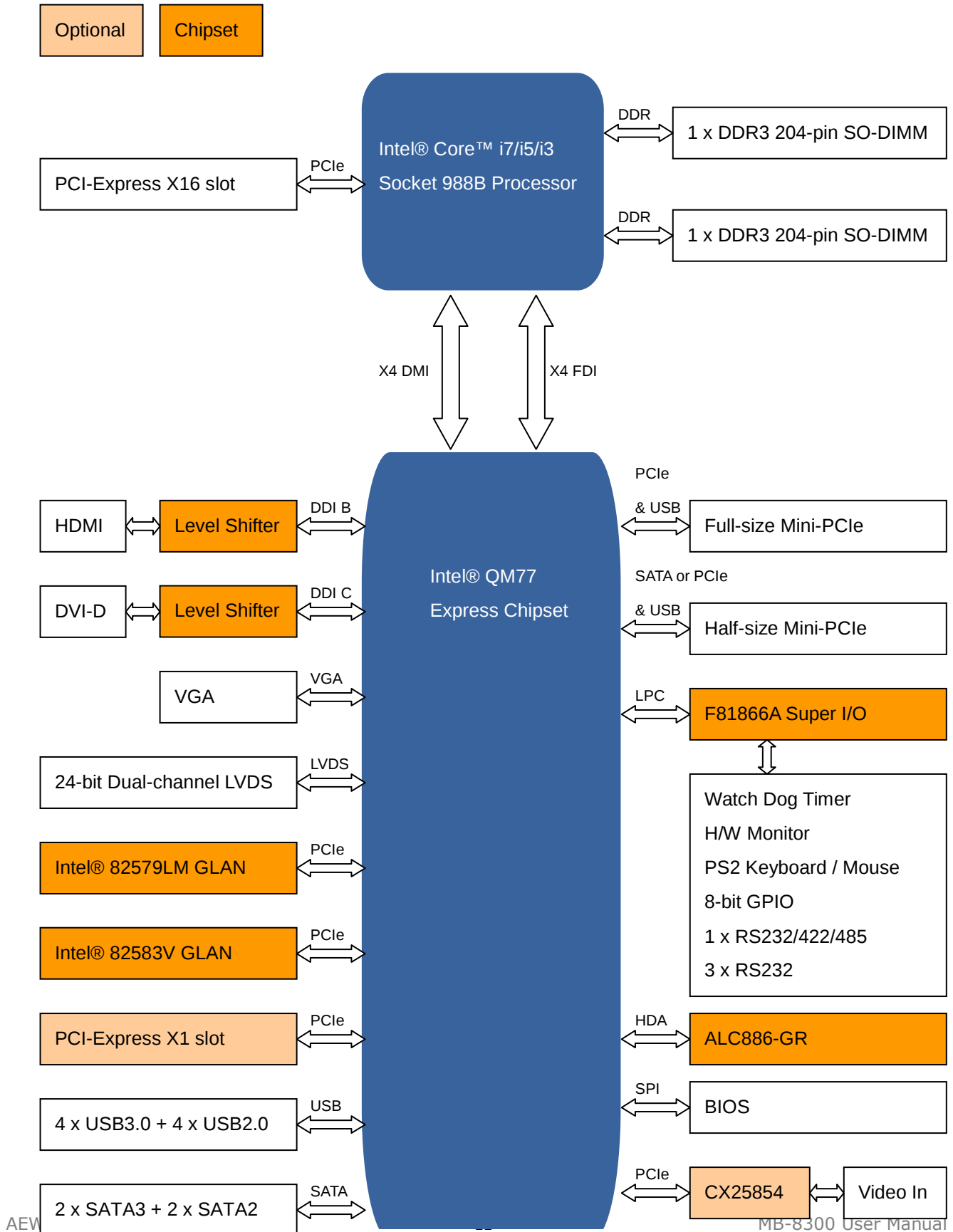
I/O	
Series Port	Internal : 1 x RS232/422/485 (COM2), 2 x RS232 External : 1 x RS232
SATA	2 x SATA 3Gb/s + 2 x SATA 6Gb/s, w/ RAID 0/1/5/10

USB	External : 4 x USB3.0 + 2 x USB2.0 Internal : 4 x USB2.0
Ethernet	1 x Intel® 82583V PCIe controller 1 x Intel® 82579LM PHY support WOL & Intel® AMT 8.0 support
Audio	Line-in/out , Mic-in
Digital I/O	8-bit GPIO interface (pitch : 2.0)
LPC	1 x LPC 2 x 10-pin header for Optional TPM module
Others	2 x cooling Fan header (Smart fan support) , 1 x PS/2 keyboard/Mouse 1 x Front Panel header for power on/off, reset, HDD/power LED indicator 1 x LVDS Backlight/inverter pin-header (w/ house) 2 x 12V/5V DC out for HDD , 1 x SMBus (5-pin wafer)

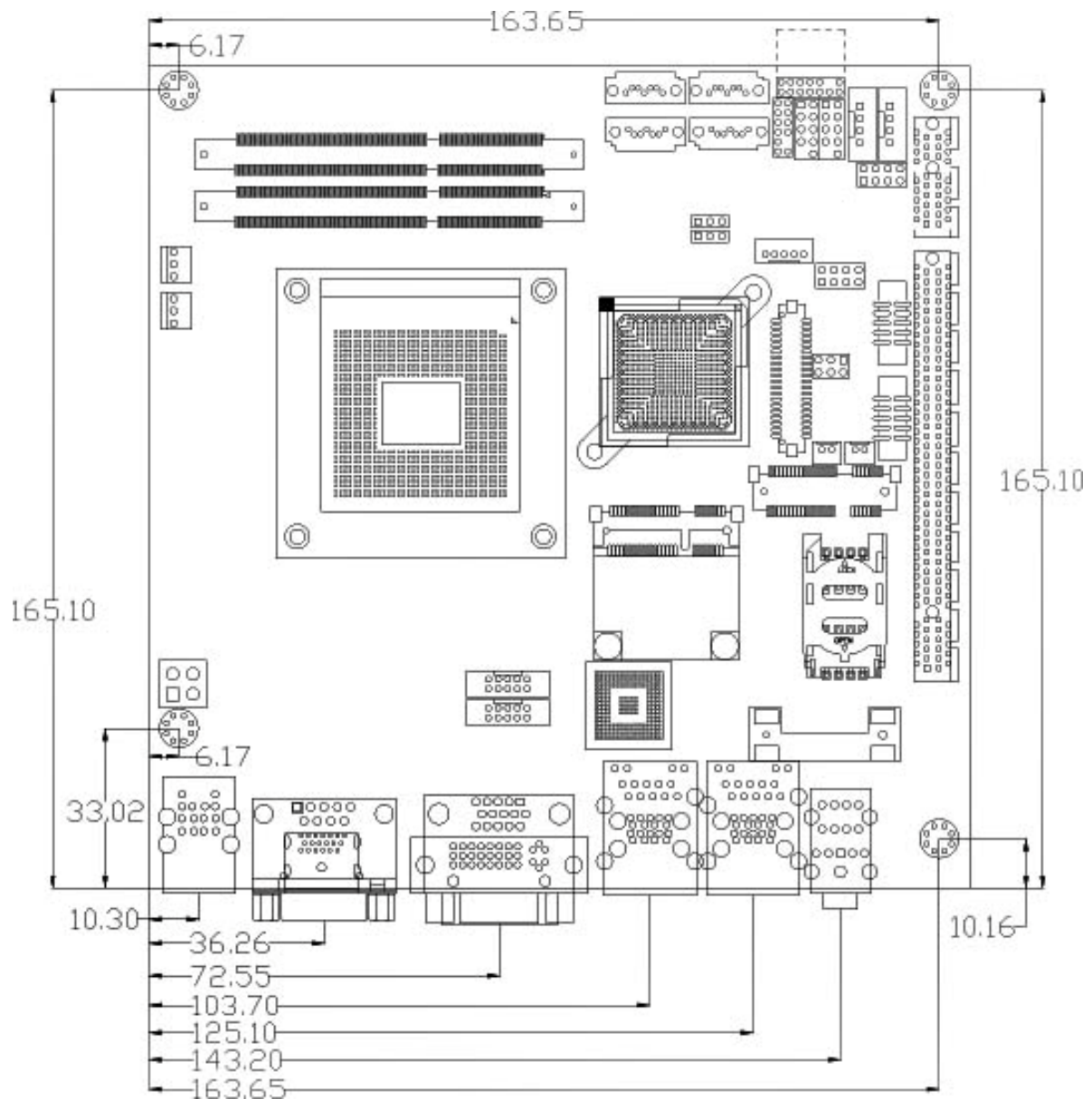
Power	
Power in	Wide range DC 8V ~ 32V input (AT/ATX mode select by jumper)
Connector	1 x internal P4 4-pin power connector

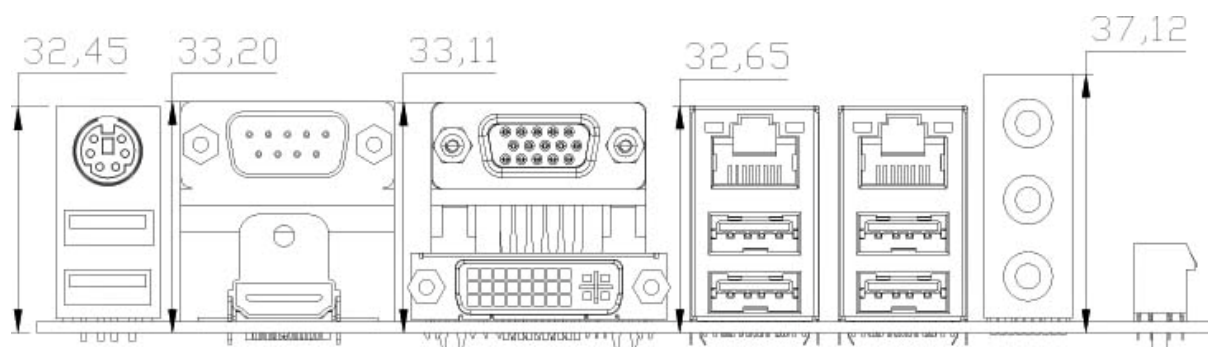
Note : All specifications and photos are subject to change without notice

1.3 Block Diagram

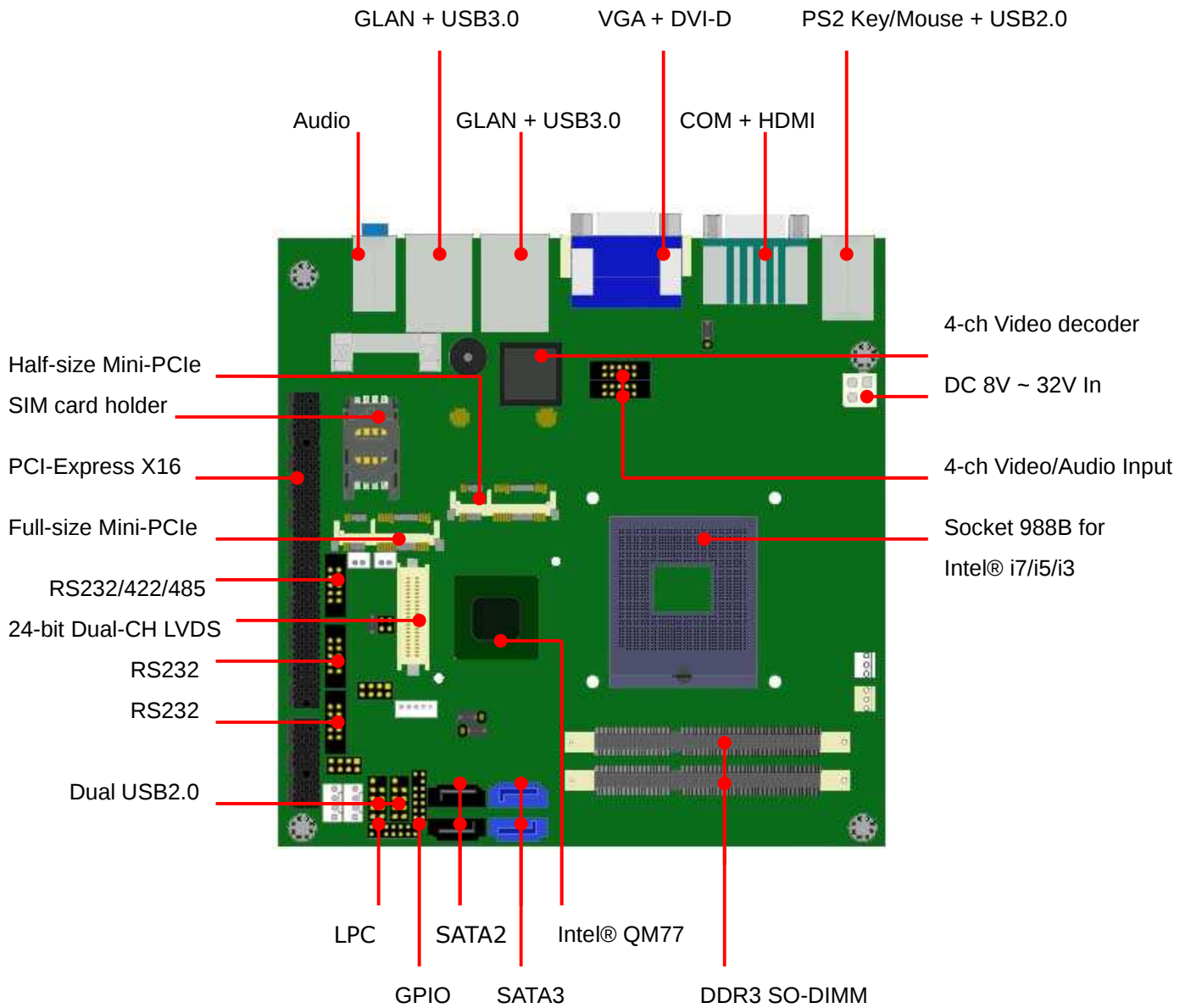


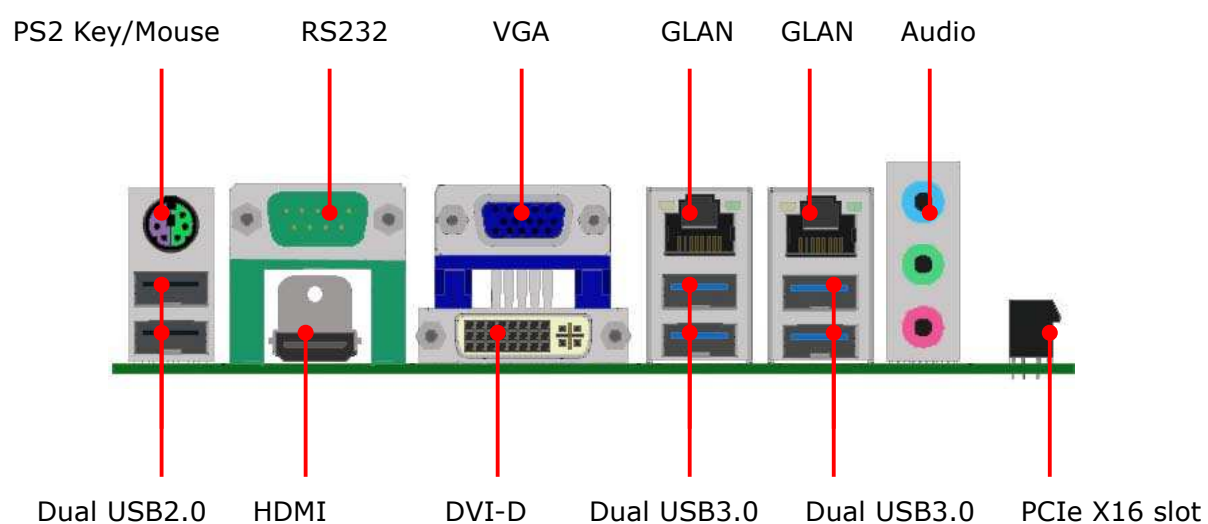
1.4 Board Layout Dimension



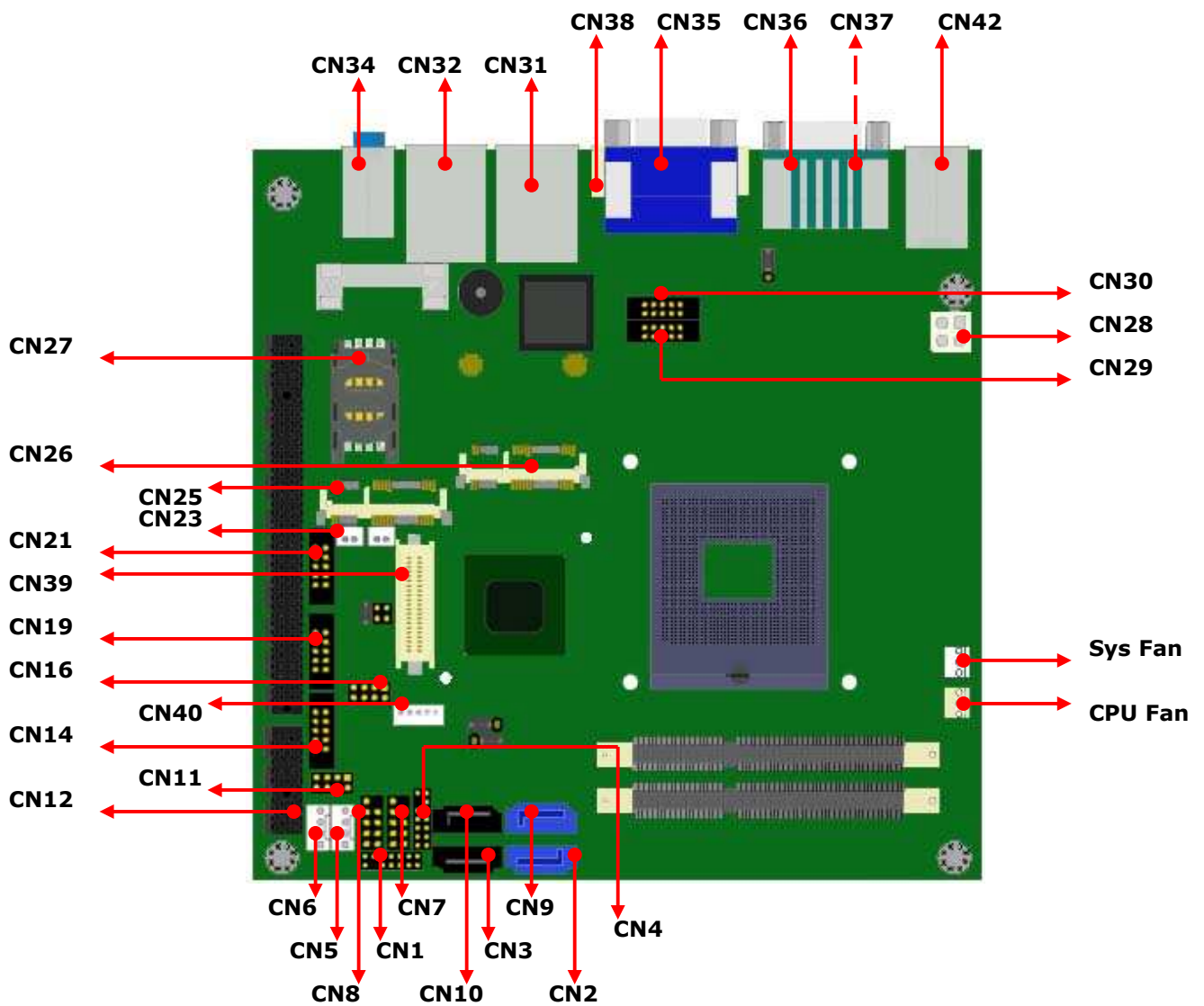


1.5 IO ports





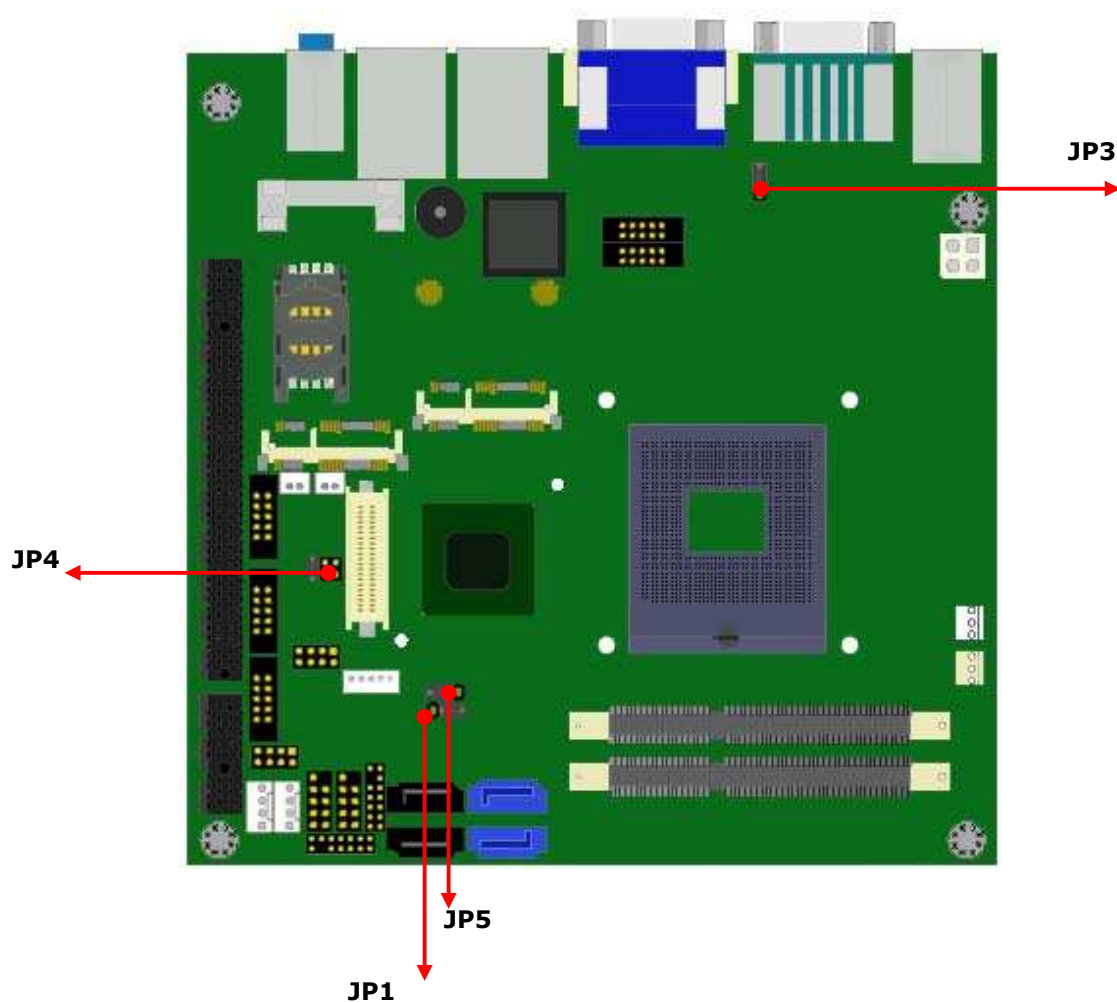
2.1 The location of onboard connectors



Label	Function
CN1	Low Pin Count
CN2	SATA3-1
CN3	SATA2-1
CN4	8-bit GPIO
CN5	SATA power-1
CN6	SATA power-2
CN7	USB 6/7
CN8	USB 8/9
CN9	SATA3-2
CN10	SATA2-2
CN11	SPI Programmer
CN12	PCI-Express X1 slot
CN13	N/C
CN14	COM4
CN15	N/C
CN16	Power on/off, Reset, HDD/Power LED
CN17	N/C
CN18	N/C
CN19	COM3
CN20	N/C
CN21	COM2

Label	Function
CN22	Half-size Mini-PCle LED
CN23	Full-size Mini-PCle LED
CN24	PCI-Express X16 slot
CN25	Full-size Mini-PCle socket
CN26	Half-size Mini-PCle socket
CN27	SIM card holder
CN28	P4 4-pin power input connector
CN29	4-ch Audio input
CN30	4-ch Video input
CN31	RJ45 LAN + Dual USB 3.0
CN32	RJ45 LAN + Dual USB 3.0
CN33	PS2 keyboard/mouse + Dual USB 2.0
CN34	Audio jack
CN35	VGA connector
CN36	COM1
CN37	HDMI connector
CN38	DVI-D connector
CN39	24-bit Dual Channel LVDS connector
CN40	LVDS Backlight inverter
CN41	N/C
CN42	N/C

2.2 The location of onboard jumpers



Label	Function
JP1	CMOS Clear jumper
JP2	N/C
JP3	AT/ATX power mode jumper select
JP4	Panel Voltage (VCC) select
JP5	LVDS backlight control mode select

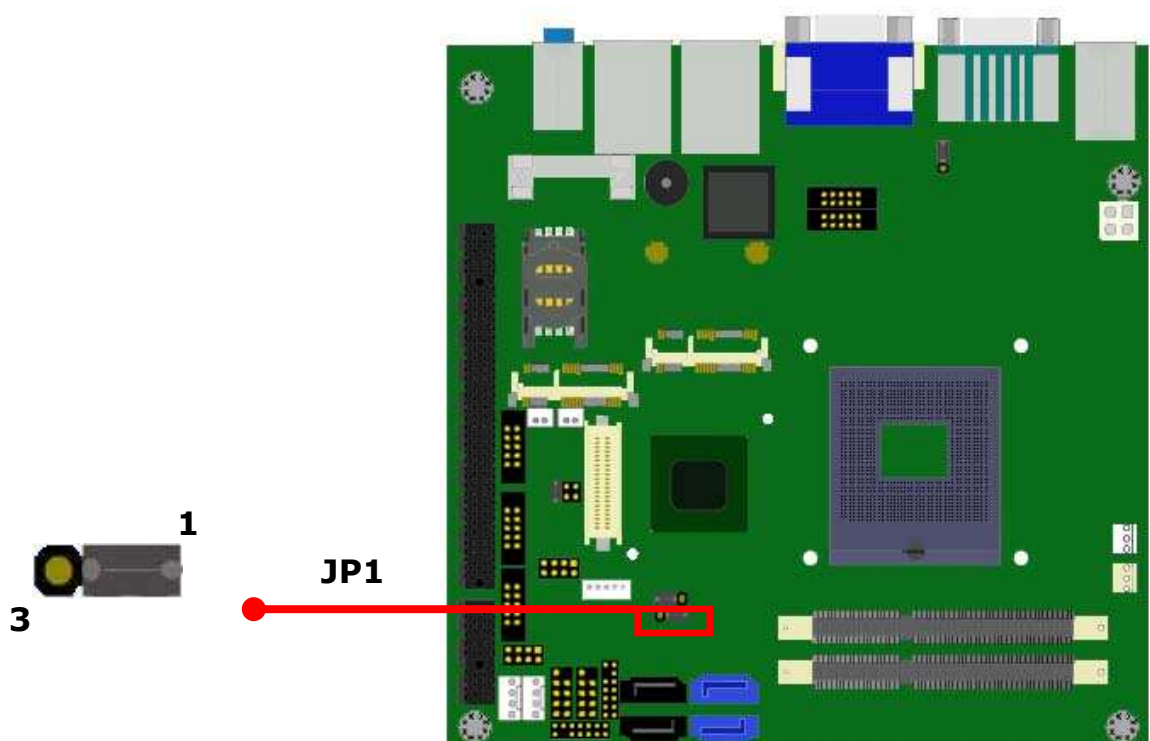
2.3 The function list of onboard jumpers setting

- 2.3.1 : JP1 for for Clear CMOS

If you want to clean the CMOS data, set jumper to 2-3 just for few seconds, Then, Move the jumper back to 1-2 pin

JP1	
Closed Pin	Result
1-2 *	Normal
2-3	Clear CMOS

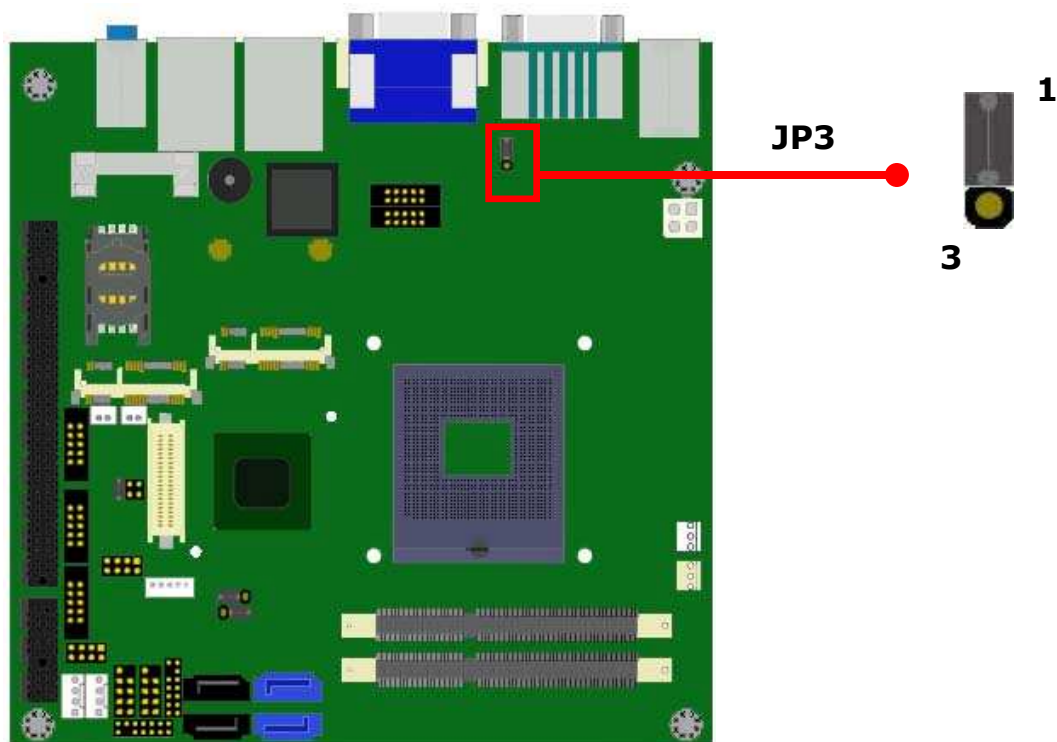
* Default setting



- 2.3.2: JP3 for ATX / AT mode

JP3	
Closed Pin	Result
1-2 *	ATX mode
2-3	AT mode

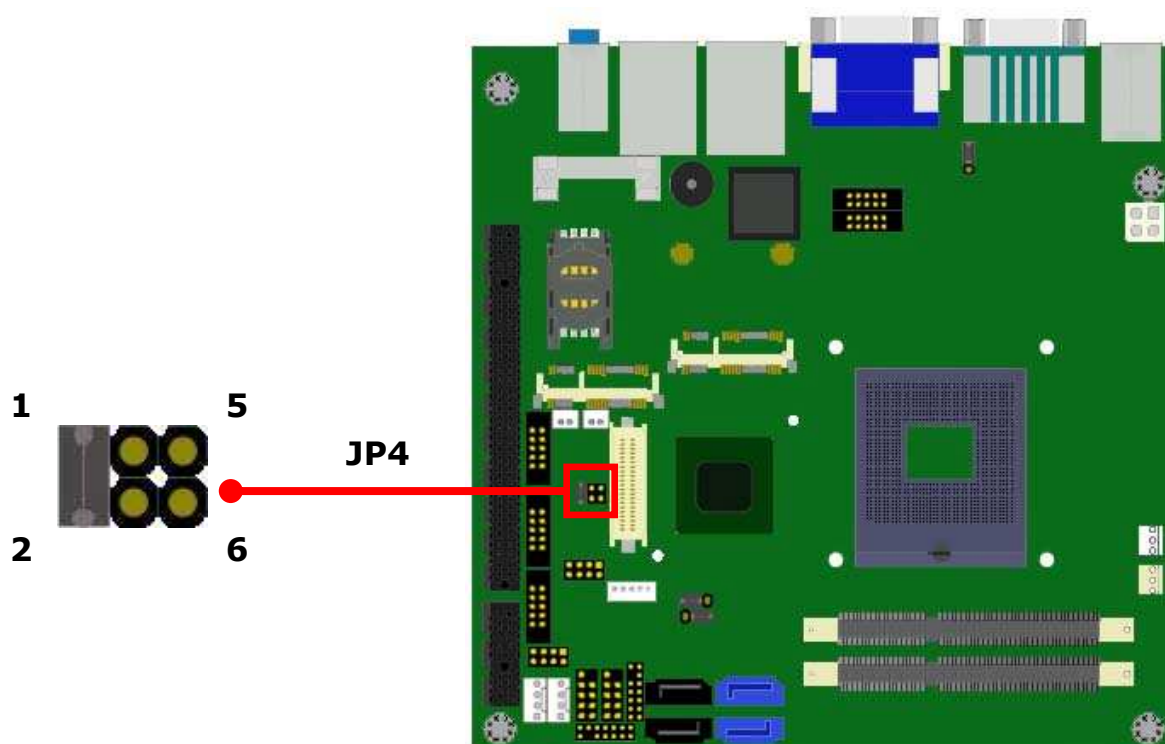
* Default setting



- 2.3.3 : JP4 for LVDS voltage select

JP4	
Closed Pin	Result
1-2 *	+3.3V
3-4	+5V
5-6	+12V

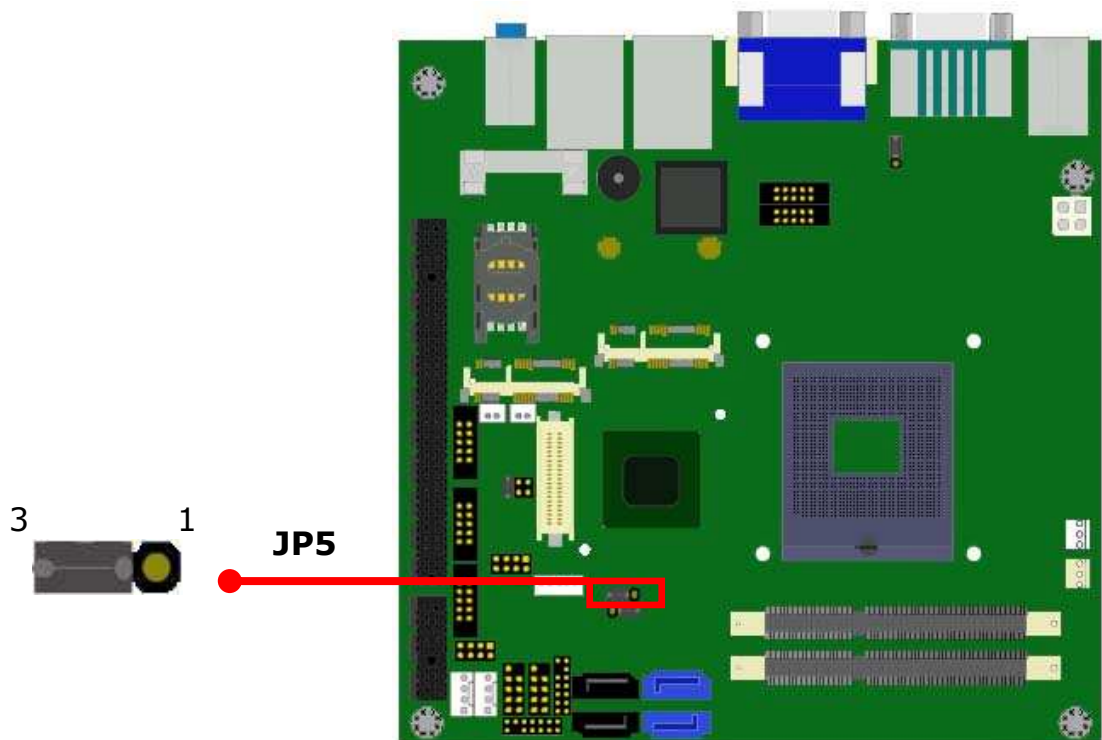
* Default setting



- 2.3.4 : JP5 for LVDS brightness control mode

JP5	
Closed Pin	Result
1-2	PWM mode
2-3 *	DC Level

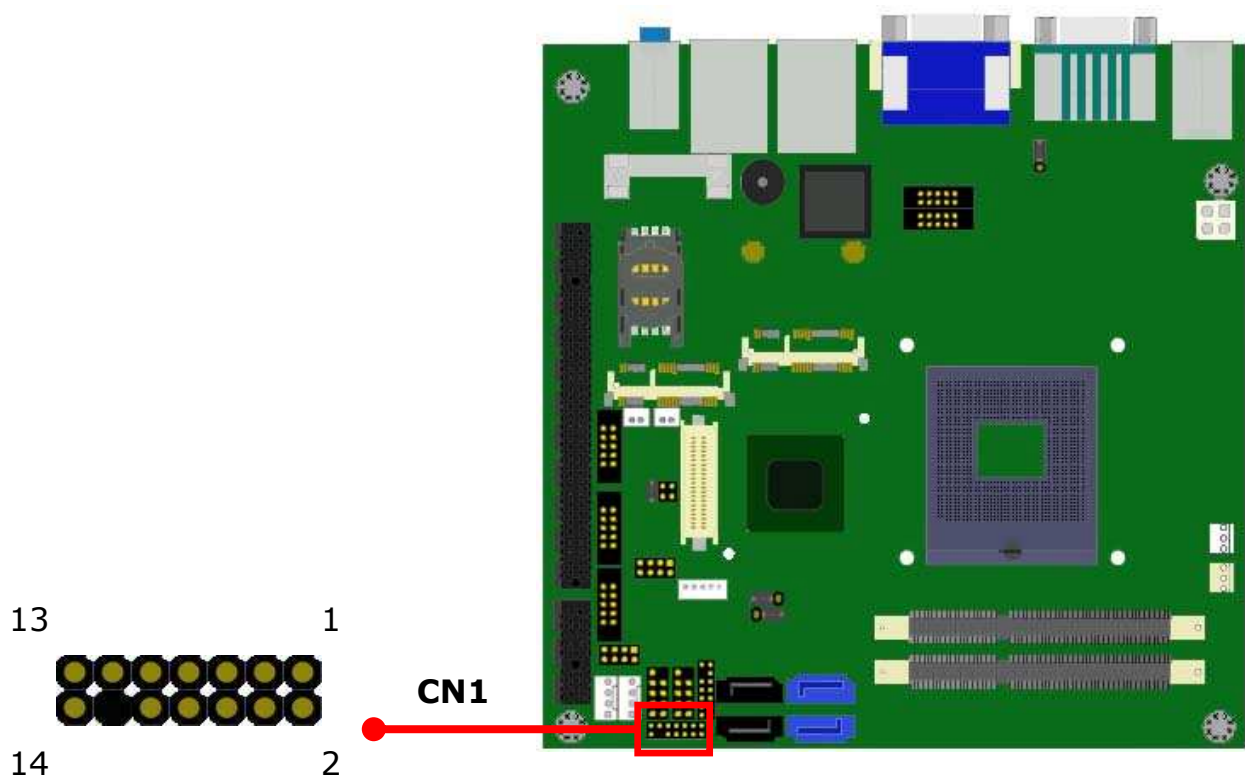
* Default setting



2.4 The pin define of onboard pin header

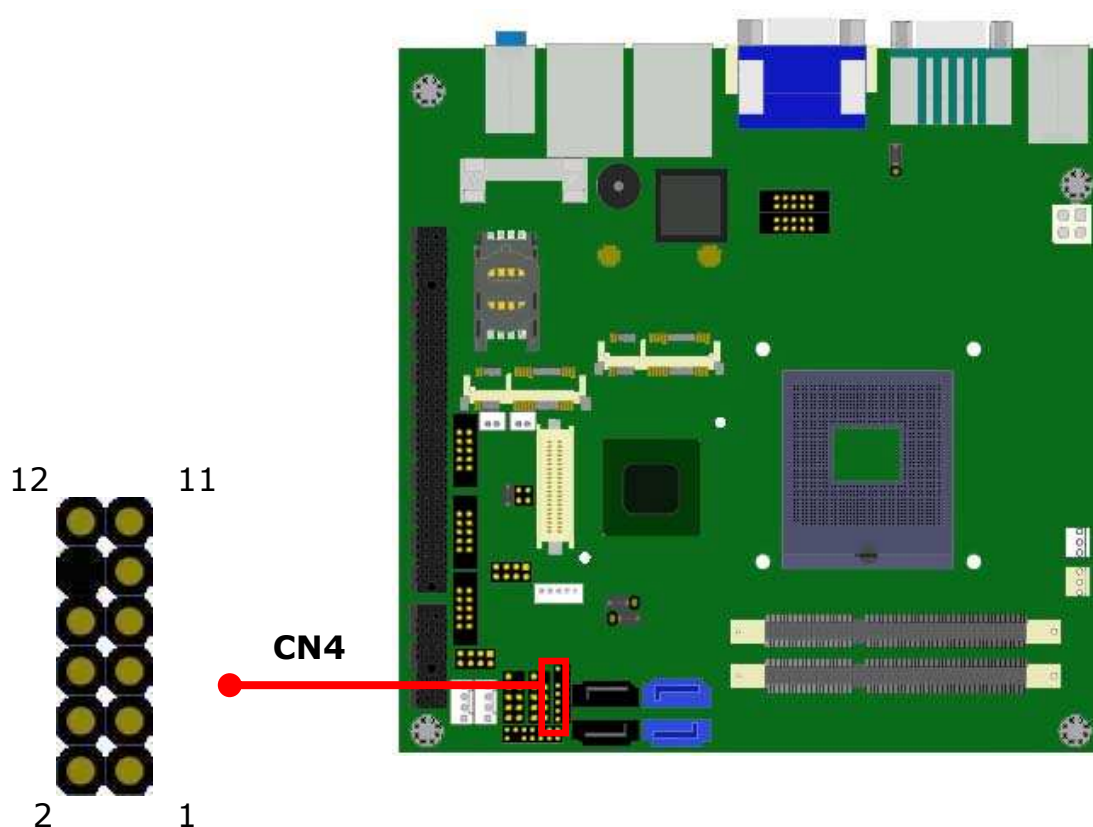
- 2.4.1 : CN1 for Low Pin Count pin-header

CN1 : 2 x 7 header , pitch 2.0 mm			
Pin	Signal	Pin	Signal
1	+3.3V	2	LAD0
3	LAD1	4	LAD2
5	LAD3	6	LFRAME
7	Reset	8	+5V
9	PORT80_PCLK	10	LPME
11	GND		Key
13	SERIRQ	14	LDRQ



2.4.2 : CN4 for 8-bit GPIO

CN4 : 2 x 6 header , pitch 2.0 mm			
Pin	Signal	Pin	Signal
1	+3.3V	2	GPI0
3	GPI1	4	GPI2
5	GPI3	6	GPO0
7	GPO1	8	GPO2
9	GPO3	10	Key
11	+5V	12	GND



- 2.4.3 : CN5 & CN6 for 4-pin HDD power

CN5 : 4-pin wafer for SATA power connector

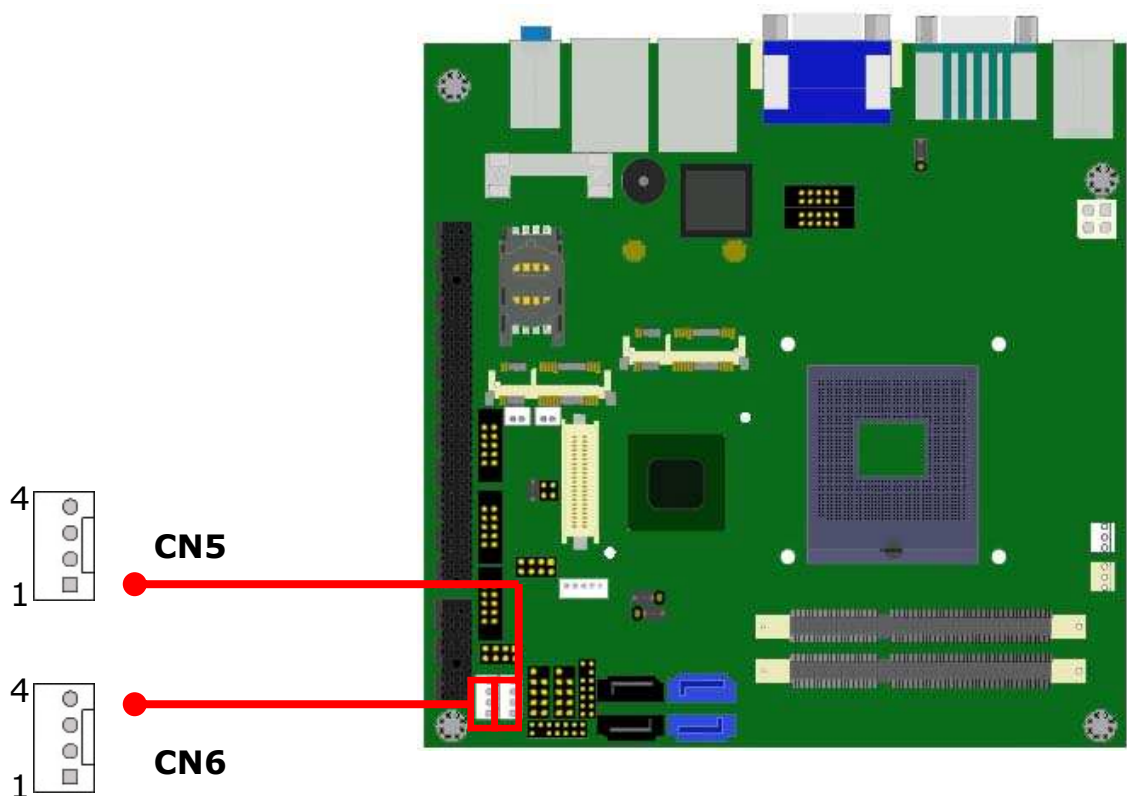
Pin	Signal	Pin	Signal
1	+12V	2	GND
3	GND	4	+5V

Note: Maximum output current 12V/1A, 5V/1A

CN6 : 4-pin wafer for SATA power connector

Pin	Signal	Pin	Signal
1	+12V	2	GND
3	GND	4	+5V

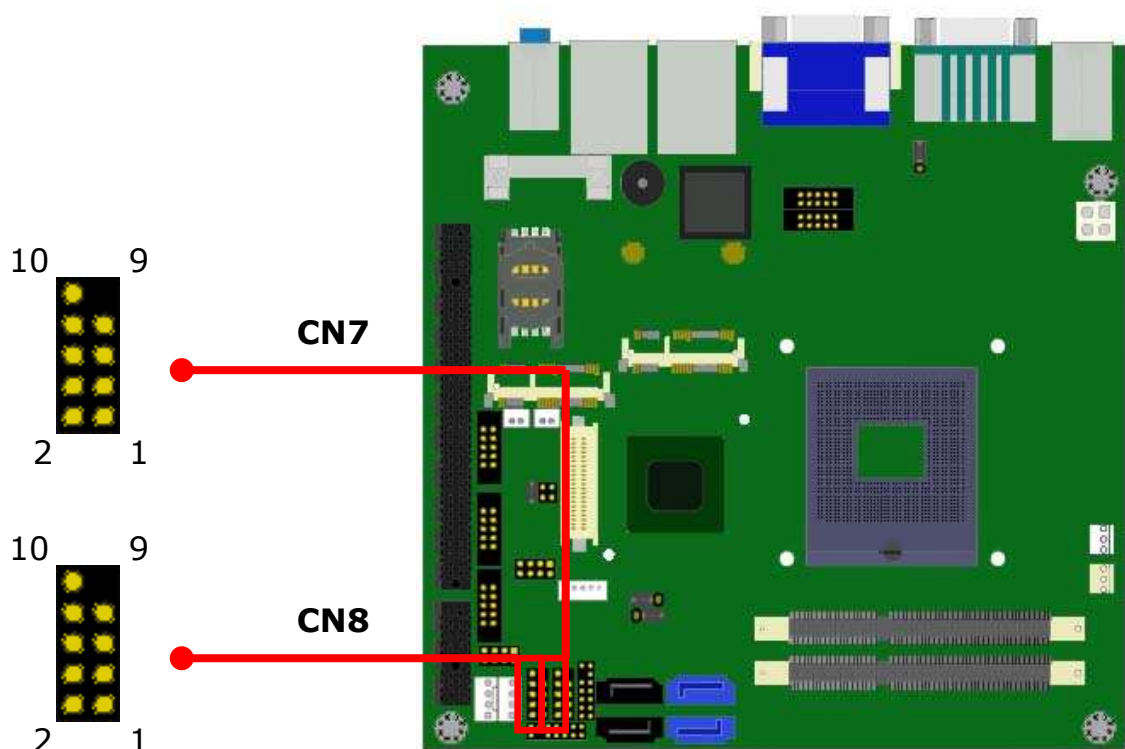
Note: Maximum output current 12V/1A, 5V/1A



- 2.4.4 : CN7 & CN8 for USB 6/7 , USB 8/9

CN7: 2 x 5 header , pitch 2.54 mm			
Pin	Signal	Pin	Signal
1	+5V	2	+5V
3	USB6_ data-	4	USB7_ data-
5	USB6_ data+	6	USB7_ data+
7	GND	8	GND
9	Key	10	GND

CN8: 2 x 5 header , pitch 2.54 mm			
Pin	Signal	Pin	Signal
1	+5V	2	+5V
3	USB8_ data-	4	USB9_ data-
5	USB8_ data+	6	USB9_ data+
7	GND	8	GND
9	Key	10	GND

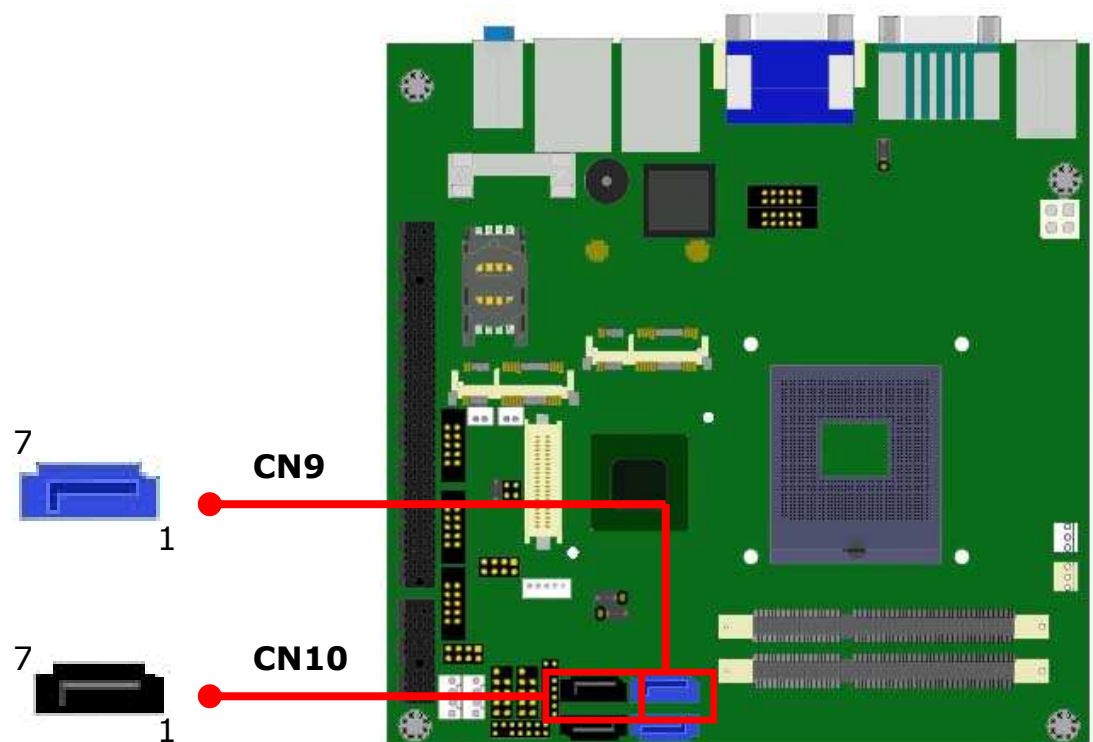


2.4.5 : CN9 for SATA 3.0 connector

CN9 : SATA 2.0 connector			
Pin	Signal	Pin	Signal
1	GND	2	TX+
3	TX-	4	GND
5	RX-	6	RX+
7	GND		

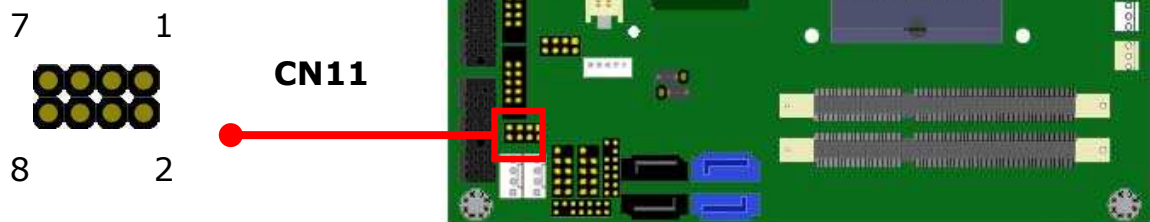
CN10 for SATA 2.0 connector

CN10 : SATA 2.0 connector			
Pin	Signal	Pin	Signal
1	GND	2	TX+
3	TX-	4	GND
5	RX-	6	RX+
7	GND		



- 2.4.6 : CN11 for SPI programmer

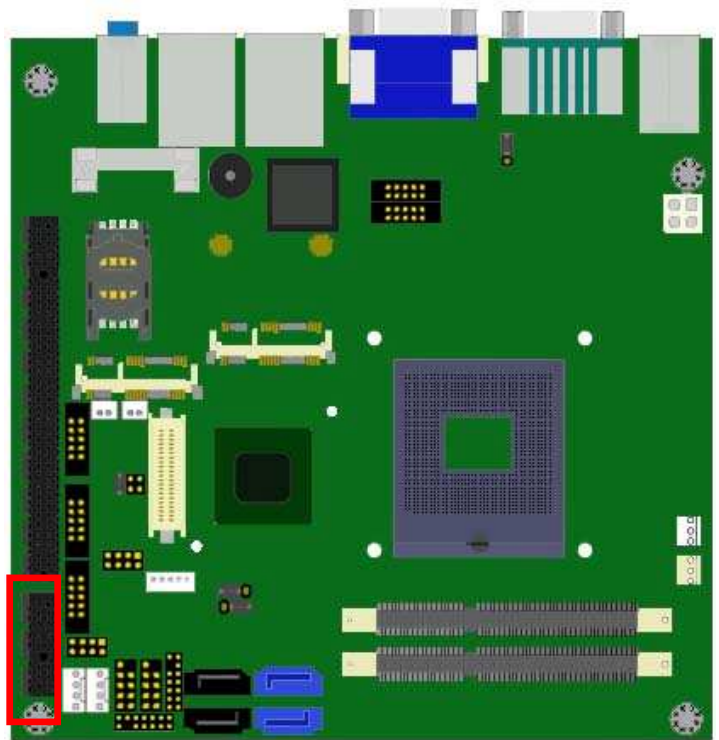
CN11 : 2 x 4 header , pitch 2.54 mm			
Pin	Signal	Pin	Signal
1	+3.3V	2	GND
3	CS_N (Chip Select)	4	SCLK (Serial Clock)
5	MISO (Master Input, Slave Output)	6	MOSI (Master Output, Slave Input)
7	N/C	8	FLASH_IO



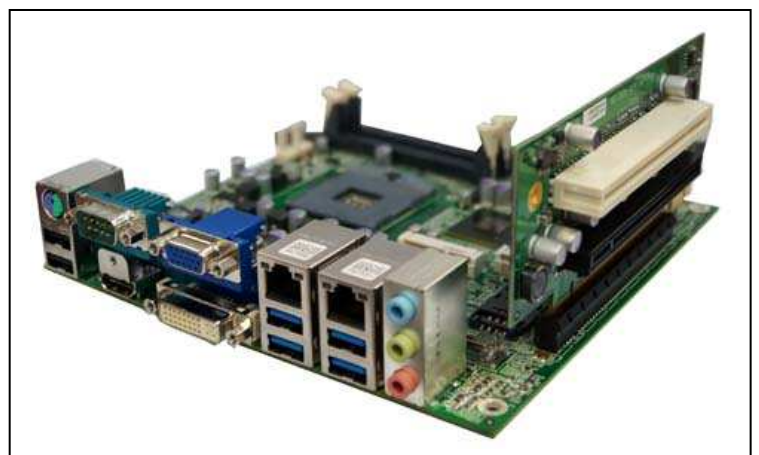
- 2.4.7 : CN12 for PCI-Express X1 slot

Note: This slot could work with AEWIN's RE-S01 r-ser card to get PCIe X16 & PCI expansion slots
Regarding the PCIe X16 slot, The Max. power that could support is 35W PCIe add-on card

Pin	Side B	Side A
1	+12V	PRSNT1
2	+12V	+12V
3	+12V	+12V
4	GND	GND
5	SMCLK	TCK
6	SMDAT	TDI
7	GND	TOD
8	+3.3V	TMS
9	RST	+3.3V
10	+3.3V AUX	+3.3V
11	WAKE	PWRGD
Key Notch		
12	Reserved	GND
13	GND	REFCLK+
14	HSOp	REFCLK-
15	HSOn	GND
16	GND	HSIp
17	PRSNT2	HSIn
18	GND	GND

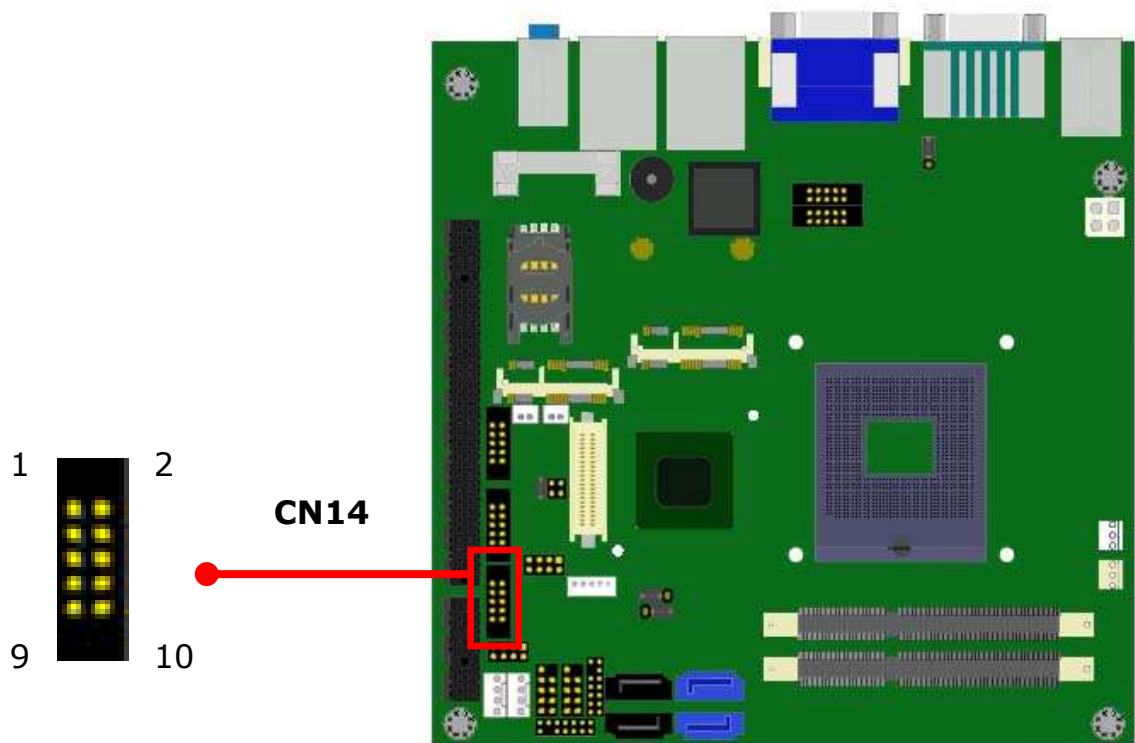


Configuration of MB-8300C with RE-S01 riser card



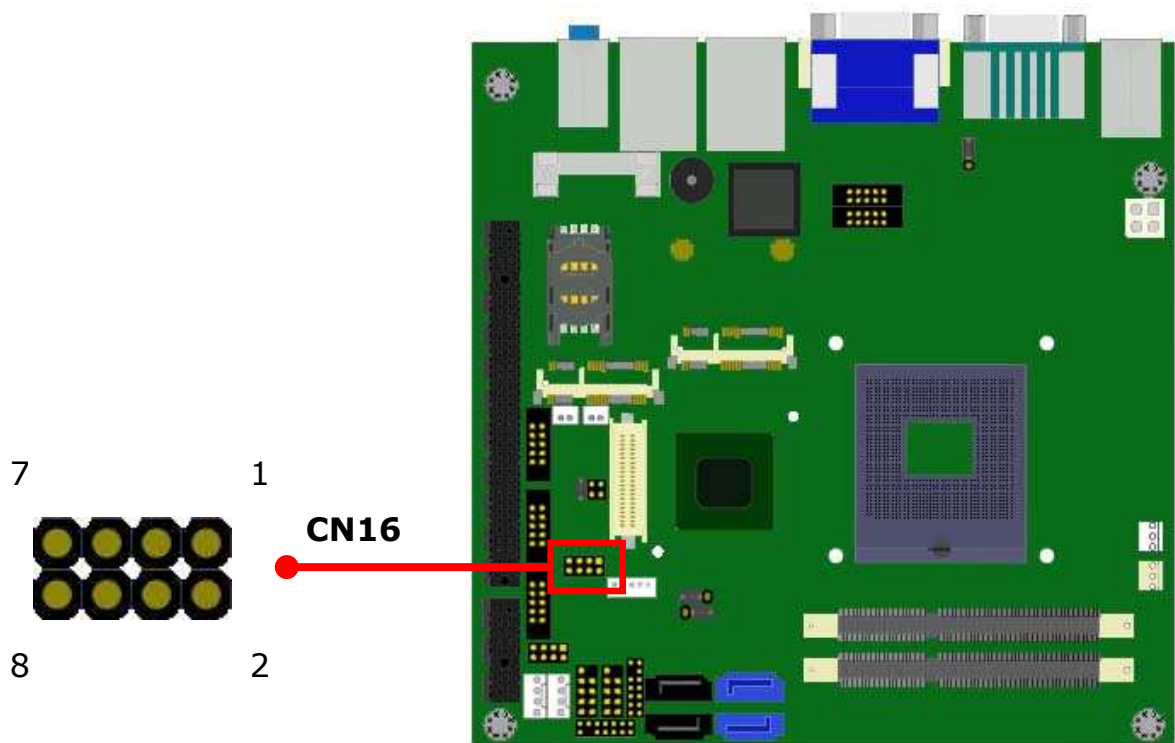
- 2.4.8 : CN14 for COM4 , RS232

CN14 : 2 x 5 header, pitch 2.00 mm			
Pin	Signal	Pin	Signal
1	DCD, Data carrier detect	2	DSR, Data set ready
3	RXD, Receive Data	4	RTS, Request to send
5	TXD, Send Data	6	CTS, Clear to se
7	DTR, Data Terminal Ready	8	RI, Ring indicator
9	GND	10	N/C



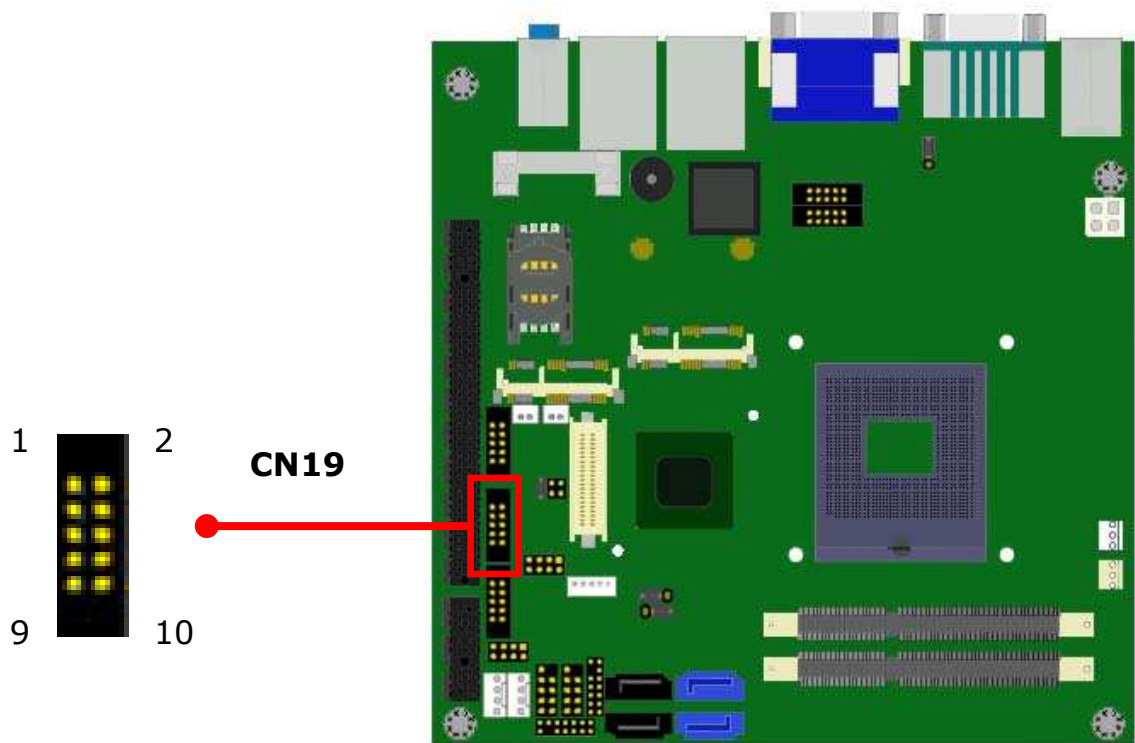
- 2.4.9 : CN16 for front Panel pin header

CN16 : 2 x 4 header , pitch 2.54 mm			
Pin	Signal	Pin	Signal
1	HDD_LED+	2	Power_LED+
3	HDD_LED-	4	GND
5	GND	6	GND
7	RESET+	8	Power_Switch



- 2.4.10 : CN19 for COM3 , RS232

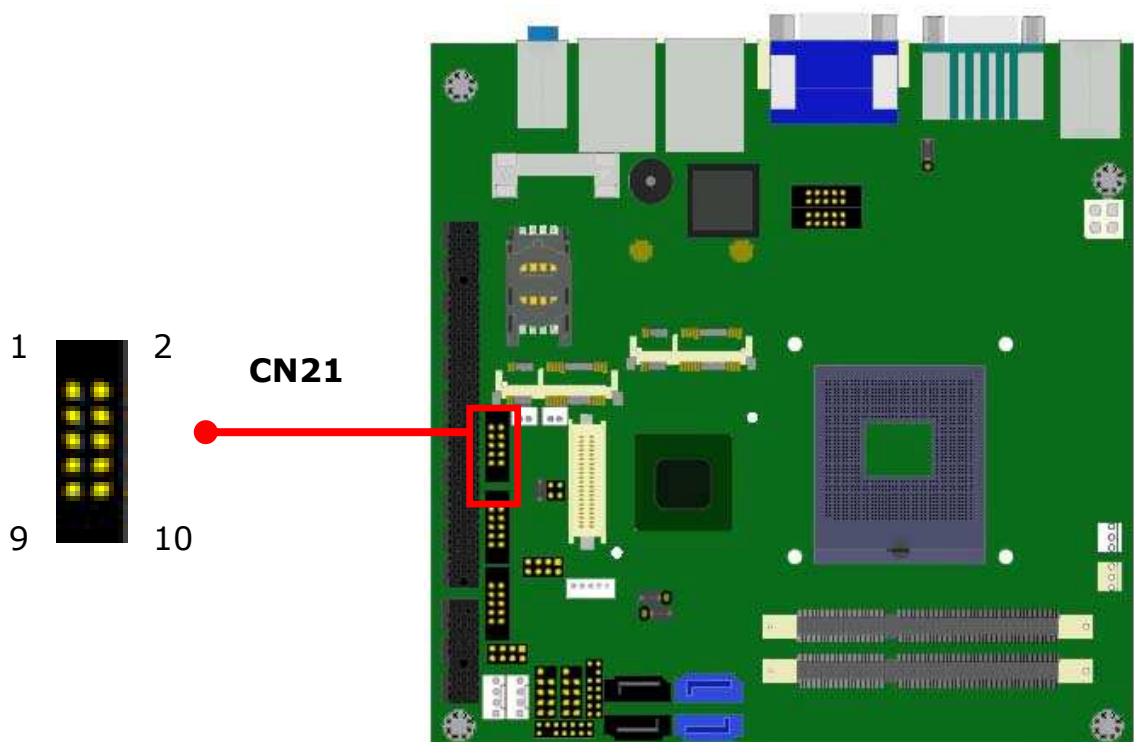
CN19 : 2 x 5 header, pitch 2.00 mm			
Pin	Signal	Pin	Signal
1	DCD, Data carrier detect	2	DSR, Data set ready
3	RXD, Receive Data	4	RTS, Request to send
5	TXD, Send Data	6	CTS, Clear to se
7	DTR, Data Terminal Ready	8	RI, Ring indicator
9	GND	10	N/C



- 2.4.11 : CN21 for COM2 , RS232/422/485

Note: COM2 RS-232/422/485 can be set by BIOS setting. Default is RS-232.

CN21 : 2 x 5 header , Pitch 2.00 mm			
Pin	RS232 mode	RS422 mode	RS485 mode
1	DCD, Data carrier detect	TXD-	TXD-
2	DSR, Data set ready		
3	RXD, Received Data	TXD+	TXD+
4	RTS, Request to send		
5	TXD, Transmitted Data	RXD+	
6	CTS, Clear to sent		
7	DTR, Data terminal ready	RXD-	
8	RI, Ring indicator		
9	GND		
10	N/C		



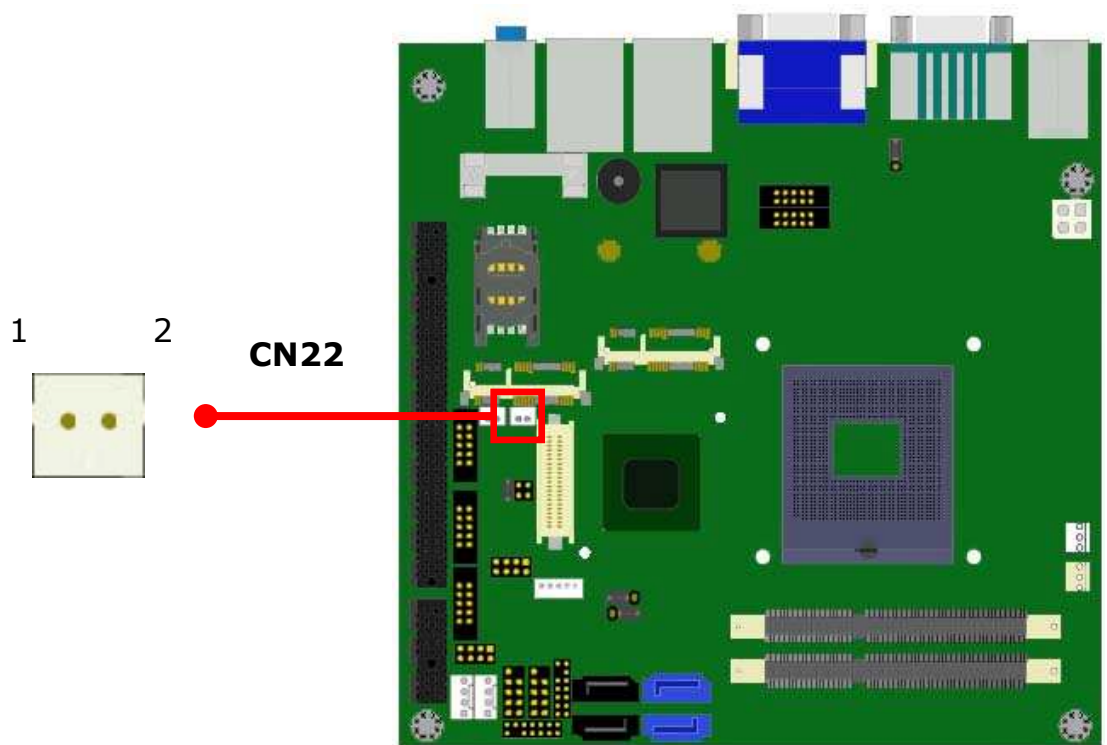
BIOS setting manual :



- 2.4.12 : CN22 for Half-size Mini-PCIE WLAN LED indicator

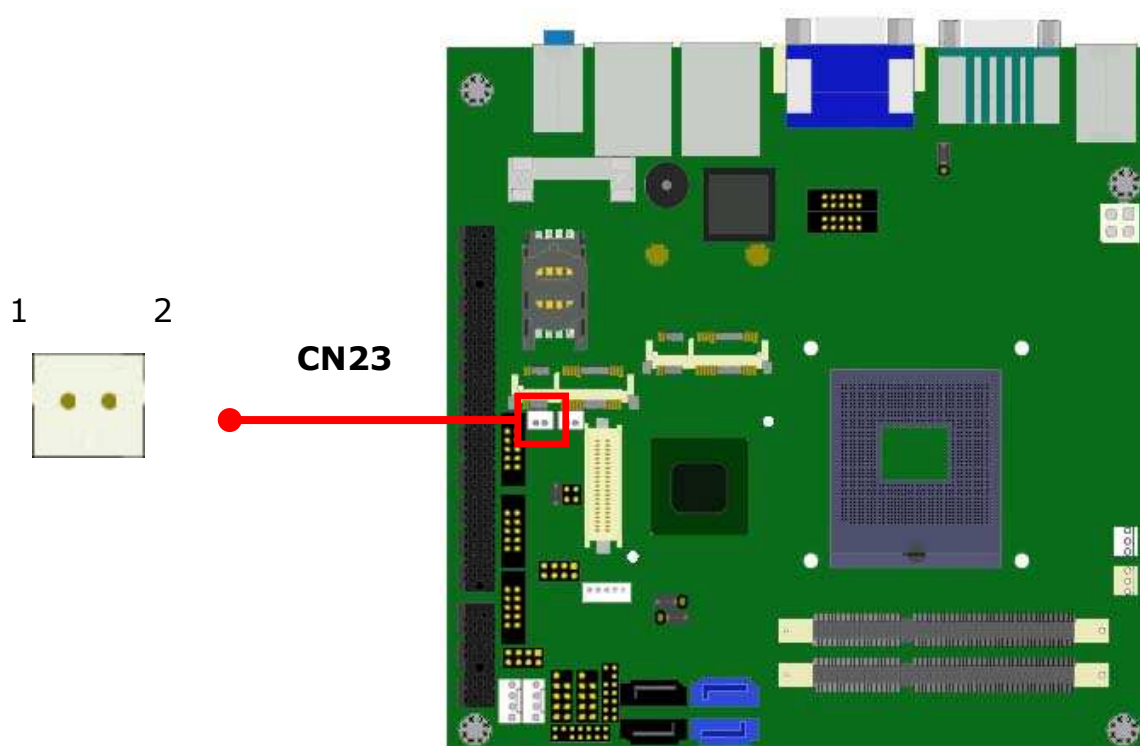
CN22 : 1 x 2 , 2-pin wafer			
Pin	Signal	Pin	Signal
1	LED_WLAN	2	+3.3V

Note: Half-size Mini-PCIE card could support SATA or PCIe signal. The default setting is SATA signal. Hardware modification required if want to change signal from SATA to PCIe.



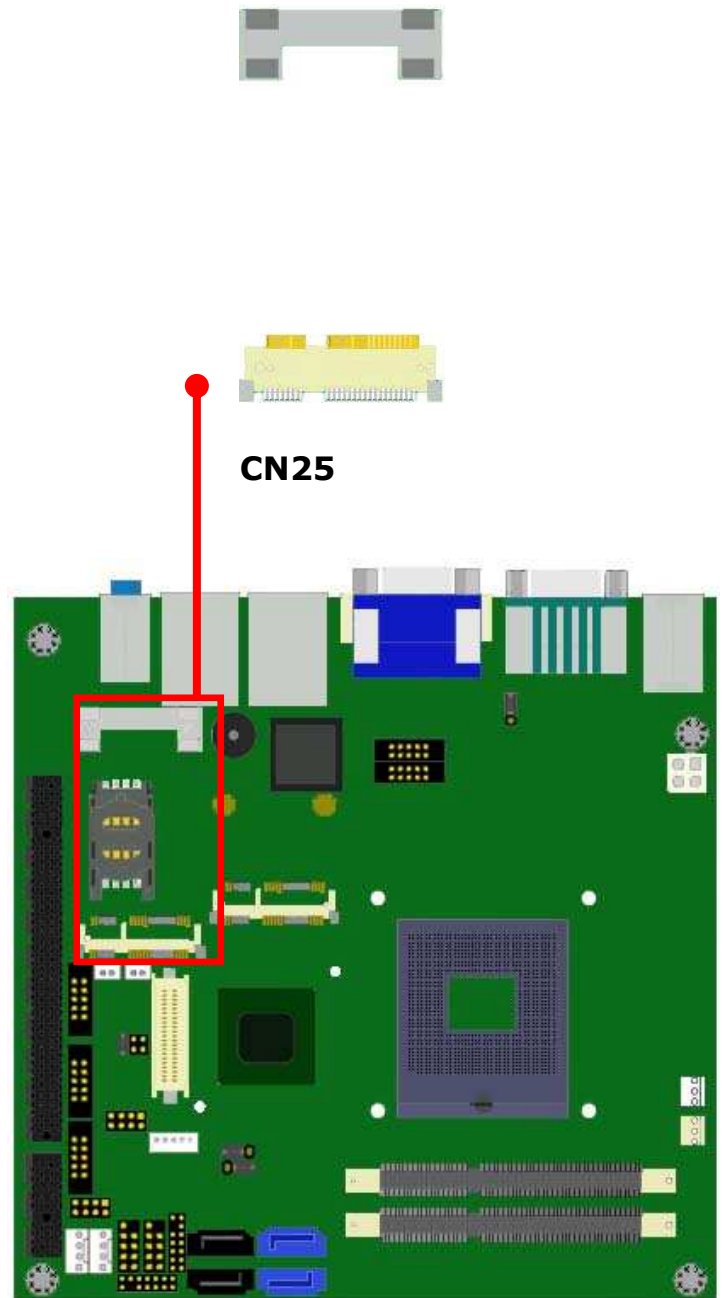
- 2.4.13 : CN23 for Full-size Mini-PCIE WLAN LED indicator

CN23 : 1 x 2 , 2-pin wafer			
Pin	Signal	Pin	Signal
1	LED_WLAN	2	+3.3V



- 2.4.14 : CN25 for Full-size Mini-PCIe socket

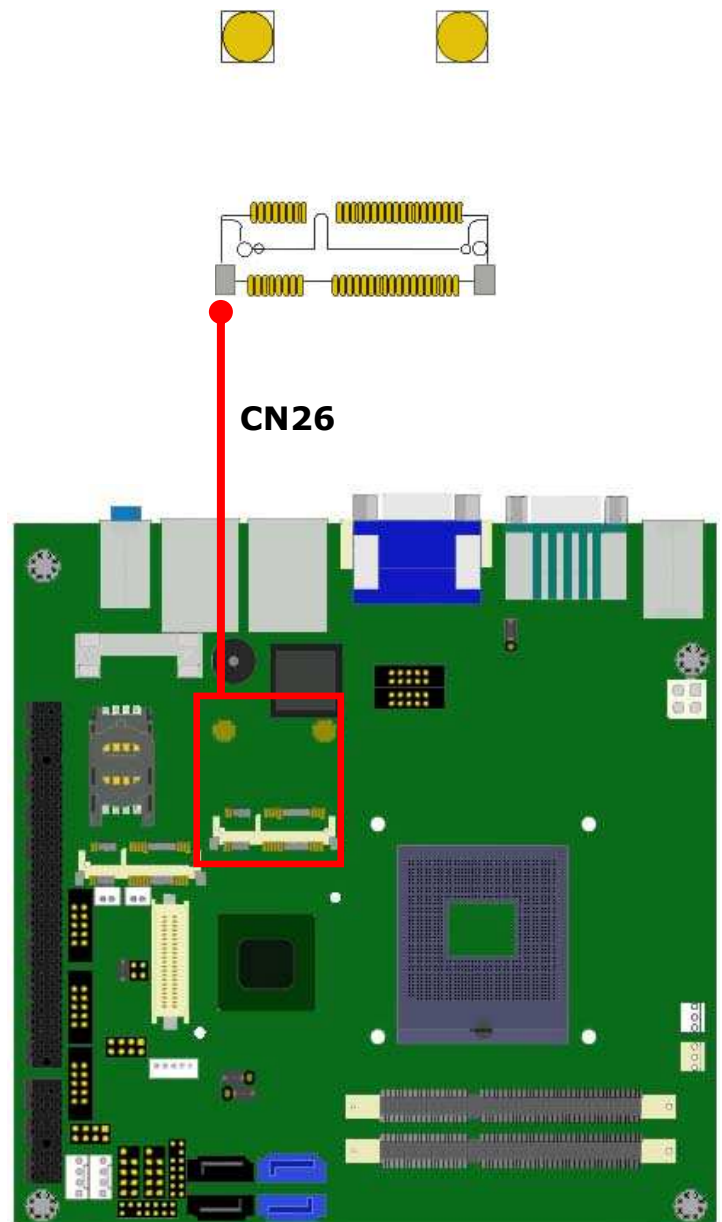
Pin	Signal	Pin	Signal
1	WAKE	27	GND
2	+3.3V AUX	28	+1.5V
3	N/C	29	GND
4	GND	30	SMBCLK
5	N/C	31	PETN0
6	+1.5V	32	SMBDATA
7	CLKREQ	33	PETP0
8	UIM_PWR	34	GND
9	GND	35	GND
10	UIM_DATA	36	USB_D-
11	REFCLK-	37	GND
12	UIM_CLK	38	USB_D+
13	REFCLK+	39	+3.3V AUX
14	UIM_RESET	40	GND
15	GND	41	+3.3V AUX
16	UIM_VPP	42	LED_WWAN
17	UIM_C8	43	GND
18	GND	44	LED_WLAN
19	UIM_C4	45	N/C
20	W_Disable	46	N/C
21	GND	47	N/C
22	PERST	48	+1.5V
23	PERN0	49	N/C
24	+3.3V AUX	50	GND
25	PERP0	51	N/C
26	GND	52	+3.3V AUX



- 2.4.15 : CN26 for Half-size Mini-PCIE socket.

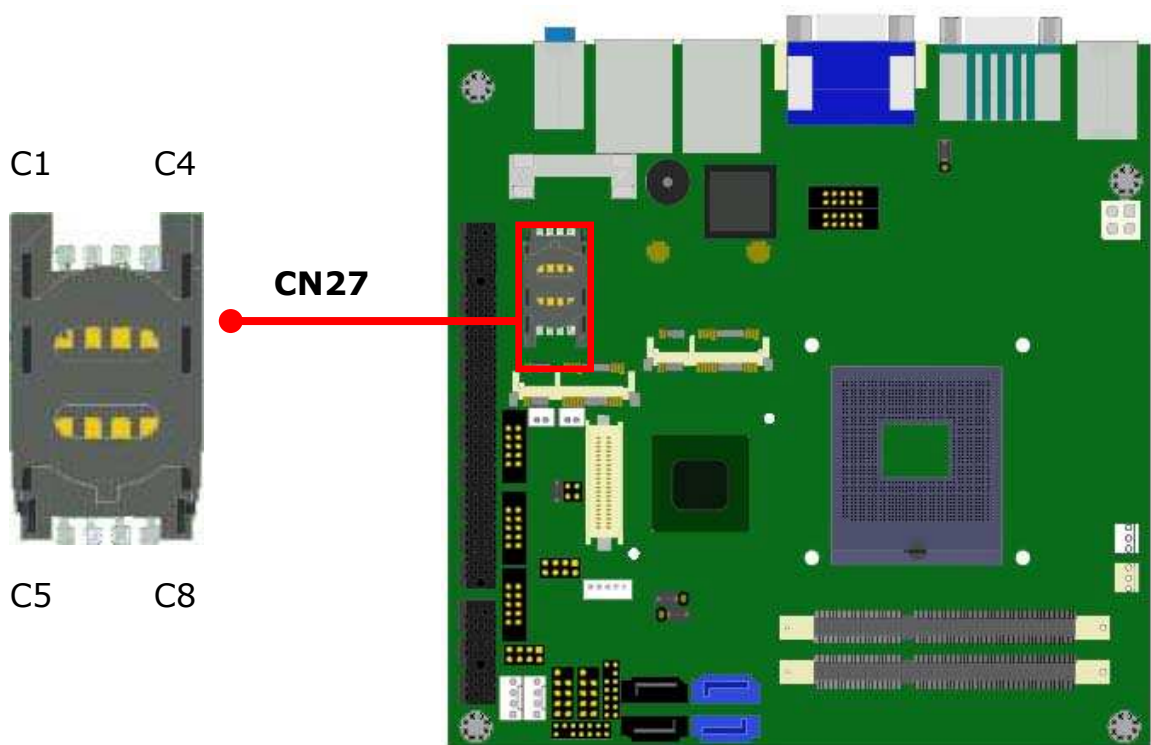
Note: Half-size Mini-PCIE card could support SATA or PCIe signal. The default setting is SATA signal. Hardware modifications required if want to change signal from SATA to PCIe.

Pin	Signal	Pin	Signal
1	WAKE	27	GND
2	+3.3V AUX	28	+1.5V
3	Reserved	29	GND
4	GND	30	SMBCLK
5	Reserved	31	PETN0
6	+1.5V	32	SMBDATA
7	CLKREQ	33	PETP0
8	Reserved	34	GND
9	GND	35	GND
10	UIM_DATA	36	USB_D-
11	REFCLK-	37	Reserved
12	Reserved	38	USB_D+
13	REFCLK+	39	Reserved
14	Reserved	40	GND
15	GND	41	Reserved
16	Reserved	42	LED_WWAN
17	Reserved	43	Reserved
18	GND	44	LED_WLAN
19	Reserved	45	Reserved
20	Reserved	46	N/C
21	GND	47	Reserved
22	PERST	48	+1.5V
23	PERN0	49	Reserved
24	+3.3V AUX	50	GND
25	PERP0	51	Reserved
26	GND	52	+3.3V



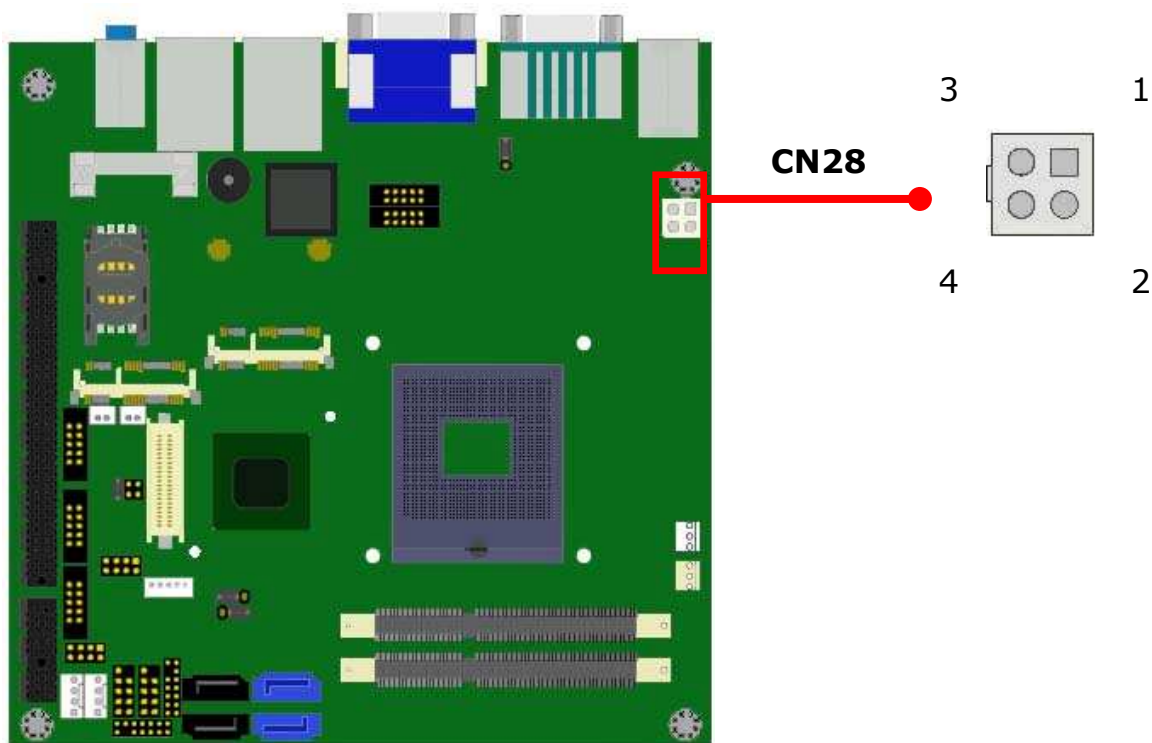
- 2.4.16 : CN27 for SIM holder

CN27 : SIM card holder			
Pin	Signal	Pin	Signal
C1	VCC	C2	RESET
C3	CLOCK	C4	Reserved
C5	GND	C6	VPP
C7	I/O	C8	Reserved



- 2.4.17 : CN28 for internal 4-pin power input

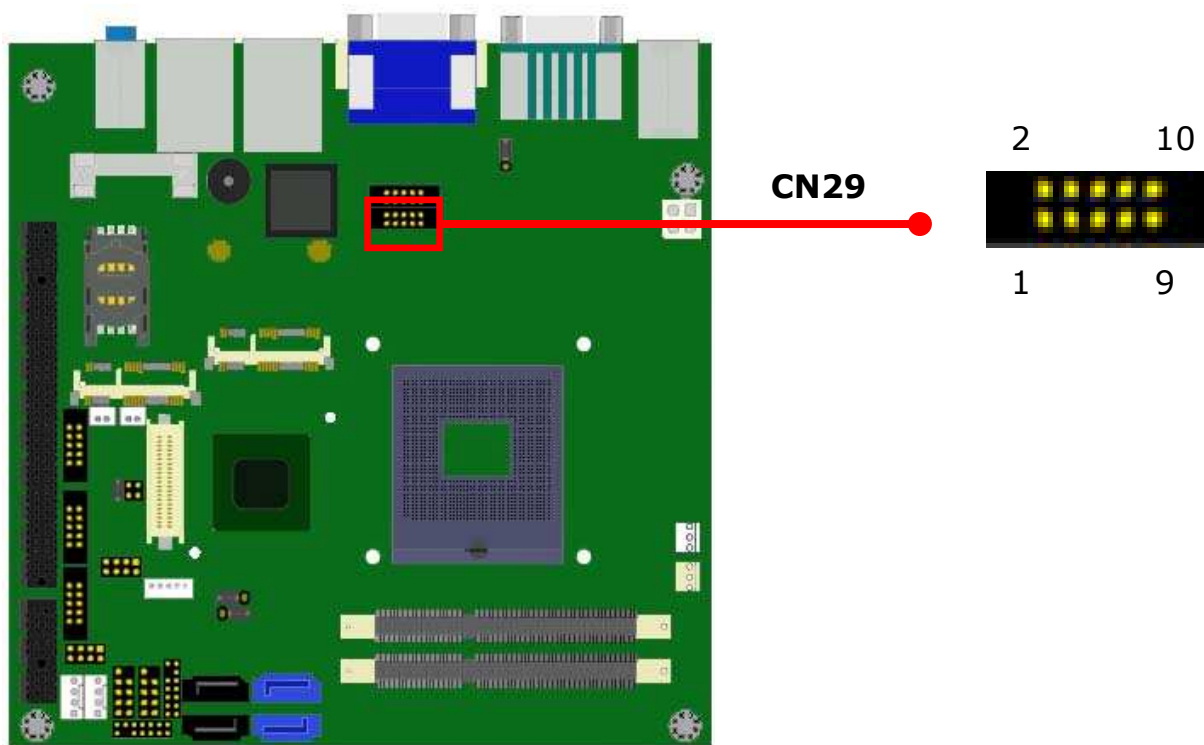
CN19 : ATX 2 x 2			
Pin	Signal	Pin	Signal
1	GND	2	GND
3	8V ~ 32V DC input	4	8V ~ 32V DC input



- 2.4.18 : CN29 for 4-ch Audio input

Note: This function for MB-8300B model only

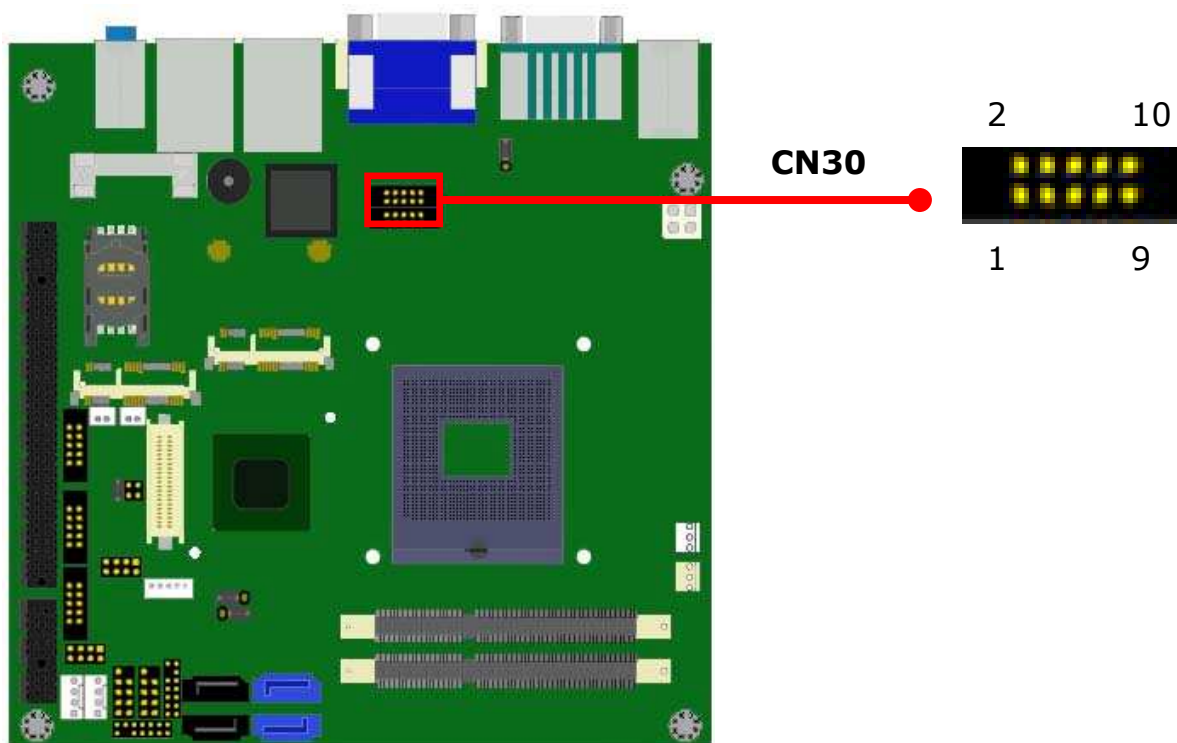
CN29 : 2 x 5 header, pitch 2.00 mm			
Pin	Signal	Pin	Signal
1	GND	2	Audio_IN_1
3	GND	4	Audio_IN_2
5	GND	6	Audio_IN_3
7	GND	8	Audio_IN_4
9	GND	10	N/C



- 2.4.19 : CN30 for 4-ch Video input

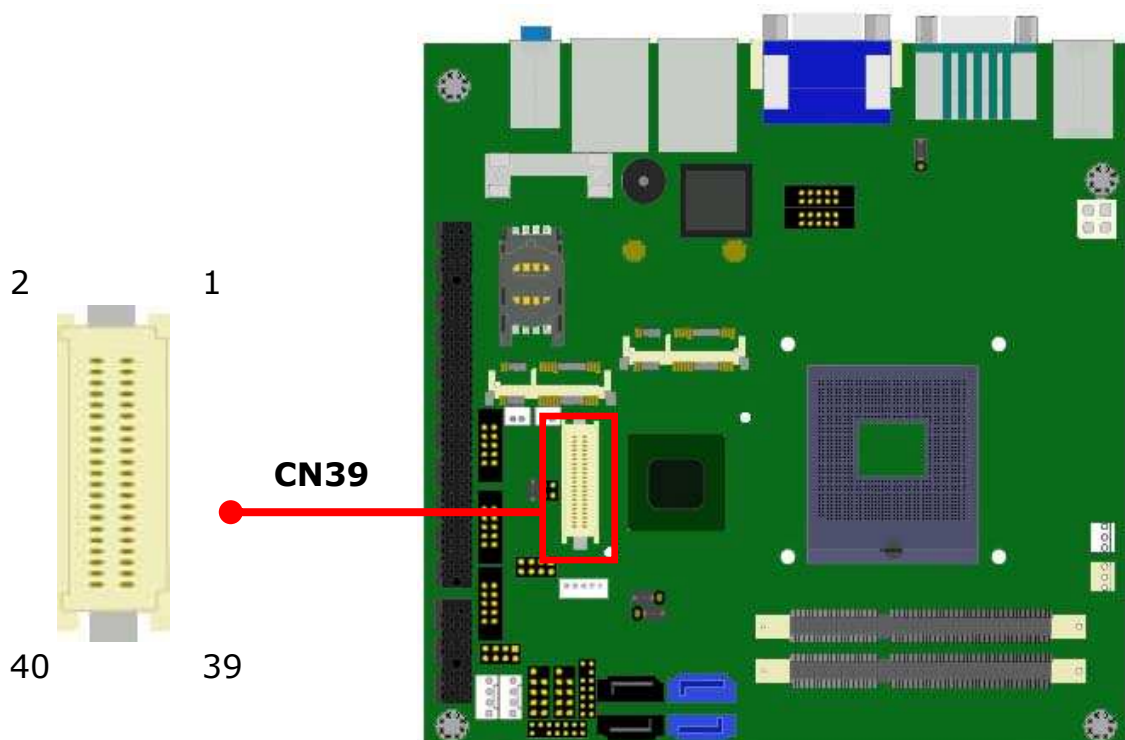
Note: This function for MB-8300B model only

CN29 : 2 x 5 header, pitch 2.00 mm			
Pin	Signal	Pin	Signal
1	GND	2	Video_IN_1
3	GND	4	Video_IN_2
5	GND	6	Video_IN_3
7	GND	8	Video_IN_4
9	GND	10	N/C

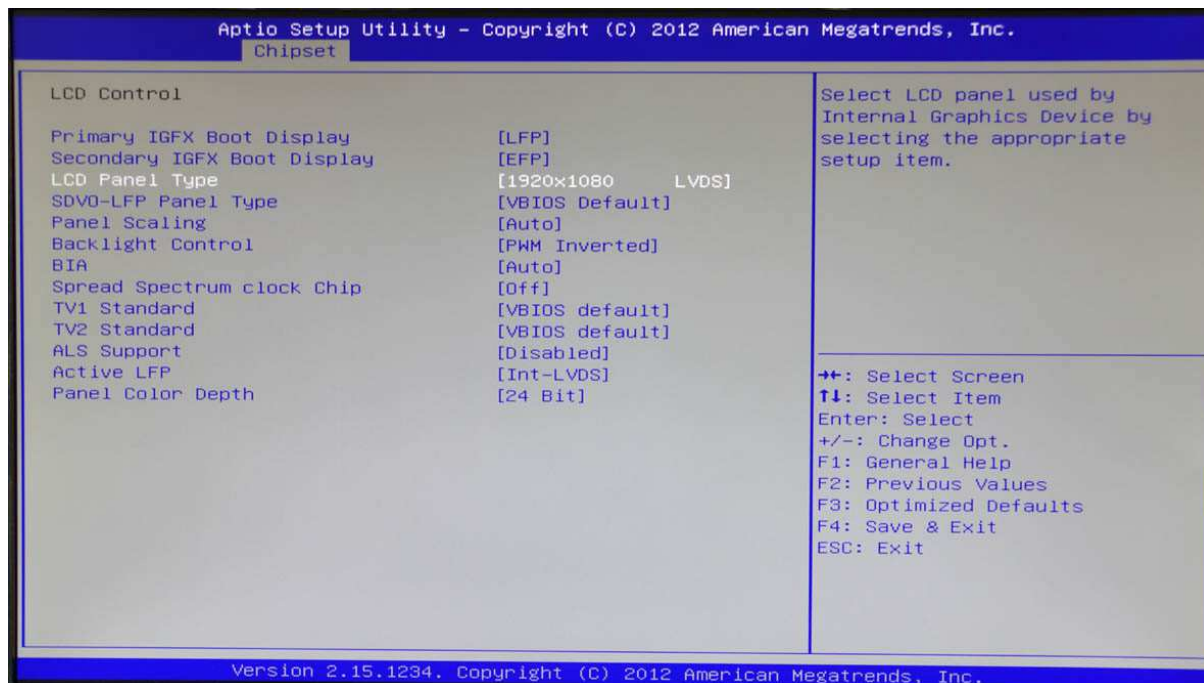


- 2.4.20 : CN39 for 24-bit Dual Channel LVDS

CN39 : connector type : DF13A-40DP-1.25V					
Pin	Signal	Pin	Signal	Pin	Signal
1	LVDS_VCC	15	LVDSA_1+	29	GND
2	LVDS_VCC	16	LVDSB_1+	30	GND
3	LVDS_VCC	17	GND	31	DDC_Clock
4	LVDS_VCC	18	GND	32	DDC_Data
5	GND	19	LVDSA_2-	33	GND
6	GND	20	LVDSB_2-	34	GND
7	LVDSA_0-	21	LVDSA_2+	35	LVDSA_3-
8	LVDSB_0-	22	LVDSB_2+	36	LVDSB_3-
9	LVDSA_0+	23	GND	37	LVDSA_3+
10	LVDSB_0+	24	GND	38	LVDSB_3+
11	GND	25	LVDSA_Clock-	39	SMB_Clock
12	GND	26	LVDSB_Clock-	40	SMB_Data
13	LVDSA_1-	27	LVDSA_Clock+		
14	LVDSB_1-	28	LVDSB_Clock+		

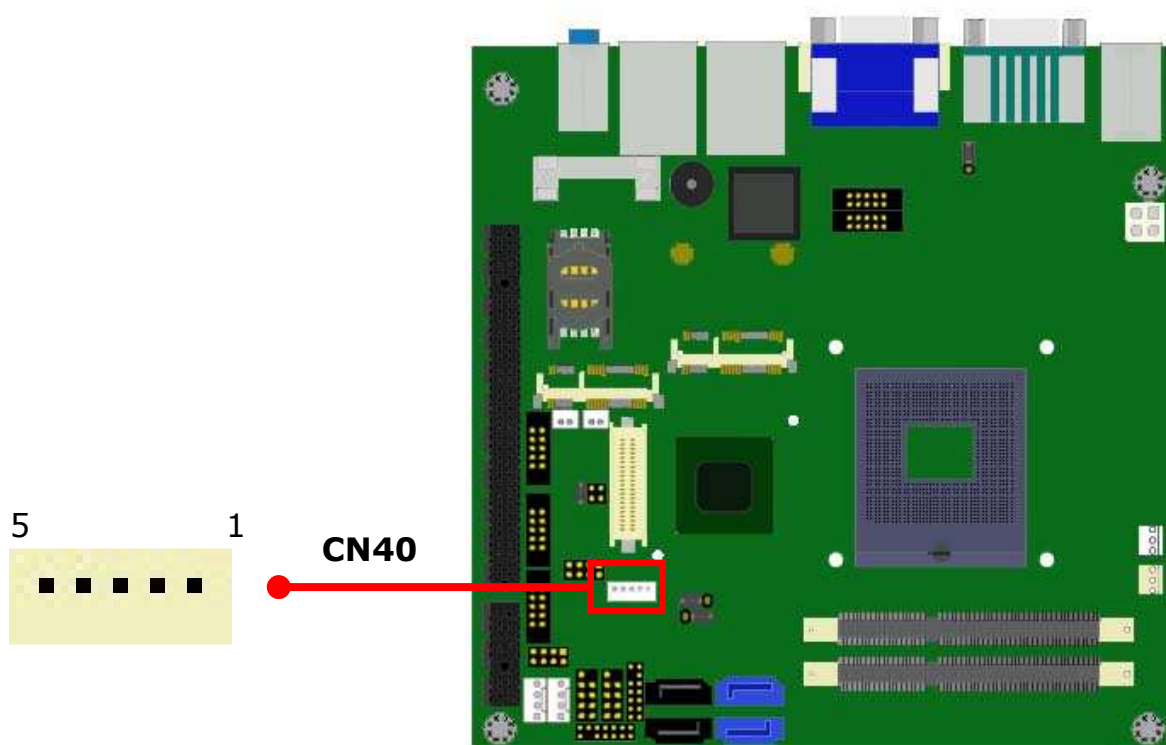


BIOS setting for LVDS Resolution:



- 2.4.21 : CN40 for LVDS backlight

CN3: 1 x 5 wafer , Pitch : Pitch 2.0 mm			
Pin	Signal	Pin	Signal
1	+12V	2	GND
3	Backlight Enable	4	Backlight Control
5	+5V		

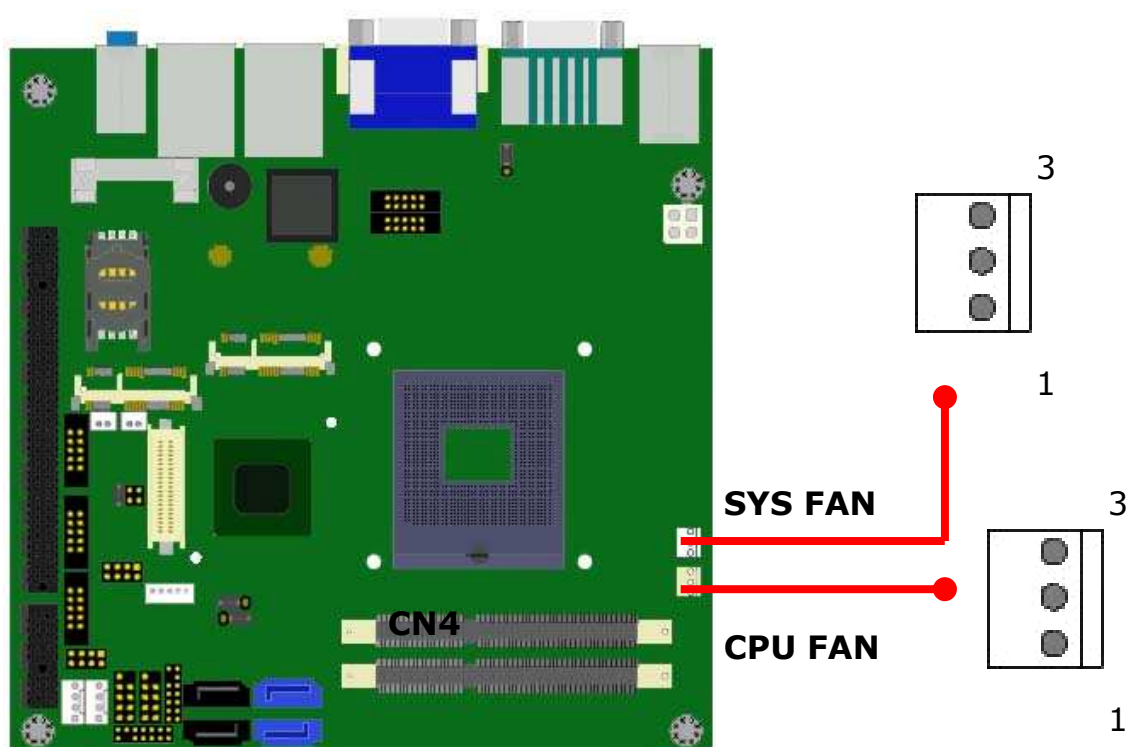


- 2.4.22 : SYS FAN for system Fan connector

CN4 : 1 x 3 wafer			
Pin	Signal	Pin	Signal
1	GND	2	+12V
3	Sense		

- 2.4.5 : CPU FAN for CPU Fan connector

CN5: 1 x 3 wafer			
Pin	Signal	Pin	Signal
1	GND	2	+12V
3	Sense		



3.1 GPIO Sample Program for DOS environment

```
//AEWIN GPIO Program for MB-8300 (DOS Version)
#include <stdio.h>
#include <dos.h>
#include <inlines/pc.h>

#define index_port 0x2E                //Super IO Index Port Address
#define data_port 0x2F                //Super IO Data Port Address

void Enter_sio_config();
void Exit_sio_config();
void help();

int main(int argc, char *argv[])
{
    int val, GPIO_SEL;

    if (argc!=2){
        help();
        return;
    }

    Enter_sio_config();
    if(strcmp(argv[1], "-r") == 0){
        printf("Read GP80~GP83\n");
        outportb(index_port, 0x8A);
        val = inportb(data_port);

        if ((val&0x01) == 0x00)
            printf("GPI80 -> Low\n");
        else
            printf("GPI80 -> High\n");

        if ((val&0x02) == 0x00)
            printf("GPI81 -> Low\n");
```

```
else
    printf("GPI81 -> High\n");

if ((val&0x04) == 0x00)
    printf("GPI82 -> Low\n");
else
    printf("GPI82 -> High\n");

if ((val&0x08) == 0x00)
    printf("GPI83 -> Low\n");
else
    printf("GPI83 -> High\n");
}
else if(strcmp(argv[1], "-84h") == 0){
    printf("(GPO84 High >> 3.3v)\n");
    GPIO_SEL = 0x10;
    outportb(index_port, 0x89);
    val = inportb(data_port)|GPIO_SEL;
    outportb(data_port, val);
}
else if(strcmp(argv[1], "-84l") == 0){
    printf("(GPO84 Low >> 0.0v)\n");
    GPIO_SEL = 0x10;
    outportb(index_port, 0x89);
    val = inportb(data_port)&(~GPIO_SEL);
    outportb(data_port, val);
}

else if(strcmp(argv[1], "-85h") == 0){
    printf("(GPO85 High >> 3.3v)\n");
    GPIO_SEL = 0x20;
    outportb(index_port, 0x89);
    val = inportb(data_port)|GPIO_SEL;
    outportb(data_port, val);
}
else if(strcmp(argv[1], "-85l") == 0){
    printf("(GPO85 Low >> 0.0v)\n");
```

```
GPIO_SEL = 0x20;
outportb(index_port, 0x89);
val = inportb(data_port)&(~GPIO_SEL);
outportb(data_port, val);
}

else if(strcmp(argv[1], "-86h") == 0){
    printf("(GPO86 High >> 3.3v)\n");
    GPIO_SEL = 0x40;
    outportb(index_port, 0x89);
    val = inportb(data_port)|GPIO_SEL;
    outportb(data_port, val);
}

else if(strcmp(argv[1], "-86l") == 0){
    printf("(GPO86 Low >> 0.0v)\n");
    GPIO_SEL = 0x40;
    outportb(index_port, 0x89);
    val = inportb(data_port)&(~GPIO_SEL);
    outportb(data_port, val);
}

else if(strcmp(argv[1], "-87h") == 0){
    printf("(GPO87 High >> 3.3v)\n");
    GPIO_SEL = 0x80;
    outportb(index_port, 0x89);
    val = inportb(data_port)|GPIO_SEL;
    outportb(data_port, val);
}

else if(strcmp(argv[1], "-87l") == 0){
    printf("(GPO87 Low >> 0.0v)\n");
    GPIO_SEL = 0x80;
    outportb(index_port, 0x89);
    val = inportb(data_port)&(~GPIO_SEL);
    outportb(data_port, val);
}

else{
    help();
}
```

```
Exit_sio_config();
return;
}

void help()
{
    printf("AEWIN GPI Program\n");
    printf("Usage:\n");
    printf("GPIO -r (Read F81866 GP80~GP83)\n");
    printf("GPIO -xh (x=84~87, and set GPx to high)\n");
    printf("GPIO -xl (x=84~87, and set GPx to low )\n");
    printf("ex:                                \n");
    printf("GPIO -84h (Set GP84 to high)\n");
}

void Enter_sio_config()
{
    outportb(index_port, 0x87);
    delay(1);           //delay some time
    outportb(index_port, 0x87);
    outportb(index_port, 0x07);
    outportb(data_port, 0x06);
}

void Exit_sio_config()
{
    outportb(index_port, 0xAA);
}
```

3.2 Watchdog timer Sample Program for DOS environment

```
//AEWIN Watch dog program for MB-8300(Dos Version)
#include <stdio.h>
#include <string.h>
#include <dos.h>
#include <stdlib.h>
#include <inlines/pc.h>

#define index_port 0x2E                //Super IO Index port address
#define data_port 0x2F                //Super IO Data port address

void Enter_sio_config();
void Exit_sio_config();
void help();

int main(int argc, char *argv[])
{
    int data_rw8, time;

    if (argc<2){
        help();
        return;
    }

    if(strcmp(argv[1], "-s") == 0){
        //Show Watchdog Register Settings
        Enter_sio_config();
        outportb(index_port, 0xF5);
        data_rw8 = inportb(data_port)&0x08;
        if(data_rw8 == 0x00){
            //second mode
            outportb(index_port, 0xF6);
            data_rw8 = inportb(data_port);
            printf("Second mode: %d second\n", data_rw8);
        }
    }
}
```

```
else{
    //minute mode
    outportb(index_port, 0xF6);
    data_rw8 = inportb(data_port);
    printf("Minute mode: %d minute\n", data_rw8);
}
}

else if(strcmp(argv[1], "-t") == 0 ){
    //Set Time-out Value
    if(argv[2] == NULL){
        help();
        return;
    }
    else{
        Enter_sio_config();
        outportb(index_port, 0xF6);
        outportb(data_port , 0x00);           //Clear Status
        outportb(index_port, 0xF5);
        data_rw8 = 0x32;
        outportb(data_port, data_rw8);
        sscanf(argv[2], "%d", &time);
        outportb(index_port, 0xF6);
        outportb(data_port,  time);
        if(time==0){
            //Disable count
            outportb(index_port, 0xF5);
            data_rw8 = inportb(data_port)&0xCF;
            outportb(data_port , data_rw8);
        }
        else{
            outportb(index_port, 0xFA);
            outportb(data_port , 0x01);           //Enable reset function
        }
        printf("Watchdog Timer will count down for %d second(s)\n", time);
    }
}

else if(strcmp(argv[1], "-m") == 0 ){
```

```
//Set Time-out Value
if(argv[2] == NULL){
    help();
    return;
}
else{
    Enter_sio_config();
    outportb(index_port, 0xF6);
    outportb(data_port , 0x00);           //Clear Status
    outportb(index_port, 0xF5);
    data_rw8 = 0x32;
    data_rw8 |= 0x08;
    outportb(data_port, data_rw8);
    sscanf(argv[2], "%d", &time);
    outportb(index_port, 0xF6);
    outportb(data_port,  time);
    if(time==0){
        //Disable count
        outportb(index_port, 0xF5);
        data_rw8 = inportb(data_port)&0xCF;
        outportb(data_port , data_rw8);
    }
    else{
        outportb(index_port, 0xFA);
        outportb(data_port , 0x01);       //Enable reset function
    }
    printf("Watchdog Timer will count down for %d minute(s)\n", time);
}
}
Exit_sio_config();
return;
}

void Enter_sio_config()                //Enter W83627EHF Configuration
{
    outportb(index_port, 0x87);
    delay(1);                          //Delay some time
```

```
    outportb(index_port, 0x87);
    outportb(index_port, 0x07);           //Super IO Selct Bank Register Number
    outportb(data_port , 0x07);         //Select logical device 7
}

void Exit_sio_config()                  //Exit W83627EHF Configuration
{
    outportb(index_port, 0xAA);
}

void help()
{
    printf("AEWIN Watchdog Timer Program\n");
    printf("Usage: WDT -s      (Show Watchdog Register Settings)\n");
    printf("Usage: WDT -t xxx (Set Time-out Value)\n");
    printf("          xxx = 1 ~ 255 seconds\n");
    printf("          xxx = 0 : Time-out Disable \n");
    printf("Usage: WDT -m xxx (Set Time-out Value)\n");
    printf("          xxx = 1 ~ 255 minutes\n");
    printf("          xxx = 0 : Time-out Disable \n");
}
```