ELAN MICROELECTRONICS CORP.

PMePV6000
user
manual
FOR
ePV Series

Version 1.5
Date:2005/01/24

ICE TOP VIEW

68mm

95mm
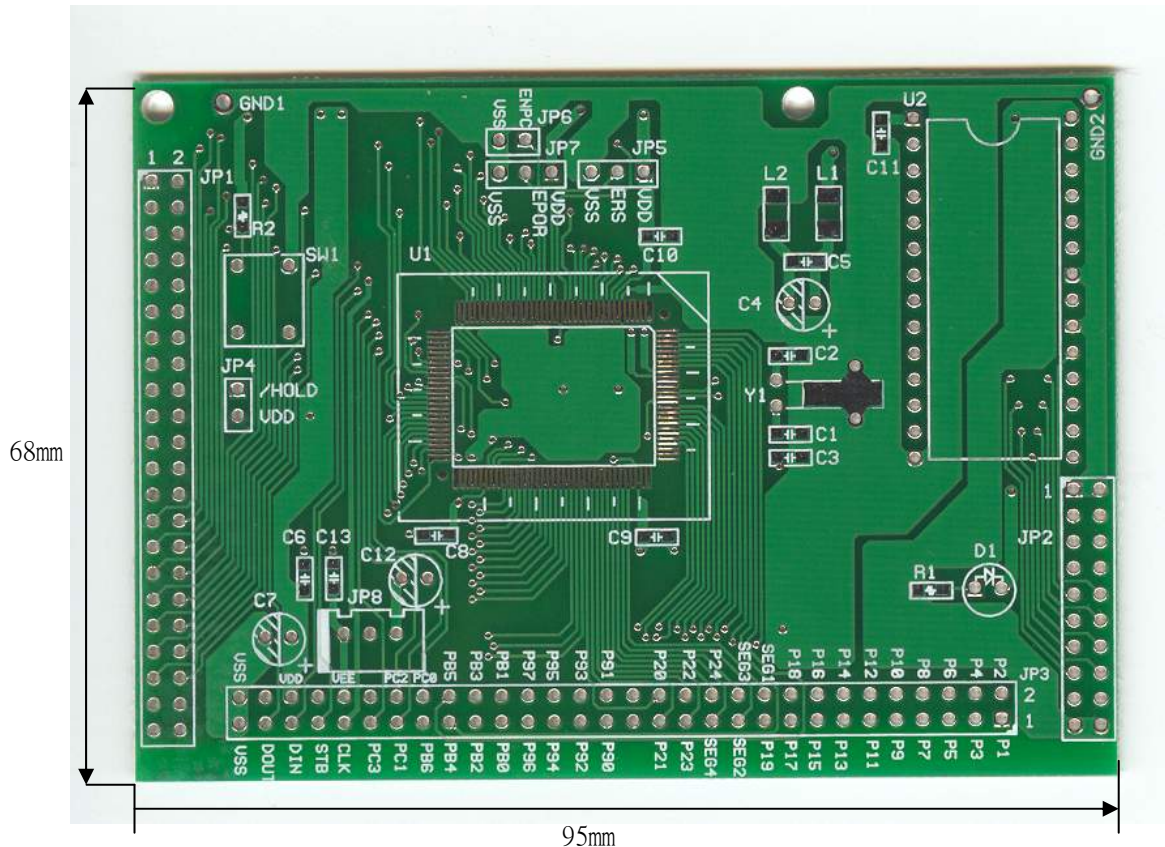
1. Installation must be Authorized by EMC.
2. Power on the E8 ICE before executing the control software, or an error message due to lack of hardware will appear.
3. E8 - ICE uses the printer port to communicate with host PC. If a user wants to use the printer, it is recommended to install another printer port.
4. Power on switch located on the left side of ICE box. By switching left or right side to turn off or turn on the ICE power.
5. Printer port and the adapter locate on the right side. The ICE connects to personal computer by printer port. The adapter connects to the power. Be sure turn power off of ICE before plug in the adapter for avoiding over current which may be burn the chip off.
6. On the top sight, we have two connections and one ePVZ6300 Romless chip and some switches. The JP1 and the JP2 connect to the second layer of ICE for the code instruction accessing. The JP3 and JP4 are more important for ICE user. They are the ePVZ6300 control signal and I/O ports. ICE user can connect these connections to user's application board. The connections are list in Table 1.1.
7. The U1 is ePVZ6300 Romless chip. The U2 is a 32K bytes ROM for on board testing one day by piggyback. If user have developed application code by this ICE. User can use PIGGYBACK (user can buy from EMC) to connect to user's application board with 32K bytes EPROM (27c256). To verify user's program.
8. The D1 is a LED that indicate power is on or off..
9. X1 is 32.768k crystal. C1 and C2 is capacitor 27p. C3 is PLL capacitor.(0.01u .. 0.047u)
10. ICE setup: (1) JP4 open (2) JP5 connect to VDD (3) JP6 open (4)JP7 connect to VDD or GND
11. PIGGYBACK setup: (1)JP5 connect to GND (2)JP4 short   (3)JP8 exterior power connect (4)JP7 connect to VDD or GND (5) U2=> 32k bytes EPROM (27C256). (User can use file "*.MIX" which generate by WICE for masking EPROM. The *.mix file is a binary file and addressing from zero.)
12. Run WICE software to enter ICE environment.
13. Power up the VDD pin of the simulator first, and then VEE pin.
14. VEE pin must connect to HV or short to GND .

Table 1: ICE Connection

	pin		pin		pin	
JP3	1	GR1/P1	11	GR11/P11/SG18	21	SG2/KS2
	2	GR2/P2	12	GR12/P12/SG17	22	SG3/KS3
	3	GR3/P3	13	GR13/P13/SG16	23	SG4/KS4
	4	GR4/P4	14	GR14/P14/SG15	24	P24/SG5/KS5
	5	GR5/P5	15	GR15/P15/SG14	25	P23/SG6/KS6
	6	GR6/P6	16	GR16/P16/SG13	26	P22/SG7/KS7
	7	GR7/P7	17	GR17/P17/SG12/KS12	27	P21/SG8/KS8
	8	GR8/P8	18	GR18/P18/SG11/KS11	28	P20/SG9/KS9
	9	GR9/P9/SG20	19	GR19/P19/SG10/KS10	29	N.C
	10	GR10/P10/SG19	20	SG1/KS1	30	N.C

	pin		pin		pin	
JP3	31	GPIO9,0	41	GPIOB,2	51	GPIOC,5/CLK
	32	GPIO9,1	42	GPIOB,3	52	VEE
	33	GPIO9,2	43	GPIOB,4	53	GPIOC,4/STB
	34	GPIO9,3	44	GPIOB,5	54	N.C
	35	GPIO9,4	45	GPIOB,6	55	GPIOC,7/DIN
	36	GPIO9,5	46	GPIOC,0	56	VDD
	37	GPIO9,6	47	GPIOC,1	57	GPIOC,6/Dout
	38	GPIO9,7	48	GPIOC,2	58	N.C
	39	GPIOB,0	49	GPIOC,3	59	VSS
	40	GPIOB,1	50	N.C	60	VSS