

BSA-3800R Service Manual

I . Introduction of the Function

BSA-3800R is Digital recorder & player, it is the representative product in DVR series of Wanlida. It can recorder the signal of video tape recorder or of vidicon or of TV to be DVD disc. It supports recording DVD+R/RW discs. It composites two groups of video input, one group of S-video, TV signal etc. Real time clock set , it can support recording programs by presenting time, channel, times of channel and quality level. For example, one DVD+R/W disc of 4.7G will cost 1 hour, 2hours, 3 hours and 4 hours differently with different recording quality level.

BSA-3800R composites video, S-video, RCA video output, 5.1 CH, Optical, Coaxial audio output. Built-in all range of TV frequency Tuner.

II . Schematic Diagram

BSA-3800R adopts NEC recorder chip ND-1100A, based on ATAPI jack. The whole system based on ES7010, ES6128 decoder chip, input origin signal and TV signal then transfer to digital signal by SAA7114 decoder. The digital signal will be recorder DVD signal after dealing by ES7010 encoder. The following is the schematic diagram of mainboard:

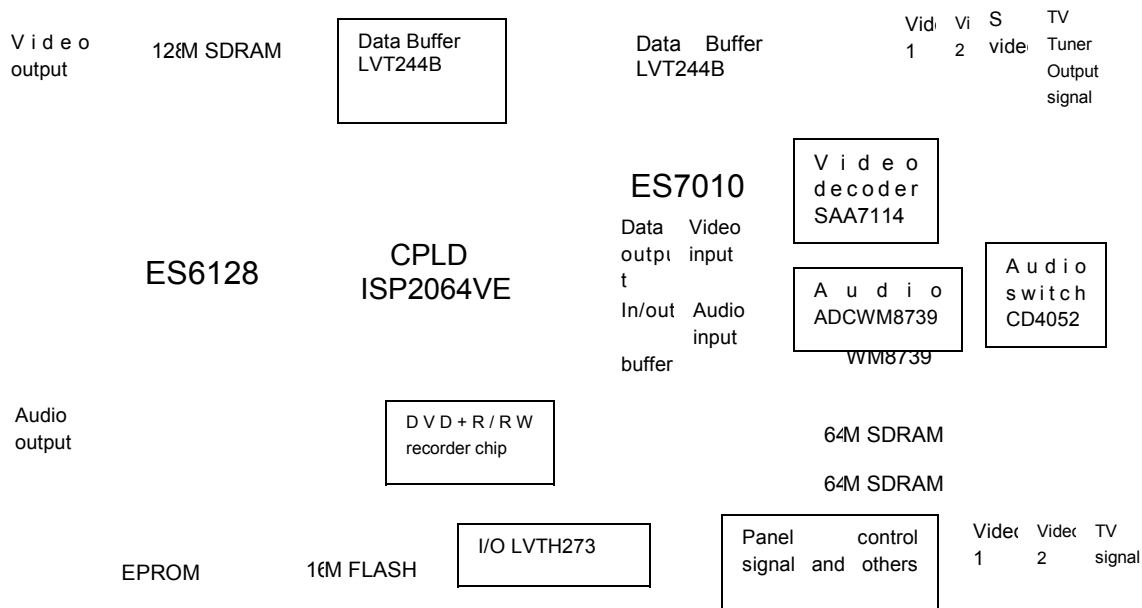
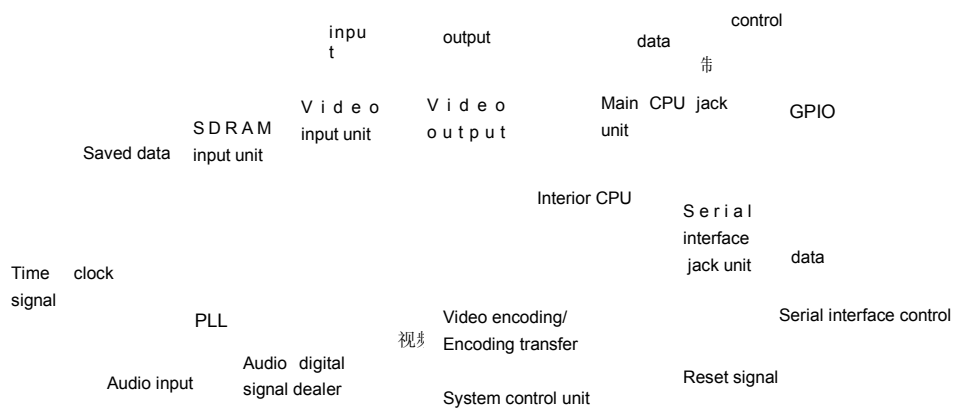


Figure 1、 BSA-3800R mainboard schematic diagram

Send one group of Video 1, or Video 2, or S-video , or TV video signal to SAA7114 video decoder. SAA7114 is apply to the control aspect of video graphic array. It has six groups of separated inputs. After interior software controlled and chosen the original signal, video AD will be transferred to digital signal and output from 81-94 pin. The digital video signal will be sent to ES7010 and ES6128 through the data buffer LVT244B. When it enter into the outside source, digital video signal will be directly outputted the video signal by ES6128 decoder. This way is called “direct signal”, it will shorten the time of transferring between signal output and video output, and it can avoid the complicated step of encoding and decoding to make the switch speed of TV

channel be faster. When it recording, the digital video signal will be recorded into DVD+R/RW disc by ES7010 decoder, and then the decoded signal will be sent to ES6128 decoder to output video signal. Three groups of audio signal will be chosen one group to output at 3 and 13 pins through 4 groups multi output CD4052. The chosen audio signal will be transferred into digital audio signal by stereo audio ADC that integrated with WM8739. The digital audio signal will be sent to ES7010 decoder. ES7010 is a MPEG2 audio&video encoding chip. The main frequency of ES70101 is 27MHz. In ES7010, there are one unit of video decoding&decoding transfer, digital audio signal dealer, one CPU with 32 bits, one video input/output unit and one system control unit. Depending on the function, it can use SDRAM. of 8M or 16M. The following is the ES7010 interior diagram:



Figure

2、ES7010 interior diagram

ES6128 exchange data with ES7010 by ISPLSI 2064. ISP LSI2064 is a logic equipment that can programme with high density. It including 64 registers, 32 universal I/O jacks, 4 special inputs, 3 special time clock inputs. It is integrated with input bus unit, output bus unit and system bus unit. The main function of ISP LSI2064 is controlling the input and output of separated I/O data by software. In whole system, it is used for controlling and harmonize the data transfer among ES7010, ES6128 and recording chip. When it loading a disc, the signal of chip communicating with ES6128. When it recording a disc, ISP LSI 2064 controlling the communication among ES7010, ES6128 and recording chip.

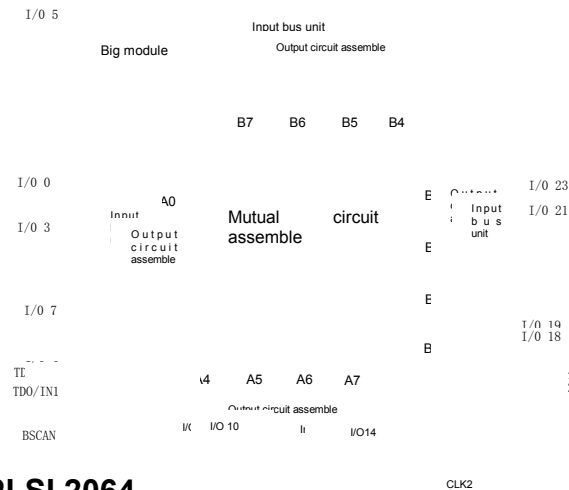


Figure 3、ISPLSI 2064

interior diagram

ES6128 is a integration with single chip and MPEG video decoder. It composites progressive video decoding function to make the picture more clear and more real. The main frequency of it is 27MHz. ES6128 can deal the audio data signal and can encode TV signal, it including 4 video DACs and supports Marovision anti-copy function. At the same time, is has DVD system induct function, system control function and manage function. It based on double CPU that is programmable multiplayer, including 32 bits RISC, 64 bits DSP (digital signal processor). The double processor can be connected through two separated data bus. Every processor has its own direct memorizer DMA and outside memorizer jacks. When the system is encoding and decoding, double processor can execute the system command and data dealing. It not only supports parallel ATAPI chip ports but also supports serial ATAPI chip ports, this will makes the audio/video data signal input be possible. The standard I₂S audio data will be input to DACs and output to ADCs. High speed EPROM, SDRAM command access, audio/video data buffer. It supports DVD、SVCD, VCD1.1、VCD2.0、Karaoke CD、MP3. Following is the interior diagram of ES6128:

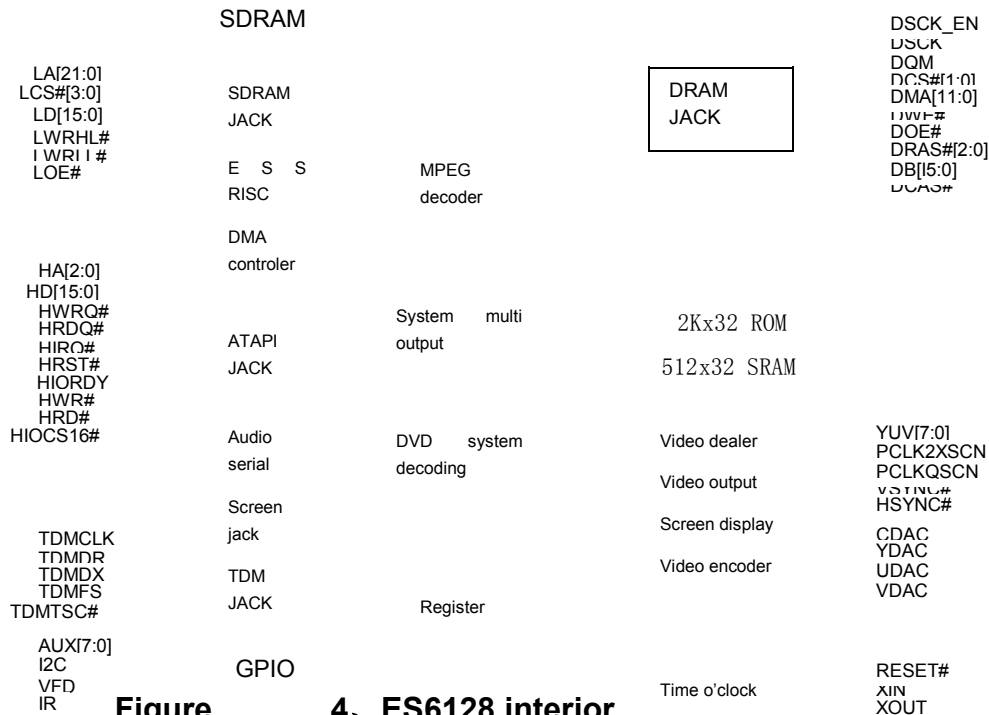


Figure 4、ES6128 interior diagram

III. Whole unit circuits analysis

1、Main chip circuit

The main frequency of ES6128 is 121MHz, and it has a external 128Mbit SDRAM, its working voltage is 3.3V or 2.0V. The working voltage of ES7010 is 3.3V or 2.5V. The voltage of workgroup that connecting between mainboard and power supply board is 5V, 3.3V respectively. Its order is 5V GND GND GND 3.3V 3.3V DVDOFF. 3.3V、2.5V、2.0V can be transferred by REG3 (REG-3.3V)、REG11(REG-2.5)、REG1(REG-ADJ) and then outputted at middle pin. The mainboard including reset circuit, time clock circuit, I2C memorize circuit, FLASH software controlling circuit.

2、Reset circuit

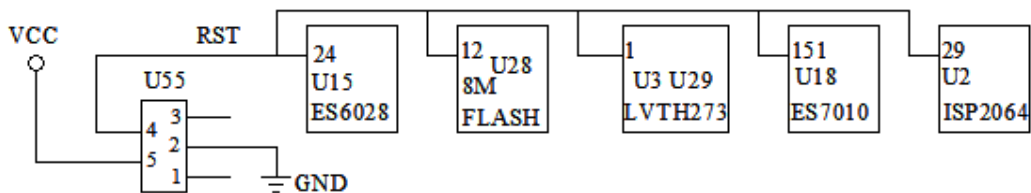
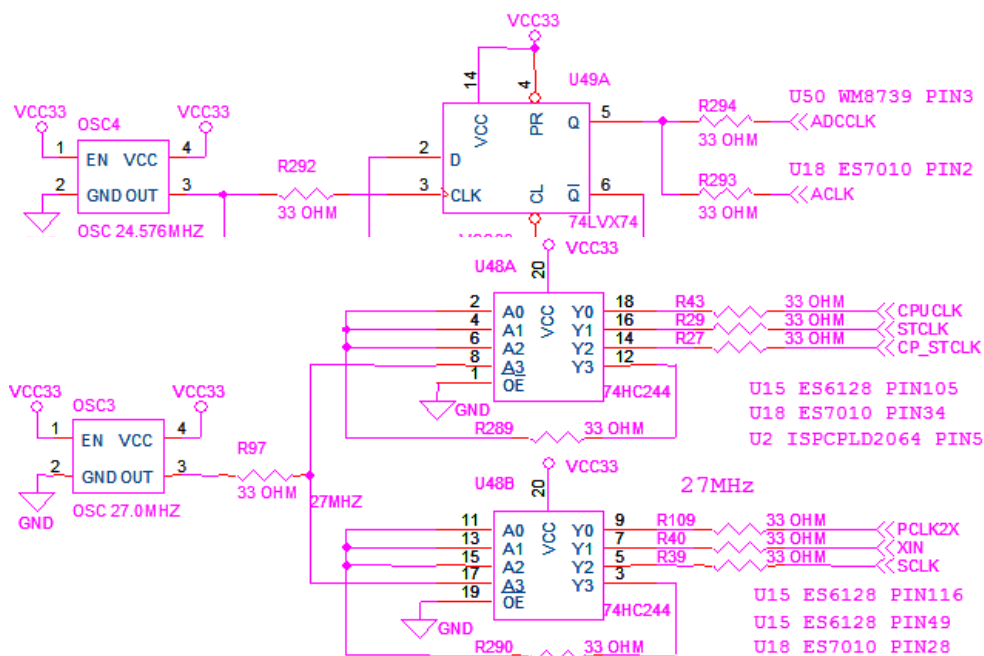


Figure 5、Reset circuit diagram

Reset circuit is composed by reset IC V6300, reset IC PIN5 to 5V power supply, PIN4 output reset signal, PIN2 to GND. After power on, PIN5 produce a signal with 4.5V level. From the above figure, you can see that reset IC U55 provides the reset signal for ES6128, ES7010, 8M FLASH, LVTH273, ISP2064. ES6128、ES7010、8M FLASH、LVTH273、ISP2064. If the reset signal can not work normally, the other composite chips can not work normally, so leading to no picture after power on.

3、The time clock circuit is composed by 27MHz, 24.576MHz crystal oscillator and their multiple frequency circuits. The time clock ACLK of WM8739 can be produced by 24.576MHz crystal oscillator's half frequency. 27MHz crystal oscillator can provide ES6128 and time clock of ES7010. The following is part circuit figure of it:



Figure

6、Time clock circuit

From the above figure, you will see that the time clock circuit is mainly composed by 27MHz, 24.576MHz crystal oscillator, so the stability of crystal oscillator count for much the reliability of system. In the case of no picture after power on, you should check 27MHz, 24.576MHz crystal oscillator besides IC reset signal. Check the output frequency of crystal frequency after passing the IC and check the frequency that reached to all composite chips.

4、I2C signal circuit controlled memory 24C01, X1226, video decoding composite SAA7114, TUNER in the whole system. I2C signal check 24C01, SAA7114, TUNER and initialize all these chips in the process of unit power on. 2C signal controlled the signal of 24C01, SAA7114, X1226, TUNER after unit worked. Following figure 7 is about the signal connecting process.

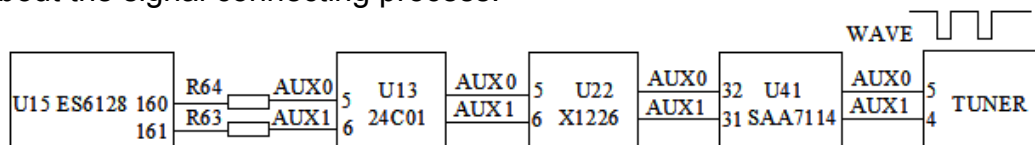


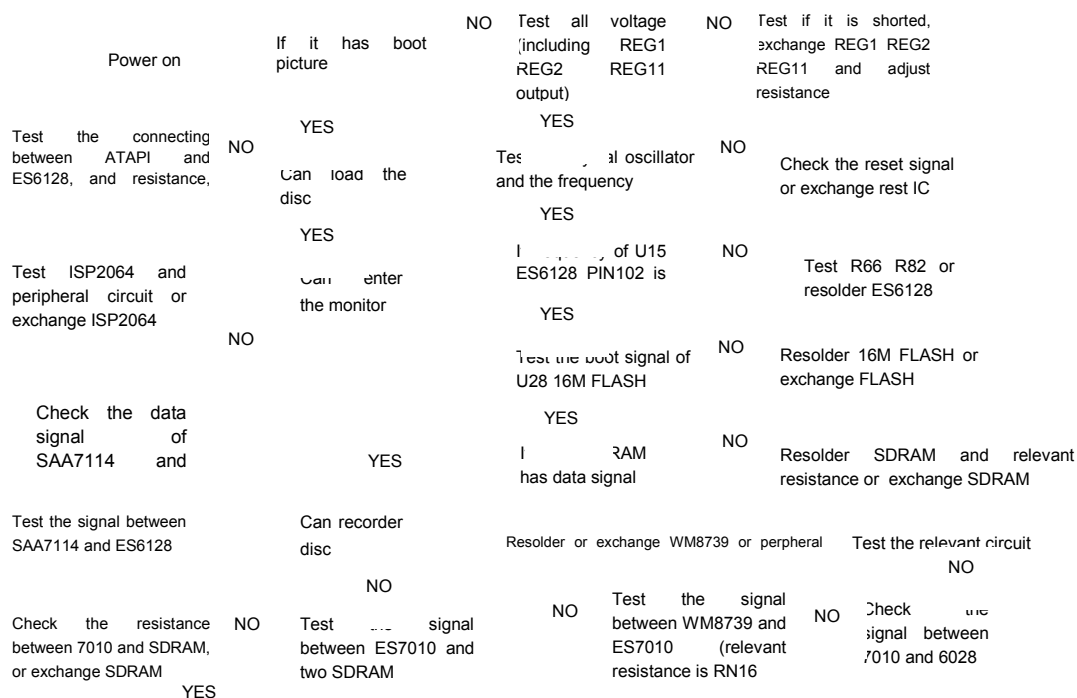
Figure 7、I2C signal connecting

(5) 16M FLASH software controls circuit, the circuit is mainly composed by FLASH and its software. The 16M FLASH controls the work of unit and the software controlling of system. 16M FLASH AM29LV1600B can be re-writable by disc.

IV Mainboard troubleshooting check flow

Mainboard troubleshooting should be in the case of all groups of output voltage of

power supply work normally. You can confirm the troubleshooting took place on chip or on mainboard by exchanging the chip. For example, some chip cannot read disc, it may relevant to the coherency of chip besides the problem of disc. In the checking of mainboard, if it can not display picture after power on, you should check and measure: (1) all the groups of voltage between power board and mainboard, the first group voltage is GND +5V +12V GND -12V, the second group voltage is +5V GND GND +3.3V +3.3V OFF. Two groups of voltage can provide to ES6128, its voltage is 3.3V and 2.0V respectively, they are transferred by REG3 and REG1 with 5V level. The voltage can be outputted on the middle pin of REG. The two groups of supply voltage provide to ES7010, and they are 3.3V and 2.5V respectively. 3.3V can be provided directly by power board, and 2.5V is transferred by REG11 with 5V level. Other chips 's supply voltage are 3.3V、5V、+12V、-12V respectively. (2) Check the output of reset signal. (3) If the 27MHz, 24.576MHz crystal oscillator can work normally, and if the frequency to every composite chip's time clock is on the normal standard. Figure 8 is the sketch map of mainboard troubleshooting flow.



Figure

8、Mainboard troubleshooting check schematic diagram

(3) If the main frequency of ES6128 is 121MHz. (4) Test the communicated signal between ES6128 and SDRAM, and test the communicated signal between ES6128 and 16M FLASH.(5) RN15 RN23 RN25 RN30。Test the resistances RN15 RN23 RN25 RN30 of ES7010.

Enter into the situation of monitor, if it takes abnormal picture, you should test the communicated signal between SAA7114 and ES6128, and test the work of SAA7114. If it hang up when it is recording, you should test the following: (1) the

communicated signal between ES7010 and 64M SDRAM. (2) Test the 8 channels of digital video signal between SAA7114 and ES7010. (3) Test the communicated signal between WM8739 and ES7010, the relevant resistance is RN16. (4) Test the output of ES7010 to LVT244B and the data buffer which composed with LVT374. Test the 8 channels of digital video signals, the relevant resistances are RN20, RN24. (5) Test the digital video data signal of data buffer to ISP2064 and ES6128.

The mainboard and output board have composite output, S-video, RCA video output,. Audio output has 5.1 CH audio output, optical , coaxial digital audio output. It includes 5.1 channel audio DA circuit, audio amplify circuit, optical and coaxial output circuits, SCART audio/video output circuit. SCART SCART circuits is composed with three SCART controlling signal lines and one audion controlling circuits. It can switch different AV situations of TV with SCART port by voltage level's high and low. Three SCART lines produce high and low voltage level signal by LVTH273 of mainboard, it can control the audion. Optical and coaxial circuit is built by the digital audio signal which outputted by mainboard then amplified by VHCU04, at last it will output at the coaxial jack. The video output circuit is mainly about mainboard output all kinds of video signals, these signals can be outputted on all kinds of video output jacks after matching the circuits. The mainboard outputs digital signal and transferred to 6 channels audio by PCM1606 DAC transfer circuits, and then the 6 channels 's audio would be output after amplified by amplified circuits. This process can build 5.1 channel output circuit. The main trouble displays that there is no sound from 5.1 channels, or no audio output from one group of 5.1 channels, or there is no audio signal from optical and coaxial. If it takes troubles, you should analyze the troubles from back to forward step by step. If it has no audio output, you should check RC4558 amplifier and the process of digital audio to simulate audio.

V.Power supply board troubleshooting flow

voltage output from transformer, exchange transformer so the trouble is removed.

(7) One BSA-3800R player cannot display picture after turning on. Check the voltage of mainboard is normal, the outputs of REG1 REG2 REG11 are normal too. Test the frequency of crystal oscillator, it's normal, then find the reset signal is abnormal, exchange the reset IC so the trouble is removed.

(8) One BSA-3800R player cannot display picture after turning on. Check voltage, reset signal and crystal oscillator circuit, all of them work normally. Then check the main frequency of main chip ES6128 PIN102 is incorrect, find the resistance of PLL is broken, re-solder the chip .

(9) One BSA-3800R player cannot display picture after turning on. Check voltage, reset signal, crystal oscillator circuit and main frequency of ES6128, all of them work normally. Find that there is no communicated signal between ES6128 and SDRAM, but there is communicated signal between ES6128 and 16M FLASH, re-solder SDRAM, but there is still no signal between them. Re-solder the resistances between ES6128 and SDRAM, so the trouble removed.

(10) One BSA-3800R cannot read, and after replacing mechanism the trouble still exists. Re-solder pins between ES6128 and APAPI port, re-solder ISP2064 and re-solder the resistances between ES6128 and APAPI port, the trouble still exists. May ISP 2064 is invalid, exchange it and the trouble removed.

(11) One BSA-3800R player cannot enter into the situation of connecting external signal, find there is one signal of 8 signal lines between the communication of SAA7114 and ES6128. Re-solder the resistances, the trouble is removed.

(12) One BSA-3800R player is hang-up when it is recording with external signal, check the communication between ES7010 and SAA7114, and check the communication between ES7010 and 64M SDRAM, all of them are work normally. Then find there is no signal between ES7010 and ISP2064, and neither signal between ES7010 and WM8739, so may WM8739 is invalid, exchange it and the trouble is removed.

(13) One BSA-3800R player is hang-up when it is recording with external signal. The signals of above are normal, but there is no signal output after passing data buffer that is composed by LVT244B and LVTH374. Find there is a bad solder dot on the LVT244B leads to LVT244B cannot work, re-solder it and the trouble is removed.

(14) One BSA-3800R player has picture but sound when it connecting with TV, find there is audio signal exists before CD4052, but not exists after it. Check the voltage of chip, re-solder it ,the trouble still exists, so find CD4052 is invalid.

(15) One BSA-3800R player's clock cannot work normally, exchange the U22 X1226 and 32.768KHZ crystal oscillator, the trouble still exists. Exchange 3.6V battery, the trouble still exists. Check diode between D4 BAT54 audion and battery, exchange BAT54, so the trouble is removed.