



SERVICE MANUAL

MF/HF MARINE TRANSCEIVER
IC-M802

INTRODUCTION

This service manual describes the latest service information for the **IC-M802 MF/HF MARINE TRANSCEIVER** at the time of publication.

To upgrade quality, any electrical or mechanical parts and internal circuits are subject to change without notice or obligation.

DANGER

NEVER connect the transceiver to an AC outlet or to a DC power supply that uses more than 16 V. This will ruin the transceiver.

DO NOT expose the transceiver to rain, snow or any liquids.

DO NOT reverse the polarities of the power supply when connecting the transceiver.

DO NOT apply an RF signal of more than 20 dBm (100 mW) to the antenna connector. This could damage the transceiver's front end.



ORDERING PARTS

Be sure to include the following four points when ordering replacement parts:

1. 10-digit order numbers
2. Component part number and name
3. Equipment model name and unit name
4. Quantity required

<SAMPLE ORDER>

1110004080	S.IC	µPC2709T	IC-M802	MAIN UNIT	5 pieces
8810005770	Screw	BiH M3×8 ZK	IC-M802	Top cover	10 pieces

Addresses are provided on the inside back cover for your convenience.

REPAIR NOTES

1. Make sure a problem is internal before disassembling the transceiver.
2. **DO NOT** open the transceiver until the transceiver is disconnected from its power source.
3. **DO NOT** force any of the variable components. Turn them slowly and smoothly.
4. **DO NOT** short any circuits or electronic parts. An insulated tuning tool **MUST** be used for all adjustments.
5. **DO NOT** keep power ON for a long time when the transceiver is defective.
6. **DO NOT** transmit power into a signal generator or a sweep generator.
7. **ALWAYS** connect a 50 dB to 60 dB attenuator between the transceiver and a deviation meter or spectrum analyzer when using such test equipment.
8. **READ** the instructions of test equipment thoroughly before connecting equipment to the transceiver.

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SECTION 1 SPECIFICATIONS

• IC-M802

■ GENERAL

- Frequency coverage :

Receive	500 kHz–29.9999 MHz
Transmit	1.6000–2.9999 MHz
	6.0000–6.9999 MHz
	12.0000–13.9999 MHz
	18.0000–19.9999 MHz
	25.0000–27.5000 MHz
DSC receive	2.1875 MHz, 4.2075 MHz, 6.3120 MHz, 8.4145 MHz, 12.5770 MHz, 16.8045 MHz

- Mode : TX/RX J3E (USB/LSB), J2B (AFSK), F1B (FSK), A1A (CW)
RX H3E
DSC J2B
- Antenna impedance : 50 Ω
- Frequency stability : ±10 Hz
- Power supply requirement : 13.6 V DC ±15% Negative ground
- Current drain (at 13.6 V DC) : RX; 3.0 A at Max. output power
TX; 30 A typical at Max. audio output
- Usable temperature range : -20°C to +55°C; -4°F to +131°F
- Dimensions (projections not included) : 240(W) × 94(H) × 238.4(D) mm; 2 7/16(W) × 3 11/16(H) × 9 3/8(D) in.
- Weight : 4.7 kg; 10.4 lb; 165.8 oz

■ TRANSMITTER

- Output power : 150, 60, 20 W PEP
- Spurious emissions : -62 dB
- Carrier suppressions : 40 dB
- Unwanted sideband suppression : 55 dB
- Microphone impedance : 600 Ω

■ RECEIVER

- Sensitivity (Receiver) :

MODE	FREQUENCY	20 dB SINAD	10 dB S/N
J3E, A1A	0.5–1.5999 MHz	30 dBµVe.m.f.	16 dBµV
	1.6–1.7999 MHz	13 dBµVe.m.f.	-1 dBµV
	1.8–29.9999 MHz	8 dBµVe.m.f.	-6 dBµV
J2B, F1B	1.6–1.7999 MHz	13 dBµVe.m.f.	-1 dBµV
	1.8–29.9999 MHz	8 dBµVe.m.f.	-6 dBµV
H3E	1.5–1.5999 MHz	44 dBµVe.m.f.	30 dBµV
	1.6–1.7999 MHz	30 dBµVe.m.f.	16 dBµV
	1.8–3.9999 MHz	24 dBµVe.m.f.	10 dBµV

(DSC)

- Squelch sensitivity :

: 0 dBµVe.m.f. (J2B; 2.1875, 4.2075, 6.3120, 8.4145, 12.5770, 16.8045 MHz)

:

MODE	Threshold	Tight
J3E at 12.230 MHz	Less than +20 dBµV	Less than +90 dBµV
H3E at 1.000 MHz	Less than +30 dBµV	Less than +110 dBµV

- Spurious response rejection : More than 70 dB
- Clarity variable range : ±150 Hz

- **RC-25 Remote controller**

■ GENERAL

• Microphone impedance	: 600 Ω
• Audio output power	: More than 5 mW at 16 Ω headphone with 10 % distortion
• Audio output impedance	: 8–16 Ω
• Usable temperature range	: -20°C to +55°C; -4°F to +131°F
• Dimension (projections not included)	: 220(W) × 110(H) × 84.4(D) mm; 8 21/32(W) × 4 11/32(H) × 3 5/16(D) in.
• Weight	: 570 g; 1.3 lb; 20.1 oz

- **SP-24 External speaker**

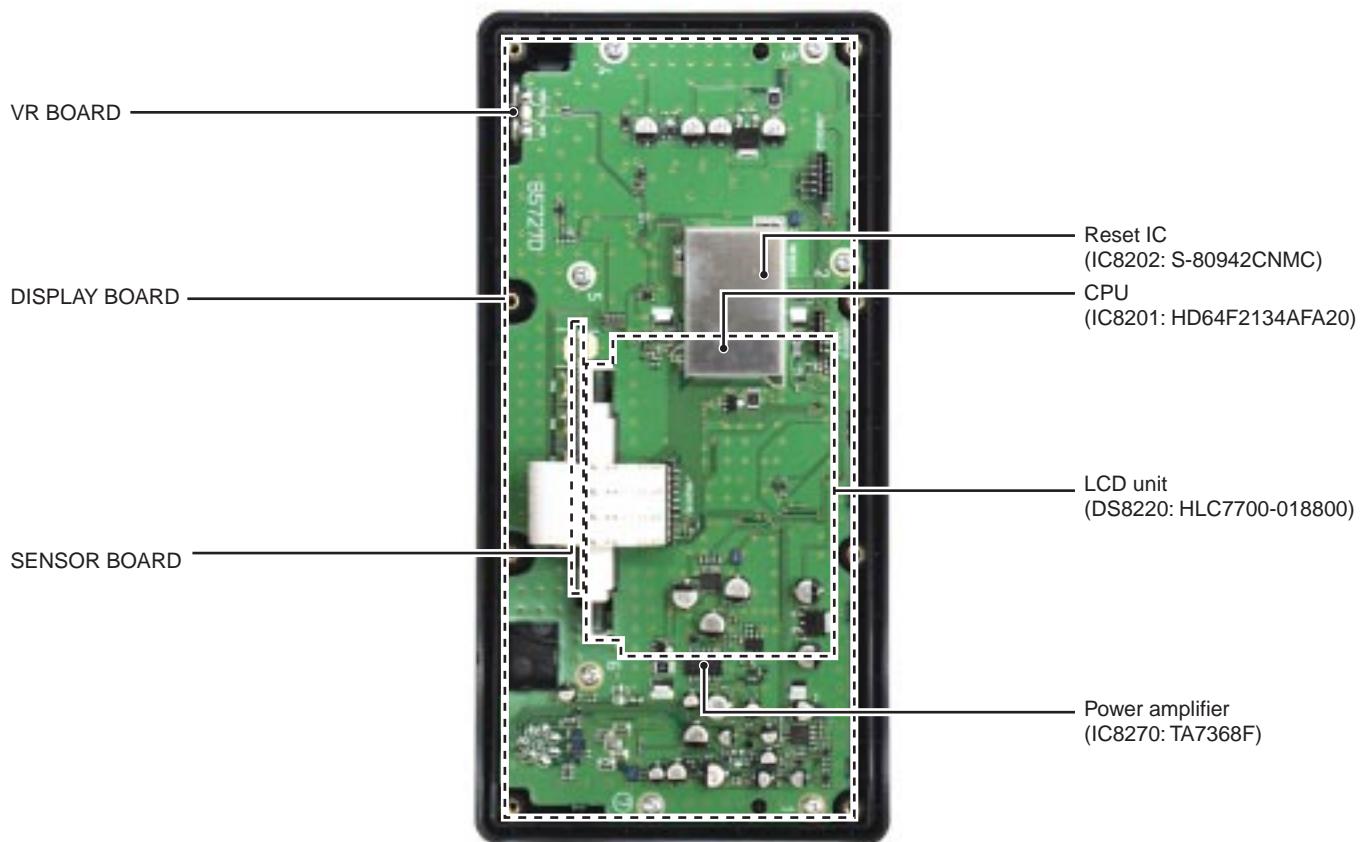
■ GENERAL

• Impedance	: 4 Ω
• Input power	: Rated input; 5 W Maximum input; 7 W
• Usable temperature range	: -20°C to +60°C; -4°F to +140°F
• Dimension (projections not included)	: 110(W) × 110(H) × 84.4(D) mm; 4 11/32(H) × 4 11/32(H) × 3 5/16(D) in.
• Weight	: 370 g; 0.82 lb; 13.1 oz

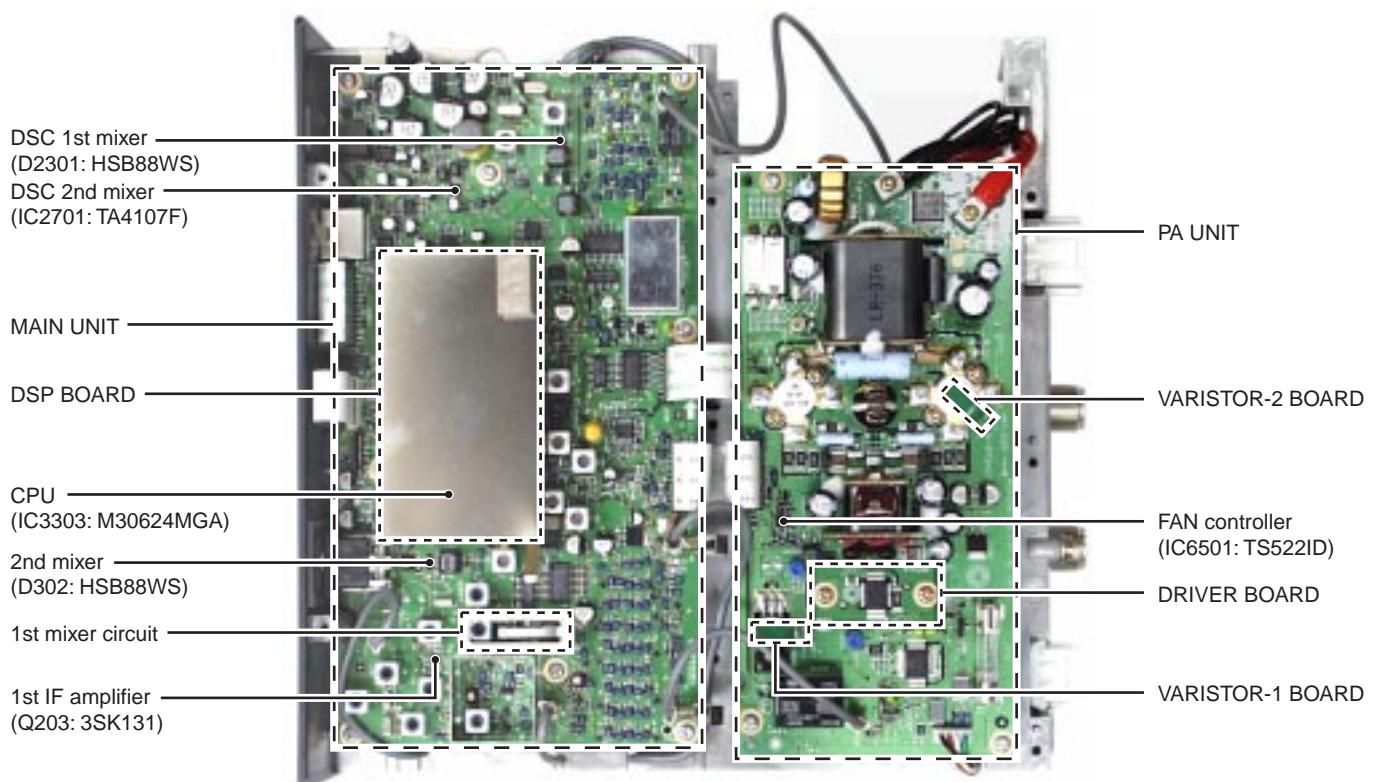
All stated specifications are subject to change without notice or obligation.

SECTION 2 INSIDE VIEWS

• RC-25



• IC-M802



SECTION 3 CIRCUIT DESCRIPTION

3-1 RECEIVER CIRCUITS

3-1-1 RF FILTER CIRCUIT (FILTER AND MAIN UNITS)

Received signals from the antenna connector are applied to the transmit/receive switching and protection relay (FILTER unit; RL7301) which is controlled by the CPU via the "TRXS" line. The signals pass through the 30 MHz cut-off low-pass filter (FILTER unit; L7321, C7321–C7323, C7325), and then applied to the MAIN unit via the J7321.

The signals pass through the transmit/receive switch (D53) and 1.6 MHz cut off high-pass filter (L51–L54, C54, C56, C57, C59, C61–C64), and are then applied to one of the bandpass filters (including one low-pass filter for below 2.0 MHz). These filters are selected by the filter control signals (B0–B8) as described in the table below.

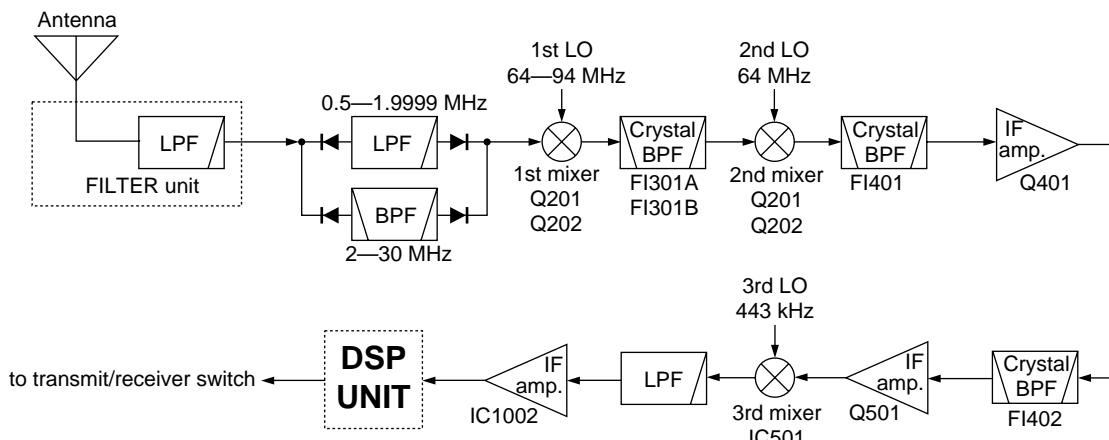
The filtered signals pass through the 33 MHz cut-off low-pass filter (L202, L203, C202–C206), and are then applied to the 1st mixer circuit (Q201, Q202).

Frequency (MHz)	LPF ctrl signal	BPF ctrl signal
0.5–1.999	L0	B0
2–2.999	L1	B1
3–4.999	L2	B2
5–6.999	L3	B3
7–9.999	L4	B4
10–13.999	L5	B5
14–17.999	L6	B6
18–19.999		B7
20–21.999	L7	
22–23.999	B8	
24–29.999		

3-1-2 1ST MIXER AND IF CIRCUITS (MAIN UNIT)

The 1st mixer circuit converts the received signals into a fixed frequency, 64.445 kHz 1st IF signal using PLL output frequency. By changing the PLL frequency, only the desired frequency is picked up at the pair of crystal filters (FI301a, FI301b) via the 64.445 kHz bandpass filter (FI201) at the next stage.

• RECEIVER CONSTRUCTION



The IF amplifier (Q203) and resonator circuits are designed between the filter pair. The PLL output signal (1LO) enters the MAIN unit via the J601 and is amplified at the 1st LO amplifier (Q601). The amplified signal is passed through the 100 MHz cut-off low-pass filter (L604, L605, C604, C606–C610) to suppress harmonics components, and then applied to the 1st mixer circuit (Q201, Q202).

3-1-3 2ND MIXER AND IF CIRCUITS (MAIN UNIT)

The 1st IF signal from the crystal filter (FI301b) is converted again into a 455 kHz 2nd IF signal at the 2nd mixer circuit (D302, L303, L304). The 2nd LO signal (2LO) from the PLL unit enters the MAIN unit via the J301 to be applied to the 2nd mixer circuit.

3-1-4 3RD MIXER AND IF CIRCUITS (MAIN UNIT)

The 2nd IF signal passes through the low-pass filter (L305, L306, C307–C311), and then applied to the IF amplifier (Q401) via the ceramic bandpass filter (FI401). The amplified signal passes through the ceramic bandpass filter (FI402), and then applied to the 3rd mixer circuit via the IF amplifier (Q501). The 2nd IF signal is converted into a 12 kHz 3rd IF signal at the 3rd mixer circuit (IC501). The 3rd LO signal (3LO) from the PLL unit enters the MAIN unit via the J3601 to be applied to the 3rd mixer circuit.

3-1-5 DSP RECEIVER CIRCUIT (MAIN AND DSP UNITS)

The DSP (Digital Signal Processor) circuit enables digital IF filter, digital noise reduction, digital PSN (Pulse Shift Network), phase demodulation, digital automatic notch, and etc.

The 3rd IF signal is applied to the IF amplifier (MAIN unit; IC1002, pin 5) after being passed through the low-pass filter (MAIN unit; IC1002, pins 3, 1). The amplified 12 kHz 3rd IF signal is amplified at the differential amplifiers (IC651a/b), and is then applied to the A/D convertor section in the CODEC IC (IC501) on the DSP board (EX-2432). At the same time, the converted signal is level-shifted 5 V to 3.3 V in the IC (IC501).

The level-shifted signal is applied to the DSP IC (IC301) for the digital IF filter, demodulator, automatic notch and noise reduction, etc.

The output signal from the DSP IC is applied to the D/A converter section in the CODEC IC (IC501) to convert into the analog audio signals. Also the signals are level-shifted 3.3 V to 5 V at the level converter section in the IC (IC501).

The level-shifted audio signals are passed through the active filter (IC701a), and then applied to the MAIN unit via J901 (pin 17) as the DRAF signal.

3-1-6 AGC CIRCUIT (DSP AND MAIN UNITS)

The AGC (Automatic Gain Control) circuit reduces IF amplifier gain and attenuates IF signal to keep the audio output at a constant level.

The receiver gain is determined by the voltage on the AGC1 line from the DSP unit. The D/A converter for the AGC (IC102) supplies control voltage to the AGC1 line and sets the receiver gain with the [RF/SQL] control.

The 3rd IF signal from the CODEC IC (IC501) is detected at the AGC detector section in the DSP IC (IC301). The output signal from the DSP IC is level-shifted at the level converter (IC101) and applied to the D/A converter (IC102). The AGC voltage is amplified at the buffer amplifier section in the IC102 and applied to the MAIN unit to control the AGC1 line.

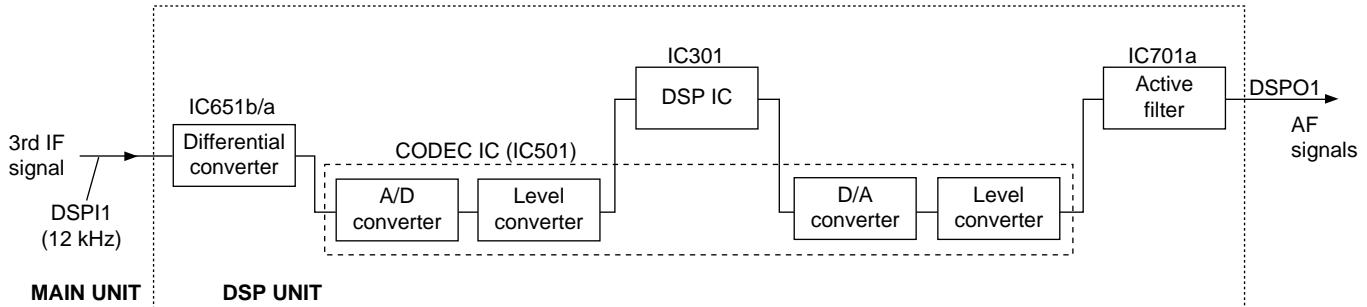
When receiving strong signals, the detected voltage increases and the AGC1 voltage decreases. As the AGC1 voltage is used for the bias voltage of the IF amplifiers (MAIN unit; Q203, Q401, Q501), IF amplifier gain is decreased.

3-1-7 AF AMPLIFIER CIRCUIT (DSP AND MAIN UNITS)

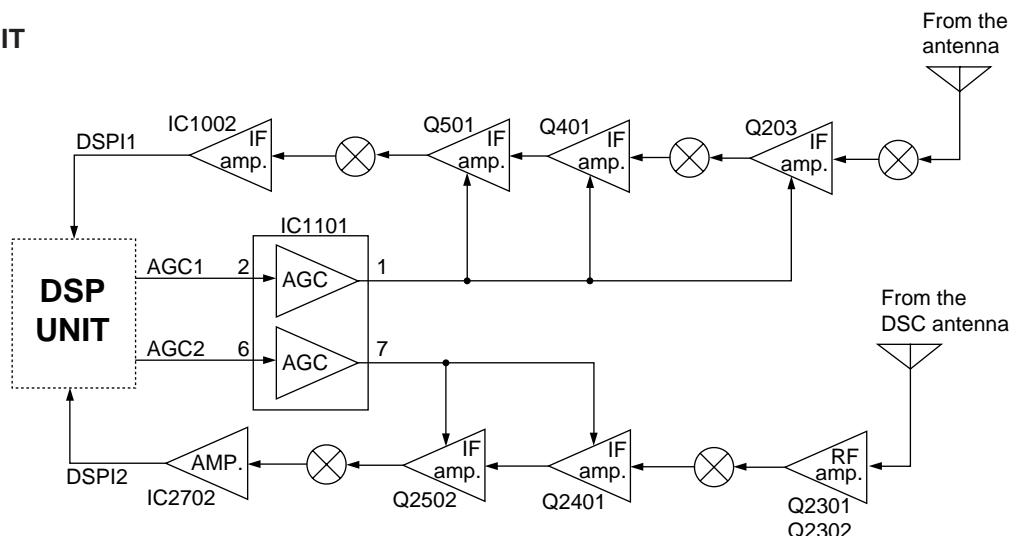
The AF amplifier amplifies the audio signals to the suitable driving level for the speaker.

The AF signals from the DSP unit are passed through the transmit/receive switch (MAIN unit; IC1703, pins 1, 6) via the "DSPO1" signal, and then applied to the AF amplifier (MAIN unit; IC1702, pin 5) after being passed through the low-pass filter (MAIN unit; IC1702, pins 3, 1). The amplified signal passes through the the SQL gate (MAIN unit; IC1701, pins 7, 1) which is controlled by the CPU (IC3303, pin 49) via the "SQLC" signal. The signal is applied to the electronic volume IC (MAIN unit; IC1603, pin 2) which can control the volume attenuation by AFG voltage from the CPU (MAIN unit; IC3303). Beep, tone, side tone, monitor signals are applied to the the electronic volume IC too. The signal is applied to the AF mute swtich, and then amplified at the AF power amplifier (IC1601, pin 1). The amplified signal is applied to the speaker (SP-24) after being passed through the speaker jack (J1451) via the "AFO" signal.

• DSP CIRCUIT



• AGC CIRCUIT



3-2 TRANSMITTER CIRCUITS

3-2-1 MICROPHONE AMPLIFIER CIRCUIT (RC-25, MAIN AND DSP UNITS)

The microphone amplifier circuit amplifies microphone audio signal to a level needed for the DSP circuit.

Audio signals from the [MIC] connector (J8701, pin 1) are amplified at the AF amplifier (IC8280, pin 3), and then applied to the gate modulator IC (MAIN unit; IC2001, pin 3) via the J2051, pin 1 as "FMOD" signal. The signal is applied to the DSP unit after being passed through the limitter amplifier and low-pass filter IC (MAIN unit; IC1051, pins 3, 7) as "DSPI1" signal.

3-2-2 DSP TRANSMITTER CIRCUIT (DSP UNIT)

The DSP (Digital Signal Processor) circuit enables PSN (Phase Shift Network)/Low Power/Phase modulator, transmitter monitor, side tone, and etc.

The microphone audio signals from the MAIN unit via the "DISPI1" line are amplified at the differential amplifiers (IC651a/b), and are then applied to the A/D converter section in the CODEC IC (IC501). at the same time, the converted signals are level-shifted 5 V to 3.3 V in the IC (IC501).

The level shifted signals are applied to the DSP IC (IC301) and modulated at the DSP IC to produce the 12 kHz transmitter IF signal.

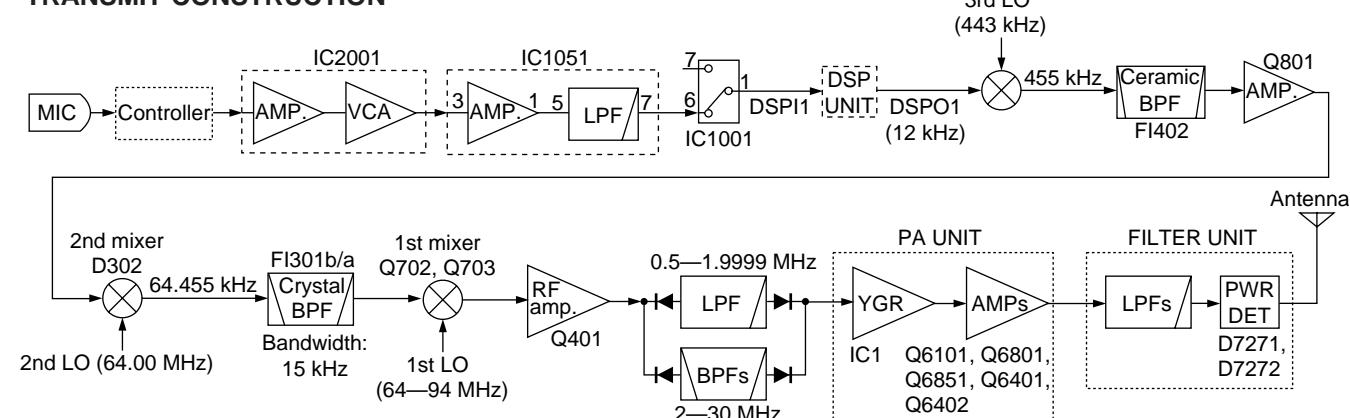
The modulated IF signal from the DSP IC is applied to the D/A convertor section in the CODEC IC (IC501) to convert into the analog IF signal. Also the signal is level-shifted 3.3 V to 5 V at the level converter section in the IC (IC501).

The level-shifted IF signal is passed through the active filter (IC701a), and then applied to the MAIN unit via J901 (pin 17) as the "DSPO1" signal.

3-2-3 SPEECH COMPRESSOR CIRCUIT (DSP UNIT)

The DSP (Digital Signal Processor) circuit enables PSN. The speech compressor compresses the transmitter audio input signals to increase the average output level (average talk power).

• TRANSMIT CONSTRUCTION



When the speech compressor function is ON, the level-shifted signal from the CODEC IC (IC501) is applied to the DSP IC (IC301) and compressed at the DSP IC to obtain an average audio level.

At the same time, the compressed signals are modulated at the DSP IC and applied to the D/A converter section in the CODEC IC (IC501).

3-2-4 IF AMPLIFIER AND MIXER CIRCUITS (MAIN UNIT)

The modulated 3rd IF signal from the DSP unit ("DSPO1" signal: 12 kHz) passes through the transmit/receive switch (IC1703, pins 1, 5), and then applied to the 3rd mixer circuit (IC901, pin 3). The applied 3rd IF signal is mixed with the 3rd LO signal from the DDS circuit (PLL unit; IC5701) to produce a 455 kHz 2nd IF signal.

The 2nd IF signal is output from IC901, pin 5 and passes through the ceramic bandpass filter (FL402) to suppress the unwanted signals via the D404. The filtered 2nd IF signal is amplified at the 2nd IF amplifier (Q801), and then applied to the 2nd mixer circuit after being passed through the D303 and low-pass filter (L304–L306, C304–C311, R306).

The 2nd IF signal is mixed with 64 MHz 2nd LO signal, coming from the PLL circuit, at the 2nd mixer circuit (D302) to obtain 64.455 MHz 1st IF signal. The 1st IF signal is passed through the crystal bandpass filter (FI301A, FI301B) to cut off the unwanted signals. The signal is applied to the transmitter mixer circuit (Q702, Q703) to obtain the desired signal via the transmit/receive switch (D301) and attenuator.

The operating (transmitting) frequency is produced at the 1st IF mixer circuit (Q210, Q202) by mixing the 1st IF and 1st LO signals. The mixed signal is then applied to the RF circuit.

3-2-5 RF CIRCUIT (MAIN, PA UNITS AND DRIVER BOARD)

The RF circuit amplifies operating (transmitting) frequency to obtain 150 W of RF output.

The signal from the 1st IF mixer is passed through one of the low-pass filter or bandpass filters (Refer to page 4-1 bandpass filters used), and then applied to the YGR amplifier (IC1, pin 1) after being passed through the attenuator (R5–R7). The amplified signal passes through the low-pass filter (L1–L3, C1–C7) and attenuator (R1–R3), and then applied to the PA unit via J1.

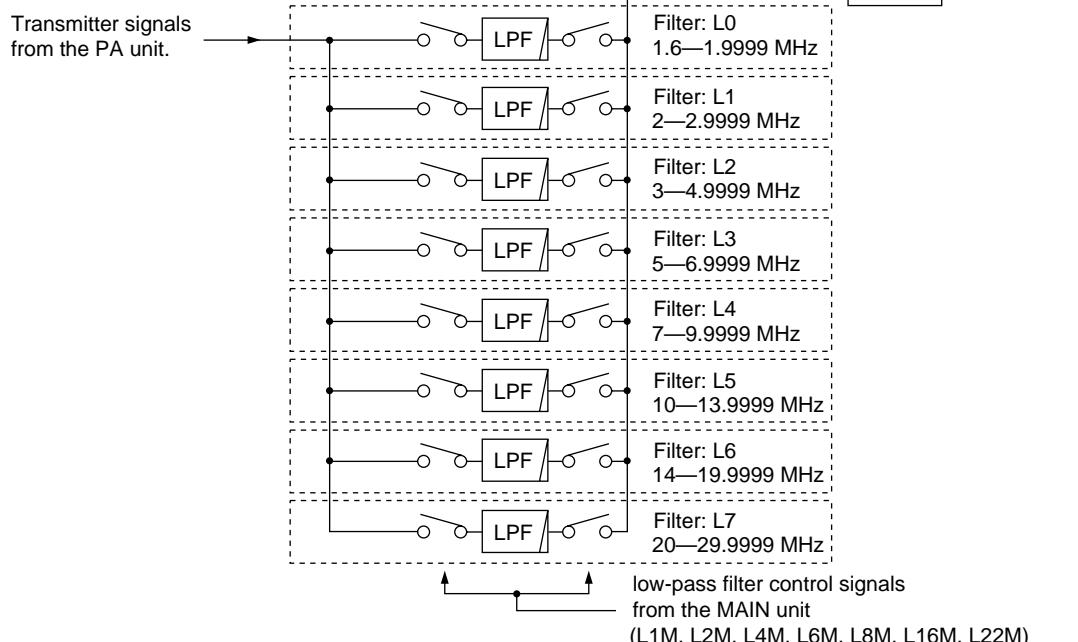
The signal applied from the MAIN unit is amplified at 2 amplifiers (PA unit; Q6101 and Q6801). A part of output signal from 2 amplifiers are applied to the amplifiers to improve the frequency characteristic by feedback. The amplified signal is applied to the drive amplifier (DRIVER board; Q6851) via the J6301 as "DRV1" signal. The signal from the DRIVER board passes through the impedance converter (PA unit; L6301), and then applied to push-pull amplifiers (PA unit; Q6401, Q6402) to obtain a stable 150 W of RF output power. A part of the RF output power returns to the amplifiers to obtain a stable gain between 1.6 MHz and 27.5 MHz bands by using feedback transformer (L6404). The output power is applied to the filter unit via the J6401 as "FLIN" signal.

The amplified signal is applied to the one of the 8 low-pass filters which are composed chebychev type.

• 1.6–1.9999 MHz signal

The signal is applied to the relay (FILTER unit; RL7031) which is controlled by the band control IC (MAIN unit; IC3602) as the "L1M" signal via the buffer amplifier (MAIN unit; IC1301, pin 11). The signal passes through the low-pass filter (FILTER unit; L7036–L7038, C7033–C7039, C7041–C7045, C7049), and then applied to the RL7032.

• RF FILTER CIRCUIT



• 2–2.9999 MHz signal

The signal is applied to the relay (FILTER unit; RL7061) which is controlled by the band control IC (MAIN unit; IC3602) as the "L2M" signal via the buffer amplifier (MAIN unit; IC1301, pin 12). The signal passes through the low-pass filter (FILTER unit; L7066–L7068, C7063–C7084, C7086, C7087), and then applied to the RL7062.

• 3–4.9999 MHz signal

The signal is applied to the relay (FILTER unit; RL7091) which is controlled by the band control IC (MAIN unit; IC3602) as the "L4M" signal via the buffer amplifier (MAIN unit; IC1301, pin 12). The signal passes through the low-pass filter (FILTER unit; L7096–L7098, C7094–C7096, C7098–C7101, C7105–C7107), and then applied to the RL7092.

• 5–6.9999 MHz signal

The signal is applied to the relay (FILTER unit; RL7121) which is controlled by the band control IC (MAIN unit; IC3602) as the "L6M" signal via the buffer amplifier (MAIN unit; IC1301, pin 14). The signal passes through the low-pass filter (FILTER unit; L7126–L7128, C7124–C7132, C7136, C7137), and then applied to the RL7122.

• 7–9.9999 MHz signal

The signal is applied to the relay (FILTER unit; RL7151) which is controlled by the band control IC (MAIN unit; IC3602) as the "L8M" signal via the buffer amplifier (MAIN unit; IC1301, pin 15). The signal passes through the low-pass filter (FILTER unit; L7066–L7068, C7063–C7084, C7086, C7087), and then applied to the RL7152.

• 10–13.9999 MHz signal

The signal is applied to the relay (FILTER unit; RL7181) which is controlled by the band control IC (MAIN unit; IC3602) as the "L12M" signal via the buffer amplifier (MAIN unit; IC1301, pin 16). The signal passes through the low-pass filter (FILTER unit; L7182–L7184, C7183–C7188, C7193), and then applied to the RL7182.

• 14–19.999 MHz signal

The signal is applied to the relay (FILTER unit; RL7211) which is controlled by the band control IC (MAIN unit; IC3602) as the "L16M" signal via the buffer amplifier (MAIN unit; IC1301, pin 17). The signal passes through the low-pass filter (FILTER unit; L7216–L7218, C7214, C7216, C7217, C7221–C7225), and then applied to the RL7212.

• 20–29.999 MHz signal

The signal is applied to the relay (FILTER unit; RL7241) which is controlled by the band control IC (MAIN unit; IC3602) as the "L22M" signal via the buffer amplifier (MAIN unit; IC1301, pin 18). The signal passes through the low-pass filter (FILTER unit; L7245, L7246, L7248, C7242–C7244, C7247–C7249, C7253, C7254), and then applied to the RL7242.

The filtered signal is applied to the antenna connector after being passed through the RL7301 and J7311.

3-2-6 ALC CIRCUIT (FILTER AND MAIN UNITS)

The ALC (Automatic Level Control) circuit controls the gain of IF amplifiers in order for the transceiver to output a constant RF power set by the [RF PWR] control even when the supplied voltage shifts, etc.

The RF power level is detected at the power detector circuit (Filter unit; D7271) to be converted into DC voltage and applied to the MAIN unit as the FOR signal.

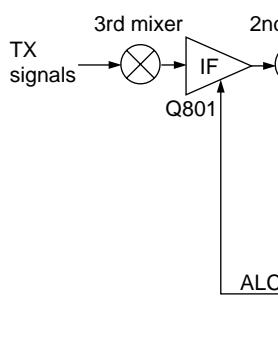
The FOR signal is applied to the comparator (IC1201, pin 2). The "POCV" signal, controlled by the [RF PWR] control via the I/O expander (IC3606, pin 5), is also applied to the other input (pin 3) for reference. The compared signal is output from pin 1 and applied to the IF amplifier in the MAIN (Q801) unit to control amplifying gain.

When the FOR signal exceeds the POCV voltage, ALC bias voltage from the comparator controls the IF amplifiers. This adjusts the output power to a specified level from the [RF PWR] control until the FOR and POCV voltages are equalized.

3-2-7 APC CIRCUIT (FILTER AND MAIN UNITS)

The APC (Automatic Power Control) circuit protects the power amplifiers on the PA unit from high SWR and excessive current.

• ALC CIRCUIT



• SWR APC CIRCUIT (FILTER AND MAIN UNITS)

The reflected wave signal appears and increases on the antenna connector. When the antenna is mismatched, D7272 of the power detector circuit (D7271, D7272, L7272) in the FILTER unit detects the signal and applies it to the ALC amplifier (IC1201, pin 9) in the MAIN unit as "REF" signal. The output signal decreases the bias voltage of the RF APC amplifier to reduce the output power.

• CURRENT APC CIRCUIT (FILTER AND MAIN UNITS)

The power transistor current is detected from the different voltage between both terminals of a 0.012Ω resistor (R6651) on the PA board. The detected voltage is applied to the differential amplifier (IC6501). When the current of the final transistors is more than 30 A, the detected voltage is applied to the APC amplifier controller (IC1201) in the MAIN unit to reduce the gate-2 voltage of the IF amplifier (Q801) and thus reduce the output power.

3-2-8 RF METER CIRCUIT (MAIN UNIT)

The output of ALC amplifier (IC1201, pin 12) is applied to the CPU (IC3303, pin 97) as "RFML" signal to indicate the transmit power level on the display.

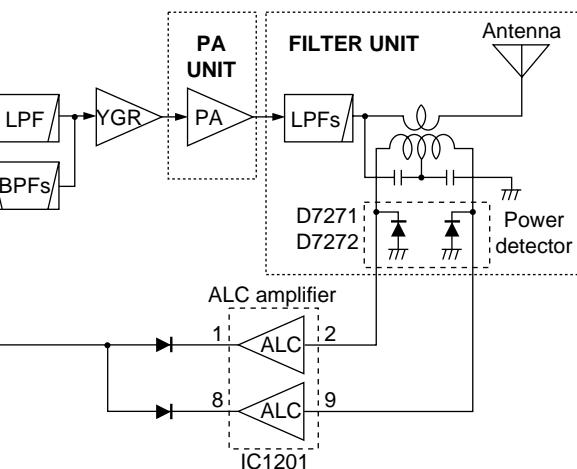
For antenna current meter indication, the "ANTM" signal from the optional AT-130E is applied to the meter amplifier (IC1201, pin 12) via the J6701 in the PA unit.

3-2-9 MONITOR CIRCUIT (DSP AND MAIN UNITS)

The microphone audio signals can be monitored to check voice characteristics.

A portion of the transmit IF signal from the DSP IC (IC301) is mixed with a 12 kHz LO signal to demodulate into the AF signals. The demodulated signals are level-shifted 3.3 V to 5 V at the level converter (IC102) to convert into the analog AF signals. The AF signals are then applied to the MAIN unit as "MONI" signal.

The "MONI" signal from the DSP unit is amplified at the AF amplifier (MAIN unit; IC1603, pin 2), and then applied to the VCA amplifier to control the AF volume (pin 7). The amplified signal passes through to the AF mute circuit (MAIN unit; IC1602, pins 1, 7) which is controlled by the CPU via the expander IC (MAIN unit; IC3603) as the "AFMS" signal. The AF signal is applied to the AF power amplifier circuit (MAIN unit; IC1601, pin 1) to drive a speaker.



3-3 PLL CIRCUITS

3-3-1 GENERAL

The PLL circuits generate a reference frequency (32.000 MHz); 1st LO frequencies (64.485–94.455 MHz); 2nd LO frequency (64 MHz), 3rd LO frequency (433.000 kHz).

The 1st LO PLL adopts a mixer-less dual loop PLL system. The BFO uses a DDS and a 2nd LO as a fixed frequency double that the crystal oscillator.

3-3-2 1ST LO PLL (PLL UNIT)

The 1st LO PLL contains a main and reference loop as a dual loop system.

The reference loop generates an approximate 10.5 MHz frequency using a DDS circuit, and the main loop generates a 64.485 to 94.455 MHz frequency using the reference loop frequency.

(1) REFERENCE LOOP PLL

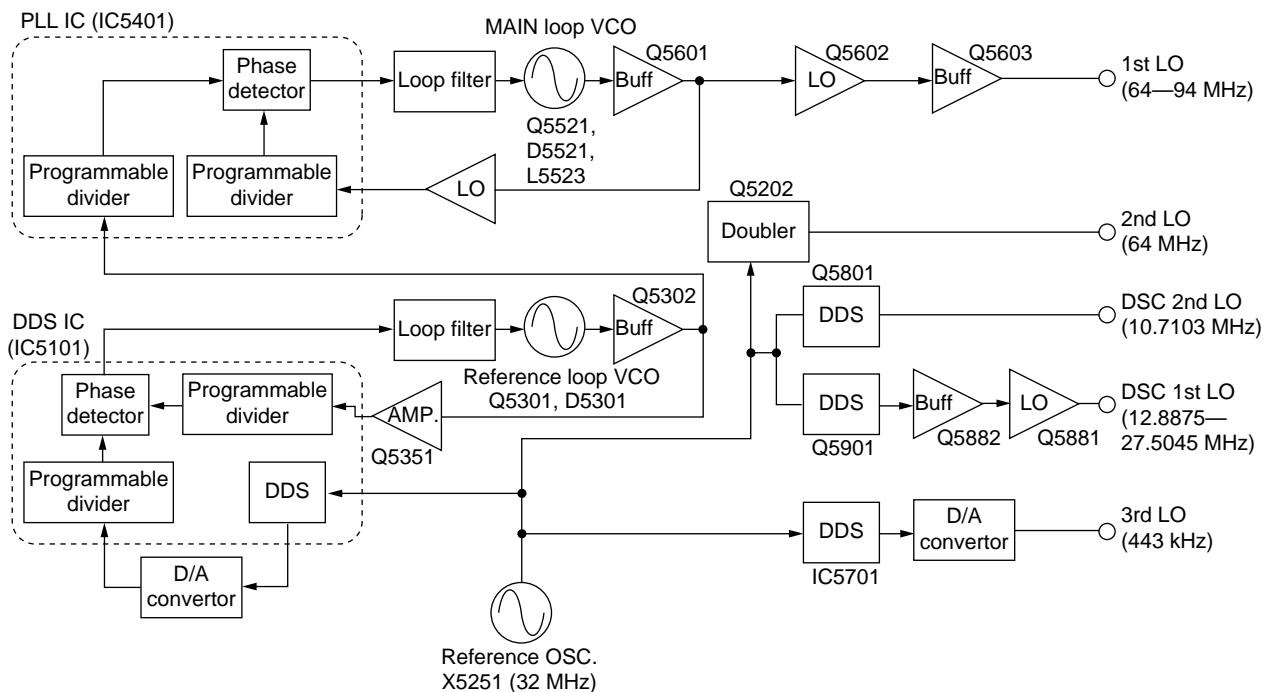
The oscillated signal at the reference VCO (Q5301, D5301) is amplified at the amplifiers (Q5302, Q5351) and is then applied to the DDS IC (IC5101, pin 46). The signal is then divided and detected on phase with the DDS generated frequency.

The detected signal output from the DDS IC (pin 56) is converted into DC voltage (lock voltage) at the loop filter (R5136, R5146, C5112) and then fed back to the reference VCO circuit (Q5301, D5301).

(2) MAIN LOOP PLL

The oscillated signal at one of the main loop VCOs (Q5521, D5521, L5523) is amplified at the buffer amplifiers (Q5601, Q5604) and is then applied to the PLL IC (IC5401, pin 6). The signal is then divided and detected on phase with the reference loop output frequency.

• PLL CIRCUIT



The detected signal output from the PLL IC (pin 2) is converted into a DC voltage (lock voltage) at the loop filter and then fed back to one of the VCO circuits (Q5521, D5521, L5523).

The oscillated signal from the buffer amplifier (Q5601) is also applied to the MAIN unit as a 1st LO signal after being amplified at LO (Q5602) and buffer (Q5603) amplifiers or passed through the bandpass filter (L5602–L5604, C5610–C5616).

3-3-3 2ND LO AND REFERENCE OSCILLATOR CIRCUITS (PLL UNIT)

The reference oscillator (X5251, CR604) generates a 32.0 MHz frequency for the 2 DDS circuits as a system clock and for the LO output. The oscillated signal is doubled at the doubler circuit (Q5202) and the 64.0 MHz frequency is picked up at the double tuned filter (L5203, L5204). The 64.0 MHz signal is applied to the RF circuit as a 2nd LO signal.

3-3-3 3RD LO CIRCUIT (PLL UNIT)

The DDS IC (IC5701) generates a 10-bit digital signal using the 32 MHz system clock. The digital signal is converted into an analog wave signal at the D/A converter (R5701–R5720). The converted analog wave is passed through the bandpass filter (L5781–L5783, C5781–C5785) and then applied to the MAIN unit as the 3rd LO signal (433.000 kHz) via the attenuator (R5781–R5783).

3-4 DSC CIRCUITS

3-4-1 DSC BANDPASS FILTER AND RF AMPLIFIER CIRCUITS (MAIN UNIT)

The RF circuit filters out-of-band signals and amplifiers signals within the range of frequency coverage.

(1) 2-7 MHz signals

The signals from the antenna connector (J2101) pass through the bandpass filter (L2201, L2202, L2207, L2210, L2213, L2216, L2219, L2220, L2225, L2228, L2231, C2201, C2204, C2207, C2210, C2213, C2216, C2219, C2222, C2225, C2228, C2234, C2240) to obtain desired 2-6 MHz signals and suppress any undesired signals.

(2) 8 MHz signals

The signals from the antenna connector (J2101) pass through the bandpass filter (L2204, L2208, L2211, L2214, L2217, L2221, L2222, L2229, L2232, C2202, C2205, C2208, C2210, C2211, C2214, C2217, C2220, C2223, C2226, C2236, C2241) to obtain desired 8 MHz signals and suppress any undesired signals.

(3) 12 MHz signals

The signals from the antenna connector (J2101) pass through the bandpass filter (L2235-L2242, L2244, L2245, C2229, C2235, C2237, C2239, C2243-C2250, C2252-C2254, C2263, C2264) to obtain desired 12 MHz signals and suppress any undesired signals.

(4) 16 MHz signals

The signals from the antenna connector (J2101) pass through the bandpass filter (L2205, L2206, L2209, L2212, L2215, L2218, L2223, L2224, L2230, L2233, C2203, C2206, C2209, C2212, C2215, C2218, C2221, C2224, C2226, C2227, C2238, C2242) to obtain desired 16 MHz signals and suppress any undesired signals.

The filtered signals are amplified at the RF amplifiers (Q2301, Q2302), and then applied to the 1st mixer circuit (D2301).

3-4-2 DSC 1ST MIXER AND 1ST IF CIRCUITS (MAIN UNIT)

The 1st mixer circuit converts the received signal into a fixed frequency of the 1st IF signal with a PLL output frequency. By changing the PLL frequency, only the desired frequency will pass through a crystal filter at the next stage of the 1st mixer.

The signals from the RF circuit are mixed at the 1st mixer (D2301) with a 1st LO signal coming from the PLL circuit to produce a 10.7 MHz 1st IF signal.

The 1st IF signal is applied to the crystal bandpass filters (FI231) to suppress out-of-band signals. The filtered 1st IF signal is amplified at the 1st IF amplifiers (Q2401, Q2501), then applied to the 2nd mixer circuit (IC2701, pin 3).

3-4-3 DSC 2ND IF AND DSP CIRCUITS (MAIN AND DSP UNITS)

The 2nd mixer circuit converts the 1st signal into a 2nd IF signal. A double conversion superheterodyne system (which converts receive signals twice) improves the image rejection ratio and obtain stable receiver gain.

The 1st IF signal from the 1st IF amplifiers is applied to the 2nd mixer IC (IC2701, pin 3), and is mixed with the 2nd LO signal to the converted into a 12 kHz 2nd IF signal.

The 12 kHz 2nd IF signal from the 2nd mixer (IC2701, pin 5) is applied to the 2nd IF amplifier (IC2702, pin 3), and then passes through the low-pass filter (IC2702, pins 5, 7). The signal is applied to the DSP unit as the "DSPI2" signal.

The signal is amplified at the differential amplifier (DSP unit; IC601), and is then applied to the A/D converter section in the CODEC IC (DSP unit; IC501). At the same time, the converted signal is level-shifted 5 V to 3.3 V in the IC (DSP unit; IC501).

The level-shifted signal is applied to the DSP IC (DSP unit; IC301) for the digital IF filter, demodulator and noise reduction, etc.

The output signal from the DSP IC is applied to the D/A converter section in the CODEC IC (DSP unit; IC501) to convert into the analog audio signals. Also the signals are level-shifted 3.3 V to 5 V at the level converter section in the IC.

The level-shifted audio signals are passed through the active filter (IC701b), and then applied to the MAIN unit via the J1102 (pin 21) as the "DSPO2" signal.

3-4-4 DSC AGC CIRCUIT (DSP AND MAIN UNITS)

The receiver gain is determined by the voltage on the AGC2 line from the DSP unit. The D/A converter for the AGC (IC102) supplies control voltage to the AGC2 line and sets the DSC receiver gain.

The 3rd IF signal from the CODEC IC (IC501) is detected at the AGC detector section in the DSP IC (IC301). The output signal from the DSP IC is level-shifted at the level converter (IC101) and applied to the D/A converter (IC102). The AGC voltage is amplified at the buffer amplifier section in the IC102 and applied to the MAIN unit to control the AGC2 line.

When receiving strong signals, the detected voltage increases and the AGC2 voltage decreases. As the AGC2 voltage is used for the bias voltage of the IF amplifiers (MAIN unit; Q2401, Q2501), IF amplifier gain is decreased.

3-5 FRONT UNIT (RC-25)

3-5-1 LCD CIRCUIT (DISPLAY AND CONNECT BOARDS)

The LCD (CONNECT board; DS8220) is controlled by the LCD driver (CONNECT board; IC8240) via the drive signals (CL1, RES, CS, RS, WR, RD, DB0-DB7) from the display board. The LCD's back light employs 24 LED (DS8300-DS8323). The LED are controlled by dimmer (Q8300, Q8301) and dimmer controller (IC8280) circuits.

3-5-2 MICROPHONE CIRCUIT (DISPLAY BOARD)

The AF signals from the microphone are amplified at the AF amplifier (IC8280) via the "MIC" signal. The signal is applied to the IC-M802's main unit through the J8903 via the "MI" signal.

3-5-3 GROUP AND CHANNEL DIALS CIRCUIT (DISPLAY AND SENSOR BOARDS)

• GROUP DIAL

The signals from the group dial (SENSOR board; S8801) is applied to the FRONT CPU (IC8201, pins 76, 77) via the "GPA" and "GPB" signals.

• CHANNEL DIAL

The signals from the channel dial (SENSOR board; S8802) is applied to the FRONT CPU (IC8201, pins 74, 75) via the "CHA" and "CHB" signals.

3-5-4 KEY'S BACK LIGHTS CIRCUIT (DISPLAY BOARD)

The key's back lights compose DS8340–DS8350, and are controlled by the dimmer circuit (Q8340–Q8345). The dimmer circuit is controlled by the CPU via the "DIMMER" signal from the FRONT CPU.

3-5-5 RESET CIRCUIT (DISPLAY BOARD)

The reset IC (IC8202) resets the FRONT CPU (IC8201) when IC-M802 is power ON or OFF.

3-6 POWER SUPPLY CIRCUITS

3-6-1 PA UNIT VOLTAGE LINE

LINE	DESCRIPTION
HV13	The voltage from an external power supply.
HV	The same voltage as the HV13 line passed through the fuse (F6701).
S13V	The same voltage as the HV13 line passes through the switching relay (RL6701).
PA8V	8 V for transmitter circuits regulated by the +8 regulator circuit (IC6601).
PAT8	8 V for transmitter circuits regulated by the T8 regulator circuit (Q6601, Q6602).

3-6-2 DISPLAY BOARD (RC-25) VOLTAGE LINE

LINE	DESCRIPTION
HV13	The voltage from PA and MAIN units via the J8602.
10V	Common 10 V converted from the 13 V line and regulated by the +10 regulator circuit (IC8301).
5V	Common 5 V converted from the 5 V line and regulated by the +5 regulator circuit (IC8290).

3-6-3 PLL UNIT VOLTAGE LINE

LINE	DESCRIPTION
HV13	The voltage from PA and MAIN units via the J5001.
H5V	Common 5 V line from the MAIN unit via the J5071.
5V	Common 5 V converted from the 13 V line and regulated by the +5 regulator circuit (IC5001).
8VL	Common 8 V converted from the 13 V line and regulated by the +8 regulator circuit (IC5031).

3-6-4 MAIN UNIT VOLTAGE LINE

LINE	DESCRIPTION
S13V	The voltage from the PA unit via the J1901.
R13V	Receive 13 V converted from the S13V line and regulated by the R13 regulator circuit (IC1303).
8V	Common 8 V converted from the 13 V line and regulated by the +8 regulator circuit (IC1502).
-5V	Common -5 V converted from the 13 V line and regulated by the -5 V DC-DC converter (IC1551, D1551, D1552)
T8	Transmit 8 V converted from the S13V line and regulated by the T8 regulator circuit (Q1501).
R8	Receive 8 V converted from the S13V line and regulated by the R8 regulator circuit (Q1502).
5V	Common 5 V converted from the 13 V line and regulated by the +5 regulator circuit (IC1501).
H5V	Common 5 V converted from the 5 V line and regulated by the +5 regulator circuit (IC3101).

3-7 PORT ALLOCATIONS

3-7-1 MAIN CPU (MAIN UNIT; IC3303)

Pin number	Port name	Description
1	TXS	Outputs T8 regulator circuit (MAIN unit; Q1501) control signal.
2	RXS	Outputs R8 regulator circuit (MAIN unit; Q1502) control signal.
3	PSEL	Outputs serial strobe signal to the latch control IC (IC3601, pin 1).
4	BEEP	Outputs beep audio signals.
6	PDAT	Outputs serial data signal for the PLL circuits.
7	PCK	Outputs serial clock signal for the PLL circuit.
11	UNLK	Input port for PLL unlock signal.
19	PWRS	Outputs switching relay (PA unit; RL6701) control signal.
23	DRES	Outputs the RESET signal.
24	STBO	Outputs strobe signal for the DSP unit.
26	RQO	Outputs data signal for the DSP unit.
27	RQI	Outputs serial data signal for the DSP unit.
28	DO	Input port for the serial data signal from the DSP unit.
29	NMEI	Input port for the NMEA signal.
30	NMEO	Outputs NMEA signal for PLL unit.
32	NMEI2	Input port for the GPS signal from the PLL unit.
33	LSTB	Outputs serial strobe signal for the IC3602, IC3603.
34	DSTB	Outputs strobe signal for the D/A converter IC (IC3606).
37	LCK	Outputs serial clock signal for IC3602–IC3605, D/A converter IC (IC3606) and DSP unit.
38	LDAT	Outputs serial data signal for the IC3602, IC3603 and D/A converter IC (IC3606).
47	MSTB	Outputs serial strobe signal for the IC3603, IC3605.
48	PSTB	Outputs PLL strobe control signal for the LATCH controller (IC3601).
49	SQLC	Outputs squelch control signal for the SQL switching IC (IC1701).
79	STAT	Outputs the optional antenna tuner start control signal for the PA unit via the J1901.
80	KEYS	Input port for the optional antenna tuner key control signal via the PA unit.
89	EDT	I/O port for the serial data signals from/to the EEPROM (IC3301, pin 5).

MAIN CPU (MAIN UNIT; IC3303)—continued

Pin number	Port name	Description
90	ECK	Outputs serial clock signal for the EEPROM (IC3301, pin 6).
92	ASEN	Input port for the SEND signal from the accessory connector (J1801).
93	TEMP	Input port for the power amplifier temperature signal from the PA unit.
94	NSEN	Input port for the SEND signal from the AF/MOD connector (PLL unit; J5051, pin 8) via the J3602.
97	RFML	Input port for the RF/ANTC meter's voltage from the meter amplifier (IC1201, pin 14).

3-7-2 OUTPUT EXPANDER IC FOR THE PLL STROBE (IC3601)

Pin number	Port name	Description
4	PST1	Outputs strobe signal for the DDS IC (PLL unit; IC5101, pin 91).
5	PST2	Outputs strobe signal for the PLL IC (PLL unit; IC5401, pins 13).
6	PST3	Outputs strobe signal for the DDS IC (PLL unit; IC5701, pin 41).
7	PST4	Outputs strobe signal for the DDS IC (PLL unit; IC5801, pin 41).
14	PST5	Outputs strobe signal for the DDS IC (PLL unit; IC5901, pin 41).

3-7-3 OUTPUT EXPANDER IC FOR THE FILTER UNIT (IC3602)

Pin number	Port name	Description
4	L0S	Outputs 0.5–1.999999 MHz low-pass filter control signal.
5	L1S	Outputs 2–2.999999 MHz low-pass filter control signal.
6	L2S	Outputs 3–4.999999 MHz low-pass filter control singnal.
7	L3S	Outputs 5–6.999999 MHz low-pass filter control signal.
11	L7S	Outputs 20–30 MHz low-pass filter control signal.
12	L6S	Outputs 14–19.999999 MHz low-pass filter control signal.
13	L5S	Outputs 10–13.999999 MHz low-pass filter control signal.
14	L4S	Outputs 7–9.999999 MHz low-pass filter control signal.

3-7-4 OUTPUT EXPANDER IC FOR BANDPASS FILTERS (IC3604)

Pin number	Port name	Description
4	B0S	Outputs 0.5–1.999999 MHz bandpass filter control signal.
5	B1S	Outputs 2–2.999999 MHz bandpass filter control signal.
6	B2S	Outputs 3–4.999999 MHz bandpass filter control signal.
7	B3S	Outputs 5–6.999999 MHz bandpass filter control signal.
11	B7S	Outputs 18–23.999999 MHz bandpass filter control signal.
12	B6S	Outputs 14–17.999999 MHz bandpass filter control signal.
13	B5S	Outputs 10–13.999999 MHz bandpass filter control signal.
14	B4S	Outputs 7–9.999999 MHz bandpass filter control signal.

3-7-7 D/A CONVERTER IC (IC3606)

Pin number	Port name	Description
5	POCV	Outputs transmit power control signal. The signal is applied to the ALC amplifier (IC1201, pin 3) and RF amplifier (Q701).
6	AFG	Outputs the VCA control signal. The signal is applied to the VCA amplifier (IC1603, pin 8) and RC-25 controller via the J2051, pin 5.
11	FANS	Outputs the cooling fan control signal. The signal is applied to the fan controller (PA unit; IC6501, Q6501).
13	TRXS	Outputs the transmitter/receiver switching signal. The signal is applied to the switching relay circuit (FILTER unit; RL7301).

3-7-5 OUTPUT EXPANDER IC (IC3603)

Pin number	Port name	Description
4	NMS	Outputs NBDP MOD control signal for the gate mod IC (IC2001, pin 10).
5	EMS	Outputs external MOD control signal for the gate mode IC (IC2001, pin 11).
6	MMS	Outputs microphone MOD control signal for the gate mode IC (IC2001, pin 9).
11	NMRS	Outputs NMEA/RS-232C switching signal.
13	AFMS	Outputs AF mute control signal.
14	CSEN	Outputs SEND control signal for the buffer amplifier (IC1303, pin 6).

3-7-6 OUTPUT EXPANDER IC (IC3605)

Pin number	Port name	Description
4	B8S	Outputs 24–30 MHz bandpass filter control signal.
5	DBP1	Outputs bandpass filter control signal for DSC circuit (For 2.1875, 4.2075, 6.3120 MHz).
6	DBP2	Outputs bandpass filter control signal for the DSC circuit (8.4145 MHz).
7	DBP3	Outputs bandpass filter control signal for the DSC circuit (12.5770 MHz).
14	DBP4	Outputs bandpass filter control signal for the DSC circuit (16.8045 MHz).

**3-7-8 FRONT CPU PORT ALLOCATIONS (RC-25,
DISPLAY BOARD; IC8201)**

Pin number	Port name	Description
17	DSEL	Outputs DISTRESS LED control signal.
21	FUNK	Input port for the F switch.
22	TXK	Input port for the [TX] switch.
23	RXK	Input port for the [RX] switch.
24	FCHK	Input port for the [FREQ/CH] switch.
25	T0K	Input port for the [0] switch.
26	ENTK	Input port for the [ENT] switch.
28	PWRK	Input port for the [POWER] switch.
31	PMUT	Input port for the phone plug insert detection signal.
32	FPTT	Input port for the [PTT] switch from the microphone connector (J8701, pin 5).
33	AFGL	Input port for the AF volume control.
34	M1AD	Input port for the using microphone detection signal.
35	M2AD	Input port for the switch signal on the microphone.
36	M3AD	Input port for the microphone connecting detection.
41–46	CL1 RS RD WR <u>RES</u> <u>CS</u>	Output LCD drive signals.
48–55	DB7–DB0	Output LCD drive signals.
57	T9K	Input port for the [9] switch.
58	T8K	Input port for the [8] switch.
59	T7K	Input port for the [7] switch.
60	T6K	Input port for the [6] switch.
61	T5K	Input port for the [5] switch.
62	T4K	Input port for the [4] switch.
63	T3K	Input port for the [3] switch.
64	T2K	Input port for the [2] switch.
65	T1K	Input port for the [1] switch.
66	EMLK	Input port for [e-mail]/[DSC]/[TS] switches.
67	SETK	Input port for the [SET] switch.
68	MODK	Input port for the [MODE] switch.
69	TUNK	Input port for the [TUNE] switch.
70	DALK	Input port for [CANCEL]/[TELCALL]/[TXF] switches.
71	CSCK	Input port for [DSC]/[CALL]/[CH0]/[SELCALL] switches.
72	DSTK	Input port for the [DISTRESS]/[CALL]/[ALM] switches.

SECTION 4 ADJUSTMENT PROCEDURES

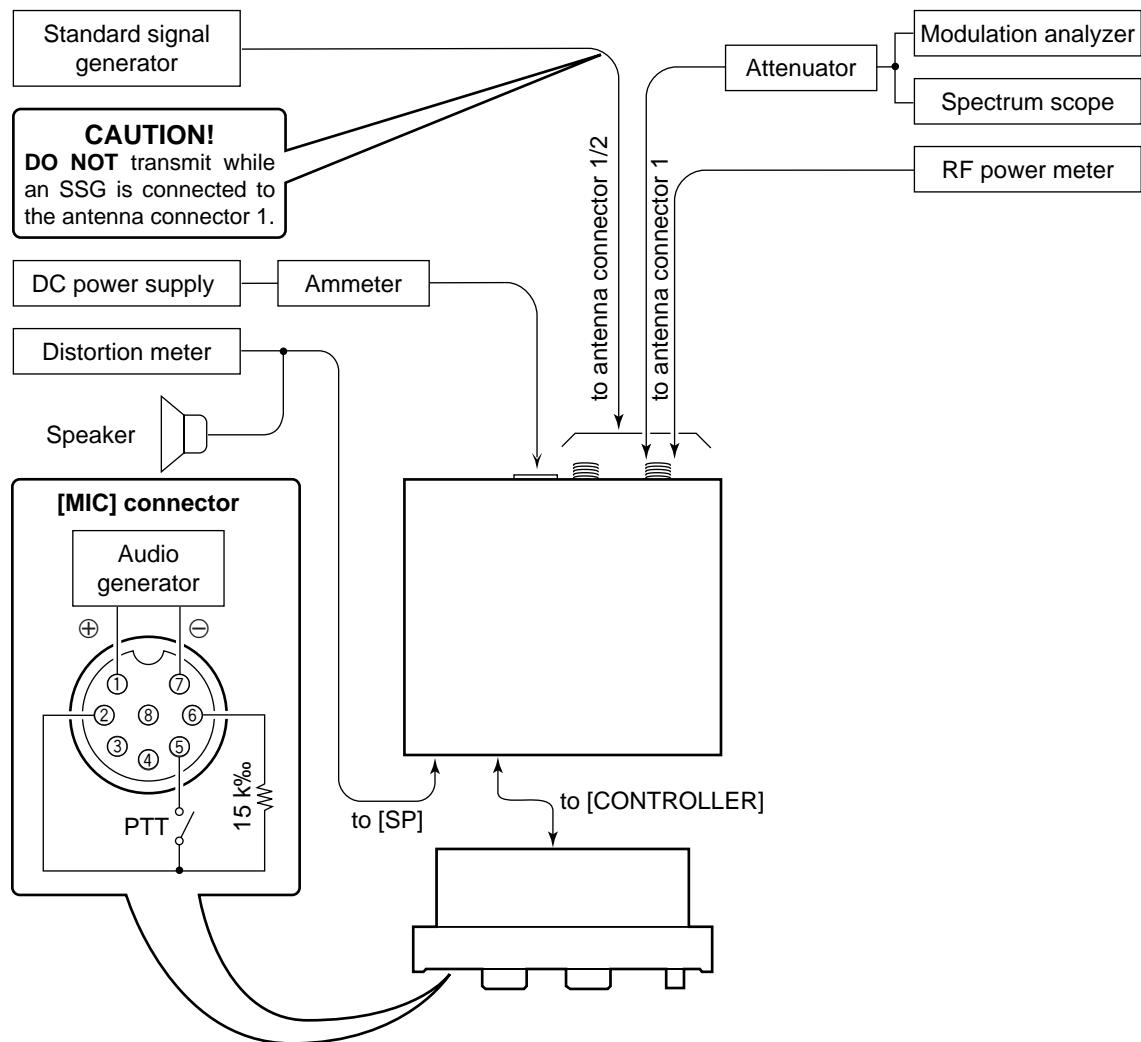
4-1 PREPARATION

When adjusting the contents on page 4-9, a JIG CABLE (see illustration as shown below) is required. Some adjustments must be performed on the "ADJUSTMENT MODE". Entering the "ADJUSTMENT MODE", see the next page.

■ REQUIRED TEST EQUIPMENT

EQUIPMENT	GRADE AND RANGE		EQUIPMENT	GRADE AND RANGE	
DC power supply	Output voltage Current capacity	: 7.2 V DC : 5 A or more	Audio generator	Frequency range Output level	: 300–3000 Hz : 1–500 mV
RF power meter (terminated type)	Measuring range Frequency range Impedance SWR	: 1–10 W : 300–600 MHz : 50 Ω : Less than 1.2 : 1	Attenuator	Power attenuation Capacity	: 40 or 50 dB : 10 W or more
Frequency counter	Frequency range Frequency accuracy Sensitivity	: 0.1–600 MHz : ±1 ppm or better : 100 mV or better	Standard signal generator (SSG)	Frequency range Output level	: 120–600 MHz : 0.1 μV–32 mV (−127 to −17 dBm)
FM deviation meter	Frequency range Measuring range	: DC–600 MHz : 0 to ±5 kHz	DC voltmeter	Input impedance	: 50 kΩ/V DC or better
Digital multimeter	Input impedance	: 10 MΩ/V DC or better	Oscilloscope	Frequency range Measuring range	: DC–20 MHz : 0.01–20 V
			AC millivoltmeter	Measuring range	: 10 mV–10 V

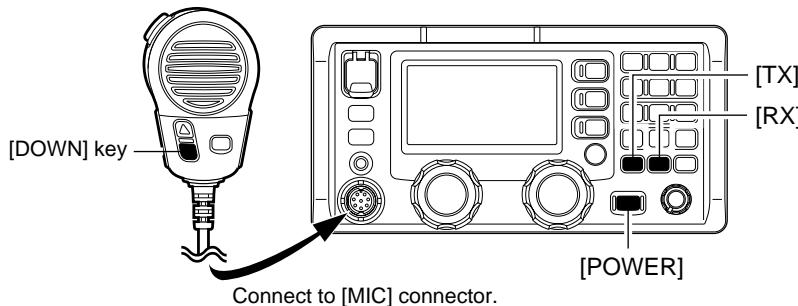
• CONNECTION



■ ENTERING THE ADJUSTMENT MODE

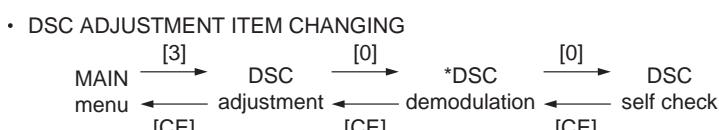
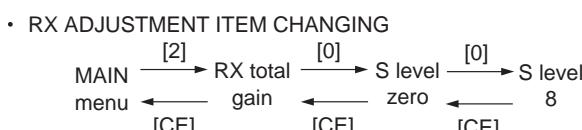
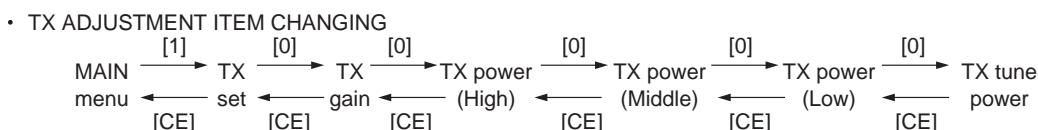
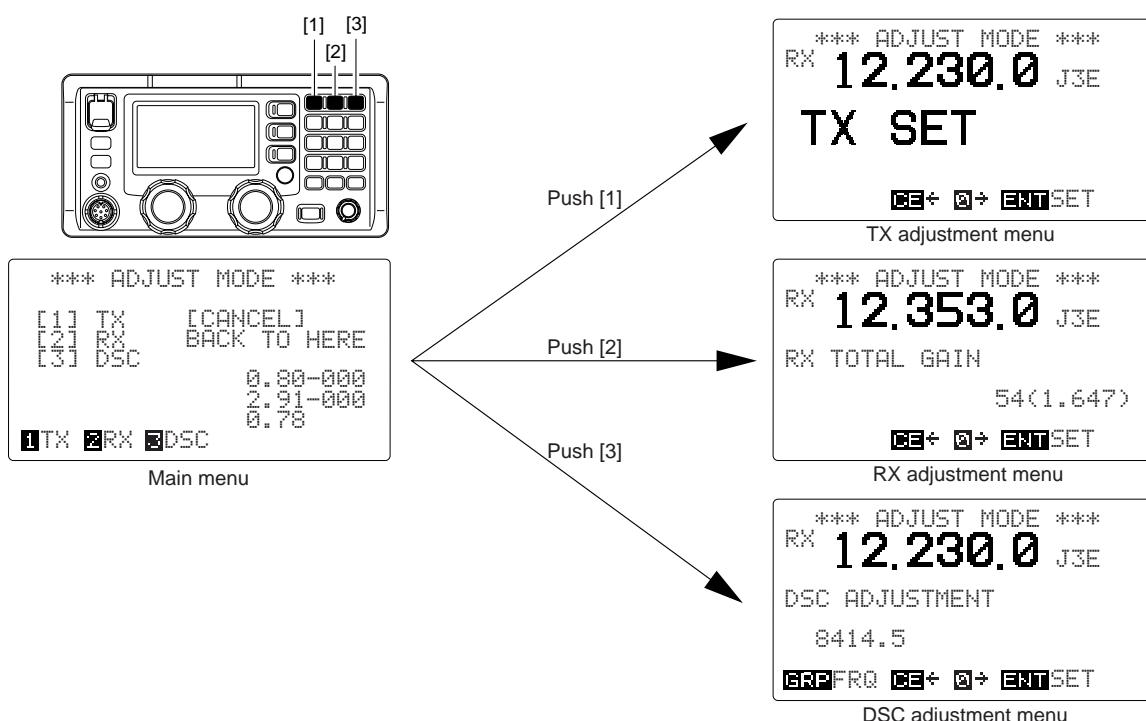
- ① Turn the power OFF.
- ② Push and hold the HM-135's [DOWN] key, [TX] and [RX] buttons, then turn the power ON.

NOTE: Inserting the 15 kΩ resistor between 2 pin and 6 pin of HM-135 works same as pushing and holding HM-135's [DOWN] key (See the illustration on page 4-2).



■ OPERATING ON THE ADJUSTMENT MODE

- Enter the TX adjustment from the MAIN menu
 - Enter the RX adjustment from the MAIN menu
 - Enter the DSC adjustment from the MAIN menu
 - Change the next adjustment item
 - Return to the pre-adjustment item
- : Push the [1] button
 : Push the [2] button
 : Push the [3] button
 : Push the [0] button
 : Push the [CE] button

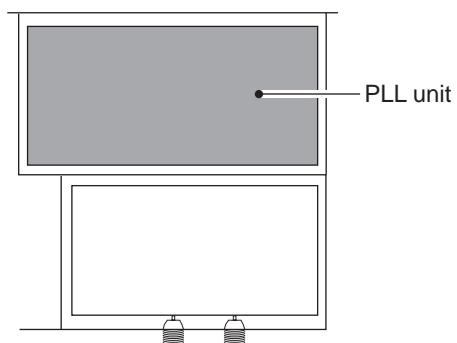


***NOTE:** DO NOT adjust the "DSC DEMODULATION". Thus, after adjusting the "DSC adjustment", push the [0] button twice to skip it.

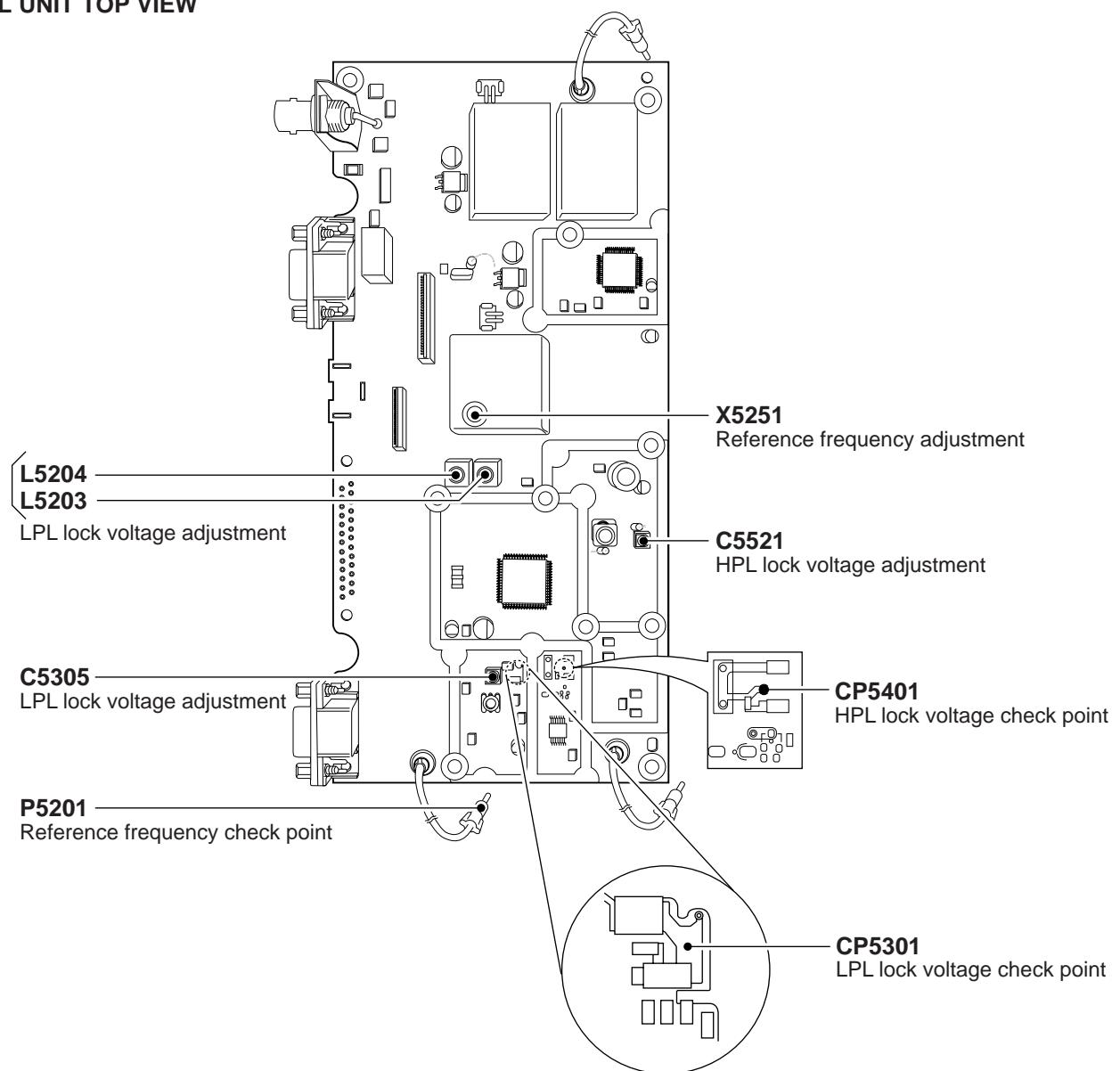
4-2 PLL UNIT ADJUSTMENT

ADJUSTMENT		ADJUSTMENT CONDITIONS		MEASUREMENT		VALUE	ADJUSTMENT	
				UNIT	LOCATION		UNIT	ADJUST
LPL LOCK VOLTAGE	1	<ul style="list-style-type: none"> • Operating frequency : 0.5000 MHz • Mode : J3E • Receiving 		PLL	Connect a digital multi meter or oscilloscope to check point CP5301.	1.0 V	PLL	C5305
	2					Less than 4.0 V		Verify
HPL LOCK VOLTAGE	1	<ul style="list-style-type: none"> • Operating frequency : 29.9999 MHz • Mode : J3E • Receiving 		PLL	Connect a digital multi meter or oscilloscope to check point CP5401.	4.2 V	PLL	C5521
	2					More than 0.8 V		Verify
REFERENCE FREQUENCY	1	<ul style="list-style-type: none"> • Wait for 5 minutes after power ON. • Terminate P5201 on the PLL unit to ground with a 50 Ω resister. • Receiving 		PLL	Connect an RF voltmeter to check point P5201.	Maximum level	PLL	L5203, L5204
	2				Connect a frequency counter to check point P5201.	63.999990 MHz		X5251

• IC-M802 BOTTOM VIEW



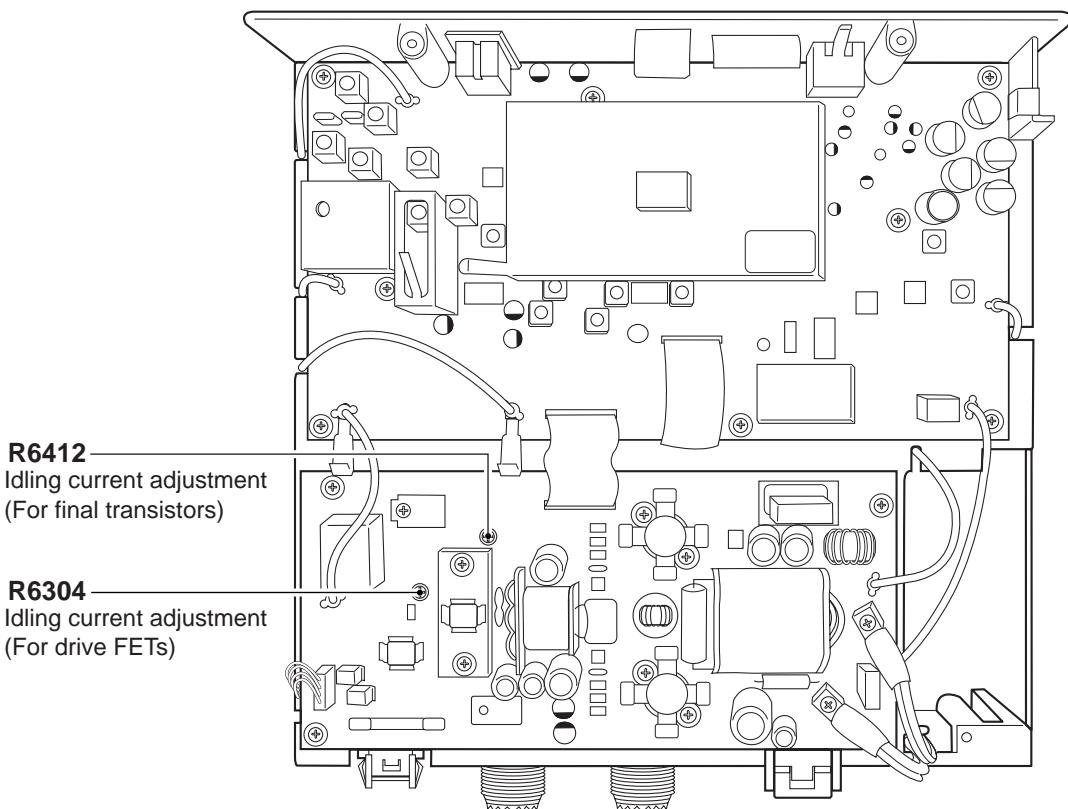
• PLL UNIT TOP VIEW



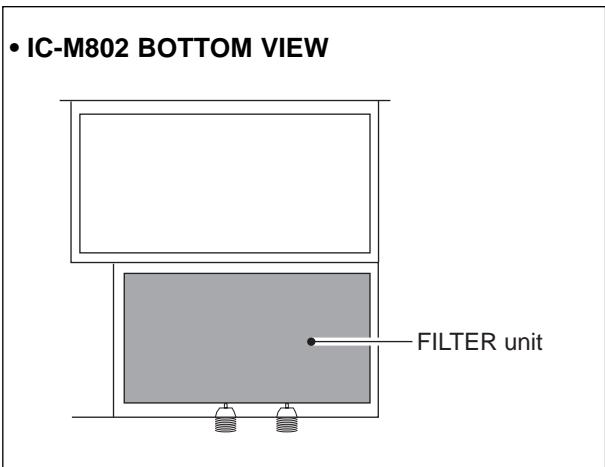
4-3 PA AND FILTER UNITS ADJUSTMENT

ADJUSTMENT		ADJUSTMENT CONDITIONS	MEASUREMENT		VALUE	ADJUSTMENT	
			UNIT	LOCATION		UNIT	ADJUST
IDLING CURRENT (For drive FETs)	1	<ul style="list-style-type: none"> • Operating frequency : 12.2350 MHz • Mode : J3E • Apply no audio signal to the [MICROPHONE] connector. • Disconnect J6702. • Preset R6304 and R6412 on the PA unit to max. counter clockwise. • Connect a dummy load or RF power meter to the [ANT] connector. • Transmitting 	Rear panel	Connect an ammeter (10 A) between an external power supply and the transceiver.	2.0 A	PA	R6304
(For final transistors)	2	<ul style="list-style-type: none"> • Transmitting • After adjustment, connect J6701 to the PA unit. 			0.7 A		R6412
SWR DETECTOR	1	<ul style="list-style-type: none"> • Operating frequency : 22.0000 MHz • Mode : J3E • Connect the CP7332 (FOR line) on the FILTER unit to ground. 	Rear panel	Connect an RF power meter to the [ANT] connector.	140 W	Audio generator	Output level
	2	<ul style="list-style-type: none"> • Connect an audio generator to the [MICROPHONE] connector and set as: Frequency : 1.5 kHz • Transmitting 	FIL-TER	Connect a DC voltmeter to check point, CP7331.	Minimum level	FIL-TER	C7275
<ul style="list-style-type: none"> • After adjustment, disconnect CP7332 (FOR line) on the FILTER unit from ground. 							

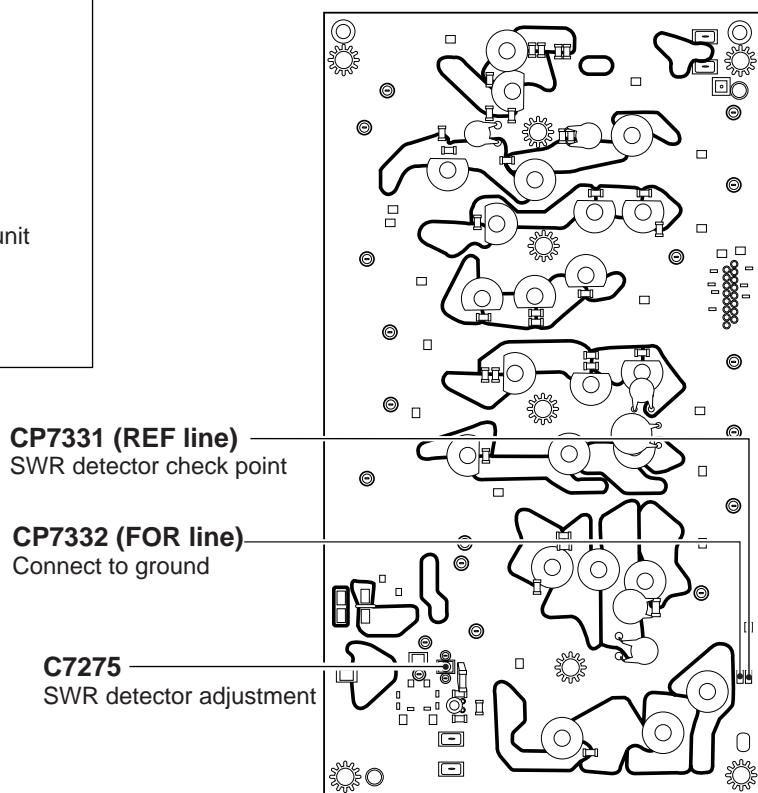
• IC-M802 TOP VIEW



• FILTER UNIT BOTTOM VIEW



• IC-M802 BOTTOM VIEW



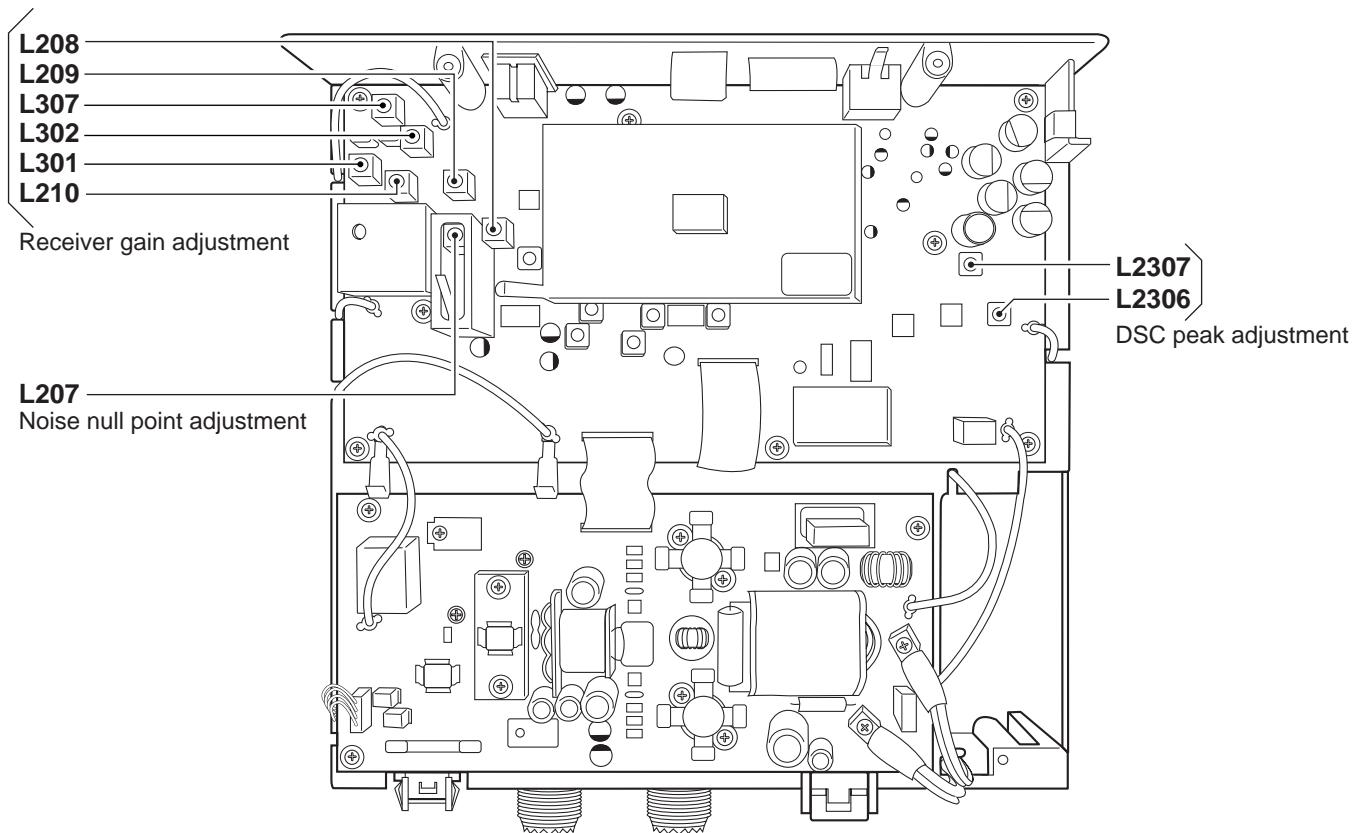
4-4 RECEIVER ADJUSTMENTS

"TOTAL GAIN", "S-METER" and "DSC PEAK" adjustments must be performed at "ADJUSTMENT MODE".

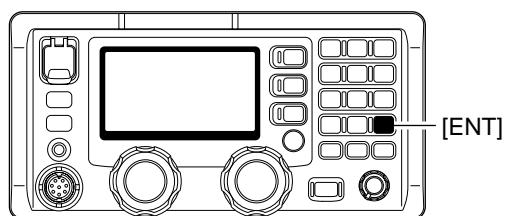
ADJUSTMENT		ADJUSTMENT CONDITIONS	MEASUREMENT		VALUE	ADJUSTMENT									
			UNIT	LOCATION		UNIT	ADJUST								
NOISE NULL POINT	1	<ul style="list-style-type: none"> Operating frequency : 0.5 MHz Mode : H3E Preset L402 and L405 to max. clockwise. Preset L401, L403, L404 and L801 to max. counter clockwise. Set the standard signal generator to OFF (no signal output). Receiving 	Front panel	Connect an AC millivoltmeter to the [SP] jack with a 4 Ω dummy load.	Minimum output level	MAIN	L207								
RECEIVER GAIN	1	<ul style="list-style-type: none"> Operating frequency : 12.2310 MHz Mode : J3E Connect a standard signal generator to the [ANT] connector and set as: <table> <tr><td>Frequency</td><td>: 12.2310 MHz</td></tr> <tr><td>Level</td><td>: 0.5 μV*</td></tr> <tr><td></td><td>(-113 dBm)</td></tr> <tr><td>Modulation</td><td>: OFF</td></tr> </table> Receiving 	Frequency	: 12.2310 MHz	Level	: 0.5 μV*		(-113 dBm)	Modulation	: OFF	Front panel	Connect an AC millivoltmeter to the [SP] jack with a 4 Ω dummy load.	Maximum output level	MAIN	L208, L209, L210, L301, L302, L307
Frequency	: 12.2310 MHz														
Level	: 0.5 μV*														
	(-113 dBm)														
Modulation	: OFF														
TOTAL GAIN	1	<ul style="list-style-type: none"> While pushing HM-135's "DOWN" key, [TX] and [RX] switches, then turn power ON. Push [2] switch to enter the RX adjustment mode. Connect a standard signal generator to the [ANT] connector and set as: <table> <tr><td>Frequency</td><td>: 12.3540 MHz</td></tr> <tr><td>Level</td><td>: 320 μV*</td></tr> <tr><td></td><td>(-57 dBm)</td></tr> <tr><td>Modulation</td><td>: OFF</td></tr> </table> Receiving 	Frequency	: 12.3540 MHz	Level	: 320 μV*		(-57 dBm)	Modulation	: OFF	<ul style="list-style-type: none"> Push the [ENT] switch to write the adjustment value in the memory. 				
Frequency	: 12.3540 MHz														
Level	: 320 μV*														
	(-57 dBm)														
Modulation	: OFF														
S-METER	1	<ul style="list-style-type: none"> Set the standard signal generator to OFF (no signal output). 	<ul style="list-style-type: none"> Push the [ENT] switch to write the adjustment value in the memory. 												
	2	<ul style="list-style-type: none"> Set the standard signal generator as: <table> <tr><td>Frequency</td><td>: 12.3540 MHz</td></tr> <tr><td>Level</td><td>: 10 mV*</td></tr> <tr><td></td><td>(-27 dBm)</td></tr> </table> Receiving 	Frequency	: 12.3540 MHz	Level	: 10 mV*		(-27 dBm)	<ul style="list-style-type: none"> Push the [ENT] switch to write the adjustment value in the memory. 						
Frequency	: 12.3540 MHz														
Level	: 10 mV*														
	(-27 dBm)														
DSC PEAK	1	<ul style="list-style-type: none"> Push [3] switch to enter the DSC adjustment mode. LCD displayed :"DSC ADJUSTMENT". DSC frequency : 8.4145 MHz Connect a standard signal generator to the [DSC ANT] connector and set as: <table> <tr><td>Frequency</td><td>: 8.4145 MHz</td></tr> <tr><td>Level</td><td>: 0.5 μV*</td></tr> <tr><td></td><td>(-113 dBm)</td></tr> <tr><td>Modulation</td><td>: OFF</td></tr> </table> Receiving 	Frequency	: 8.4145 MHz	Level	: 0.5 μV*		(-113 dBm)	Modulation	: OFF	Front panel	Connect an AC millivoltmeter to the [SP] jack with a 4 Ω dummy load.	Maximum output level	MAIN	L2306, L2307
Frequency	: 8.4145 MHz														
Level	: 0.5 μV*														
	(-113 dBm)														
Modulation	: OFF														
DSC SELF CHECK	1	<ul style="list-style-type: none"> LCD displayed :"DSC SELF CHECK" DSC frequency : 8.4145 MHz Connect an RF power meter to the [DSC ANT] connector on the rear panel. Receiving 	<ul style="list-style-type: none"> Push the [ENT] switch, and then verify to return to the "Main menu" on the LCD display. When this check is failure, displayed "NG" on LCD, and beep sound. 												

*The output level of the standard signal generator (SSG) is indicated as the SSG's open circuit.

• IC-M802 TOP VIEW



• RC-25 FRONT PANEL

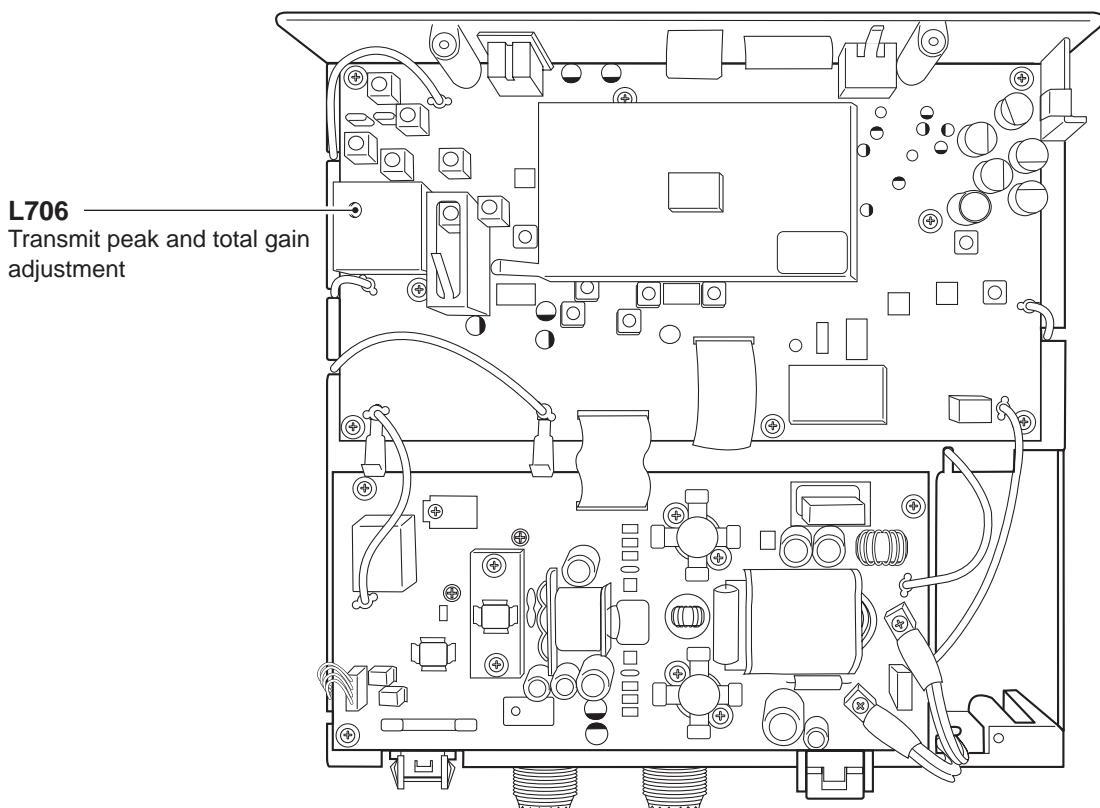


4-5 TRANSMITTER ADJUSTMENTS

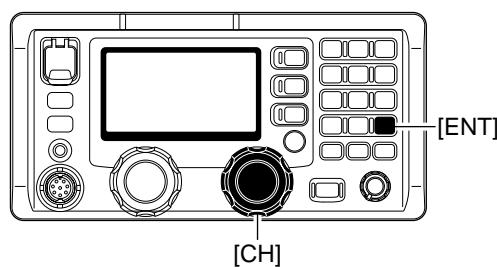
The following adjustments must be performed at "ADJUSTMENT MODE" after "SWR DETECTOR" and "RECEIVER" ADJUSTMENTS in the SECTION 4-3 and 4-4.

ADJUSTMENT		ADJUSTMENT CONDITIONS	MEASUREMENT		VALUE	ADJUSTMENT					
			UNIT	LOCATION		UNIT	ADJUST				
TRANSMIT PEAK AND TOTAL GAIN (TX PEAK)	1	<ul style="list-style-type: none"> While pushing HM-135's DOWN key, [TX] and [RX] switches, turn power ON. Connect an audio generator to the [MICROPHONE] connector and set as: <table> <tr><td>Frequency</td><td>: 1.5 kHz</td></tr> <tr><td>Level</td><td>: 10 mV</td></tr> </table> Push [1] switch to enter the TX adjustment mode (LCD displayed: "TX SET"). Push [ENT] switch, then the transceiver is transmitting. 	Frequency	: 1.5 kHz	Level	: 10 mV	Rear panel	Connect an RF power meter to the [ANT] connector.	Maximum output power	MAIN	L706
Frequency	: 1.5 kHz										
Level	: 10 mV										
2	<ul style="list-style-type: none"> After setting 75 W, push the [ENT] key to write the adjustment value in the memory. 	75 W	RC-25 [CH] DIAL								
TX POWER (HIGH)	1	<ul style="list-style-type: none"> LCD displayed : "TX POWER HIGH" Set the audio generator as: <table> <tr><td>Frequency</td><td>: 1.5 kHz</td></tr> <tr><td>Level</td><td>: 100 mV</td></tr> </table> 	Frequency	: 1.5 kHz	Level	: 100 mV	Rear panel	Connect an RF power meter to the [ANT] connector.	140 W	RC-25	[CH] DIAL
Frequency	: 1.5 kHz										
Level	: 100 mV										
2	<ul style="list-style-type: none"> After setting 140 W, push the [ENT] key to write the adjustment value in the memory. 										
(MIDDLE)	1	<ul style="list-style-type: none"> LCD displayed : "TX POWER MID" Set the audio generator as: <table> <tr><td>Frequency</td><td>: 1.5 kHz</td></tr> <tr><td>Level</td><td>: 100 mV</td></tr> </table> 	Frequency	: 1.5 kHz	Level	: 100 mV	Rear panel	Connect an RF power meter to the [ANT] connector.	70 W	RC-25	[CH] DIAL
Frequency	: 1.5 kHz										
Level	: 100 mV										
2	<ul style="list-style-type: none"> After setting 70 W, push the [ENT] key to write the adjustment value in the memory. 										
(LOW)	1	<ul style="list-style-type: none"> LCD displayed : "TX POWER LOW" Set the audio generator as: <table> <tr><td>Frequency</td><td>: 1.5 kHz</td></tr> <tr><td>Level</td><td>: 100 mV</td></tr> </table> 	Frequency	: 1.5 kHz	Level	: 100 mV	Rear panel	Connect an RF power meter to the [ANT] connector.	20 W	RC-25	[CH] DIAL
Frequency	: 1.5 kHz										
Level	: 100 mV										
2	<ul style="list-style-type: none"> After setting 20 W, push the [ENT] key to write the adjustment value in the memory. 										
TUNE POWER	1	<ul style="list-style-type: none"> LCD displayed : "TX POWER TUNE" Set the audio generator as: <table> <tr><td>Frequency</td><td>: 1.5 kHz</td></tr> <tr><td>Level</td><td>: 100 mV</td></tr> </table> 	Frequency	: 1.5 kHz	Level	: 100 mV	Rear panel	Connect an RF power meter to the [ANT] connector.	10 W	RC-25	[CH] DIAL
Frequency	: 1.5 kHz										
Level	: 100 mV										
2	<ul style="list-style-type: none"> After setting 10 W, push the [ENT] key to write the adjustment value in the memory. 										

• IC-M802 TOP VIEW



• RC-25 FRONT PANEL



[PA UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
C6708	4030006880	S.CERAMIC	C1608 JB 1H 472K-T
C6801	4030011600	S.CERAMIC	C1608 JB 1E 104K-T
RL6701	6330001470	RELAY	AJS1311
J6301	6510021530	CONNECTOR	IMSA-9230B-1-04Z024-T
J6302	6510021530	CONNECTOR	IMSA-9230B-1-04Z024-T
J6401	6510007020	CONNECTOR	TMP-J01X-V6
J6411	6510021390	CONNECTOR	IMSA-9230B-1-03Z024-T
J6412	6510021390	CONNECTOR	IMSA-9230B-1-03Z024-T
J6501	6510003390	CONNECTOR	B03B-EH-S
J6701	6510022540	S.CONNECTOR	20FMN-BMTTR-A-TBT
J6702	6510007020	CONNECTOR	TMP-J01X-V6
J6703	6510003400	CONNECTOR	B04B-EH-S
F6701	5210000060	FUSE	FGB 5A
F6702	5220000230	HOLDER	S-N5054
F6703	5220000230	HOLDER	S-N5054
W6401	9054500690	WIRE	76/98/020/X98/X98
W6402	9054500690	WIRE	76/98/020/X98/X98
W6601	9045201001	WIRE	74/98/040/X98/X98
W6701	7030008240	S.JUMPER	ERJ12YJ0R00U
EP6001	0910054694	PCB	B 5724D
EP6601	6910000630	BEAD	FSRH070140RN000B (FSOH070RN)
EP6602	6510018330	TERMINAL	F4053A
EP6603	6510018330	TERMINAL	F4053A

[FILTER UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
L7215	6200005010	S.COIL	NL 252018T-100J
L7216	6140002290	COIL	LR-242 (T68-10)
L7217	6140002290	COIL	LR-242 (T68-10)
L7218	6140002290	COIL	LR-242 (T68-10)
L7241	6200005010	S.COIL	NL 252018T-100J
L7244	6200005010	S.COIL	NL 252018T-100J
L7245	6140002330	COIL	LR-262 (T68-6)
L7246	6140002330	COIL	LR-262 (T68-6)
L7248	6140002330	COIL	LR-262 (T68-6)
L7271	6200002040	S.COIL	NL 252018T-101J
L7272	6140003490	COIL	LR-391
L7273	6200002040	S.COIL	NL 252018T-101J
L7321	6140001460	COIL	LR-170
L7331	6200002040	S.COIL	NL 252018T-101J
L7332	6200002040	S.COIL	NL 252018T-101J
R7271	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R7272	7030003490	S.RESISTOR	ERJ3GEYJ 272 V (2.7 kΩ)
R7273	7030003520	S.RESISTOR	ERJ3GEYJ 472 V (4.7 kΩ)
R7274	7030008190	S.RESISTOR	ERJ12YJ330U (33 Ω)
R7275	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R7276	7030003490	S.RESISTOR	ERJ3GEYJ 272 V (2.7 kΩ)
R7277	7030003520	S.RESISTOR	ERJ3GEYJ 472 V (4.7 kΩ)
R7301	7030003200	S.RESISTOR	ERJ3GEYJ 100 V (10 Ω)
R7314	7540000230	ABSORBER	SA05 401N
R7315	7030010650	S.RESISTOR	ERJ12YJ823U (82 kΩ)
C7031	4510005480	ELECTROLYTIC	50 MV 1 HC
C7032	4030011600	S.CERAMIC	C1608 JB 1E 104K-T
C7033	4320000651	DIP MICA	KD20C 102J5A
C7034	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7035	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7036	4320000981	DIP MICA	KD20C 222J5A
C7037	4010005360	CERAMIC	HM11SJ SL 301J 500V
C7038	4010005880	CERAMIC	HM95SJ SL 271J 500V
C7039	4320000951	DIP MICA	KD20C 272J5A
C7041	4030011120	S.CERAMIC	GRM31M2C2H100JV01 (GRM42-6 CH)
C7042	4010005930	CERAMIC	HM11SJ SL 391J 500V
C7043	4010005930	CERAMIC	HM11SJ SL 391J 500V
C7044	4010005930	CERAMIC	HM11SJ SL 391J 500V
C7045	4010005370	CERAMIC	HM11SJ SL 331J 500V
C7046	4030011600	S.CERAMIC	C1608 JB 1E 104K-T
C7049	4010005850	CERAMIC	HM95SJ SL 181J 500V
C7061	4510005480	ELECTROLYTIC	50 MV 1 HC
C7062	4030011600	S.CERAMIC	C1608 JB 1E 104K-T
C7063	4030011730	S.CERAMIC	GRM31M2C2H101JV01 (GRM42-6 CH)
C7064	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7065	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7066	4010005370	CERAMIC	HM11SJ SL 331J 500V
C7067	4010005880	CERAMIC	HM95SJ SL 271J 500V
C7068	4010005880	CERAMIC	HM95SJ SL 271J 500V
C7069	4010005880	CERAMIC	HM95SJ SL 271J 500V
C7070	4030011730	S.CERAMIC	GRM31M2C2H101JV01 (GRM42-6 CH)
C7071	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7072	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7073	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7074	4010005880	CERAMIC	HM95SJ SL 271J 500V
C7075	4010005370	CERAMIC	HM11SJ SL 331J 500V
C7076	4010005370	CERAMIC	HM11SJ SL 331J 500V
C7077	4010005370	CERAMIC	HM11SJ SL 331J 500V
C7078	4010005930	CERAMIC	HM11SJ SL 391J 500V
C7079	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7080	4030012480	S.CERAMIC	GRM31M2C2H121JV01 (GRM42-6 CH)
C7081	4030012480	S.CERAMIC	GRM31M2C2H121JV01 (GRM42-6 CH)
C7082	4010005930	CERAMIC	HM11SJ SL 391J 500V
C7083	4010005930	CERAMIC	HM11SJ SL 391J 500V
C7084	4030011550	S.CERAMIC	GRM31M2C2H680JV01 (GRM42-6 CH)
C7085	4030011600	S.CERAMIC	C1608 JB 1E 104K-T
C7086	403001120	S.CERAMIC	GRM31M2C2H100JV01 (GRM42-6 CH)
C7087	4030012480	S.CERAMIC	GRM31M2C2H121JV01 (GRM42-6 CH)
C7091	4510005480	ELECTROLYTIC	50 MV 1 HC
C7092	4030006880	S.CERAMIC	C1608 JB 1H 472K-T
C7094	4010005370	CERAMIC	HM11SJ SL 331J 500V
C7095	4010005930	CERAMIC	HM11SJ SL 391J 500V
C7096	4010005930	CERAMIC	HM11SJ SL 391J 500V
C7098	4010005370	CERAMIC	HM11SJ SL 331J 500V
C7099	4010006410	CERAMIC	HM13SJ SL 471J 500V
C7100	4030011730	S.CERAMIC	GRM31M2C2H101JV01 (GRM42-6 CH)
C7101	4010005360	CERAMIC	HM11SJ SL 301J 500V
C7102	4030006880	S.CERAMIC	C1608 JB 1H 472K-T

S.=Surface mount

[VARISTOR-1 BOARD]

REF NO.	ORDER NO.	DESCRIPTION	
D6431	1790001760	S.VARISTOR	MA30-(TX)
C6431	4030008920	S.CERAMIC	C1608 JB 1H 473K-T
EP6431	0910054761	PCB	B 5746A

EX-2432
[DSP UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
IC1	1130010010	S.IC	TC7WHU04FU
IC3	1130010540	S.IC	SN74AHC74PWR
IC4	1130010540	S.IC	SN74AHC74PWR
IC51	1130010010	S.IC	TC7WHU04FU
IC101	1130010520	S.IC	SN74HCT244APWR
IC102	1110004770	S.IC	BU9480F-E2
IC152	1180002320	S.REG	TK11218CMCL
IC161	1180002020	S.REG	BA033FP-E2
IC171	1180002270	S.REG	TK11250CMCL
IC201	1130010680	S.IC	M29W102BB-70N1
IC202	1130008040	S.IC	TC7SH04FU
IC203	1130008810	S.IC	TC7SH32FU (TE85L)
IC301	1140010330	S.IC	TMS320VC33PGEA120
IC401	1130010550	S.IC	SN74AHC595PWR
IC403	1130010530	S.IC	SN74AHCT374PWR
IC404	1130010530	S.IC	SN74AHCT374PWR
IC405	1130004670	S.IC	BU4021BF-T1
IC406	1130010540	S.IC	SN74AHC74PWR
IC501	1190001610	S.IC	AK4528-VF
IC601	1110005420	S.IC	BA15532F-E2
IC651	1110005420	S.IC	BA15532F-E2
IC701	1110001900	S.IC	μPC4570G2-T1
IC801	1130010560	S.IC	SN74AHC244PWR
Q201	1590001660	S.TRANSISTOR	XP4312 (TX)
Q301	1590000430	S.TRANSISTOR	DTC144EUA T106
Q302	1590000430	S.TRANSISTOR	DTC144EUA T106
D301	1750000370	S.DIODE	DA221 TL
D302	1750000370	S.DIODE	DA221 TL
D303	1750000370	S.DIODE	DA221 TL
D304	1750000370	S.DIODE	DA221 TL
D901	1790000970	S.DIODE	MA729 (TX)
X1	6050009860	S.XTAL	CR-566 (12.288 MHz)
X51	6050011270	S.XTAL	CR-709 (60.000 MHz)
L1	6200003280	S.COIL	NL 252018T-2R2J
L2	6200003280	S.COIL	NL 252018T-2R2J
L3	6200003280	S.COIL	NL 252018T-2R2J
L51	6200003280	S.COIL	NL 252018T-2R2J
L52	6200001980	S.COIL	NL 252018T-1R0J
L101	6200003280	S.COIL	NL 252018T-2R2J
L102	6200003280	S.COIL	NL 252018T-2R2J
L301	6200003280	S.COIL	NL 252018T-2R2J
L302	6200009840	S.COIL	LQH 3C 2R2M 34
L401	6200003280	S.COIL	NL 252018T-2R2J
L403	6200003280	S.COIL	NL 252018T-2R2J
L404	6200003280	S.COIL	NL 252018T-2R2J
L405	6200003280	S.COIL	NL 252018T-2R2J
L406	6200003280	S.COIL	NL 252018T-2R2J
L501	6200003280	S.COIL	NL 252018T-2R2J
L502	6200003280	S.COIL	NL 252018T-2R2J
L601	6200002040	S.COIL	NL 252018T-101J
L651	6200002040	S.COIL	NL 252018T-101J
L701	6200002040	S.COIL	NL 252018T-101J
L801	6200003280	S.COIL	NL 252018T-2R2J
R1	7030003800	S.RESISTOR	ERJ3GEYJ 105 V (1 MΩ)
R2	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R3	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R51	7030003800	S.RESISTOR	ERJ3GEYJ 105 V (1 MΩ)
R52	7030003320	S.RESISTOR	ERJ3GEYJ 101 V (100 Ω)
R151	7030007240	S.RESISTOR	ERJ12YJ680U (68 Ω)
R161	7030008190	S.RESISTOR	ERJ12YJ330U (33 Ω)
R162	7030007510	S.RESISTOR	ERJ12YJ270U (27 Ω)
R171	7030007180	S.RESISTOR	ERJ12YJ150U (15 Ω)
R201	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R303	7030003480	S.RESISTOR	ERJ3GEYJ 222 V (2.2 kΩ)
R304	7030003480	S.RESISTOR	ERJ3GEYJ 222 V (2.2 kΩ)
R305	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R306	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R307	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R308	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R309	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)

S.=Surface mount

[DSP UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
C757	4510006220	S.ELECTROLYTIC	ECEV1CA101UP
C758	4510004630	S.ELECTROLYTIC	ECEV1CA100SR
C801	4030006880	S.CERAMIC	C1608 JB 1H 472K-T
C802	4030006860	S.CERAMIC	C1608 JB 1H 102K-T
C901	4030006960	S.CERAMIC	C1608 CH 1H 050C-T
J901	6910013900	CONNECTOR	IMSA-9180S-22A
J902	6910012460	CONNECTOR	IMSA-9180S-30A
W1	7030003860	S.JUMPER	ERJ3GE JPW V
W2	7030008240	S.JUMPER	ERJ12YJ0R00U
W3	7030003860	S.JUMPER	ERJ3GE JPW V
W4	7030008240	S.JUMPER	ERJ12YJ0R00U
W5	7030003860	S.JUMPER	ERJ3GE JPW V
W151	7030008240	S.JUMPER	ERJ12YJ0R00U
W152	7030003860	S.JUMPER	ERJ3GE JPW V
W171	7030008240	S.JUMPER	ERJ12YJ0R00U
W201	7030003860	S.JUMPER	ERJ3GE JPW V
W202	7030003860	S.JUMPER	ERJ3GE JPW V
W203	7030003860	S.JUMPER	ERJ3GE JPW V
W302	7030003860	S.JUMPER	ERJ3GE JPW V
W303	7030003860	S.JUMPER	ERJ3GE JPW V
W308	7030003860	S.JUMPER	ERJ3GE JPW V
W309	7030003860	S.JUMPER	ERJ3GE JPW V
W310	7030003860	S.JUMPER	ERJ3GE JPW V
W311	7030003860	S.JUMPER	ERJ3GE JPW V
W312	7030003860	S.JUMPER	ERJ3GE JPW V
W501	7030003860	S.JUMPER	ERJ3GE JPW V
W502	7030003860	S.JUMPER	ERJ3GE JPW V
W503	7030003860	S.JUMPER	ERJ3GE JPW V
W504	7030003860	S.JUMPER	ERJ3GE JPW V
W505	7030008240	S.JUMPER	ERJ12YJ0R00U
W901	7030003860	S.JUMPER	ERJ3GE JPW V
W902	7030008240	S.JUMPER	ERJ12YJ0R00U
W903	7030003860	S.JUMPER	ERJ3GE JPW V
W904	7030003860	S.JUMPER	ERJ3GE JPW V
W952	7030003860	S.JUMPER	ERJ3GE JPW V
EP1	0910054518	PCB	B 5666H
EP2	6910012350	S.BEAD	MMZ1608Y 102BT

RC-25
[FRONT UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
W8001	8900009531	CABLE	OPC-942A (P=1 N=10 L=170)
W8005	8900008860	CABLE	OPC-875 (N:20 L:70)

[DISPLAY UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
IC8201	1140010240	S.IC	HD64F2134AFA20 (EX-2545A)
IC8202	1130005770	S.IC	S-80942CNMC-G9C
IC8250	1130005720	S.IC	TC7W04F (TE12L)
IC8260	1130007350	S.IC	TC74HC4066AF
IC8270	1110001810	S.IC	TA7368F (TP1)
IC8280	1110002700	S.IC	NJM2904M-T1
IC8290	1180001070	S.IC	TA7805F (TE16L)
IC8301	1110005580	S.IC	AN8010M-(E1)
IC8310	1180001250	S.IC	TA7808F (TE16L)
Q8251	1590001650	S.TRANSISTOR	XP4601 (TX)
Q8260	1590000680	S.TRANSISTOR	DTC114EUA T106
Q8280	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q8281	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q8282	1590000680	S.TRANSISTOR	DTC114EUA T106
Q8300	1540000440	S.TRANSISTOR	2SD1619-T-TD
Q8301	1540000440	S.TRANSISTOR	2SD1619-T-TD
Q8340	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q8341	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q8342	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q8343	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q8344	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q8345	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
D8201	1750000370	S.DIODE	DA221 TL
D8203	1750000370	S.DIODE	DA221 TL
D8204	1750000550	S.DIODE	1SS355 TE-17
D8205	1750000550	S.DIODE	1SS355 TE-17
D8250	1750000550	S.DIODE	1SS355 TE-17
D8280	1750000370	S.DIODE	DA221 TL
D8701	1750000550	S.DIODE	1SS355 TE-17
X8211	6050010501	S.XTAL	CR-636A (14.7456 MHz)
L8201	6200001830	S.COIL	NL 322522T-100J
L8202	6200005010	S.COIL	NL 252018T-100J
L8203	6200005010	S.COIL	NL 252018T-100J
L8206	6200003950	S.COIL	HF50ACC 322513-T
L8270	6200003260	S.COIL	NL 322522T-101J
L8286	6200005010	S.COIL	NL 252018T-100J
L8290	6200003950	S.COIL	HF50ACC 322513-T
L8701	6200001830	S.COIL	NL 322522T-100J
R8201	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8202	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8203	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8204	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8205	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R8206	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8207	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8208	7030003650	S.RESISTOR	ERJ3GEYJ 563 V (56 kΩ)
R8209	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R8210	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8211	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8212	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8213	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8214	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8215	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8216	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8218	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R8219	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R8221	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R8222	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)

S.=Surface mount

[DISPLAY UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
DS8321	5040002600	LED	TLYU1002
DS8322	5040002600	LED	TLYU1002
DS8323	5040002600	LED	TLYU1002
DS8340	5040002310	S.LED	SML-311YTT86
DS8341	5040002310	S.LED	SML-311YTT86
DS8342	5040002310	S.LED	SML-311YTT86
DS8343	5040002310	S.LED	SML-311YTT86
DS8344	5040002310	S.LED	SML-311YTT86
DS8345	5040002310	S.LED	SML-311YTT86
DS8346	5040002310	S.LED	SML-311YTT86
DS8347	5040002310	S.LED	SML-311YTT86
DS8348	5040002310	S.LED	SML-311YTT86
DS8349	5040002310	S.LED	SML-311YTT86
DS8350	5040002310	S.LED	SML-311YTT86
W8201	7030000010	S.JUMPER	MCR10EZHZ JPW (000)
W8202	7030000010	S.JUMPER	MCR10EZHZ JPW (000)
W8238	7030003860	S.JUMPER	ERJ3GE JPW V
W8242	7030003860	S.JUMPER	ERJ3GE JPW V
W8270	7030003860	S.JUMPER	ERJ3GE JPW V
W8271	7030003860	S.JUMPER	ERJ3GE JPW V
W8280	7030003860	S.JUMPER	ERJ3GE JPW V
W8281	7030003860	S.JUMPER	ERJ3GE JPW V
W8296	7030003860	S.JUMPER	ERJ3GE JPW V
W8340	7030003860	S.JUMPER	ERJ3GE JPW V
EP8201	0910054714	PCB	B 5727D
EP8205	6910012350	S.BEAD	MMZ1608Y 102BT
EP8292	6910012350	S.BEAD	MMZ1608Y 102BT
EP8293	6910012350	S.BEAD	MMZ1608Y 102BT
EP8294	6910012350	S.BEAD	MMZ1608Y 102BT
EP8295	6910012350	S.BEAD	MMZ1608Y 102BT
EP8296	6910012350	S.BEAD	MMZ1608Y 102BT

[VR UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
R8901	7210002360	VARIABLE	TP96N97-15F-10KB-1301
EP8901	0910054742	PCB	B 5730B

[CONNECT UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
IC8240	1140010010	S.IC	HD66421TBOL
L8220	6200001830	S.COIL	NL 322522T-100J
L8221	6200001830	S.COIL	NL 322522T-100J
R8241	7030003830	S.RESISTOR	ERJ3GEYJ 185 V (1.8 MΩ)
R8242	7030003840	S.RESISTOR	ERJ3GEYJ 225 V (2.2 MΩ)
R8243	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8244	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8245	7030003640	S.RESISTOR	ERJ3GEYJ 473 V (47 kΩ)
R8246	7030003830	S.RESISTOR	ERJ3GEYJ 185 V (1.8 MΩ)

[JACK UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
J8601	6510022620	S.CONNECTOR	10FMN-BMTTR-A-TBT
J8602	6510023060	CONNECTOR	TCS7186-01-201
EP8601	0910054961	PCB	B 5812A

DS8220	5030002280	LCD	HLC7700-018800
EP8220	0910054783	PCB	B 5751C

[SENSOR UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
S8801	2250000220	ENCODER	TP90N937E20-15F-1540
S8802	2250000220	ENCODER	TP90N937E20-15F-1540
EP8801	0910054732	PCB	B 5729B

S.=Surface mount

HM-135
[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
R1	7010006890	RESISTOR	R20J 12 kΩ
R2	7010006910	RESISTOR	R20J 33 kΩ
R3	7010006880	RESISTOR	R20J 6.8 kΩ
R4	7010006900	RESISTOR	R20J 15 kΩ
C1	4010008030	CERAMIC	DD104 B 471K 50V
MC1	7700002120	MICROPHONE	KUC2123-030245
S1	2260002340	SWITCH	SKHHAM024A
S2	2260002340	SWITCH	SKHHAM024A
S3	2260002340	SWITCH	SKHHAM024A
W1	9018490010	WIRE	71/98/010/X98/X98 <KN>
W2	9018490010	WIRE	71/98/010/X98/X98 <KN>
EP1	0910051480	PCB	B 5324
EP2	9018230010	TUBE	IRRAX 0.7(d) L=5mm

SP-24
[CHASSIS UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
SP1	2510001170	SPEAKER	077P0401 <KS>
W1	8900011090	CABLE	OPC-1104
EP1	8930006760	M.OTHER	SR-2P-4

[SW BOARD]

REF NO.	ORDER NO.	DESCRIPTION	
S1	2260002330	SWITCH	SKHHP014A
EP1	0910051490	PCB	B 5325

S.=Surface mount

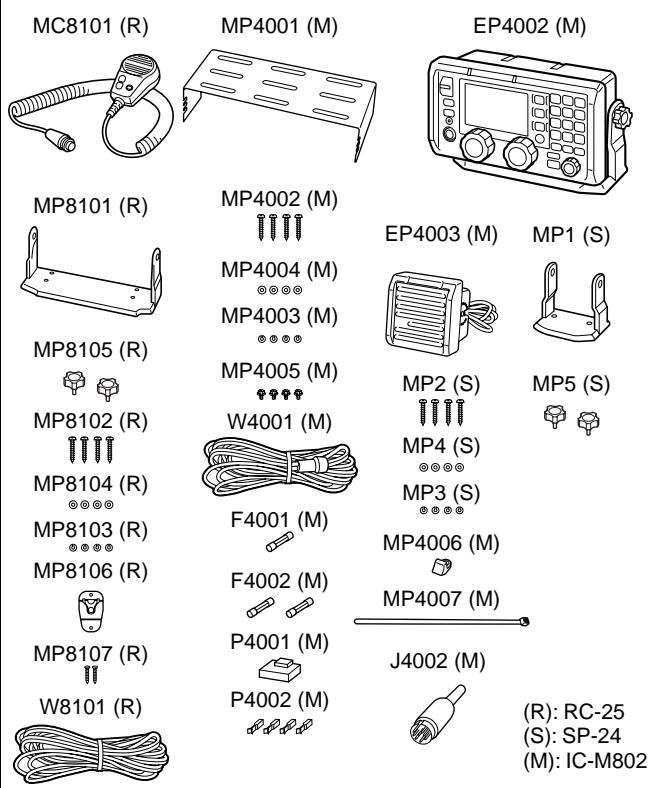
[MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J1451	6450000140	Connector HSJ0807-01-010	1
J1801	6450000170	Connector TCS4480-01-1111	1
J2051	6510023180	Connector TCS7282-01-211	1
EP2	0880000900	DSP board	1
MP201	8510014610	2429 MIX case	1
MP301	8510000180	117 shield plate	1
MP302	8930054530	2355 earth spring	1
MP701	8510011970	2177 OSC case	1
MP702	8510014600	2429 OSC cover	1
MP1501	8510013140	2241 DC-A case	1
MP2051	8930057990	2429 control plate Y630	1

[PLL UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J5051	6510023120	Connector CD6109SA1J0	1
J5081	6510023120	Connector CD6109SA1J0	1
J5091	6510023040	Connector 01K1621	1
MP5801	8510006941	DTMF shield case-1	1
MP5901	8510006941	DTMF shield case-1	1

ACCESSORIES



[ACCESSORIES]

• IC-M802

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J4002	6450001280	Connector TCP0587-71-5401	1
P4001	5610000290	Connector XMP-06V	1
P4002	5610000300	Terminal BXA-001T-P0.6	4
F4001	5210000090	Fuse FGB 30A	1
F4002	5210000060	Fuse FGB 5A	2
W4001	8900011041	Cable OPC-1107A	1
EP4002	0880000960	RC-25	1
EP4003	0880001000	SP-24	1
MP4001	8010018980	2429 bracket	1
MP4002	8810001490	Screw A0 5 × 20 SUS	4
MP4003	8850000500	Spring washer SUS	4
MP4004	8850000180	Flat washer SUS	4
MP4005	8810009300	Setscrew (J)	4
MP4006	8950005720	Tie mount (CL8)	1
MP4007	8950005710	Release tie (RELK2R)	1

• RC-25

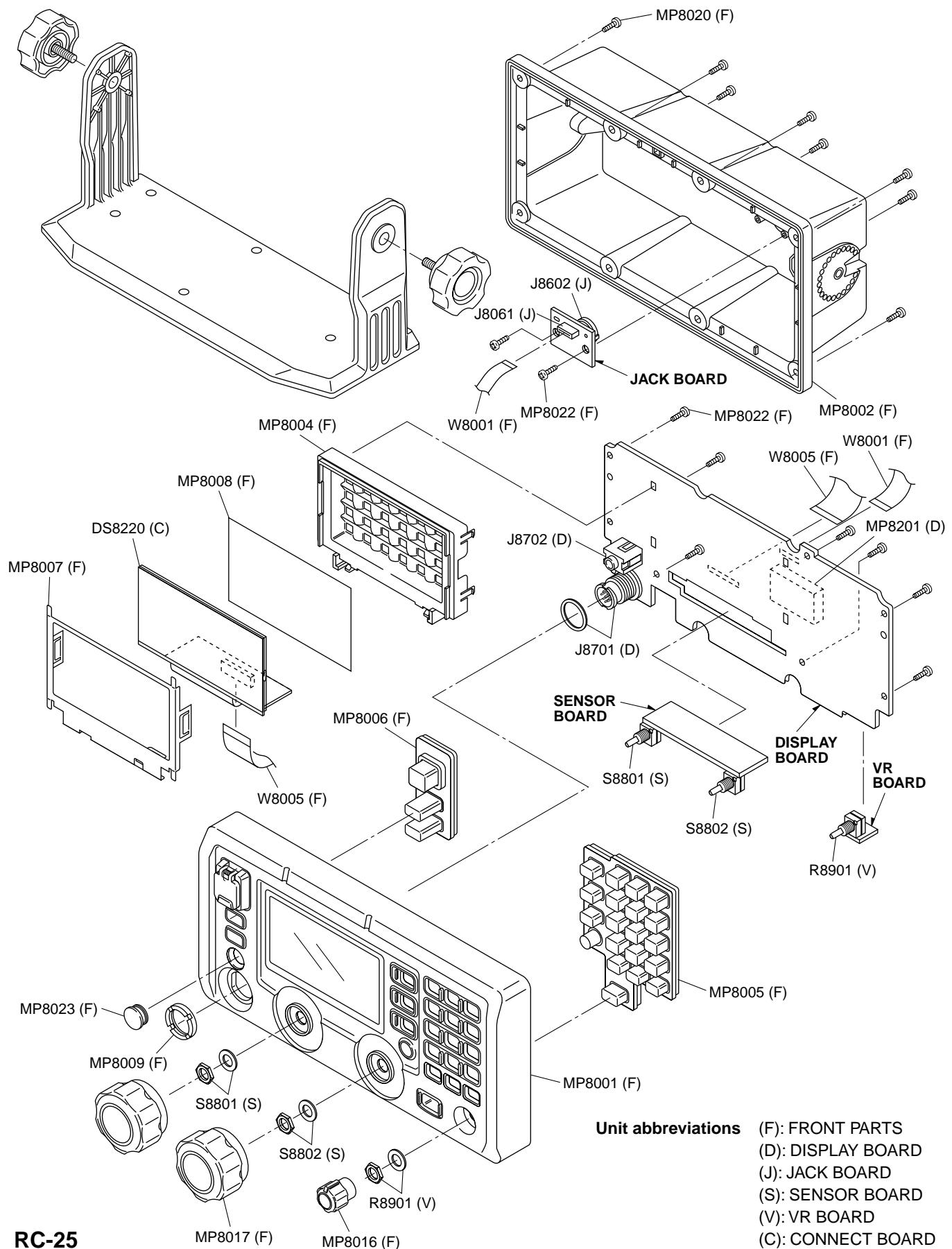
REF. NO.	ORDER NO.	DESCRIPTION	QTY.
MC8101	0800006490	HM-135	1
W8101	8900011070	Cable OPC-1106	1
MP8101	8010018860	2455 bracket	1
MP8102	8810001490	Screw A0 5 × 20 SUS	4
MP8103	8850000500	Spring washer SUS	4
MP8104	8850000180	Flat washer SUS	4
MP8105	8610010561	2040 knob bolt-1	2
MP8106	8950005110	2289 mic hanger	1
MP8107	8810004700	Screw A0 3 × 16 SUS	2

• SP-24

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
MP1	8010018870	2456 bracket	1
MP2	8810001490	Screw A0 5 × 20 SUS	2
MP3	8850000500	Spring washer SUS	2
MP4	8850000180	Flat washer SUS	2
MP5	8610010561	2040 knob bolt-1	2

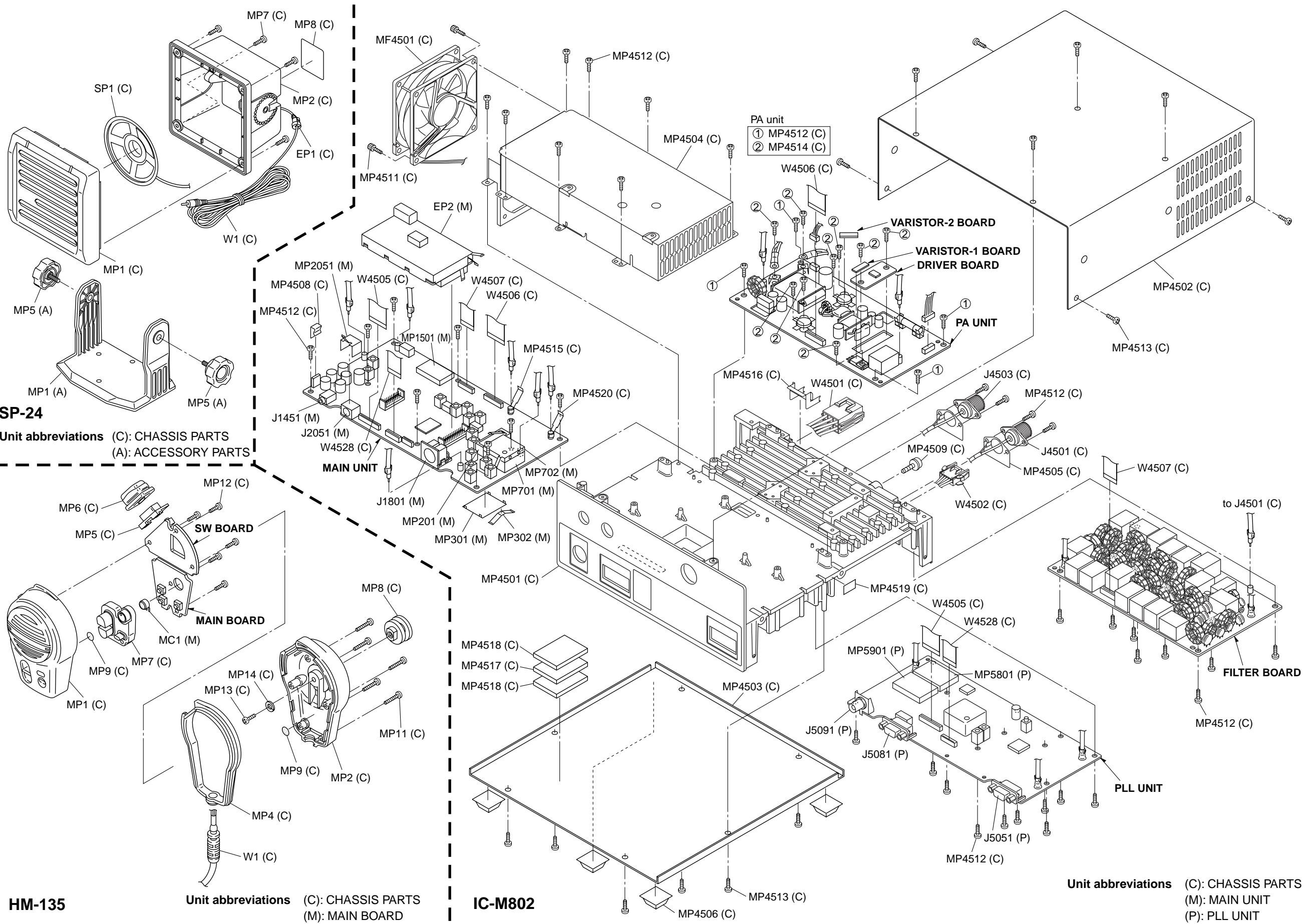
Screw abbreviations

- PH: Pan head
- BiH: Binding head
- BT, BO: Self-tapping
- SUS: Stainless
- NI: Nickel
- NI-ZU: Nickel-Zinc
- ZK: Black



Unit abbreviations

(F): FRONT PARTS
(D): DISPLAY BOARD
(J): JACK BOARD
(S): SENSOR BOARD
(V): VR BOARD
(C): CONNECT BOARD



SECTION 7 SEMI-CONDUCTOR INFORMATION

• TRANSISTOR AND FET'S

2SA1037AK S (Symbol: FS)	2SA1213 Y (Symbol: NY)	2SA1586 GR (Symbol: SG)	2SC2873 Y (Symbol: M)	2SC4116 BL (Symbol: LL)
2SC4116 GR (Symbol: LG)	2SC4213 B (Symbol: AB)	2SC4215 O (Symbol: QO)	2SC4673 D TD (Symbol: CO)	2SD1585 K (Symbol: None)
2SD1619 T TD (Symbol: DB)	2SD1664 T100Q (Symbol: DAQ)	2SK210 GR (Symbol: YG)	2SK508 K52 T2B (Symbol: K52)	2SK2171 4 TD (Symbol: KM)
2SK2973 (Symbol: K1)	3SK131 T2 MAS (Symbol: V11)	DTA114EUA T106 (Symbol: 14)	DTC114EUA T106 (Symbol: 24)	PD55003 (Symbol: PD55003)
PD55015S (Symbol: PD55015)	SD1487 (Symbol: SD1487)	XP4312 (Symbol: 7T)	XP4601 (Symbol: 5C)	

• DIODES

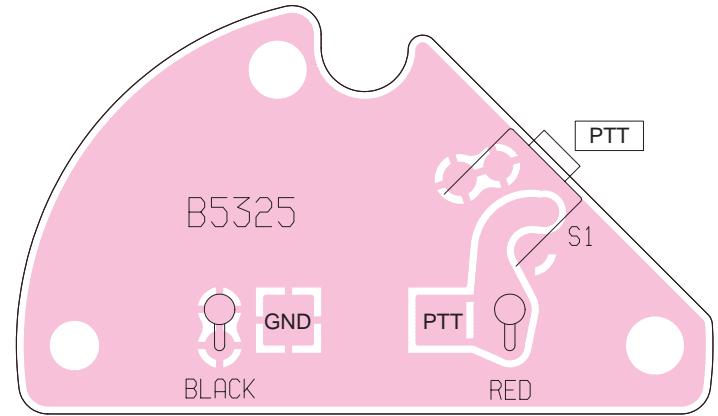
1SS301 (Symbol: B3)	1SS322 (Symbol: A9)	1SS355 (Symbol: A)	1SS375-TL (Symbol: FH)	DA221 TL (Symbol: K)
DAP22 TL (Symbol: P)	DF40SC4	HSB88WSTR (Symbol: Silver line)	KV1470 TL (Symbol: F7)	MA2S111 (Symbol: A)
MA2S728 (Symbol: B)	MA30-(TX) (Symbol: 3B)	MA77 (Symbol: 4B)	MA80WK (Symbol: M1Y)	MA338 (Symbol: 6H)
MA729 (Symbol: 2B)	MA742 (Symbol: M1U)	MA8043 H (Symbol: 4^3)	MA8051 M (Symbol: 5-1)	MA8091 M (Symbol: 9-1)
RB706F-40 T106 (Symbol: 3J)	RD10M B3 (Symbol: 103)	TLYU1002	UM9957F/TR	

SECTION 8 BOARD LAYOUTS

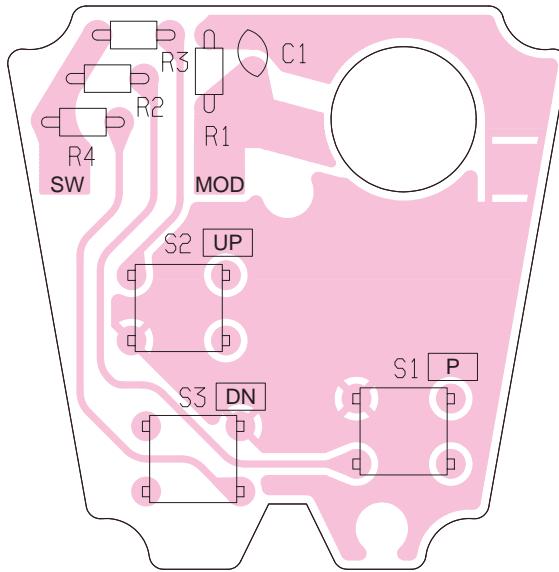
8-1 HM-135

8-1-1 SW BOARD

• TOP VIEW



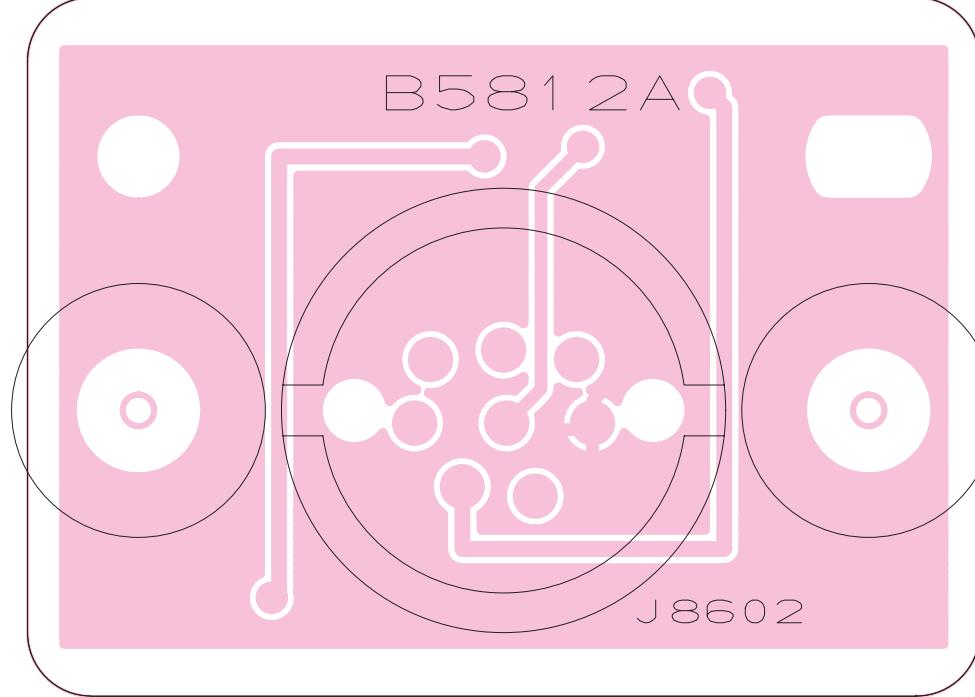
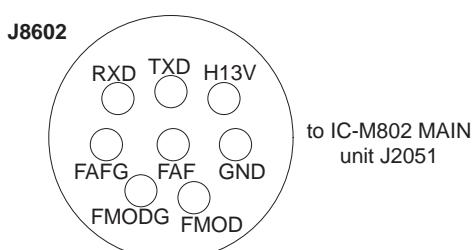
8-1-2 MAIN BOARD • TOP VIEW



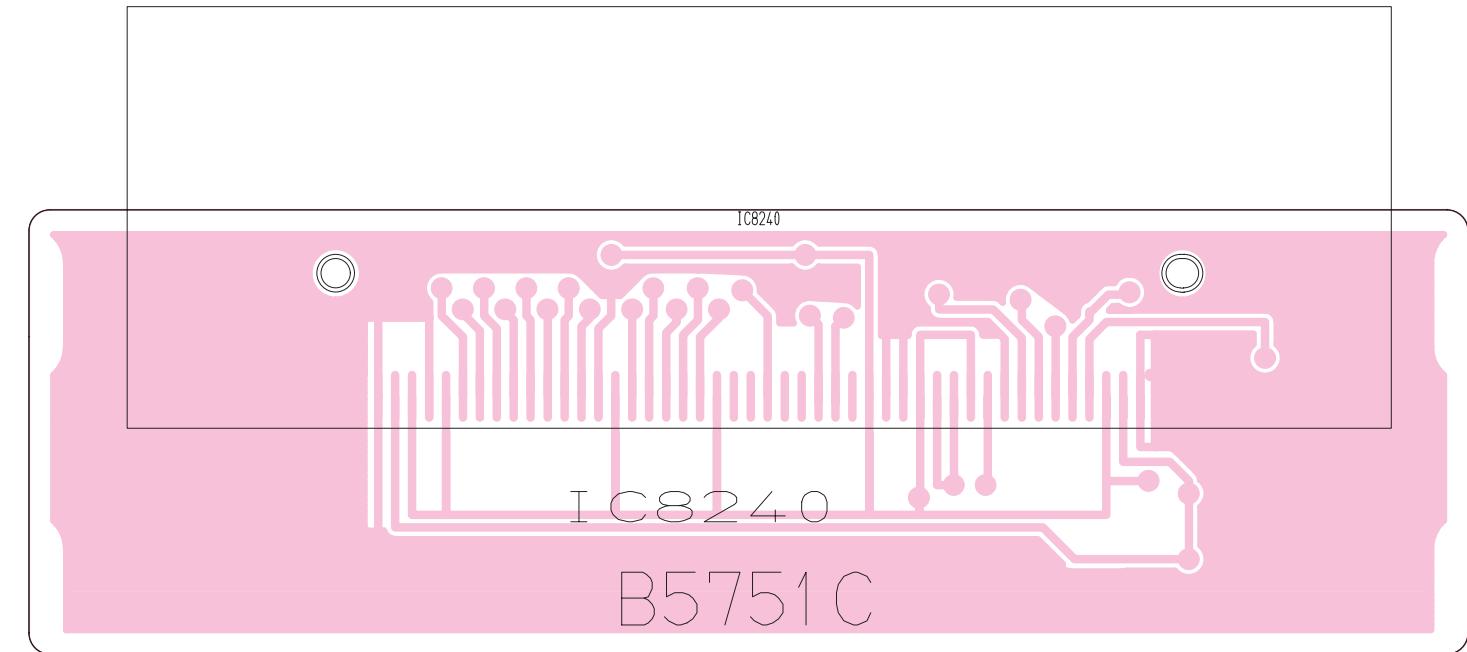
8-2 RC-25

8-2-1 JACK BOARD

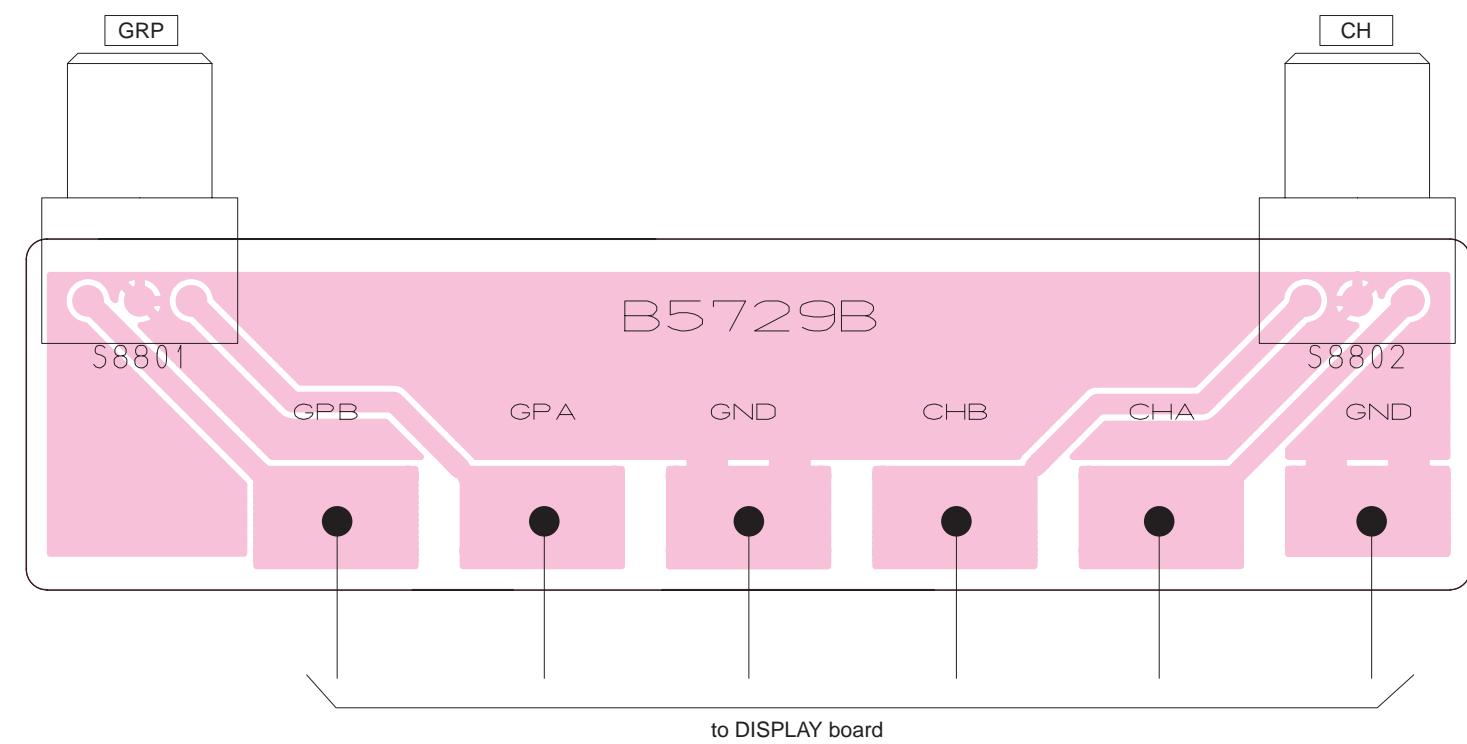
• TOP VIEW



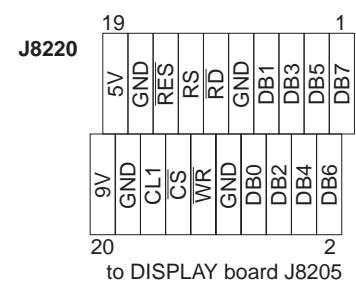
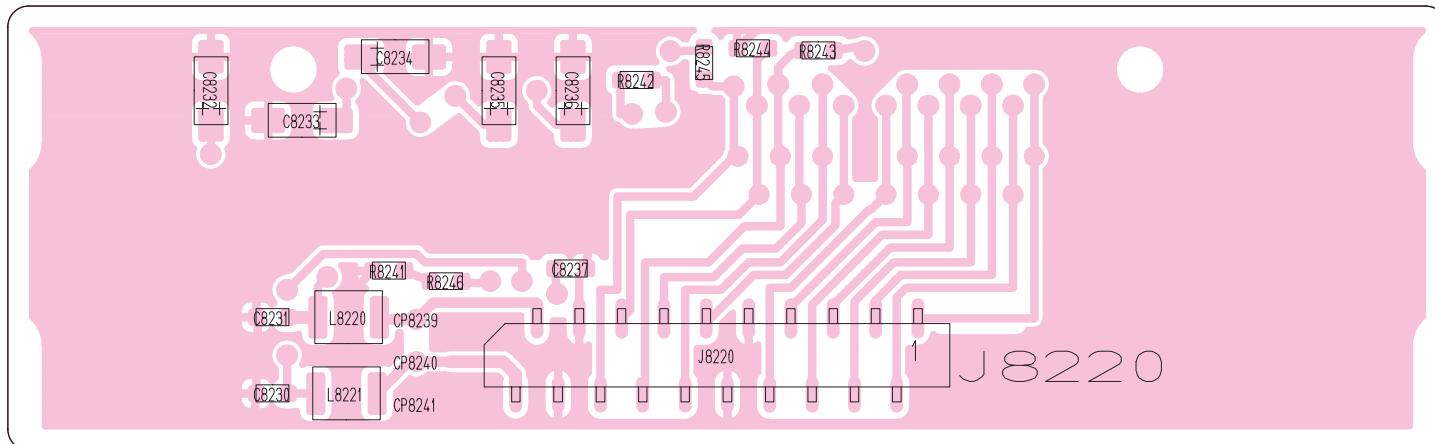
8-2-2 CONNECT BOARD • TOP VIEW



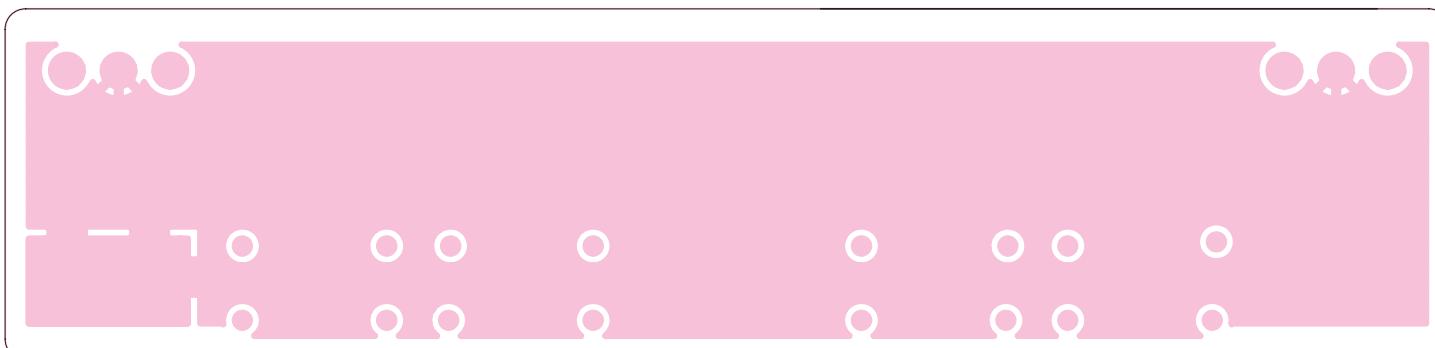
8-2-3 SENSOR BOARD • TOP VIEW



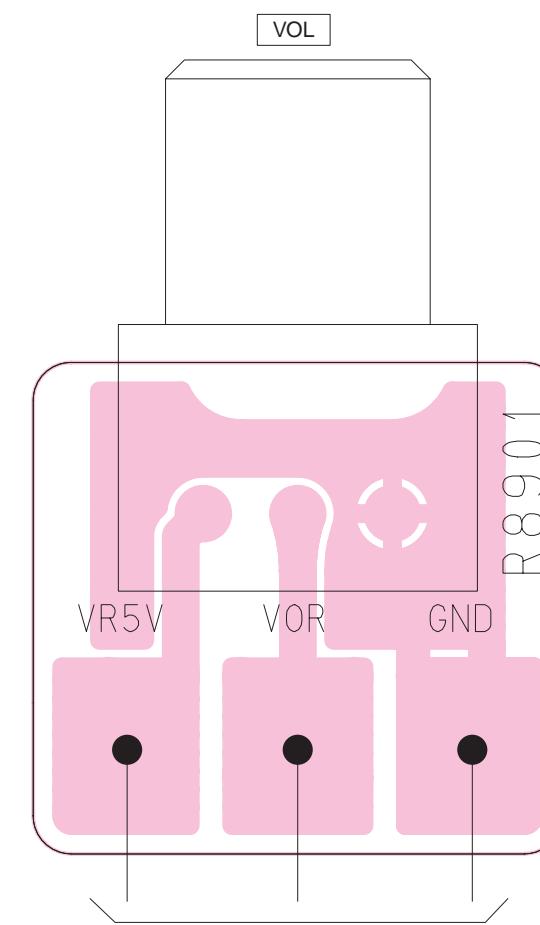
• BOTTOM VIEW (CONNECT BOARD)



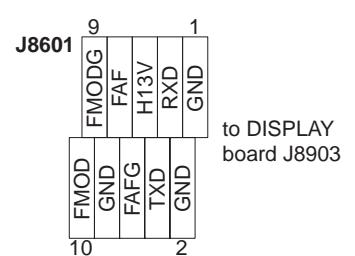
• BOTTOM VIEW (SENSOR BOARD)



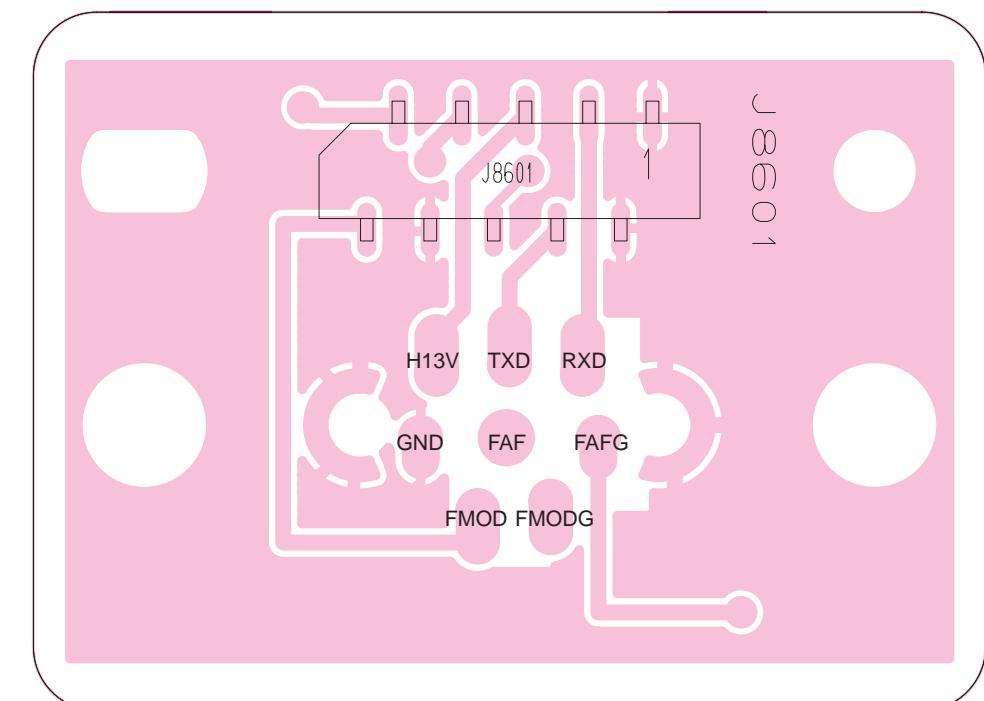
8-2-4 VR BOARD



• BOTTOM VIEW (JACK BOARD)

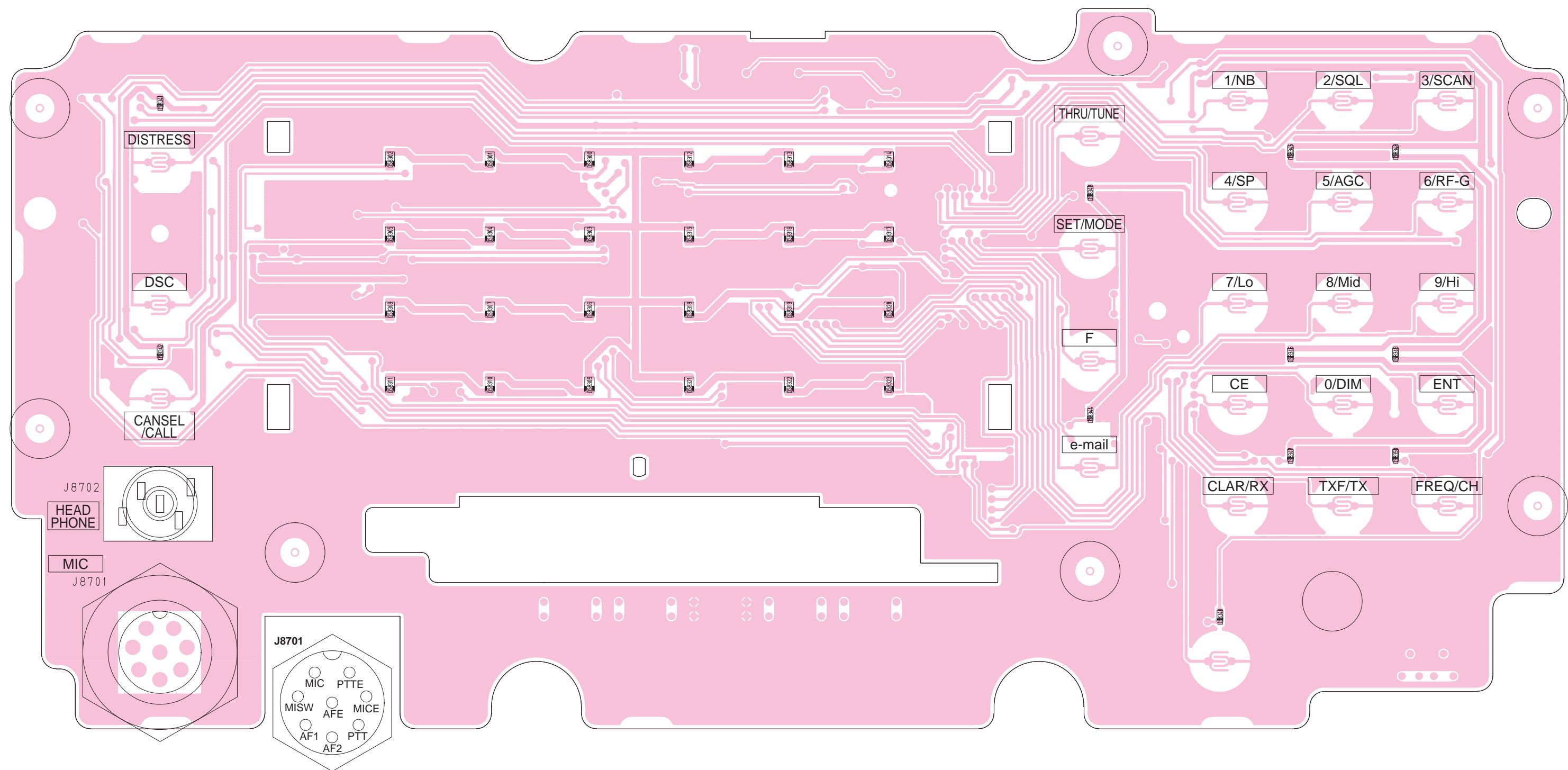


to DISPLAY board J8903

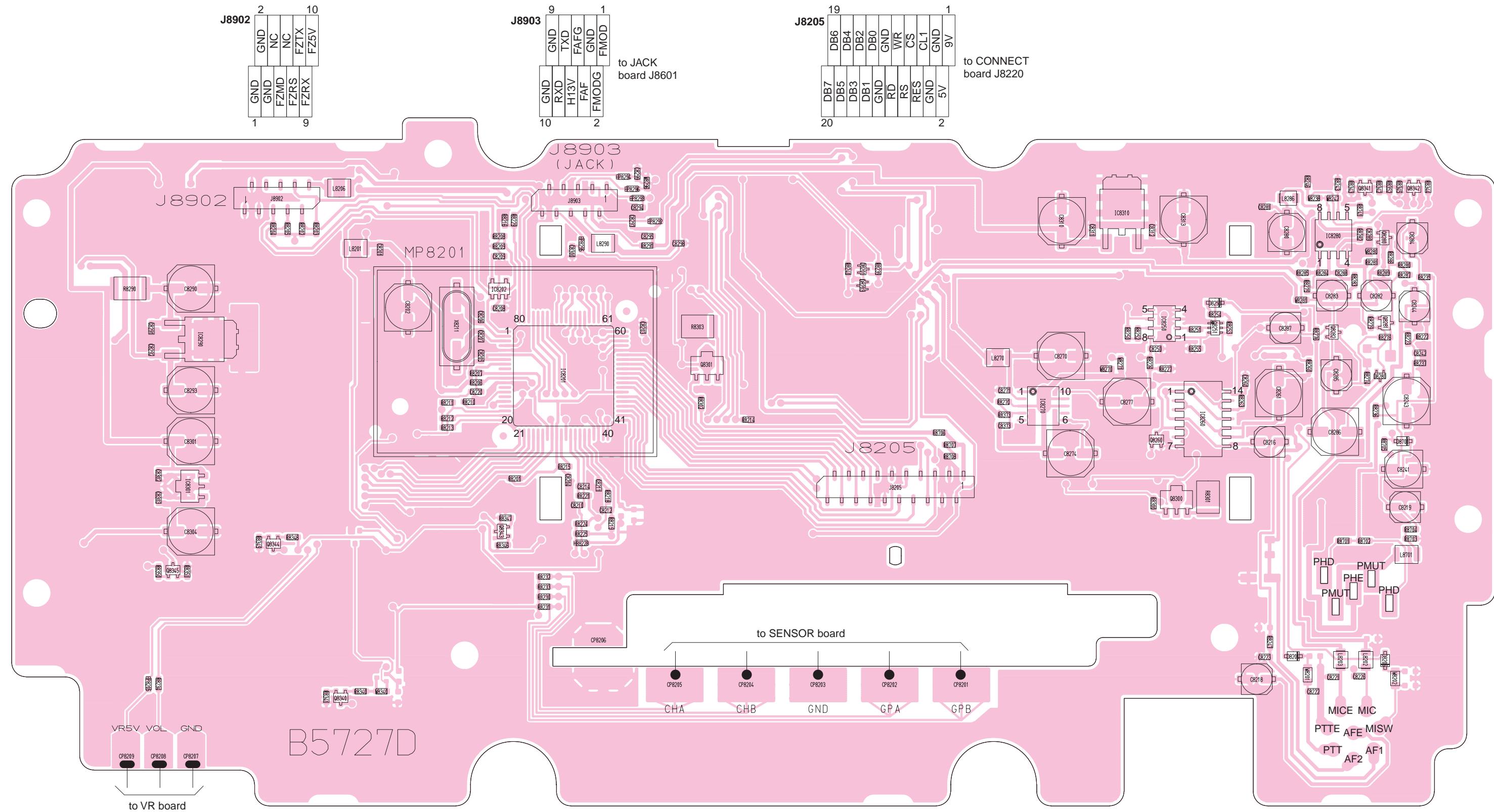


8-2-5 DISPLAY BOARD

• TOP VIEW



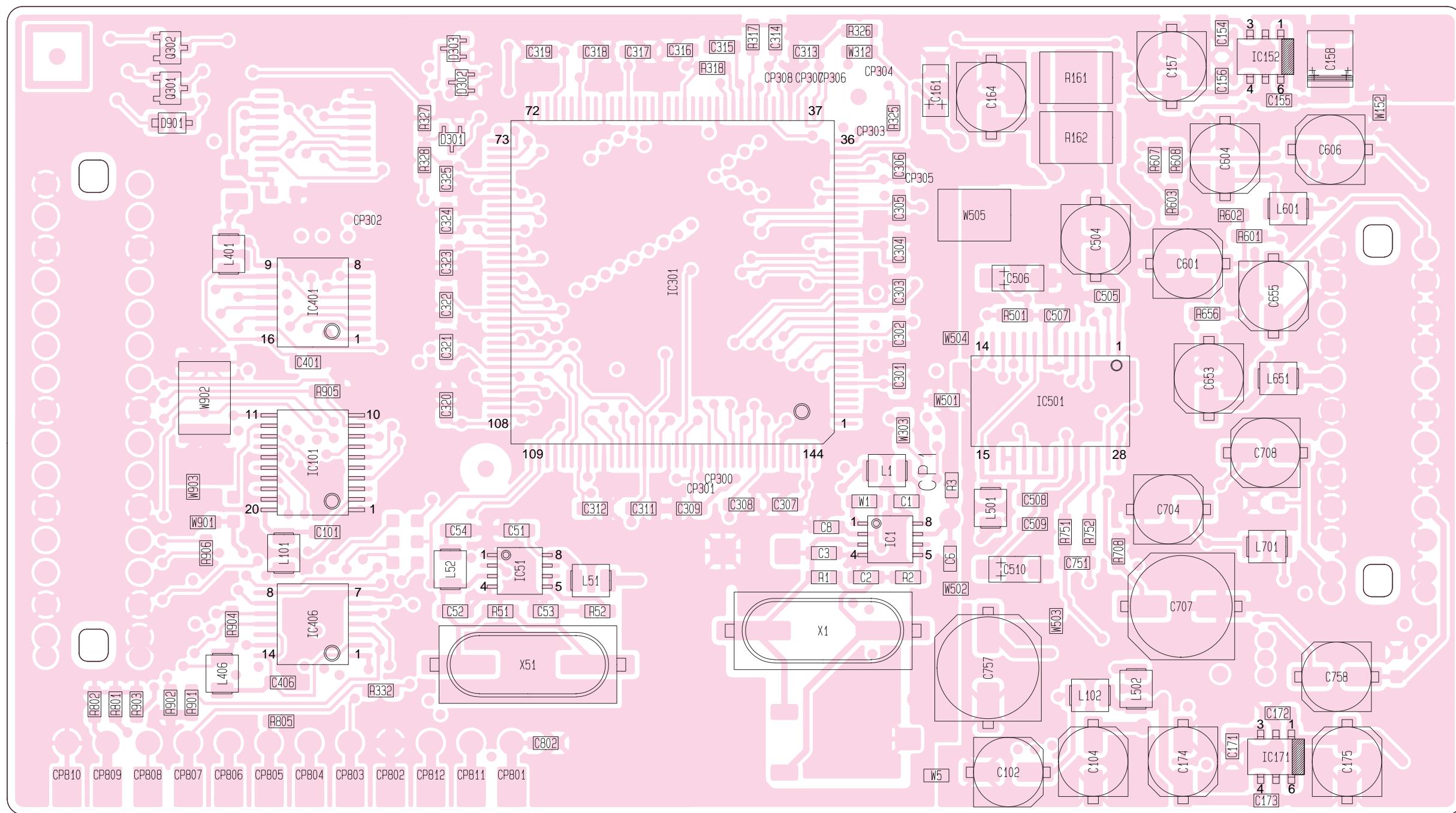
• BOTTOM VIEW (DISPLAY BOARD)



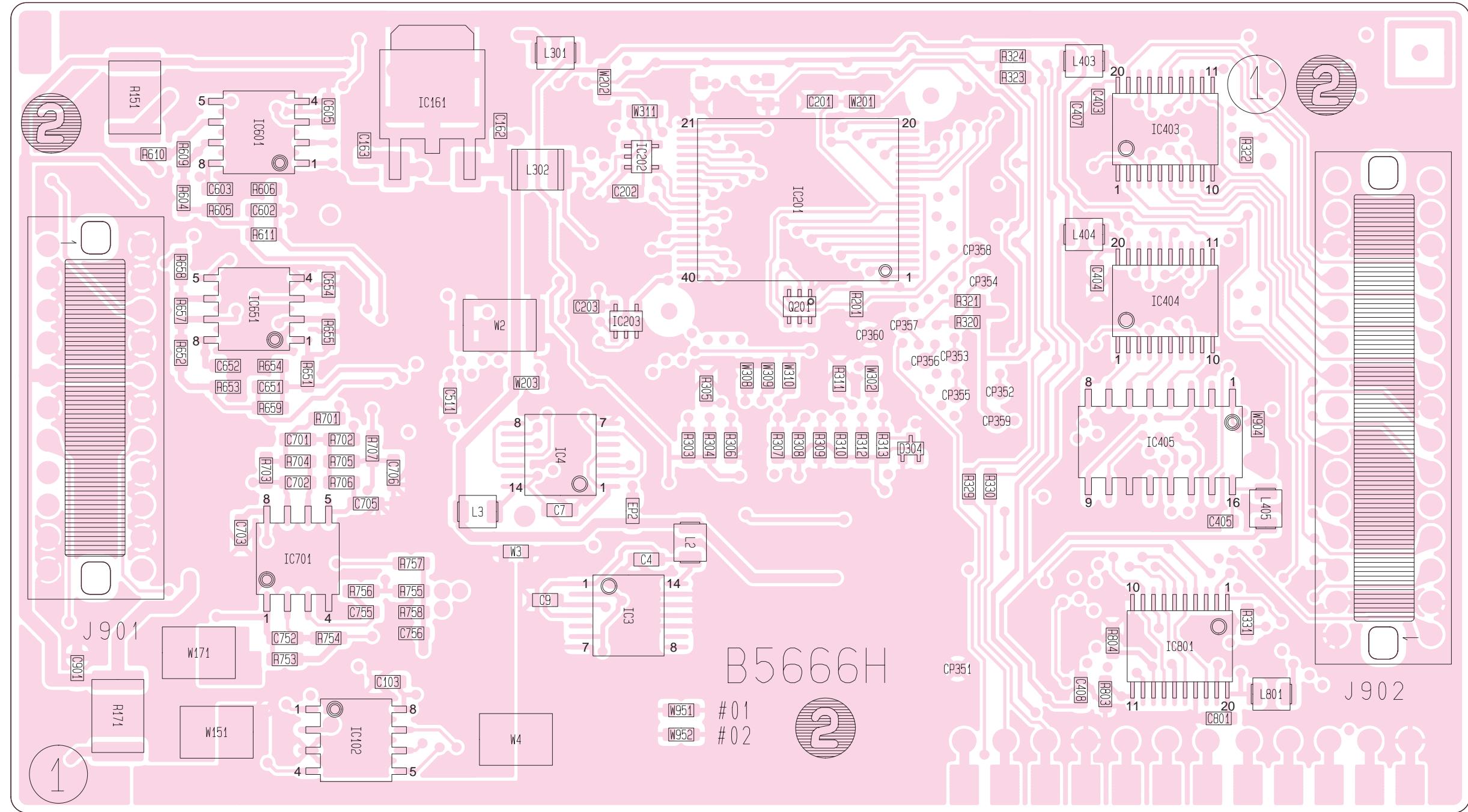
8-3 IC-M802

8-3-1 DSP BOARD

• TOP VIEW



• BOTTOM VIEW (DSP BOARD)



MAIN unit J1102	
DSPI1	GND
GND	GND
GND	DSPI2
GND	GND
8V	8V
GND	GND
AGC2	AGC1
GND	GND
DSPO1	GND
GND	GND
GND	DSPO2

J901	
1	DSPI1
2	GND
1	GND
2	DSPI2
1	GND
2	GND
1	8V
2	8V
1	GND
2	GND
1	AGC2
2	AGC1
1	GND
2	GND
1	DSPO1
2	GND
1	GND
2	GND
1	GND
2	DSPO2

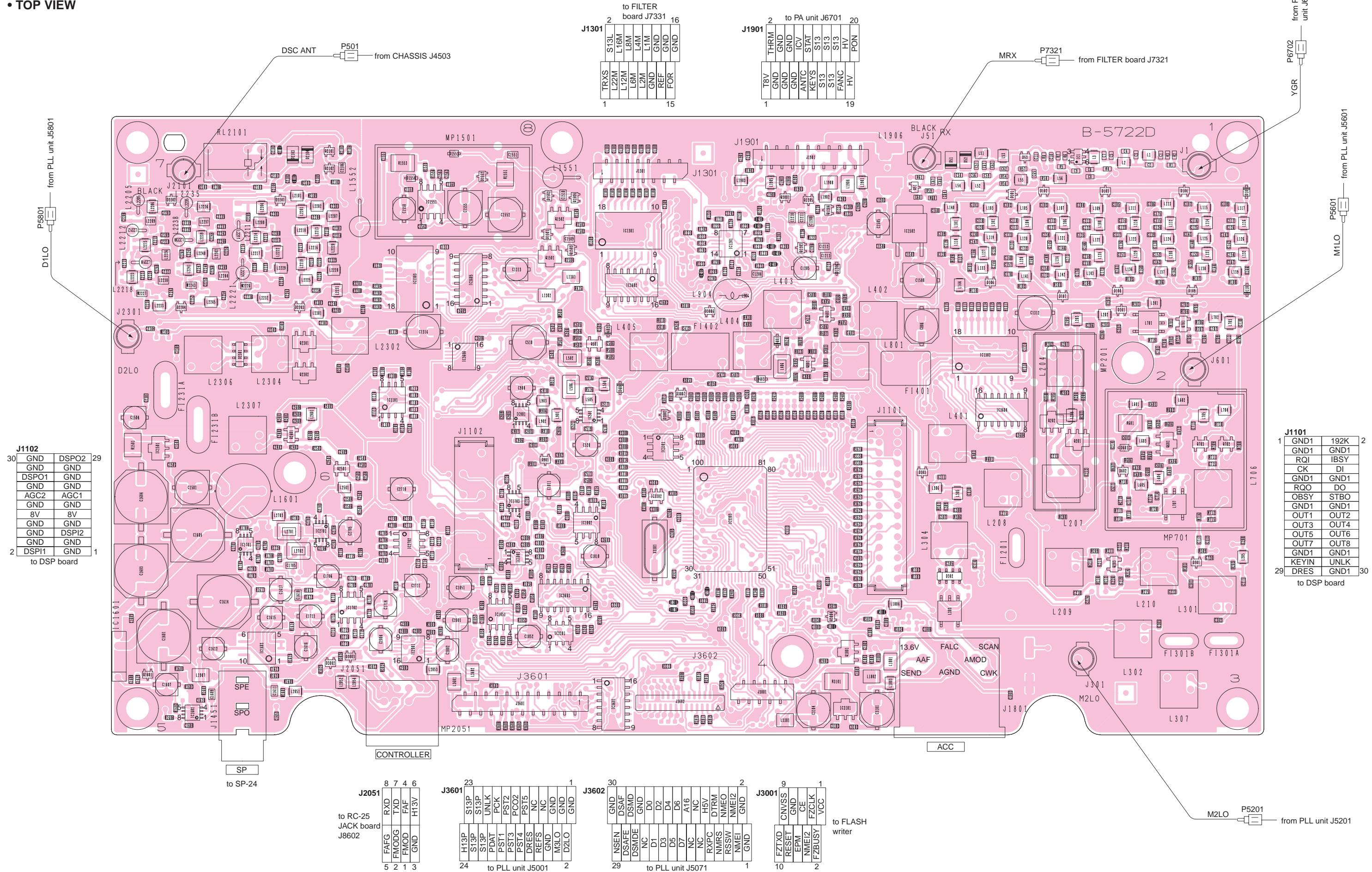
to MAIN unit J1102

J902	
30	DRES
29	GND1
1	KEYIN
2	UNLK
30	GND1
29	GND1
1	OUT7
2	OUT8
30	OUT5
29	OUT6
1	OUT3
2	OUT4
30	OUT1
29	OUT2
1	GND1
2	GND1
30	OBSY
29	STBO
1	RQO
2	DO
30	GND1
29	CK
1	DI
30	RQI
29	IBSY
1	GND1
2	GND1
30	192K
29	192K
1	192K

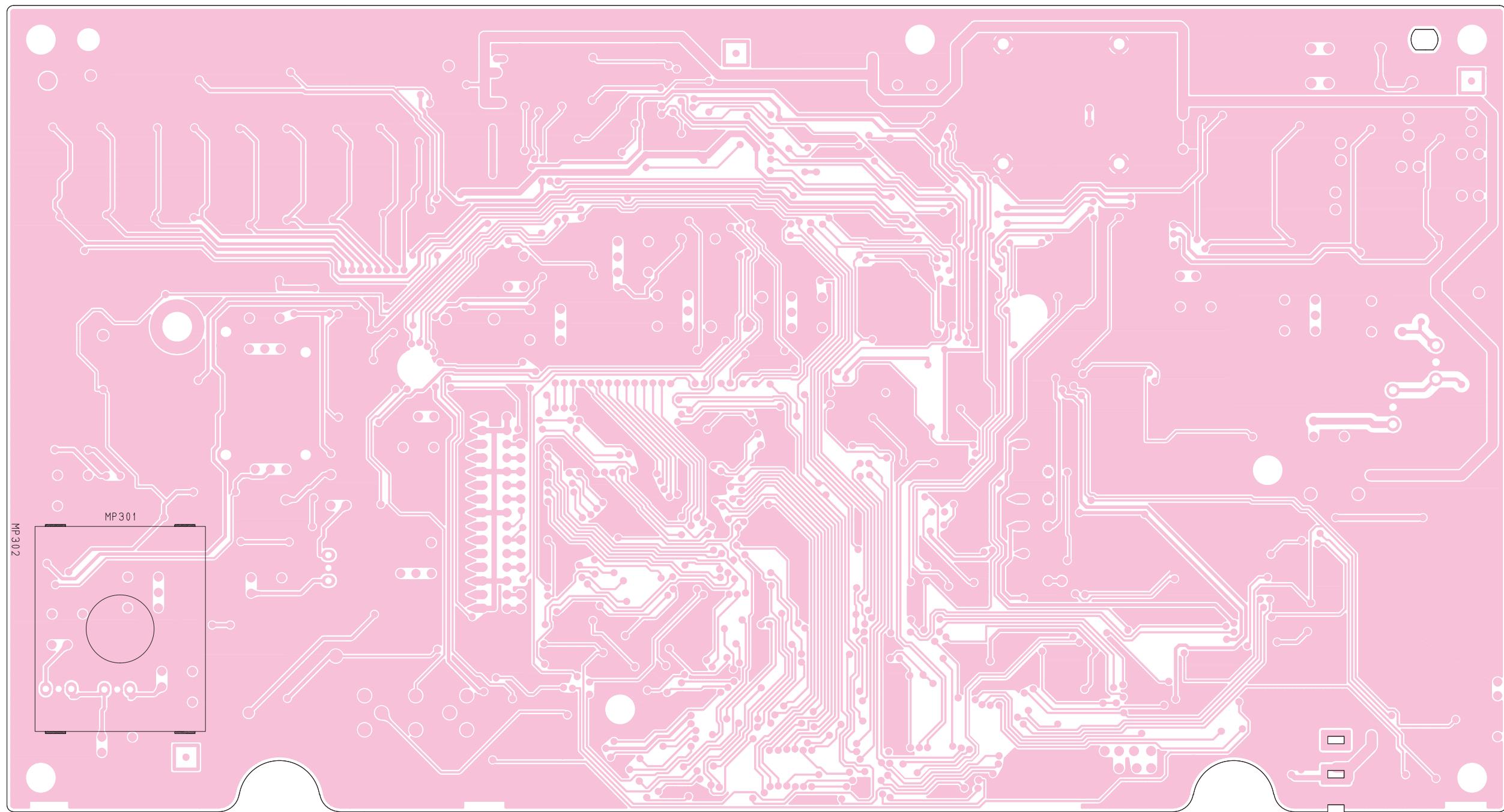
to MAIN unit J1101

8-3-2 MAIN UNIT

• TOP VIEW

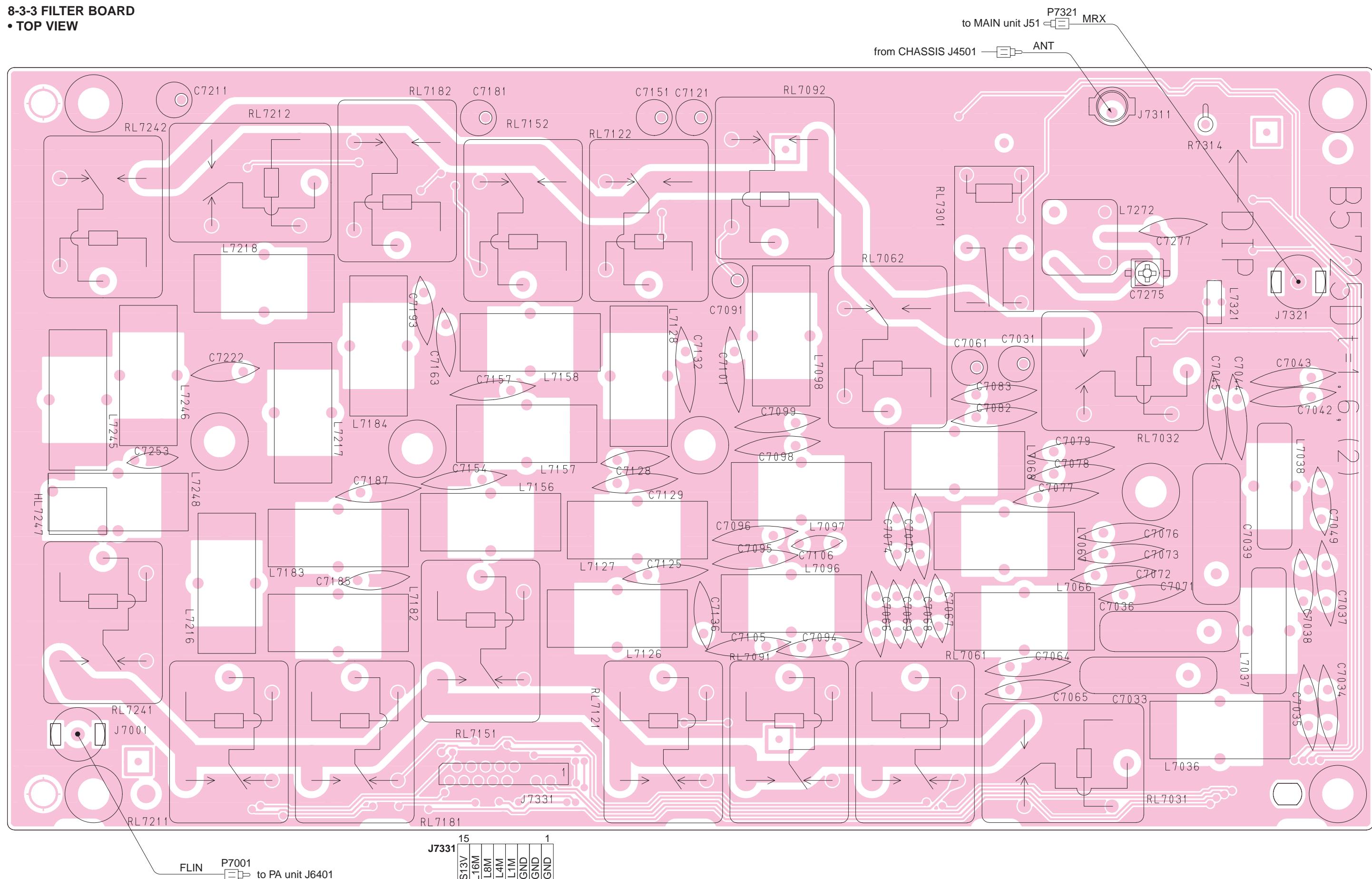


• BOTTOM VIEW (MAIN UNIT)

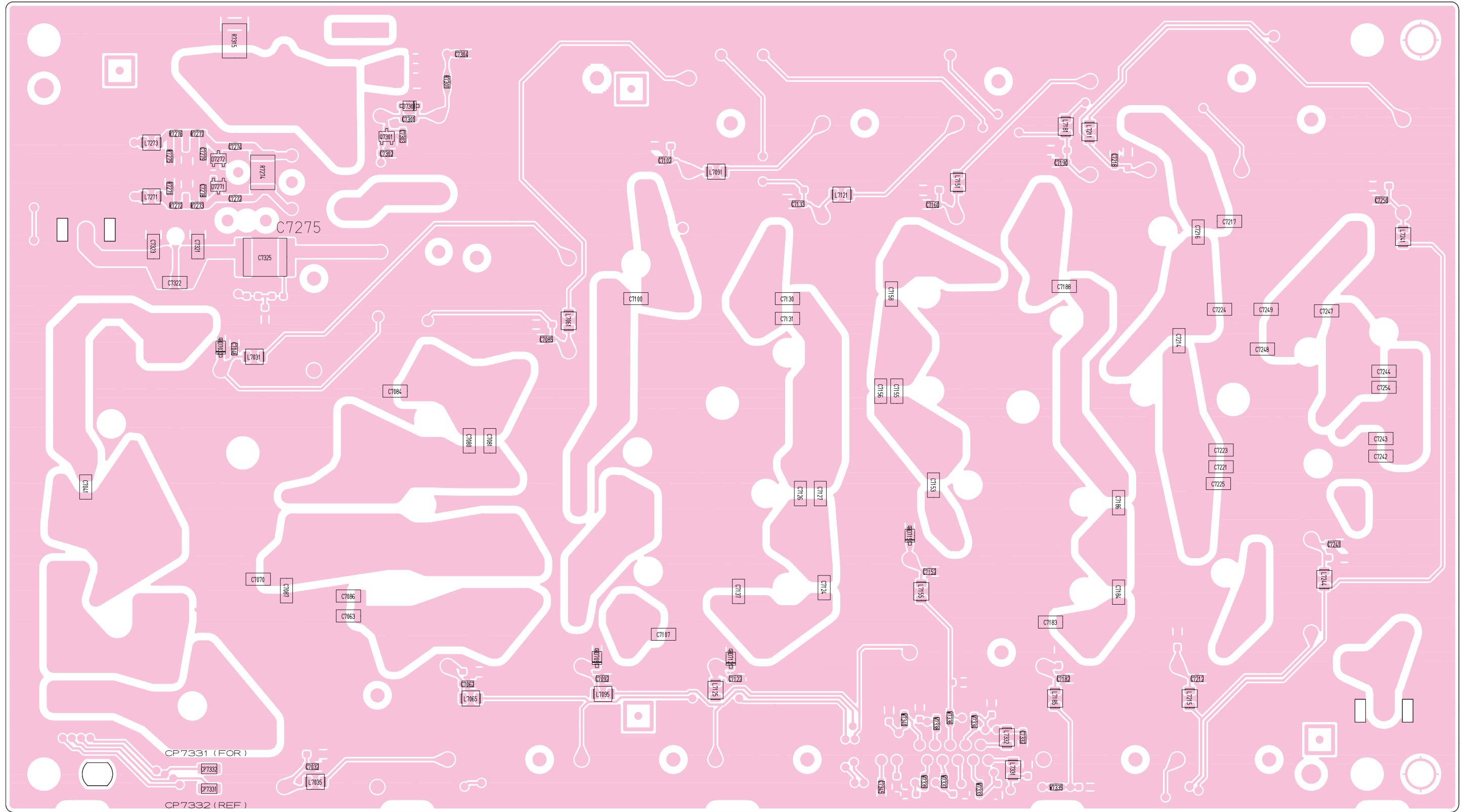


8-3-3 FILTER BOARD

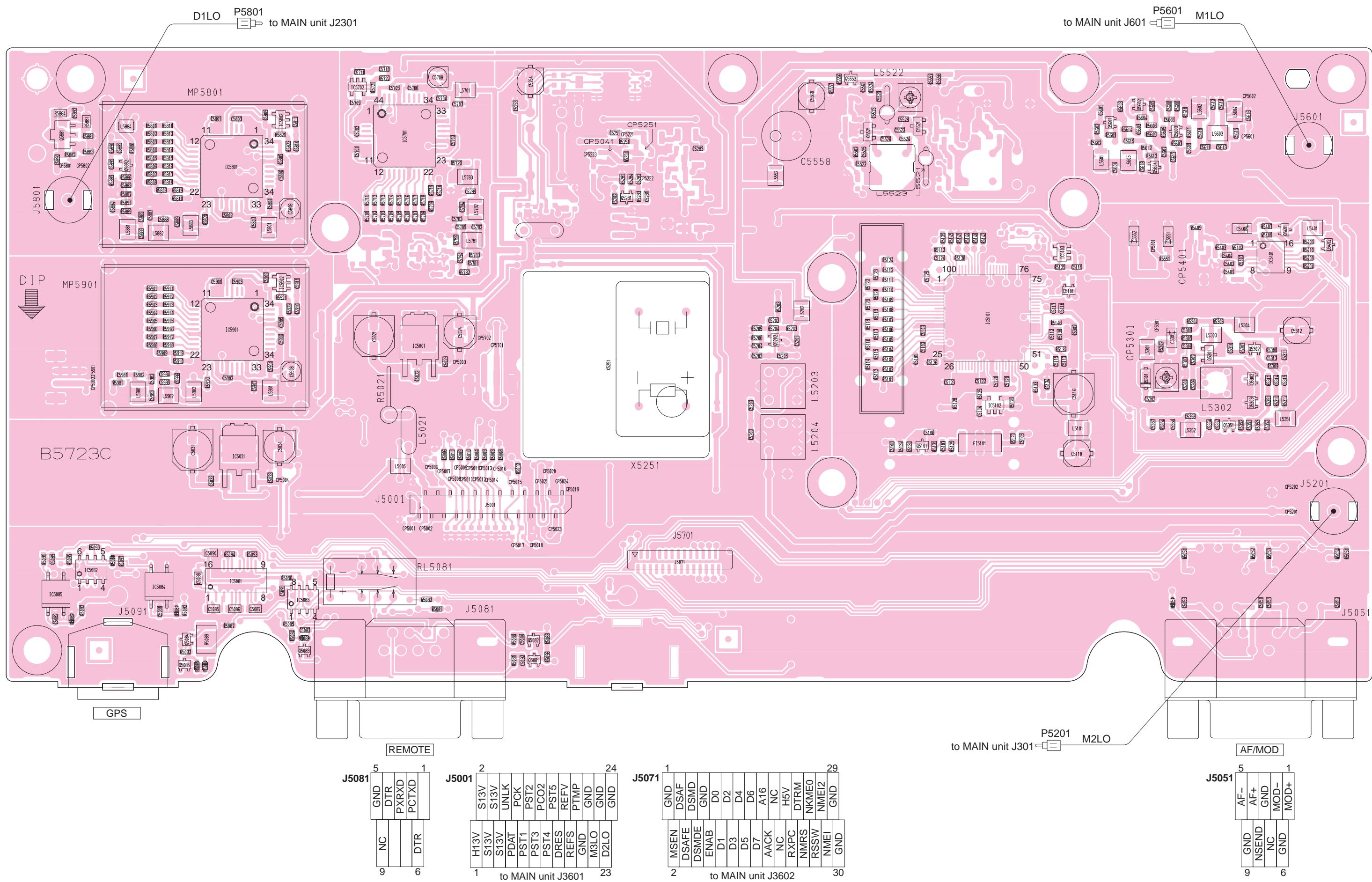
• TOP VIEW



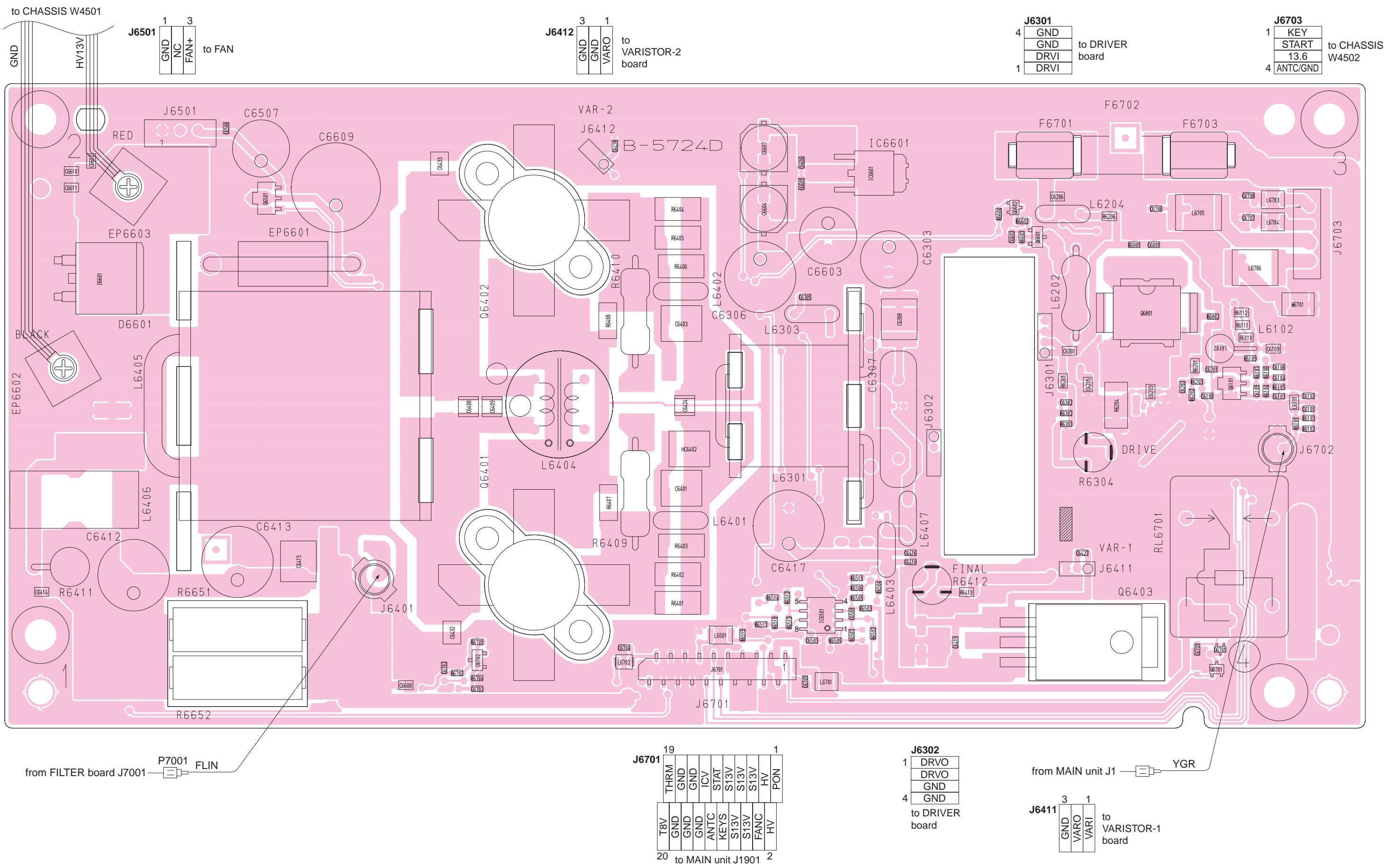
• BOTTOM VIEW (FILTER BOARD)



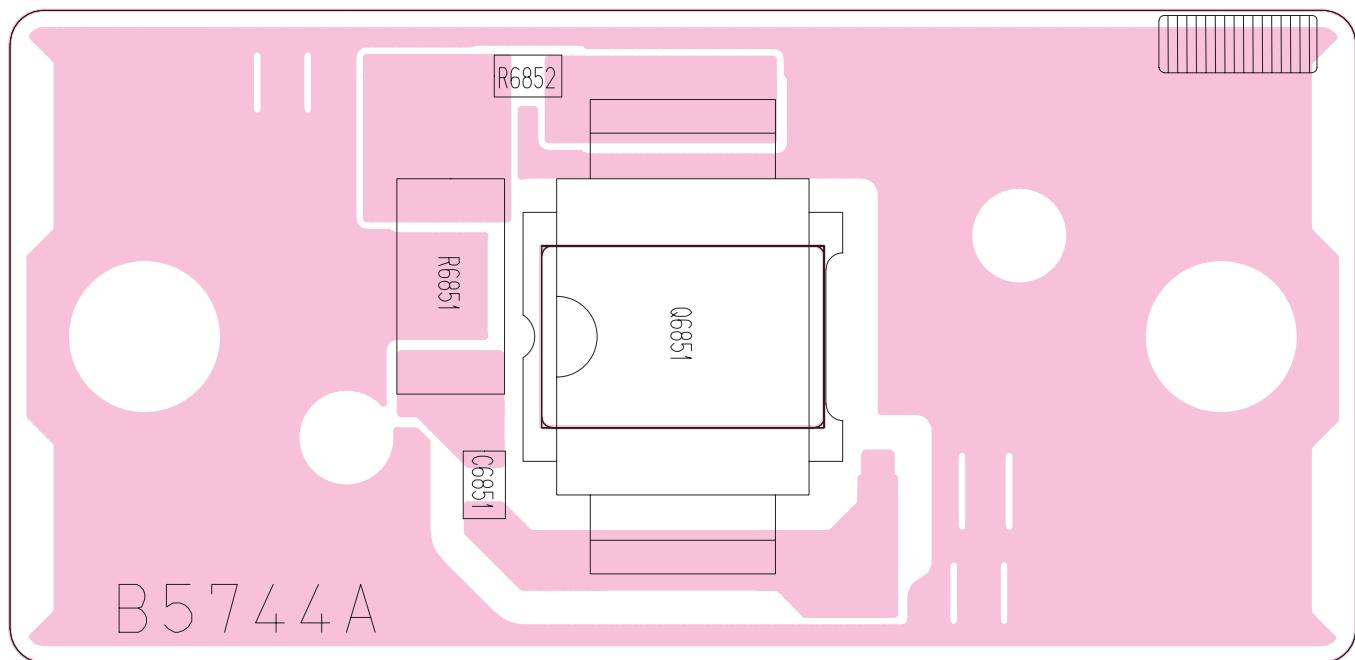
8-3-4 PLL UNIT



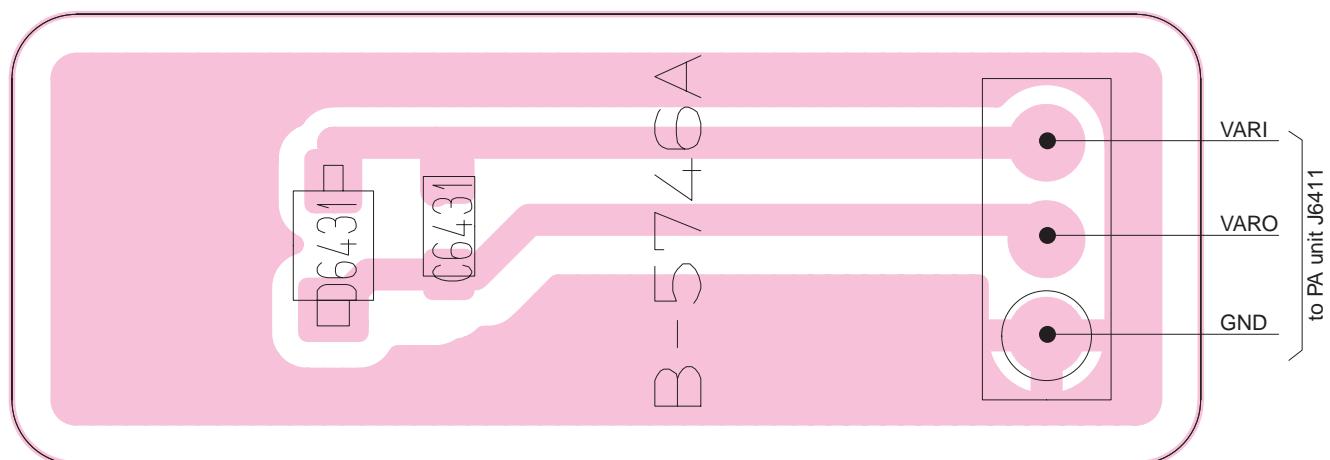
8-3-5 PA UNIT



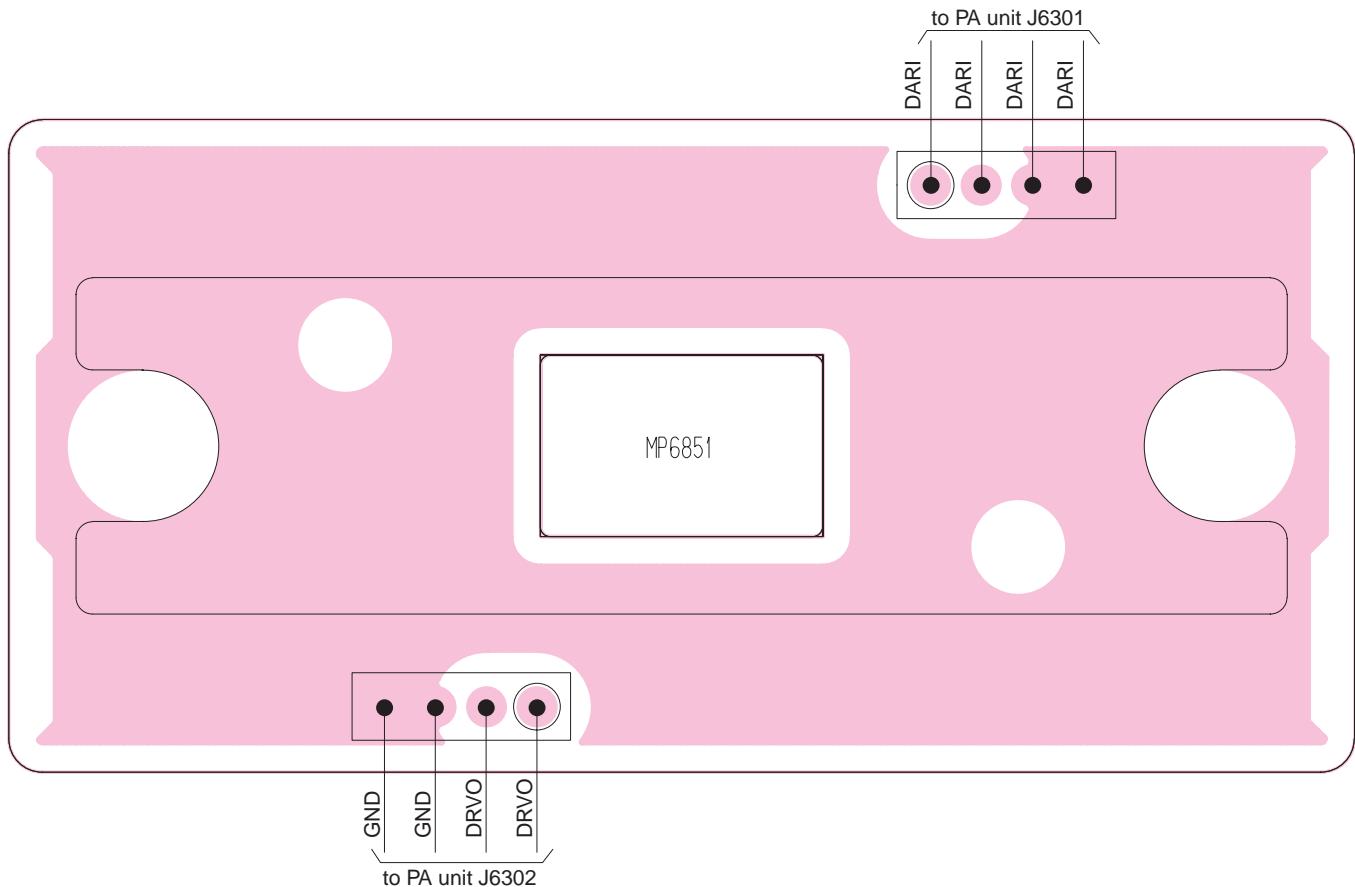
8-3-6 DRIVER BOARD
• TOP VIEW



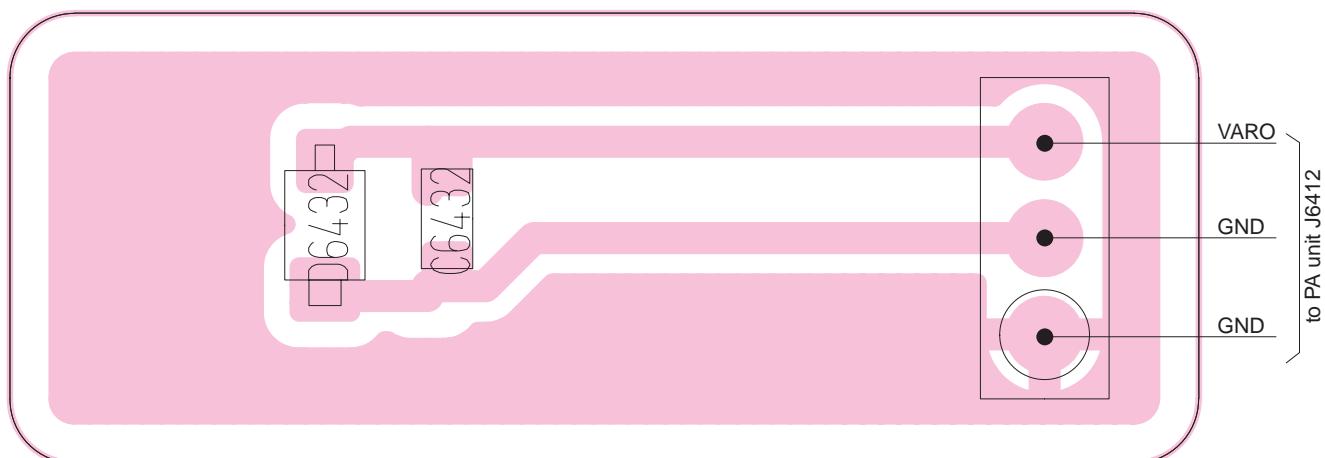
8-3-7 VARISTOR-1 BOARD



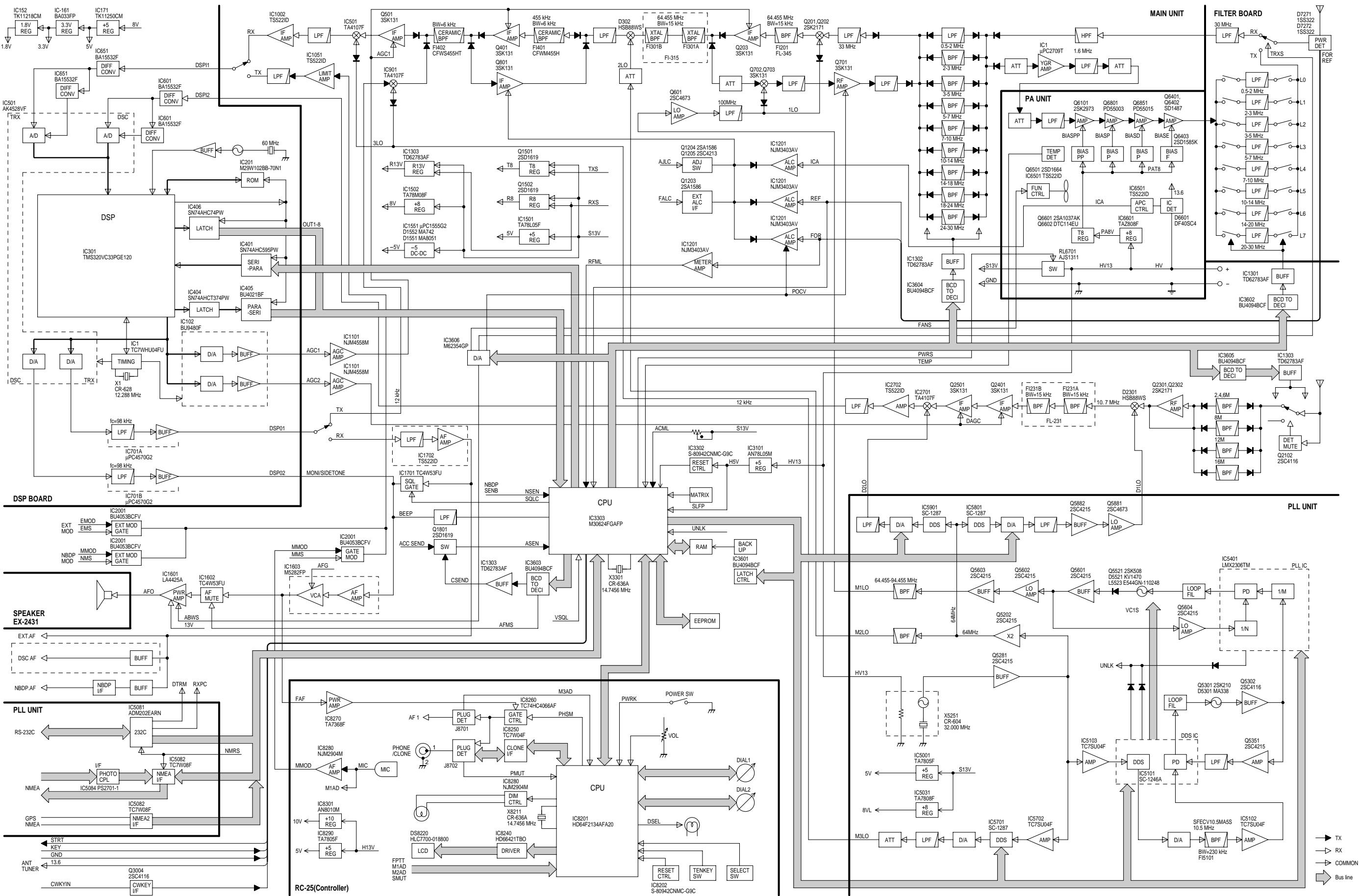
• BOTTOM VIEW (DRIVER BOARD)



8-3-8 VARISTOR-2 BOARD

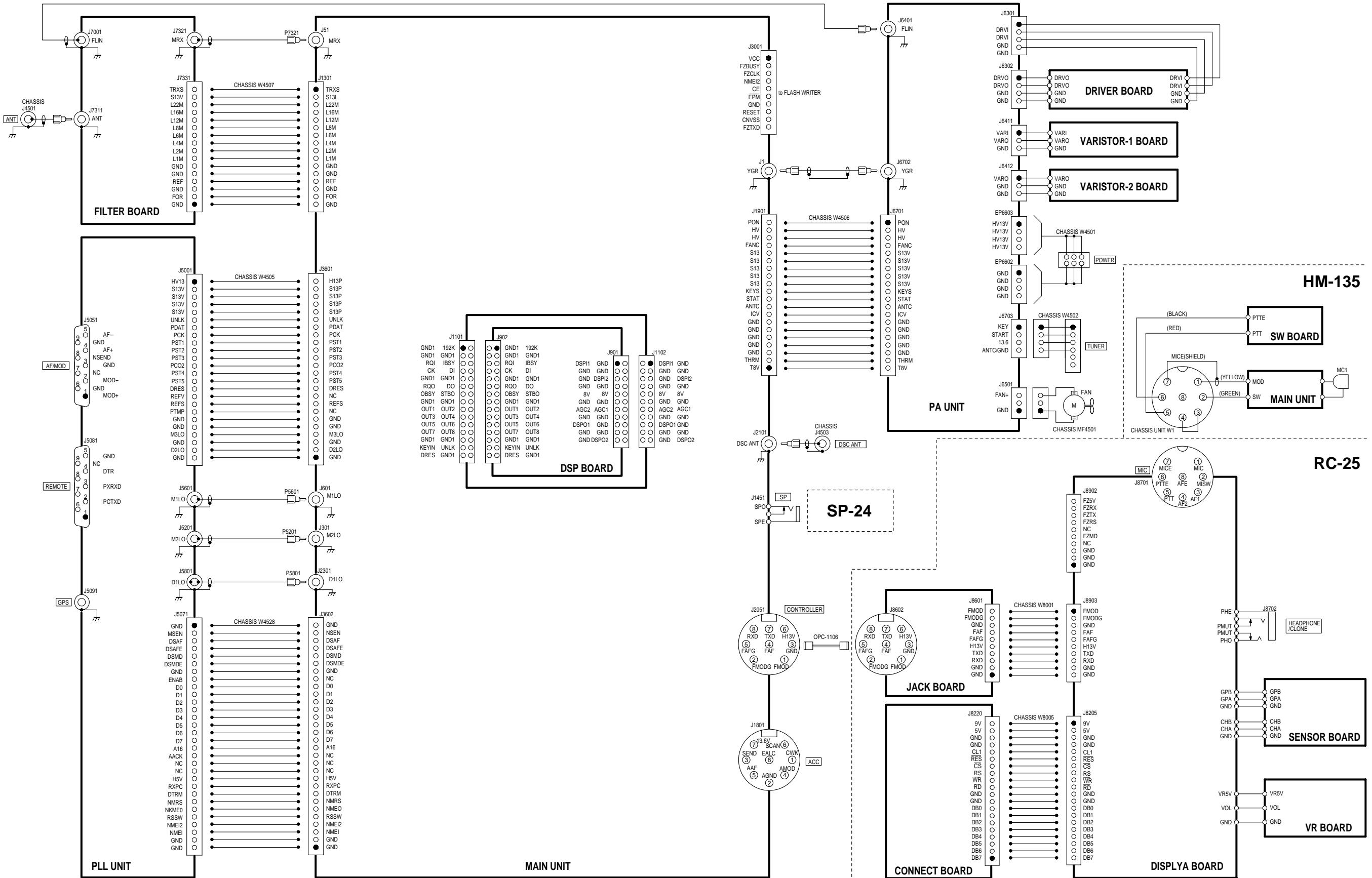


SECTION 9 BLOCK DIAGRAM



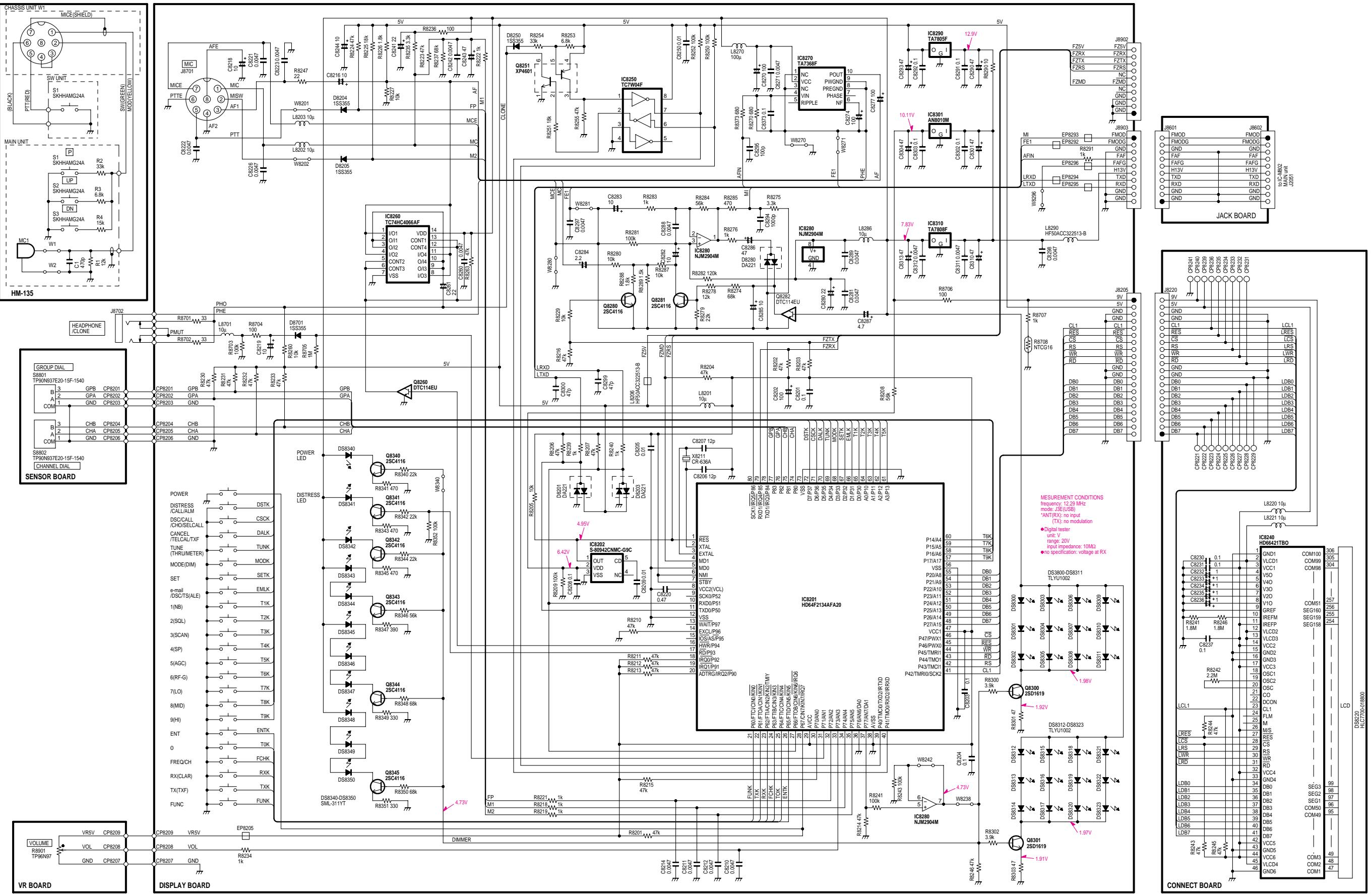
SECTION 10 WIRING DIAGRAM

IC-M802



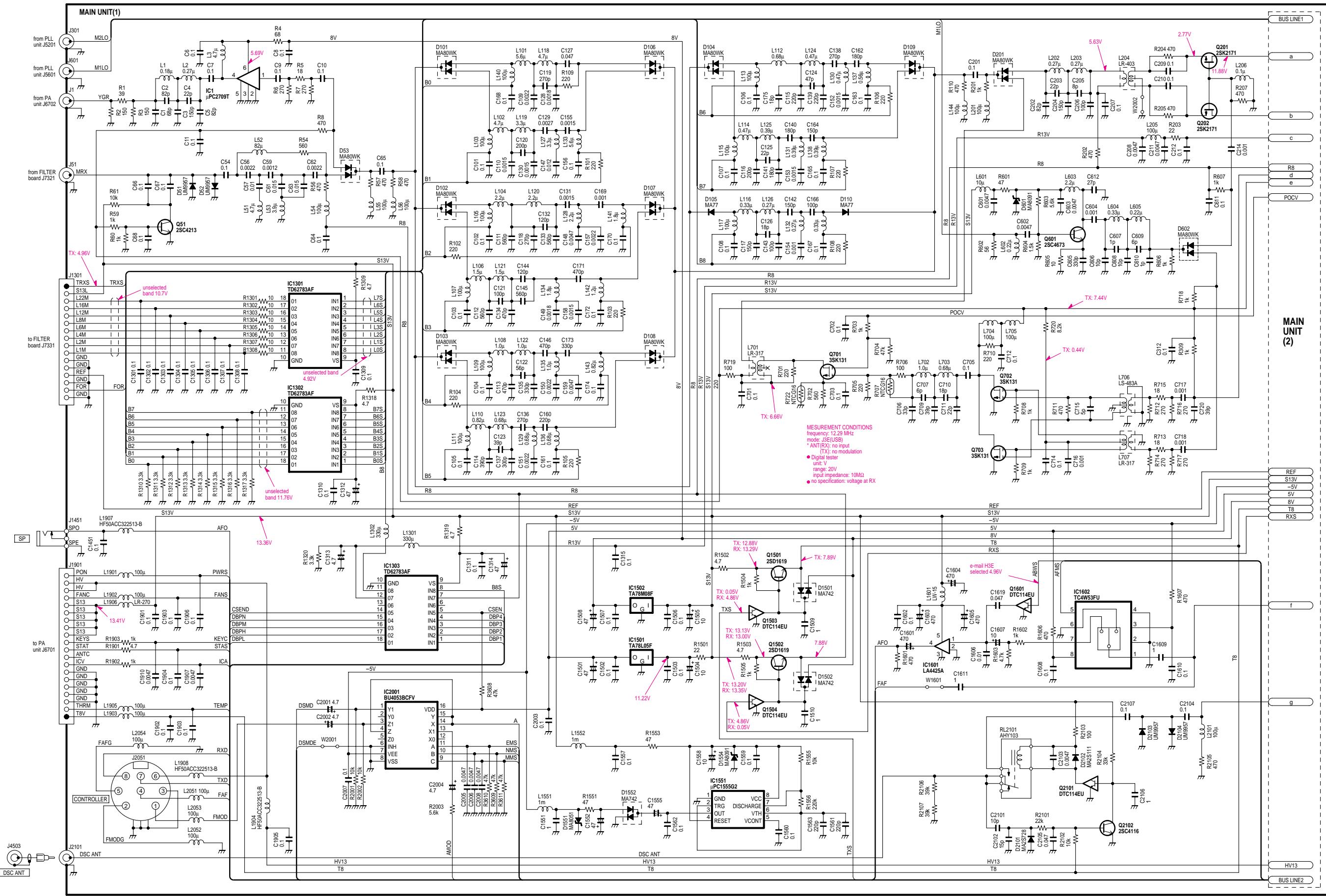
SECTION 11 VOLTAGE DIAGRAMS

11-1 RC-21 and HM-135

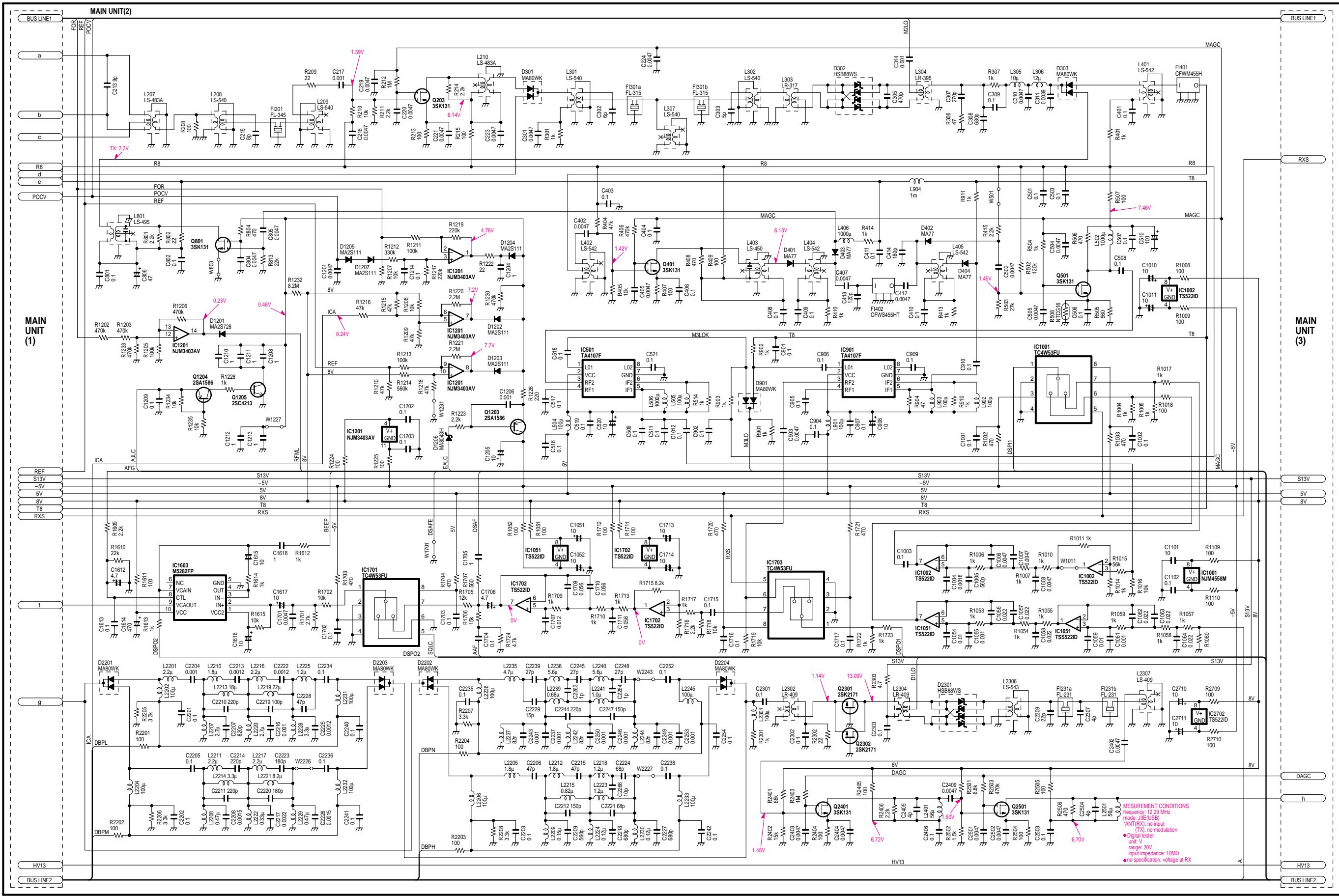


11-2 IC-M802

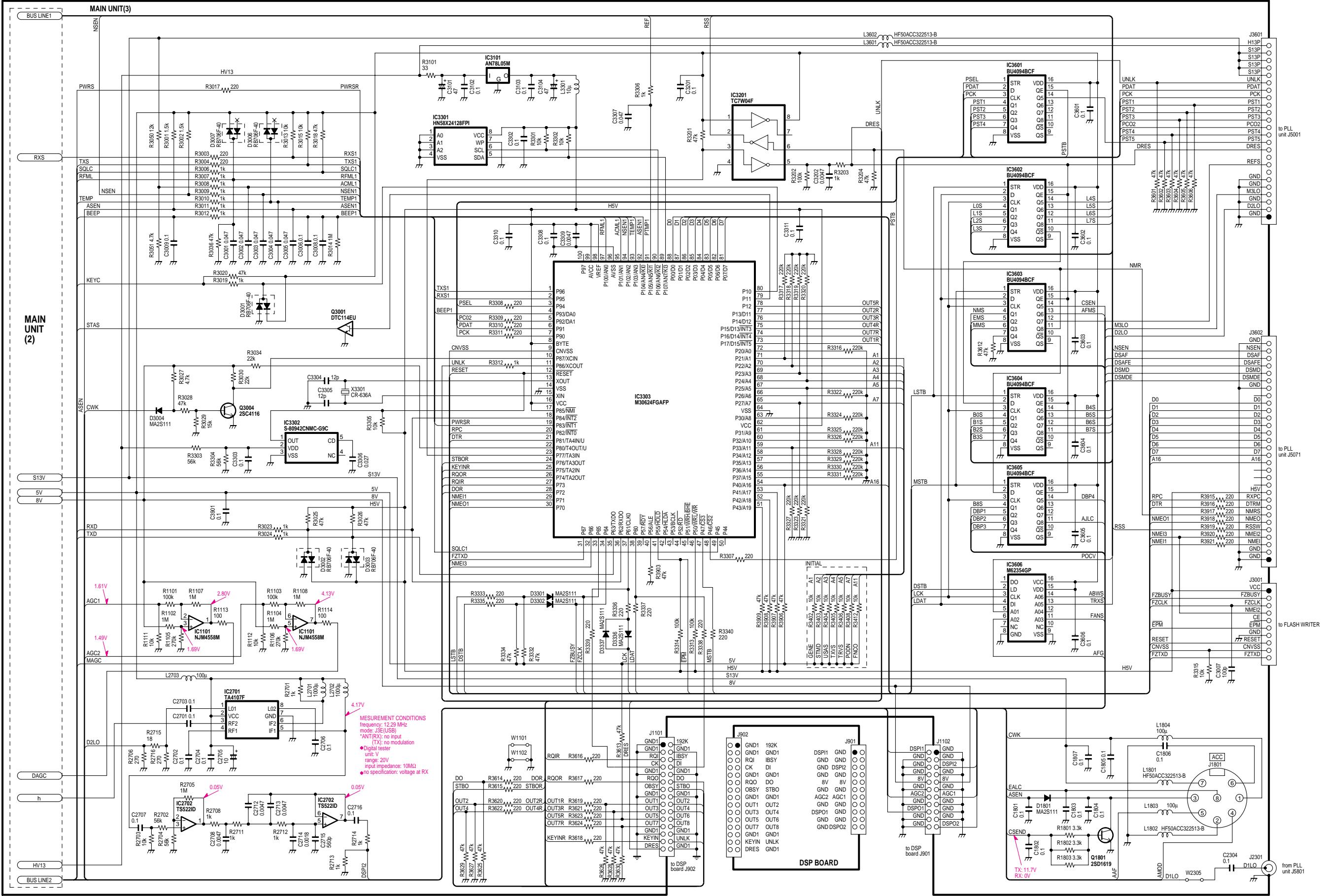
11-2-1 MAIN UNIT (1)



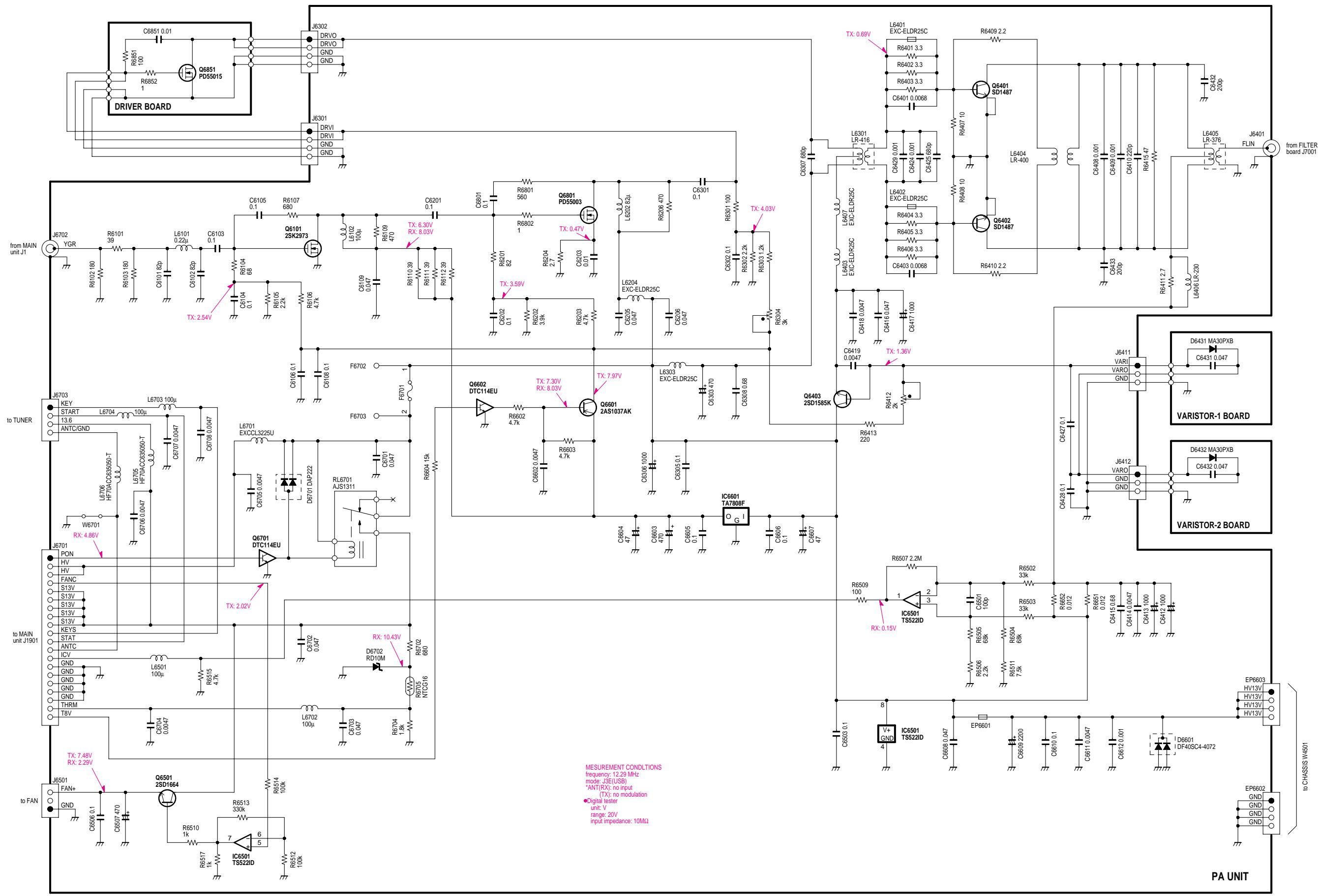
11-2-2 MAIN UNIT (2)



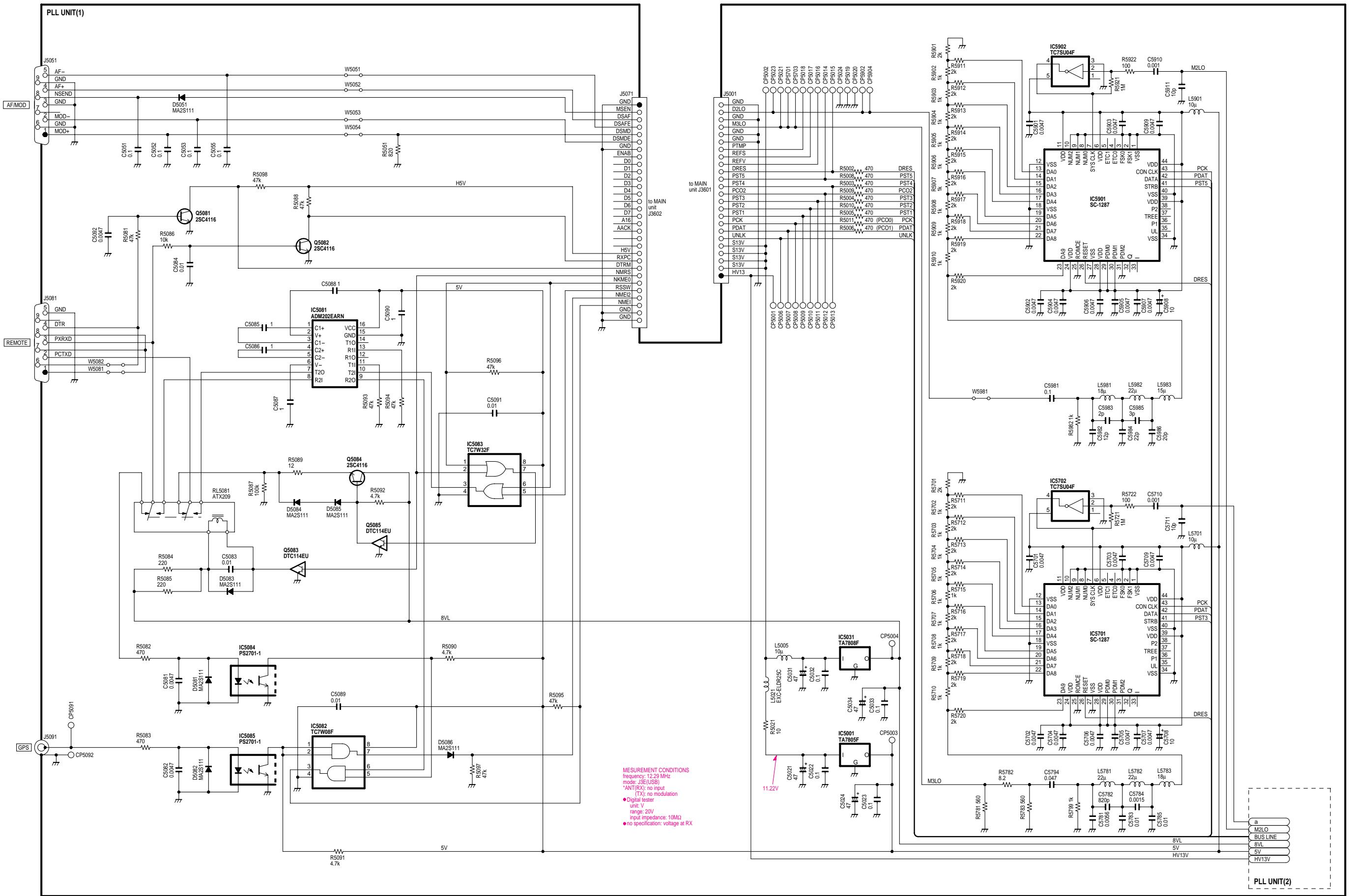
11-2-3 MAIN UNIT (3)



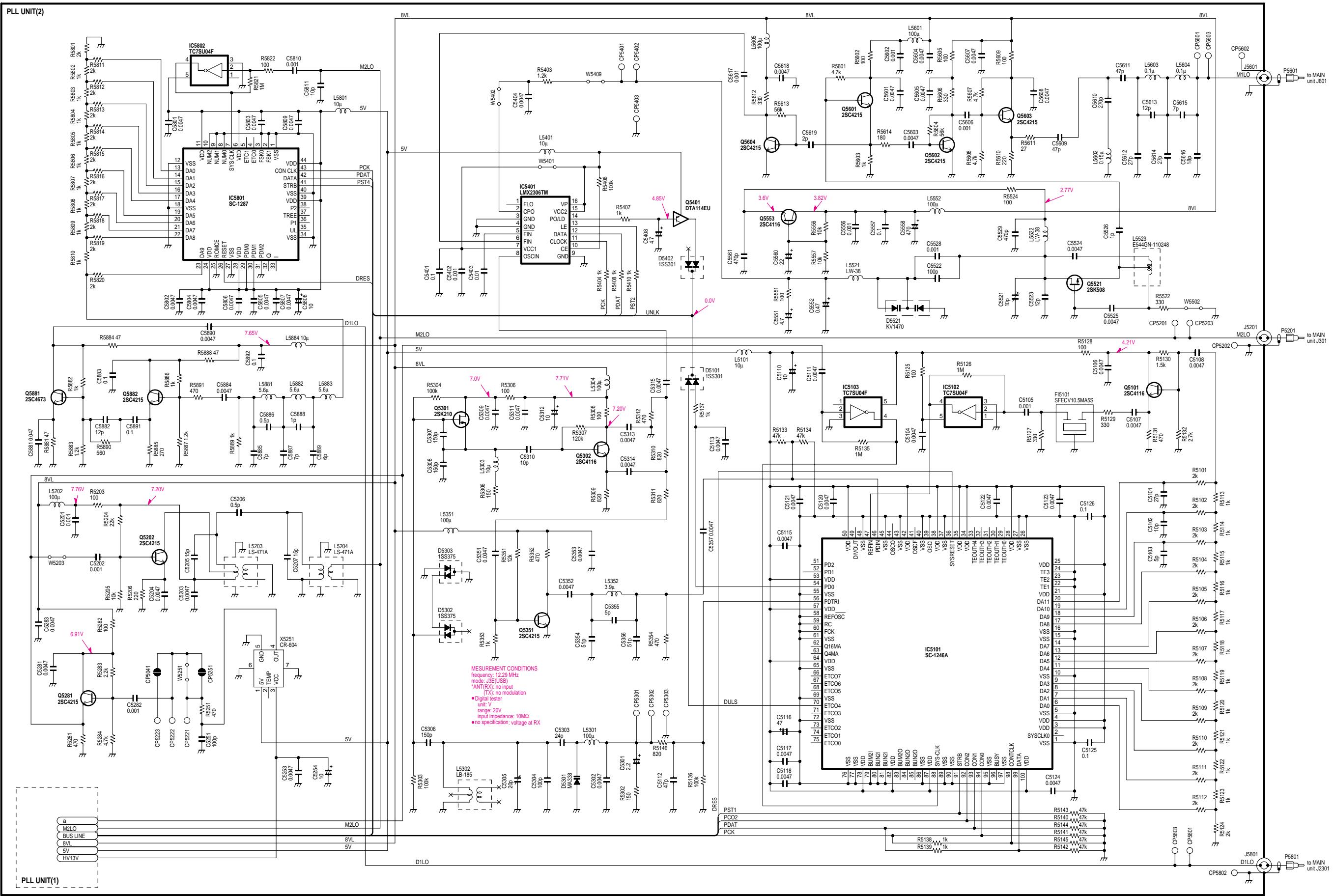
11-2-4 PA UNIT



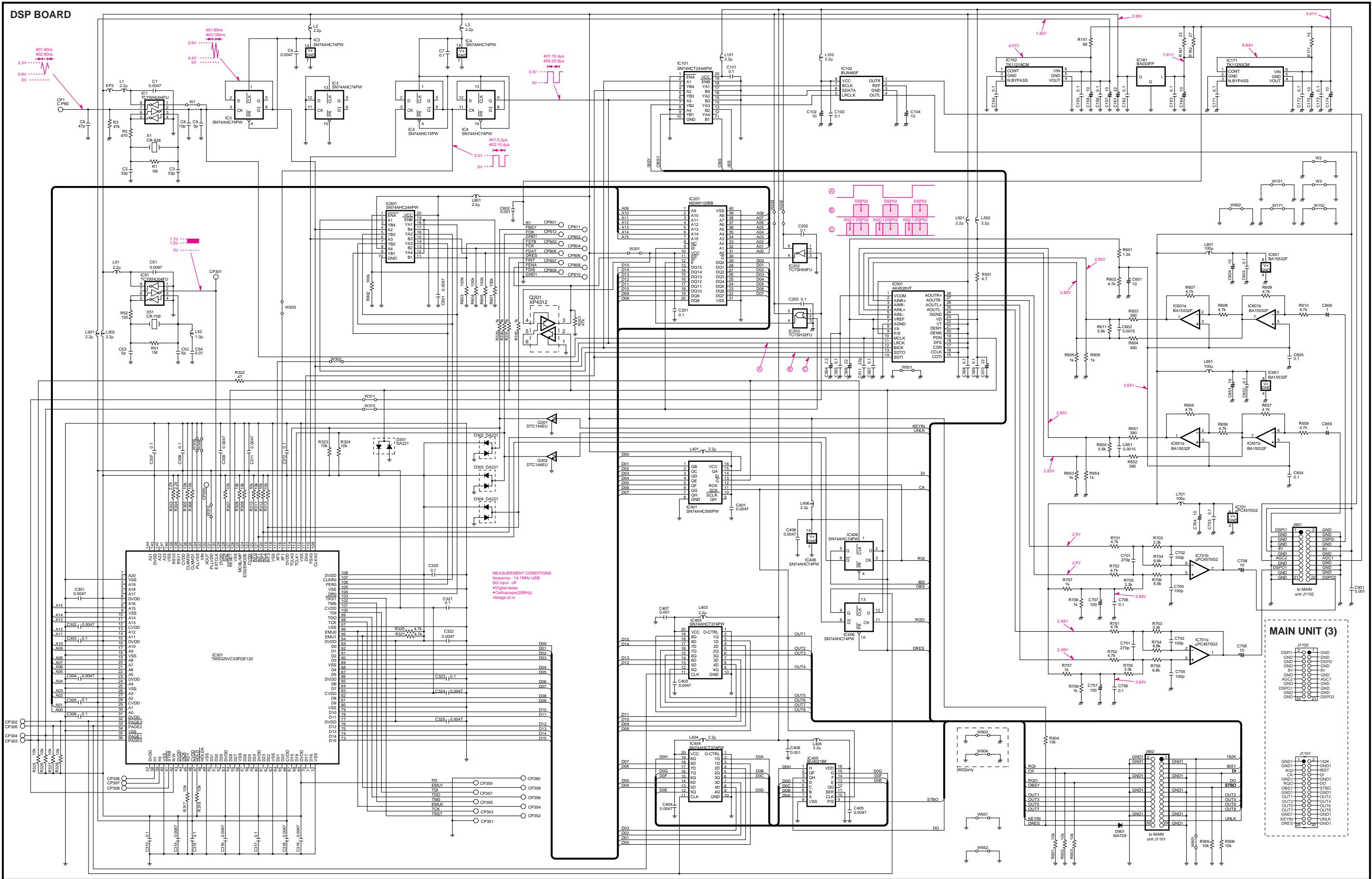
11-2-5 PLL UNIT (1)



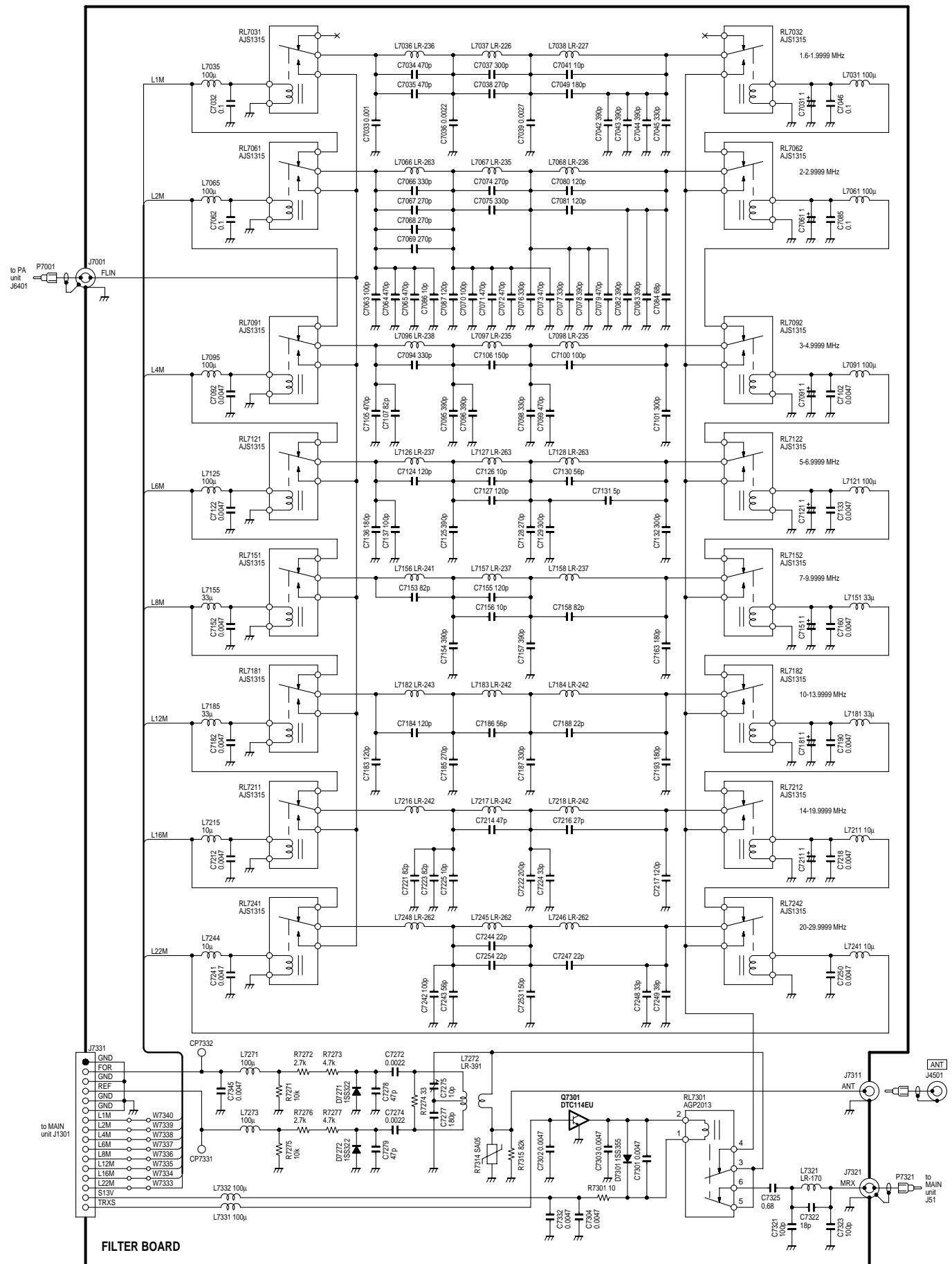
11-2-6 PLL UNIT (2)



11-2-7 DSP BOARD



11-2-8 FILTER BOARD



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