THE PROOF IS ON THE INSIDE AND OUT

After you have reviewed the PFS-875, look inside, your will be pleased to know we are using a BGA FPGA and it is made in the USA. This device provides many features to be crammed into one RU chassis

OK - the PFS-875 is envisioned as a product of the future. The unit is comprised of a 4 field TBC/frame synchronizer with full transcoding on the analog inputs and a user configurable set of triple analog outputs as well as SDI input and output. All processing is completely digital and includes a full proc amp as well as adjustable output levels and timing.

The PFS-875 is designed as a standalone gen-lock-able frame sync with a variety of both analog and digital inputs and outputs. The developmental was aimed to produce a broadcast quality unit as an initial offering, and then to potentially use portions of this new platform in a variety of new products (modules for existing card frame products, digital proc amp, etc.).

The PFS-875 provides dual standard composite and SDI timebase correction and synchronization to an external reference. The unit will decode multiple composite standards (NTSC, NTSC-J, PAL-B,G,H,I,N). All analog inputs are sampled with 10 bit resolution and decoded using a 3 line digital comb filter.







LINK ELECTRONICS, INC. + 2137 Rust Avenue + Cape Girardeau, MO 63703-7668 Phone 573 334 4433 FAX 573 334 9255



FRAME SYNCHRONIZER **MODEL PFS-875**



FEATURES

- Input/output Select Menu Short-cut Button
- Looping Reference Input
- Color Bar Black Generator DAC & ADC 12 Bit Processing
- AES Audio Delay up to 640 mS
- User and Factory Default Settings Analog I/O Video Proc Amp Settings
- SDI, CV, CAV (YUV), Y/C & S-Video I/O
- Embed Audio move to a different group
- Bi-Directional Digital Analog Decoder
- Front Panel VFD Display for Function Set-Up

There are four video input and output formats supported PFS-875 has the feature to embed and de-embed two by the PFS-875; Composite Video, Component Analog channels of stereo AES audio. The De-embedded AES Video (YUV), Y/C (S-VHS) and SDI. The component analog video input is YUV (SMPTE), and component output is YUV (SMPTE/Beta-cam). The analog output audio is output through 2 separate BNCs on the rear panel. Similarly, AES input for embedding can be input through 2 separate BNCs. It has the feature of is on three BNC connectors which can be programmed choosing to embed the de-embedded audio and move from the front panel to output all 3 composite, or the audio to another group or channels. It also has the composite and Y/C, YUV SMPTE, or YUV BETA. feature of delaying the audio to match the video delay.

The PFS-875 provides dual standard composite and One of the new developments to be included in this SDI correction and synchronization to an external product is an all-digital composite NTSC/PAL encoder. analog reference. The synchronizer decodes multiple composite standards (NTSC, PAL). All analog inputs are sampled with 12-bit resolution and decoded using The encoding process allows for an image quality superior to any of the off-the-shelf encoder ICs available on the market at present, and also gives a a 5 line adaptive comb filter. It also has a built-in color superior gen-lock capability. The encoder process bar or black burst generator. The PFS-875 can also implements 12-bit outputs. synchronize non TBC signals from VHS tapes.

The PFS-875 has the feature of three built-in processing amplifiers. One "proc. amp." is for the When input video is lost, the synchronizer freezes the processing amplifiers. One "proc. amp." is for the analog output allowing you to control luma, chrominance, Sync, Burst, and Set-up levels along with a black clip and hue phase adjustment. The second "proc. amp." is for the SDI output allowing you to control luma, chrominance and set-up levels along with a black clip. The third "proc. amp." is for the analog input affecting both analog and SDI outputs, which allows you to control the luma and chrominance levels along with hue phase adjustment. video or generates a test pattern. There are three ways to freeze the video, freeze to the last known good frame, field 1, or field 2. The test patterns that can be generated are SMPTE color bar for NTSC, 75 % color bar for PAL, and black burst for both. hue phase adjustments.







PAL/NTSC 1 Rack Unit **TBC** Function Power off Bypass User friendly Menu System Two AES Stereo Channels Selectable Video Freeze SDI Video Proc Amp Settings Analog Video I/O Proc Amp Settings Infinite Horizontal and Vertical Timing Bi-Directional Digital Analog Transcoder Audio Embedding & De-embedding

INPUTS: SDI, CV, YUV, Y/C, & S-Video:

$107013.301, CV, 10V, 1/C, \alpha 3-V1060.$	
Analog inputs:	7 (CV, Y/C, YUV, & S-Video)
Composite & Y level:	1Vpp, 75 Ω, BNC
	0.525Vpp, 75 Ω, BNC
C level:	0.624Vpp, 75 Ω, BNC
S-Video:	
Serial Digital Interface (SDI):	±10%, EQ ≈ 750", 75 Ω, BNC
Gen-Lock Reference: `´.	

OUTPUTS: SDI, CV, YUV, & Y/C:

Analog ouputs:	3 (3CV, CV & Y/C, or YUV)
Composite & Y level:	1Vpp, 75 Ω, BNC
U/V: 0.707Vpp (Beta	a)/0.525Vpp (SMPTE), 75 Ω, BNC
C level:	
Serial Digital SDI (2 BNCs):	0.8Vpp ±10%, 75 Ω, BNC
EAV-SAV	Meets SMPTE ITU-656

SIGNAL PROCESSING:

ADC/DAC Encoding/Decoding: 12 bit
Component YUV Pipeline:
Video Range:
Chroma Range:
Set-Up Range:
Burst Amplitude:
Sync Tip Adjust:
Horizontal Timing:
Vertical Timing:
Sub-carrier Timing:
Hue Phase:

FREQUENCY RESPONSE AND SIGNAL TO NOISE:

regulation response and signal to hoise.	
Frequency Response:	+/- 2.5 dB at 4.22 MHz
Tilt:	<1% ref. 30Hz square wave
Hum:	>70 dB, 1.0Vp-p
Overshoot & Ringing:	
S/N Ratio:	
Differential Gain:	
Differential Phase:	
K-Factor:	
Line Rate Tilt:	,
Field Rate Tilt:	



1. YUV input SMPTE levels only

- 2. NComposite inpu
- 3. Y input.
- 4. C input
- 5. S-Video input
- 6. SDI input
- 7. SDI outputs

- 8. AES inputs
 9. AES outputs
 10. Analog outputs

- 11. Looping reference input 12. RS-232 connectors for future use
- 13. Fuse holder
- 14. Power receptacle, 85V-AC to 260V-AC

AES/EBU INPUT: Resolution: **AES/EBU OUTPUT:** Output: Resolution: Input: Resolution: Input: Resolution: Sampling Rate: THD: THD + N: Channel Crosstalk: FRONT PANEL CONTROLS:

AC Power:
VFD DISPLAY:
Characters:
ELECTRICAL:
Input Power: Auto Detection 90 to 264 V AC Frequency: 47 to 63 Hz Power: 30 Watts
Frequency: 47 to 63 Hz
Power:
MECHANICAL:
Height:
Width:
Depth:
ENVIRONMENTAL:
Operating Temperature:



1. Power Off/On Switch

- 2. Display, for menu selections
- Menu Selector Knob
 Input Selection Buttons
- 5. Output Format Button
- 6. CV & CAV LEDs
- 7. Freeze Button

AUDIO:

2 AES/EBU Stereo Channels
2 AES/EBU Stereo Channels 20/24-bit, 48 KH BNC (SDI Video Input
BNC (SDI Video Output)
640 ms in steps of 80 ms
>100 dB

PHYSICAL:

8. Audio Control Button 9. Proc Amp Adjustment Button 10. Output Timing Button 11. Bypass Button 12. Gen-lock Button 13. 525/625 LEDs