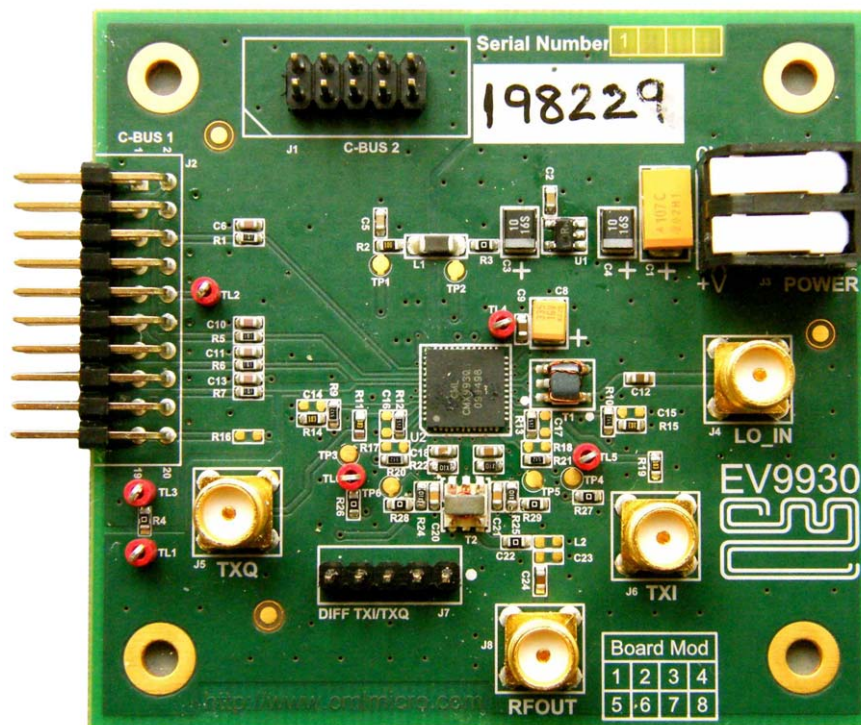


### Features

- **Operational range: 100MHz to 1GHz**
- **Allows full evaluation and investigation of the CMX993 quadrature modulator**
- **Access to RF, control and baseband signals via connectors and test points**
- **Interfaces to CMX910 or CMX981 EvKit**
- **Differential or single-ended I and Q inputs**
- **Can utilise PE0002 EvKit interface card for PC interface or work with custom interface equipment**



## 1 Brief Description

The EV9930 EvKit allows for rapid and full evaluation of the CMX993 Quadrature Modulator IC by providing a populated pcb with access to all CMX993 RF, baseband and control signals by either connectors or test points. Test access points are available to accept common test equipments such as RF and baseband signal generators and spectrum analysers. Control of the CMX993 is by registers which are accessible over the C-BUS interface (an SPI™-like serial interface). A PE0002 Interface Card (not supplied) allows the C-BUS interface to be controlled from a PC, via its USB port. Alternatively, the C-BUS interface can be controlled by an external microprocessor.

RF signal paths are matched by suitable components and the operating frequency range of the EvKit is for RF frequencies between 100MHz and 1GHz. The EV9930 baseband inputs can be configured to work with the CMX910 AIS Baseband Processor IC or the CMX981 Digital Radio Baseband Processor IC; standard test equipment or with a custom baseband system.

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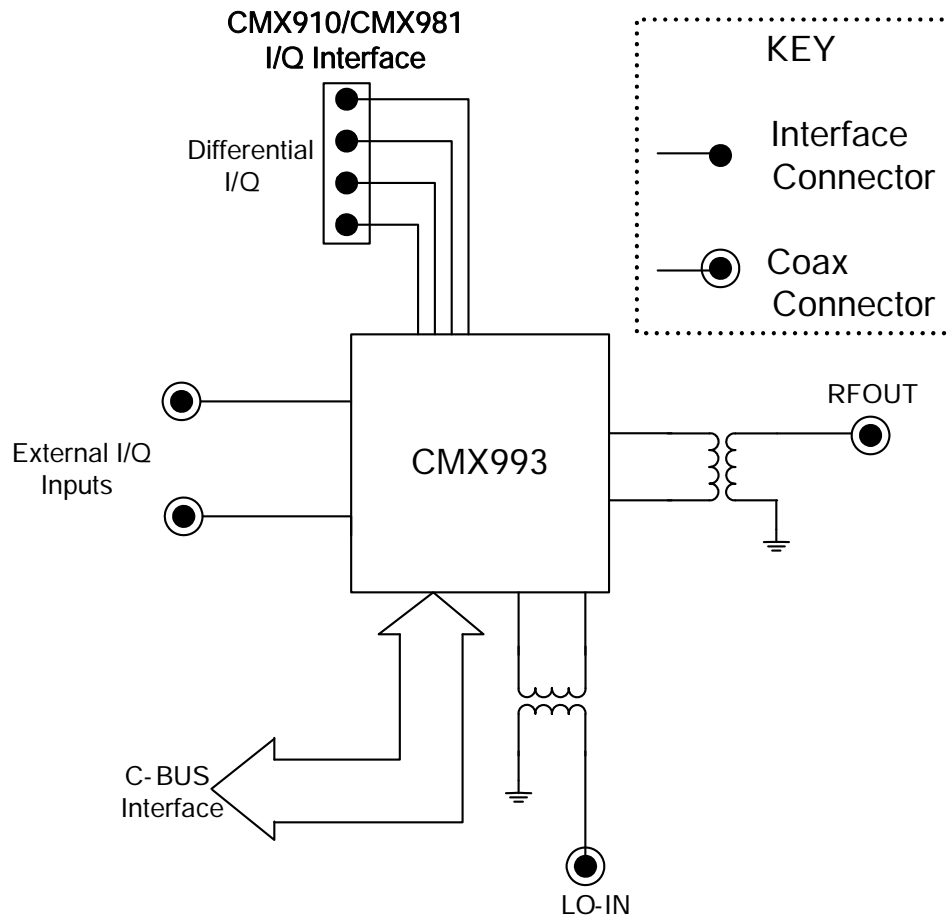
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It is always recommended that you check for the latest product datasheet version from the Datasheets page of the CML website: [[www.cmlmicro.com](http://www.cmlmicro.com)].



**Figure 1 Block Diagram**

## 2 Preliminary Information

The EV9930 provides a platform for the evaluation of the CMX993. To use the EV9930, a separate micro-controller or PC is required to program the CMX993 via its C-BUS interface. This controlling device is not included on the EV9930, but a PE0002 Interface Card (not supplied) will provide the controlling functionality from a PC, via its USB port.

### 2.1 Laboratory Equipment

The following laboratory equipment is needed to use this evaluation kit:

- Power Supply (Dual Power Supply if using PE0002).
- Spectrum Analyzer (100MHz to 1GHz)
- RF Signal Generator (100MHz to 2GHz)
- I/Q Quadrature Signal Source

For more detailed design or investigation work the customer may require other RF test equipment.

#### 2.1.1 Power Supply

The supply input voltage to the PCB is 5.0V (4.0V to 8.0V acceptable). An on-board regulator is provided to generate the power supplies used on the PCB.

The 5.0V supply should be rated at >200mA.

**NOTE: Care should be exercised with the supplies, as they are not protected for reverse polarity.**

**NOTE: When using the EV9930 kit with a PE0002 kit, power is not supplied to the PE0002 via the C-BUS connector (J2). The PE0002 must be connected directly to a separate 5Vdc regulated power supply.**

### 2.2 Handling Precautions

Like most evaluation kits, this product is designed for use in office and laboratory environments. The following practices will help ensure its proper operation.

#### 2.2.1 Static Protection

This product uses low power CMOS circuits that can be damaged by electrostatic discharge. Partially damaged circuits can function erroneously, leading to misleading results. Observe ESD precautions at all times when handling this product.

#### 2.2.2 Contents - Unpacking

Please ensure that you have received all of the items on the separate information sheet (EK9930) and notify CML within 7 working days if the delivery is incomplete.

### 2.3 Approvals

**This product is not approved to any EMC or other regulatory standard. Users are advised to observe local statutory requirements, which may apply to this product and the radio frequency signals that may emanate from it.**

### 3 Quick Start

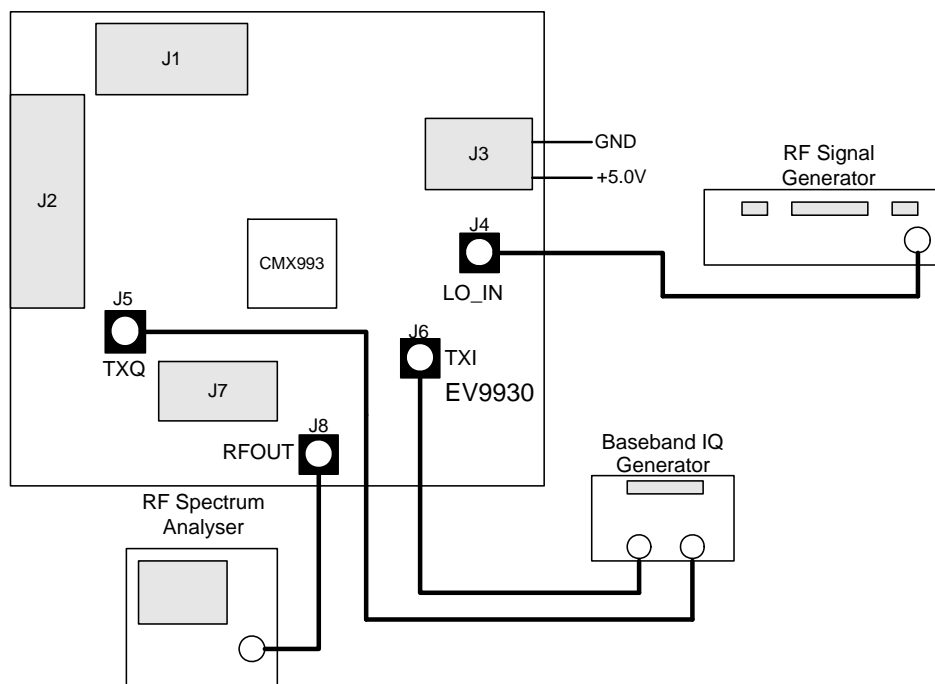
This section provides instructions, in three main steps, for users who wish to experiment immediately with CMX993 evaluation. A more complete description of the kit and its use appears later in this document. The EV9930 includes a CMX993 device that is described in its own, separate, datasheet.

**Accordingly, before using the EV9930 the user should read the current CMX993 datasheet.**

#### 3.1 Make External Connections and Apply Power

In this first step external connections are made to the EV9930 and then power is applied. Perform the following steps in sequence:

1. Connect test leads as shown below in Figure 2.
2. Connect a host  $\mu$ Controller to C-BUS interface J1 or J2 (alternatively, the CML PE0002 card).
3. Power should be applied to the main supply (5.0V nominal).



**Figure 2 Typical Evaluation Connections for EV9930**

### 3.2 Typical Configure

In this second step the EV9930 is configured as follows:

- With a single-ended I/Q baseband, input signals at 0V DC bias.
- With an LO signal applied to give an RF output at 450MHz.

Apply signals and set register values as per Table 1. Program the CMX993 registers by use of a host  $\mu$ controller or PE0002 Interface Card, connected as per section 3.1 step 2. The CMX993 datasheet gives details of the registers and commands. These can be set directly in the "C-BUS Control" tab of the ES0002xx.EXE software, which forms part of the PE0002 kit.

Signal or Register	Setting	Connector	Note
LO input	900MHz at -10 dBm	J4	This frequency is twice the RF output frequency that will be developed
I/Q baseband inputs	0V DC	J5, J6	These inputs must be connected to 0V DC; do not leave them unconnected or open circuit
General Control register (\$02)	Set the CMX993 General Control register bits 0 and 1 to their normal operating state (set both to '0') and enable all of: <ul style="list-style-type: none"> <li>• Modulator and LO ccts</li> <li>• Bias circuits</li> <li>• Filter amplifiers</li> <li>• Input amplifiers</li> </ul>		
Forward path gain control register (\$05)	0 dB		
Frequency Control register (\$08)	Divide by 2 mode (b4 cleared to '0')		This means the externally applied LO must be twice the desired RF carrier frequency

**Table 1 Quick-Start Signal Configuration**

### 3.3 Operate

Baseband I/Q signals (typically 700mV to 1.0V pk-pk) can now be applied to develop a modulated RF output of ~ +3dBm PEP.

Notes:

- The output of the CMX993's input amplifiers should be 0.35V pk-pk nominal and 0.5V pk-pk maximum. Any user changes to the input amplifier's external components and applied baseband I/Q signal levels should ensure the signal applied to the CMX993 modulator inputs MODQP / MODQN / MODIP / MODIN remain within the specified range.
- The peak to average power ratio of the user-provided modulation will determine the mean RF output power delivered by the EV9930.

## 4 Signal Lists

CONNECTOR PINOUT				
Connector Ref.	Connector Pin No.	Signal Name	Signal Type	Description
J1	1 - 10	C-BUS 2 Interface	Logic	Alternative header for C-BUS interface
J2	1 - 20	C-BUS 1 Interface	Logic	C-BUS interface from PE0002 or host / controller PC. See schematics and CMX993 datasheet for details.
J3	2	+5.0V	DC	5.0V power supply input
J3	1	GND	DC	Power supply ground
J4	N/A	LO-IN	RF	LO input can be applied at 2 x RF output frequency or 4 x RF output frequency. Nominal input level is -10dBm.
J5	N/A	TXQ	Baseband	Q input signal (non-inverting input of input amplifier)
J6	N/A	TXI	Baseband	I input signal (non-inverting input of input amplifier)
J7	1, 2, 4, 5	TXQ+, TXQ- TXI-, TXI+	Baseband	Differential I/Q input from the CMX981 evaluation board
J7	3	GND A	Baseband	Connection to analogue ground
J8	N/A	RFOUT	RF	Modulator RF output

**Table 2 Signal List**



<b>CONNECTOR PINOUT for J1</b>			
<b>Connector Pin No.</b>	<b>Signal Name</b>	<b>Signal Type</b>	<b>Description</b>
1	CSN	I/P	C-BUS enable
2	~	~	Spare pin. Leave unconnected
3	S_DATA_IN	I/P	C-BUS data input
4	~	~	Spare pin. Leave unconnected
5	S_CLOCK	I/P	C-BUS clock
6	~	~	Spare pin. Leave unconnected
7	~	~	Spare pin. Leave unconnected
8	~	~	Spare pin. Leave unconnected
9	IRQN	I/P	Interrupt request - if required. Not used
10	GND	Power	Ground

**Table 3 C-BUS 2 Interface**

<b>CONNECTOR PINOUT for J2</b>			
<b>Connector Pin No.</b>	<b>Signal Name</b>	<b>Signal Type</b>	<b>Description</b>
1	NRESET	I/P	General Reset (reset when pin is low)
2	CSN	I/P	C-BUS enable
3	~	~	Spare pin. Leave unconnected
4	S_DATA_IN	I/P	C-BUS data input
5	~	~	Spare pin. Leave unconnected
6	S_CLOCK	I/P	C-BUS clock
7	~	~	Spare pin. Leave unconnected
8	~	~	Spare pin. Leave unconnected
9	~	~	Spare pin. Leave unconnected
10	IRQN	I/P	Interrupt request - if required. Not used
11	GNDD	Power	Connection to digital ground
12	GNDD	Power	Connection to digital ground
13	~	~	Spare pin. Leave unconnected
14	~	~	Spare pin. Leave unconnected
15	~	~	Spare pin. Leave unconnected
16	~	~	Spare pin. Leave unconnected
17	~	~	Spare pin. Leave unconnected
18	~	~	Spare pin. Leave unconnected
19	VDDIO	Power	Digital power supply – if required. Not used.
20	VDDIO	Power	Digital power supply – if required. Not used.

**Table 4 C-BUS 1 Interface (for PE0002)**

<b>TEST LOOPS</b>		
<b>Test Loop Ref.</b>	<b>Default Measurement</b>	<b>Description</b>
TL1	0V	GND – Analogue ground probe point
TL2	N/A	IRQN (not used)
TL3	0V	GNDD – Digital ground probe point
TL4	1.6VDC	BVREF buffered Vref
TL5	1.6VDC	I input amplifier output
TL6	1.6VDC	Q input amplifier output

**Table 5 Test Loops**

<b>TEST POINTS</b>		
<b>Test Point Ref.</b>	<b>Default Measurement</b>	<b>Description</b>
TP1	3.3VDC	Filtered 3.3V regulator output for digital circuits
TP2	3.3VDC	3.3V regulator output for analogue circuits
TP3	N/A	Alternative bias input Q channel
TP4	N/A	Alternative bias input I channel
TP5	1.6VDC	I channel bias op-amp output
TP6	1.6VDC	Q channel bias op-amp output

**Table 6 Test Points**

**Notes:**

I/P	=	Input
O/P	=	Output
BI	=	Bidirectional
TL	=	Test Loop
TP	=	Test Point

### 5 Circuit Schematics and Board Layouts

For clarity, circuit schematics are available as separate high-resolution files. These can be obtained via the CML website.

The layout on each side of the pcb is shown in Figure 3 and Figure 4.

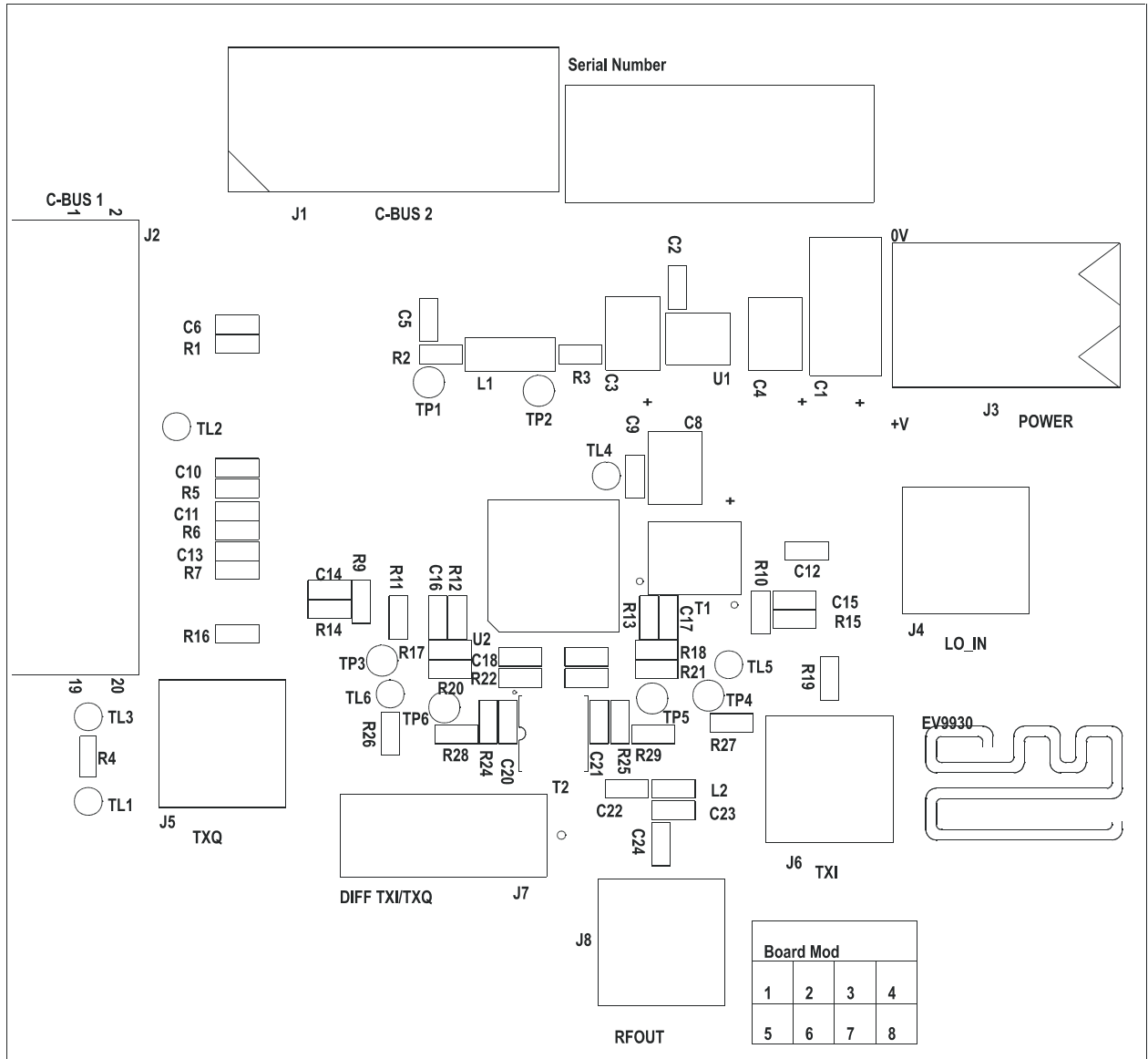


Figure 3 PCB Layout: top

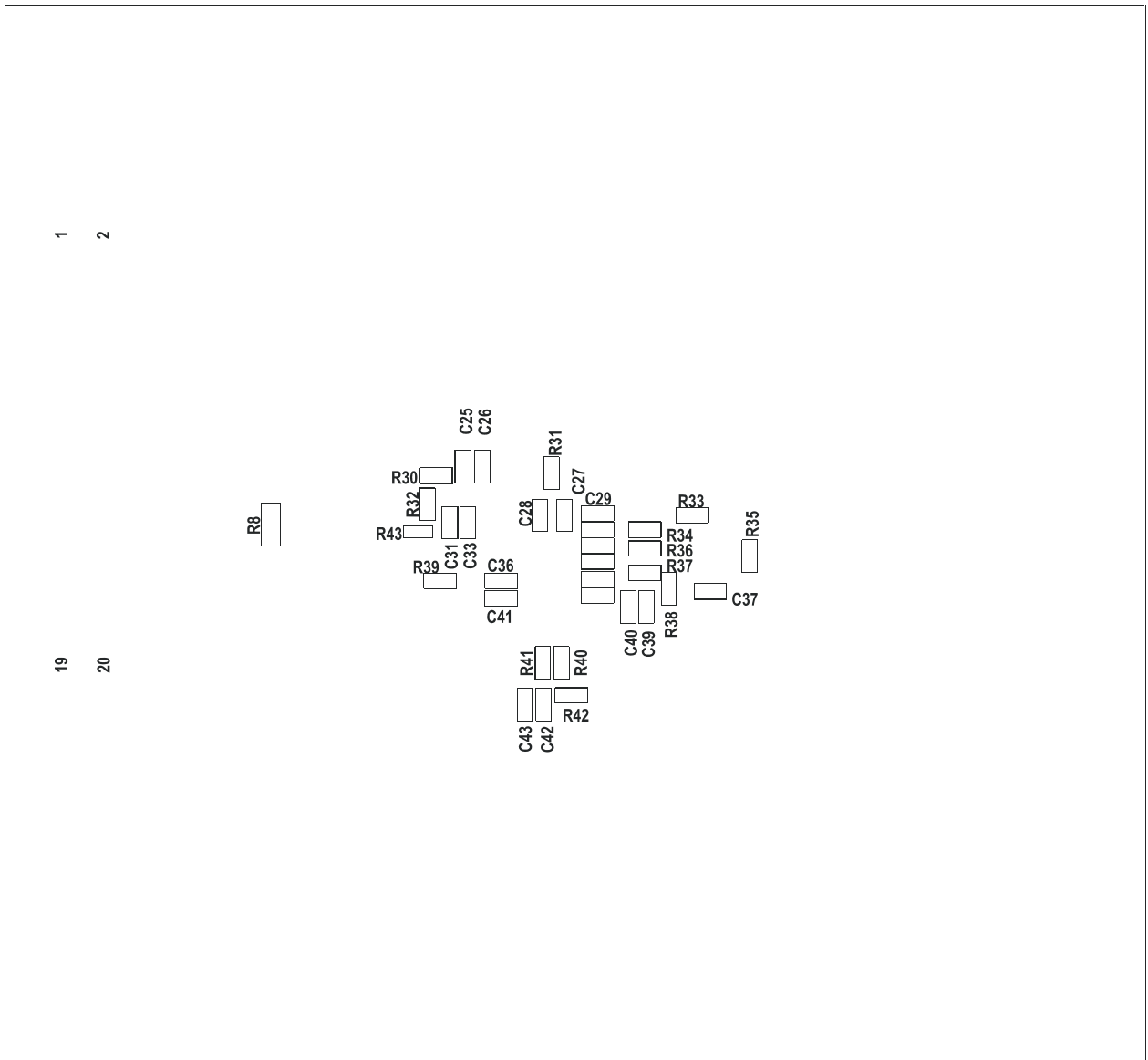


Figure 4 PCB Layout: bottom

## 6 Detailed Description

The CMX993 datasheet (available at [www.cmlmicro.com](http://www.cmlmicro.com)) should be referred to for a detailed description of the CMX993 device.

The EV9930 functionality includes:

- Demonstration of the I/Q Modulator
- Operation at 100MHz to 1000MHz
- C-BUS Interface that allows the card to be connected to a host  $\mu$ Controller. Interface to a PC for initial test and customer evaluation is available separately with the PE0002, which allows control of all device functions from a PC via the software available on the CML website.
- Direct Interface to CMX910/CMX981 evaluation cards, which allows demonstration of:
  - GFSK, GMSK or  $\pi/4$ -DQPSK modulation
  - Burst and continuous transmission

In summary, the EV9930 allows the user to create experiments to investigate all aspects of the CMX993 device. The EV9930 is designed to allow user modification, to support the detailed investigation of each user's application.

### 6.1 Hardware Description

#### 6.1.1 Quadrature (I/Q) Modulator

The card is configured to allow the CMX993's Quadrature Modulator to operate with single-ended external inputs. The gains are set in the baseband amplifier sections to suit the input drive levels in section 6.1.3. The LO input and modulator output are matched with broadband balun transformers to allow wideband operation from 100MHz to 1GHz.

The NRESET pin is pulled high with 100k ohms. The user can pull this pin low to reset the device (or the software reset can be used).

#### 6.1.2 Local Oscillator (LO)

A local oscillator has not been provided on the EV9930. A connector is provided to allow an external oscillator source to be injected at twice or four times the RF output frequency depending on the oscillator mode selected in the CMX993 frequency control register.

#### 6.1.3 I/Q Baseband Inputs

The board allows the quadrature (I/Q) inputs to come from the CMX981 or from an alternative external source. The input can be differential or single ended. Adjusting the values around the CMX993 input amplifiers creates the required input configuration. The default setup is for single ended inputs, which are applied to TXI and TXQ. A typical input signal level would be in the region of 700mV to 1.0V pk-pk at 0V DC bias.

#### 6.1.4 Differential I/Q Interface

The EV9930 has an interface to allow connection to the CMX910 or CMX981 evaluation cards, also known as EV9100 and EV9810. The connector J7 allow differential I/Q signals to be applied directly from these boards.

#### 6.1.5 Power Supply

The input to the PCB for the CMX993 and the interface circuits is nominally 5.0V (4.0V to 8.0V is acceptable). An on-board regulator generates the 3.3V power supply used on the EV9930.

## 6.2 Software Description

The EV9930 does not require any embedded firmware. The PE0002 Interface Card, available separately from CML, and the related software zip file (ES9930xx.ZIP) can be used to control the EV9930. To use the software connect the EV9930 to a PE0002 using the connector J2 labelled C-BUS 1. First ensure the drivers supplied for the PE0002 are installed correctly. Run the ES9930xx.exe and the main application window will open with a progress bar for the initialisation process. Once the initialisation process is complete, one of two tabs can be selected.

To select a tab simply click on the corresponding name in the row at the top of the program window. Setting or clearing the check box associated with a bit of a register will cause that bit to be set or cleared when the register is next written to. If the 'Auto Write' check box is set, a register will be written whenever a check box, or list box associated with it, is modified. This removes the need to click the 'Write' button associated with that register. The program can be closed at any time by clicking the 'Close' button or by pressing 'Alt' and 'F4' keys simultaneously.

### 6.2.1 The 'CMX993 Regs (\$02 - \$08)' Tab

This tab allows all registers of the CMX993 to be controlled and a general or hardware reset to be performed. Also on this tab the PE0002 CBUS header 1 and 2 can be selected.

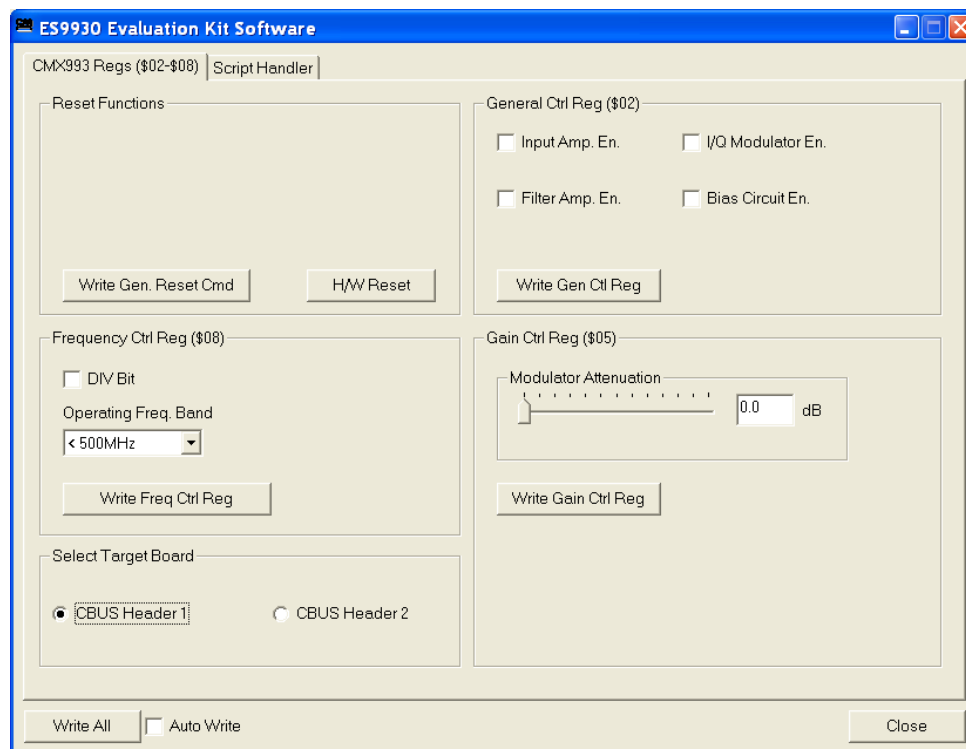
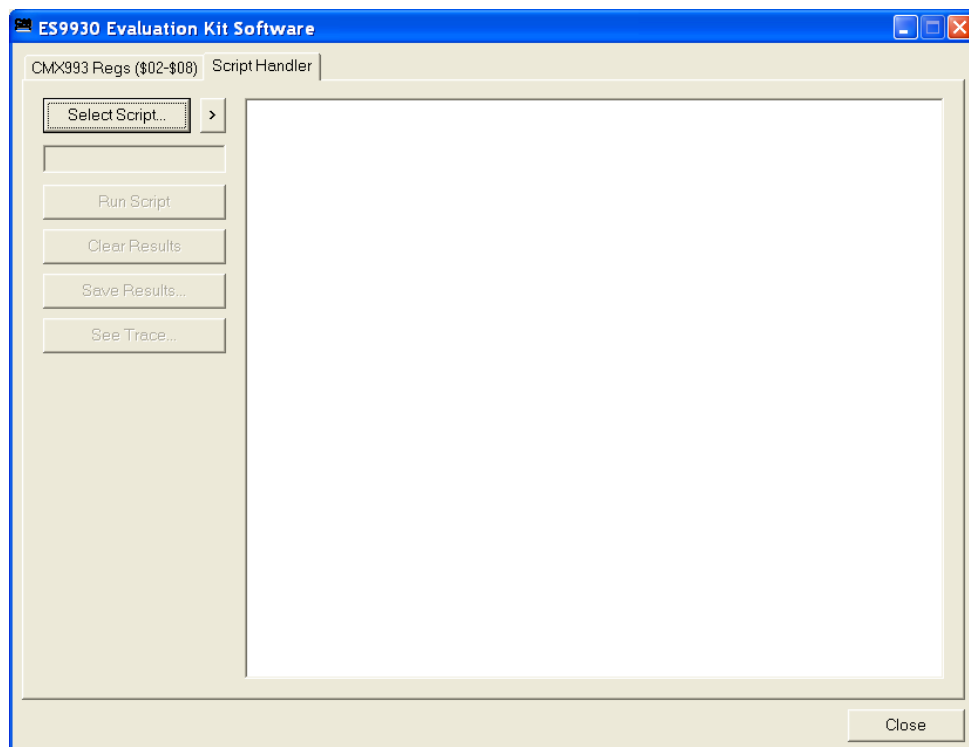


Figure 5 The 'CMX993 Regs (\$02 - \$08)' Tab

### 6.2.2 The Script Handler Tab

The Script Handler tab (shown in Figure 6) allows the execution of script files consisting of register write, read, and delay commands. These are plain text files on the PC, which are compiled via the GUI but executed by the E2 Microprocessor on the PE0002 board. The script language is documented separately in the "Script Language Reference" document, which can be downloaded with the PE0002 support package from the CML website. Control of the EV9930 does not require the use of script files.



**Figure 6 The 'Script Handler' Tab**

To select a script file, click on the 'Select Script' button. The Open File Dialog is displayed. Browse and select the script file. The folder that contains the script file will be the working folder of the script (i.e. all the files referenced in the script will be searched in this folder). Alternatively, select a script file from the recent files list. Click on the '>' button to display the list.

The results window displays the values returned by the script. These results can be saved to a text file or discarded by clicking on the 'Save Results' or 'Clear Results' buttons, respectively. When a script file is being executed the 'Run Script' button will change to be the 'Abort' button, the rest of the tab will be disabled and the other tabs cannot be selected.

After a script has finished running and when trace data is available, the 'See Trace...' button will be enabled. Up to 131072 C-BUS transactions can be logged in the PE0002 board. Click in the 'See Trace...' button to display the Trace dialog box. Note that the C-BUS transactions are only logged if the feature has been enabled in the script. For further details, see the "Script Language Reference" document, which can be downloaded with the PE0002 support package from the CML website.



### 6.3 Application Information

#### 6.3.1 Typical Performance

All results have been taken with an LO level of  $-10\text{dBm}$  and with the modulator driven from the input amplifiers. The filter amplifiers have been used to buffer BVREF to drive the other modulator inputs (see the CMX993 datasheet for a typical application circuit).

The plot in Figure 7 shows the typical image rejection and carrier leakage seen at  $450\text{MHz}$  at an output power level of  $0\text{dBm}$ . The plot in Figure 8 shows the intermodulation (IMD) performance at  $450\text{MHz}$  with a two-tone input. The output level is  $+3\text{dBm}$  PEP and the plot in Figure 9 shows the IMD performance at  $1\text{GHz}$  with the same output power. The plot in Figure 60 shows the ACP performance at  $450\text{MHz}$  with APCO C4FM modulation: the measurement is 'non-spurious adjacent channel power ratio' (TIA/EIA 102.CAAB-B), requirement =  $67\text{dBc}$ , typical performance =  $70\text{dBc}$ . Typical input drive level at the modulator, for  $0\text{dBm}$  output level at  $450\text{MHz}$ , is  $\sim 350\text{mVp-p}$  (note the input amplifiers on the EV9930 have a gain of  $\sim 0.5$ ).

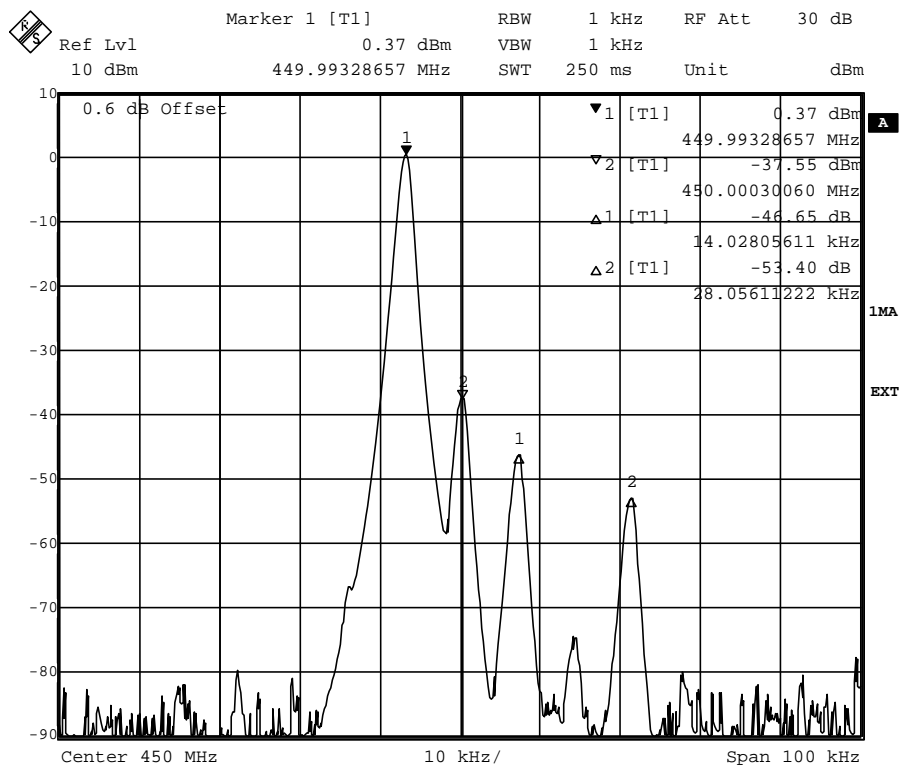


Figure 7 0dBm Output Level at 450MHz with a single tone input

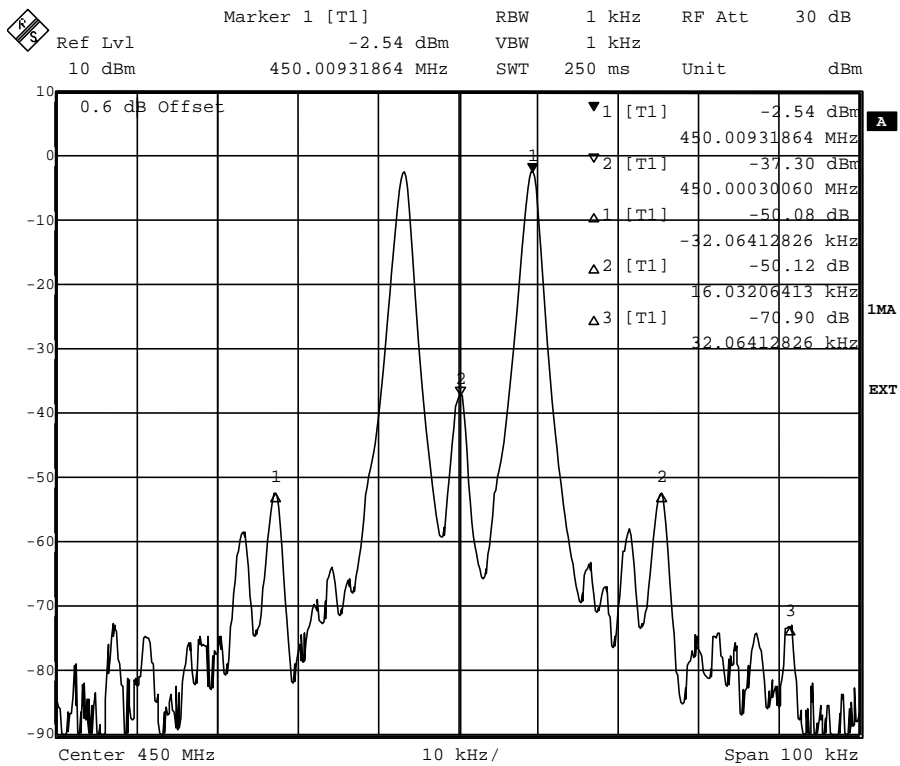


Figure 8 +3dBm PEP Output, two-tone input at 450MHz

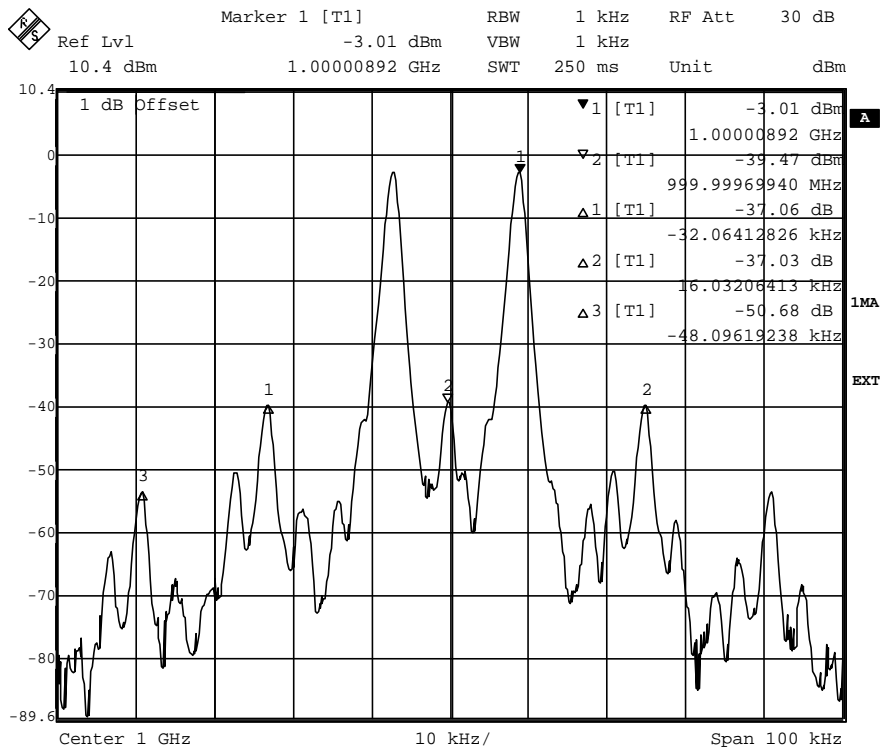


Figure 9 +3dBm PEP Output, two-tone input at 1GHz

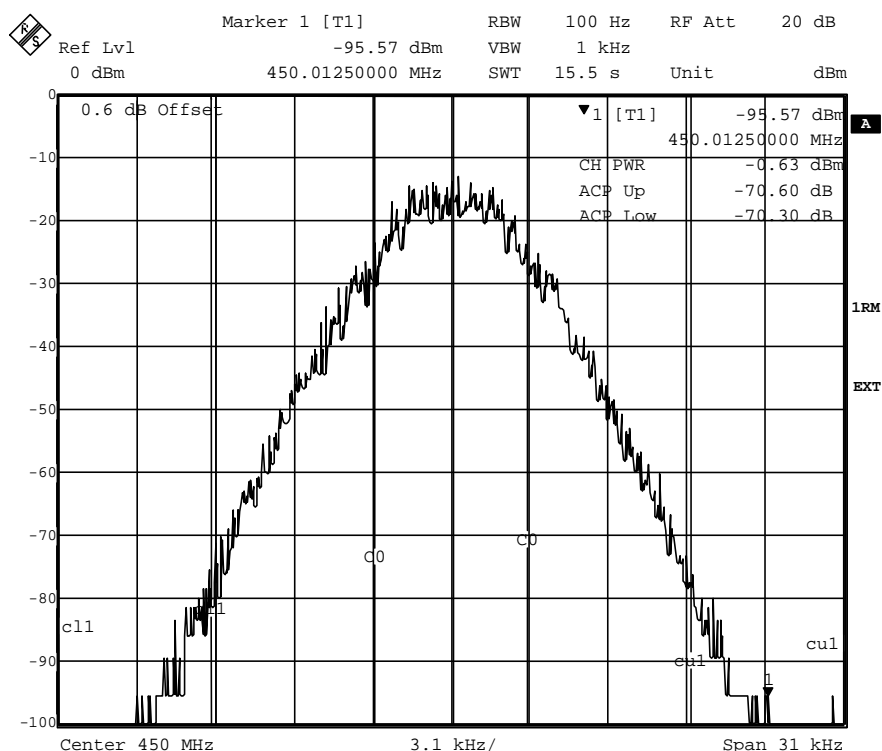


Figure 6 0dBm Output Power at 450MHz with APCO C4FM Modulation

### 6.4 Troubleshooting

The CMX993/EV9930 is a complex RF system. If incorrectly programmed or modified, results will be at variance from datasheet performance. Please study the IC datasheet, this manual and the associated schematics and layout drawings carefully when troubleshooting.

This section provides some suggestions to help users resolve application issues they might encounter.

Error Observed	Possible Cause	Remedy
No RF Output	I/Q signals not applied LO signal not applied	Try re-enabling circuitry in CMX993 or measure I/Q signal at TL5/6 Check LO signal is present at sufficient drive level
Discrete spurious present on transmission at 18kHz offset from carrier causing degrading adjacent channel power when using EV9810 to generate modulation	Insufficient isolation between EV9930 and EV9810 power supplies	Ensure sufficient de-coupling between power supplies or use separate bench power supplies for EV9810 and EV9930

Table 7 Possible Errors

## 7 Performance Specification

### 7.1 Electrical Performance

#### 7.1.1 Absolute Maximum Ratings

Exceeding these maximum ratings can result in damage to the Evaluation Kit.

	<b>Min.</b>	<b>Max.</b>	<b>Units</b>
Supply Voltage ( $V_{IN} - V_{SS}$ )	0	8.0	V
Current into or out of $V_{IN}$ , and $V_{SS}$ pins	0	+2.0	A
Current into or out of any other connector pin	-20	+20	mA

#### 7.1.2 Operating Limits

Correct operation of the Evaluation Kit outside these limits is not implied.

	<b>Notes</b>	<b>Min.</b>	<b>Max.</b>	<b>Units</b>
Supply Voltage ( $V_{IN} - V_{SS}$ )		4.0	8.0	V

**Notes:**

### 7.1.3 Operating Characteristics

For the following conditions unless otherwise specified:




$$V_{IN} - V_{SS} = 7.2V, T_{amb} = +25^{\circ}C.$$

	Notes	Min.	Typ.	Max.	Units
<b>DC Parameters</b> (Excluding PA Supply)					
I <sub>DD</sub> (CMX993 powersaved)	2	–	–	1	mA
I <sub>DD</sub> (All CMX993/EV9930 Circuitry Enabled)	2	–	103	–	mA
<b>AC Parameters</b>					
<b>Up-Converter Output</b>					
Output power		–	0	–	dBm
Attenuator Range		0	–	30	dB
Attenuator Step Size		–	2.5	–	dB
<b>I/Q Input Signal</b>					
Amplitude	3	–	0.7	1.0	Vp-p
DC Level	3	–	0	–	VDC
<b>LO Input</b>					
Input impedance		–	50	–	Ω
Frequency range		200	–	2000	MHz
Input Level		-15	–	-10	dBm
<b>C-BUS Interface</b>					
See CMX993 Datasheet					

**Notes:**

2. EV9930 PCB current consumption, not just the CMX993 current consumption.
3. At J5 / J6.

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