1.1 Overview

WARNING: This is product has exposed mains power inside the enclosure. Certain tracks on the PCB and the heatsinks contain live AC. Do not open the enclosure unless properly qualified to do so, and then only when mains is disconnected and the power supply has had time to dissipate the power stored in its capacitors. In the event that a foreign object gets into the power supply (by means of the air vents or any other opening in the enclosure), immediately disconnect the power supply and return to SSE for servicing.

The SSE Switched-mode Power Supply product was designed to replace the existing SSE power supply. It improves over the previous design in terms of improved operating efficiency, lower heat generation and intelligent monitoring. The new design has the ability to communicate its current state in real time to the attached RTU, giving a user the ability to monitor and log voltages and currents as well as battery and AC state. It also adds cost flexibility by allowing different internal OEM PSU units to be selected depending on application requirements. Finally, the new power supply was designed to be easier and quicker to manufacture and service.

Features:

- Supports various OEM PSU units
- Monitoring of main power output voltage and current
- Monitoring of battery charge / load voltage and current
- Monitoring of AC and battery status
- Low current 5V output
- · Individual output and status indicators
- · Separate switches and fuses for RTU and panel power
- Fused AC input
- Simplified wiring
- Free air convection cooling
- Battery backup (UPS) capability (using a seal lead-acid battery)
- DIN rail mounted
- · Supports old, current and next generation RTUs

Specifications:

- Base PCB designed to support 7A panel power output and 4A RTU power output
- Note: each individual connector pin can only support max 5A
- Max measurable current: 10A (panel and RTU combined), 3.5A (battery charge)
- Max measurable voltage: 14V (panel / RTU and battery)
- 5V ouput: 1A max
- Single 13.8V (12V) SLA battery only

Power supply specifications are determined by the OEM PSU module used. Here are specifications for two recommended models:

Mean Well PSC-60

- Input: 90~264VAC, 47~63Hz
- Typical input current: 1.6A
- Output: 13.8V (tunable), max 4.3A (split between DC output and battery charge)
- Battery charge: max 1.5A
- Rated power: 60W
- Short circuit, overload and over-voltage protection
- Battery low, battery polarity protection

Mean Well PSC-100

• Input: 90~264VAC, 47~63Hz

- Typical input current: 2A
- Output: 13.8V (tunable), max 7A (split between DC output and battery charge)
- Battery charge: max 2.5A
- Rated power: 100W
- Short circuit, overload and over-voltage protection
- Battery low, battery polarity protection

1.2 Installation Procedure

- Ensure that you have all the required cabling and connectors before installing the SSE Switched-mode Power Supply. As a minimum you will need: an AC power cable, RTU12 or RTUxxx (new) power cable, 4x2 Molex MicroFit connector kit, the power supply itself, and a DIN rail mount.
- 2. Mount the power supply and connect the AC power cable. Ensure that the power supply is working properly by toggling the rocker switches and observing LEDs. LEDs should switch on or off when the corresponding rocker switch is toggled on or off. The 5V and AC OK indicators should be lit, and the Status indicator should flash slowly. If this is not the case, check fuses or return the unit to SSE for servicing.
- 3. Switch both rocker switches to their 'off' position, and disconnect the AC input. Wire the panel devices and backup battery using the supplied 4x2 Molex MicroFit connector kit.
- 4. Connect RTU and panel power connectors. Attach the AC input and switch the outputs on. If a battery is connected, the Battery Status indicator should be lit. If not, the battery might be very low (and charging wait a few hours to confirm), or there might be cable fault. To fine-tune the output voltage level, open the power supply enclosure and adjust pot SVR1 on the DC side of the PSU daughter-board.

1.3 External Connectors

The diagram and tables below describe all the connectors accessible outside the enclosure.



J1: Firmware Upgrade Port: used to upgrade CPU board firmware. Connector compatible with SAM and LPC programming headers. Pins 3-8 are compatible with the Atmel AVR ICSP standard.

Pin	Name	Notes
1-2	NC	Not used
3	MISO	Data out

4	Vcc	Voltage detect
5	SCK	Clock
6	MOSI	Data in
7	Reset	Programmer reset circuit
8	GND	Ground
9-10	NC	Not used

J2: RTU Bus Power and Comms: supplies power to RTU Bus devices, used to communicate with existing RTU models (RTU12 and earlier).

Pin	Name	Notes
1	SDA	I2C data
2	SCL	I2C clock
3-15	NC	Not used (on power supply side)
16- 21	GND	Ground
22- 26	12V-14V	Power out to RTU devices (12V to 14V, 5A max)

J4: RTUxxx Power and Comms: supplies power and communicated with new RTU models (currently unnamed).

Pin	Name	Notes
1-2	12V-14V	Power out to RTUxxx devices (12V to 14V, 5A max)
3	GND	Ground
4	SDA	I2C data
5	SCL	I2C clock
6	GND	Ground

J5: Panel Power Output: supplies power to external devices, also connects to the backup battery.

Pin	Name	Notes
1	Batt Negative	Connect to the negative terminal of one 13.8V SLA battery only
2-4	GND	Ground
5	Batt Positive	Connect to the positive terminal of one 13.8V SLA battery only
6	12V-14V	Power out to external devices (12V to 14V, 5A max)
7	12V-14V	Power out to external devices (12V to 14V, 5A max)
8	5V	Power out to external devices (5V, 1A max)

J8: Mains In: connects to main AC power source.

Pin	Name	Notes
1	AC Live	Universal input support (90-264 VAC, 47-63 Hz)

737_132: SSE Switched-Mode Power Supply

2	AC Earth	Earth to power supply
3	AC	Universal input support (90-264
	Neutral	VAC, 47-63 Hz)

 NB: connect to a power source that can produce more than 2A constant. See OEM power supply data sheet for more information.

1.4 Hardware User Interface

The front panel of the power supply contains 6 LEDs and two switches. RTU power and Panel power outputs are individually switched by the rocker switches. LEDs indicate the state of the 3 power output lines (RTU power, Panel power and 5V output), battery or AC fault conditions, and basic power supply condition.

ltem	Name	Description
LED 1 - Green	AC OK	Lit when AC is connected, off when battery backup is used
LED 2 - Green	Battery Low	Lit when battery is connected, off when level battery is very low or disconnected
LED 3 - Red	Status	Slowly flashes during normal operation
LED 4 - Green	5V Output	Lit when 5V output is available
LED 5 - Green	RTU Power	Lit when RTU power output is available and switched on
LED 6 - Green	Panel Power	Lit when Panel power output is available and switched on
Switch 1	RTU Power Switch	Switches RTU power
Switch 2	Panel Power Switch	Switches Panel power

On the downward facing edge of the power supply enclosure are three fuse holders. These contain fuses for the RTU and Panel power outputs, and for the AC input.

Fuse	Name	Rating
Front	Panel fuse	5A
Middle	RTU fuse	5A
Back	AC fuse	2A or 3A depending on PSU model

See 'Enclosure' section for images.

1.5 Enclosure

Below are rendered images of the top, front and bottom of the enclosure designed for the SSE Power Supply.



Front:



Bottom:



1.6 OPC Interface

Power supply analogue and digital values are available in OPC via the IO Maps page. A user should map the following source data types into predefined AINs and DINs, after which they can be treated as any other AINs or DINs.

- AC OK -> PSU MAINS OK (DIN)
- Battery OK -> PSU BATTERY LOW (DIN)
- Load current -> PSU LOAD CURRENT (AIN)
- Load voltage -> PSU LOAD VOLTAGE (AIN)
- Battery current -> PSU BATTERY CURRENT (AIN)
- Battery voltage -> PSU BATTERY VOLTAGE (AIN)

Below is an example of such a setup. Remember to download AIN, DIN and IO Map descriptors to the RTU after OPC configuration.

8

				.g		or an interesting in					
L	ong Counti	er (LCNT)	Analog Register ((AREG)	Digital Reg	ister (DREG)	ModBus K	ENT Netwo	ork Map IO Map		
								1/0 Map D)atabase		
	Enabled	Description	RX/TX/LOCAL	System Address	Station Address	Destination Data Type	Destination Start	Destination Stop	Source Data Type	Source Start	Source Stop
		IO MAP	LOCAL	0	1	DIN	9	9	PSU MAINS OK	1	1
		IO MAP	LOCAL	0	1	DIN	10	10	PSU BATTERY LOW	1	1
	N I	IO MAP	LOCAL	0	1	AIN	9	9	PSU LOAD CURRENT	1	1
		IO MAP	LOCAL	0	1	AIN	10	10	PSU LOAD VOLTAGE	1	1
	N N	IO MAP	LOCAL	0	1	AIN	11	11	PSU BATTERY CURRENT	1	1
		IO MAP	LOCAL	0	1	AIN	12	12	PSU BATTERY VOLTAGE	1	1

1.7 Firmware Upgrade

Firmware upgrades are performed using J1. Connect a standard AVR ISP-II programmer cable to the **CENTRE** 6 pins of J1 (ie: pin 1 of the programmer cable to pin 3 of J1).

It is advisable to create a converter from 6 pin to 10 pin ribbon with pins 1 and 2 of the 10-pin connector shorted (pins 9 and 10 left open). This can be done by just crimping a new (10 pin) connector onto the existing AVR ISP-II cable, with a loop-back wire going from pin 1 to pin 2. Note: this is the same arrangement as the programming connector for SSE's Look@ display and Low Power Controller.



The new firmware can now be sent to the device using AVR Studio or the stand-alone program packaged with new firmware. Before attempting to connect to the power supply, make sure it is powered.

If the stand-alone program is used, make sure that the AVR ISP-II is connected to the computer (and its driver is installed) and then run "Update_PSU.bat". A file called "output.txt" will be created showing the output of the programmer. The following is an example of a successful firmware update: STK500 command line programmer, v 2.3 Atmel Corp (C) 2004-2009. Connected to AVRISP mkII on port USB:0000B0012964 Device parameters loaded Programming mode entered Device erased FLASH input file SSE-SMPS-737_132_V1.hex read Programming FLASH... FLASH programmed Reading FLASH... FLASH read FLASH verified successfully Programming fuse byte 0 (0xE2)... Programming fuse byte 1 (0xDF)... Programming fuse byte 2 (0xF9)... Fuse bits programmed Programming mode left Connection to AVRISP mkII closed If AVR Studio is used, connect the AVR ISP-II and run AVR Studio. Click the "Con" button, or select "Tools" -> "Program AVR" -> "Connect" from the menu. In the window that pops up, select "AVRISP

mkll" platform and "USB" port.

Platform: AVR DNE! STK500 AVRISE mkli STK500 JTAGICE mkli AVR Dragon AVRISP Tip: To auto-connect to the programmer used last time, press the "Programmer" button on the toolbar. Note that a tool cannot be used for programming as long as it is connected in a debugging session. In that case, select "Stop Debugging' first. Disconnected Mode Disconnect" button and a window entitled "AVRISP mkli" will be displayed. Make sure the s are entered as in the following screens:	Patform: AVR ONE! STK500 JTAGICE mkll AVR Dragon AVRISP Tip: To auto-connect to the programmer used last time, press the 'Programmer' button on the toolbar. Note that a tool cannot be used for programming as long as it is connected in a debugging session. In that case, select 'Stop Debugging' first. Disconnected Mode We "Connect" button and a window entitled "AVRISP mkII" will be displayed. Make sure the s are entered as in the following screens:	Platform: AVR ONE! STK500 AVRISP mkli STK500 JTAGICE mkli AVR Dragon AVRISP Tip: To auto-connect to the programmer used last time, press the 'Programmer' button on the toolbar. Note that a tool cannot be used for programming as long as it is connected in a debugging session. In that case, select 'Stop Debugging' first. Disconnected Mode e "Connect" button and a window entitled "AVRISP mkll" will be displayed. Make sure the s are entered as in the following screens:		_	
The "Connect" button and a window entitled "AVRISP mkII" will be displayed. Make sure the s are entered as in the following screens:	e "Connect" button and a window entitled "AVRISP mkll" will be displayed. Make sure the s are entered as in the following screens:	e "Connect" button and a window entitled "AVRISP mkll" will be displayed. Make sure the s are entered as in the following screens:	Platform: AVR ONE! STK600 QT600 AVRISP mkll STK500 JTAGICE mkll AVR Dragon AVRISP Tip: To auto-connect to the prog button on the toolbar. Note that a tool cannot be used 1 a debugging section. In that area	Port:	Connect Cancel Baud rate: 115200 Baud rate changes are active immediately.
			Disconnected Mode ne "Connect" button and a wir s are entered as in the followi	ndow entitled "AVRISP mkll" will be d	lisplayed. Make sure th

Programming Mode and Target Settings ISP mode Settings ISP Frequency: 250.0 kHz Getting revisions HW: 0x01, FW Major: 0x01, FW Minor: 0x0c OK ct "ATmega168" as the device to program and "ISP mode".	Device and Signatur ATmega 168 Signature not read	re Bytes	Erase Device Read Signature
Getting revisions HW: 0x01, FW Major: 0x01, FW Minor: 0x0c OK	Programming Mode : ISP mode	and Target Settings	Settings ISP Frequency: 250.0 kHz
ect "ATmega168" as the device to program and "ISP mode".	Getting revisions HW: 0	λx01, FW Major: 0x01, FW Mino	r: 0x0c OK
	Getting revisions HW: C	0x01, FW Major: 0x01, FW Minor device to program and "IS	r: 0x0c OK BP mode".

Main Program	Fuses LockBits Advanced HW Settings HW Info Auto
Fuse	Value
BOOTSZ	Boot Flash size=1024 words start address=\$1C00
BOOTRST	
RSTDISBL	
DWEN	
SPIEN	- <u>Ma</u>
WDION	
EESAVE	
BODLEVEL	
CKDIV8	
CKUUT CKCEL	
SUT CKSEL	III III III III III III III III III II
EXTENDED	0xE9
HIGH	0xDF
LOW	0xE2
etting mode and de ntering programmin leading fuses addre	vice parameters OK! g mode OK! ss 0 to 2 0xE2, 0xDF, 0xF9 OK!
e that: SZ = " Boot Flas is ticked SVEL = " Brown is NOT ticked	h size=1024 words start address=\$1C00" ∙out detection disabled" : Osc. 8MHz: Start-up time PWRDWN/RESET: 6 CK/14 CK + 65ms" T ticked

AVRISP	mkII in ISP mode with ATmega168
Main	Program Fuses LockBits Advanced HW Settings HW Info Auto
	Erase device before flash programming Verify device after programming
Fla	ush
	Ose Current Simulator/Emulator FLASH Memory
	Program Verify Read
EE	PROM
	Use Current Simulator/Emulator EEPROM Memory
	Program Verify Read
EL	F Production File Format
Ing	put ELF File:
Sa	Ive From: V FLASH V EEPROM FUSES LOCKBITS Fuses and lockbits settings
	Program Save saving to ELF