

User Manual

IDK-2108 Series

8.4" SVGA Ultra High Brightness Display Kit with LED Backlight



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- 5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

Contents

Chapter	1	Overview	1
	1.1	General Description	2
	1.2	Specifications	2
		1.2.1 LCD Panel	2
		1.2.2 LED Driver Board	2
		1.2.3 Touchscreen (R series)	
		1.2.4 Environment	
	1.3	Mechanical Characteristics	
	1.4	Functional Block Diagram	
		Figure 1.1 Function Block Diagram	
	1.5	Touchscreen Driver	
	1.6	Absolute Maximum Ratings	
		1.6.1 Absolute Ratings of TFT LCD Module	
		1.6.2 Absolute Ratings of Backlight Unit	
		1.6.3 Absolute Ratings for Environment	4
Chapter	2	Electrical Characteristics	5
	2.1	Power Specifications	6
		Table 2.1: Power Specifications	
		2.1.1 Signal Electrical Characteristics	
		Table 2.2: Signal Electrical Characteristics	
	2.2	Backlight Driving Conditions	
		Table 2.3: Backlight Driving Conditions	
Chanter	2	Signal Characteristics	0
Chapter	3	Signal Characteristics	9
	3.1	Pixel Format Image	10
	3.2	Signal Description	10
		Table 3.1: Symbol Description	10
	3.3	The Input Data Format	
		3.3.1 SEL68	
	3.4	Interface Timing	
		3.4.1 Timing Characteristics	
		Table 3.2: Timing Characteristics	
		3.4.2 Input Timing Diagram	
	3.5	Power ON/OFF Sequence	13
Chapter	4	Connector & Pin Assignment	15
	4.1	TFT LCD Module	16
	7.1	4.1.1 Connector	
		Table 4.1: Connector	
		4.1.2 Pin Assignment	
		Table 4.2: Pin Assignment	
	4.2	Backlight Unit	
	·	4.2.1 Signal for LED light bar connector	
		4.2.2 LED Driver Board	
		Table 4.3: Specifications	
		Table 4.4: Input Connector Pin Definition	
		Table 4.5: Output Connector Pin Definition	
		Figure 4.1 Dimensions	18

Chapter 5	Touchscreen & Touch Controlle	er 19
5.1	Touchscreen (Optional: for IDK-2108R Only)	
5.2	Touch Controller (Optional: for IDK-2108R Only) 5.2.1 Touch Controller Characteristics 5.2.2 Dimensions 5.2.3 Specifications 5.2.4 Product Package	21 21 21
Appendix A	Optical Characteristics	25
A.1	Optical Characteristics	26
Appendix B	Handling Precautions	29
B.1	Optical Characteristics	30

Chapter

Overview

General Description 1.1

IDK-2108 series is a Color Active Matrix Liquid Crystal Display composed of a TFT-LCD panel, a driver circuit, and backlight system. The screen format supports an SVGA screen of 800 x 600 pixels (H x V) at 16.2M colors (RGB 8-bit) or 262k colors (RGB 6-bit). All input signals are LVDS interface compatible. Driver board backlight is included.

1.2 Specifications

1.2.1 LCD Panel

Display Size: 8.4" LED backlight panel

Resolution: 800 x 600

Viewing Angle (U/D/L/R): 80°/60°/80°/80°

Brightness: 1200 cd/m2 Contrast Ratio: 600:1

Response Time (ms): 30 ms **Colors:** 6-bit (262K)/8-bit (16.2M)

Voltage: 3.3V

Power Consumption: 5.35W Signal Interface: 1 channel LVDS

Weight: R series: 338.5g N series: 260g

Dimensions (W x H x D): R series: 203 x 142.6 x 10.3 mm N series: 203 x 142.5 x 8.0 mm

1.2.2 LED Driver Board

Efficiency: 85%

Output Current & Voltage: 500 mA/8.9 V Dimensions (W x H x D): 60x16x5 mm

1.2.3 Touchscreen (R series)

Touchscreen: 4-Wire Resistive Transparency: 82.5% (Typ.) **Durability:** 1 million times

1.2.4 Environment

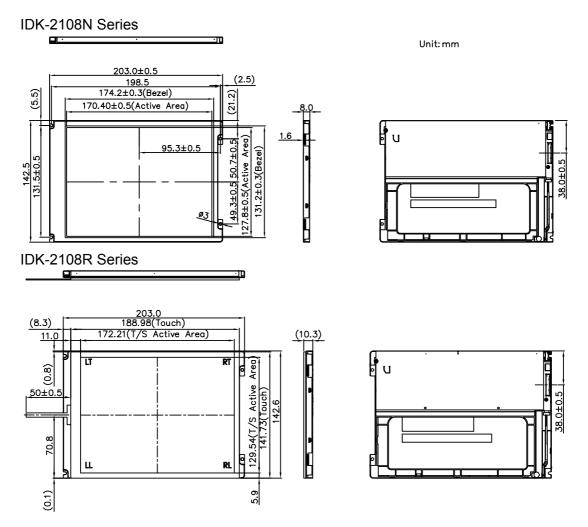
Operating Temperature: -5 ~ + 60 °C (R- series)

-20 ~ 70 °C (N- series)

Storage Temperature: -30 ~ +70 °C (R- series) $-30 \sim +85$ °C (N- series)

Humidity: 10 ~ 90% @ 39°C, non-condensing

1.3 **Mechanical Characteristics**



1.4 Functional Block Diagram

The following diagram shows the functional block of the 8.4 inch color TFT-LCD module:

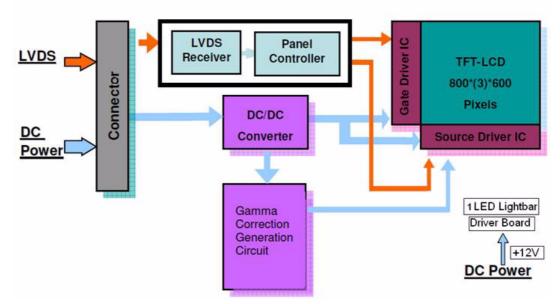


Figure 1.1 Function Block Diagram

1.5 Touchscreen Driver

The touchscreen driver CD-ROM is in the accessories box and comes with the product.

1.6 Absolute Maximum Ratings

Absolute maximum ratings of the module is as follows:

1.6.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Conditions
Logic/LCD Drive Voltage	Vin	0.3	+3.6	[Volt]	Note 1, 2

1.6.2 Absolute Ratings of Backlight Unit

Item	Symbol	Min.	Max.	Unit	Conditions
LED Light Bar Current	Led	490	500	[mA]	Note 1, 2

1.6.3 Absolute Ratings for Environment

Item	Symbol	Min.	Max.	Unit	Conditions
Operating Temperature	TOP	-5	+60	[oC]	For IDK-
Operating Humidity	HOP	10	90	[%RH]	2108R-
Storage Temperature	TST	-30	+70	[oC]	─K2SVA2E —only
Storage Humidity	HST	5	95	[%RH]	Omy

Note 1: Maximum Wet-Bulb should be 39°C and no condensation.

Note 2: Permanent damage to the device may occur if maximum values are exceeded.

Note 3: For quality performance, please refer to AUO IIS (Incoming Inspection Standard).

Chapter

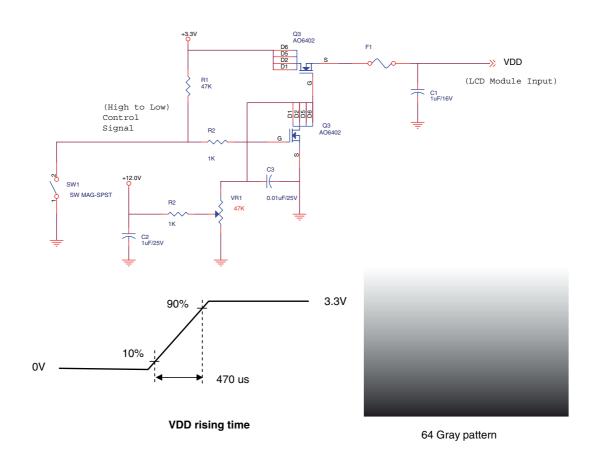
Electrical Characteristics

2.1 Power Specifications

Input power specifications are as follows:

Table 2.1: Power Specifications								
Symbol	Parameter	Min.	Тур.	Max.	Unit	Condition		
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	10%		
IDD	Input Current	-	270	330	[mA]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)		
PDD	VDD Power	-	0.9	1.2	[Watt]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)		
Rush	Inrush Current	-	-	3	[A]	Note 1		

Note 1: Measurement condition:



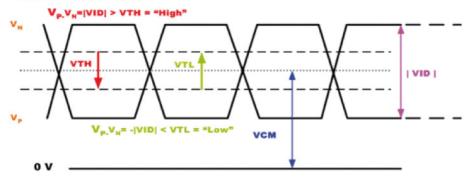
2.1.1 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off.

Table 2.2: Signal Electrical Characteristics								
Symbol	Parameter	Min.	Тур.	Max.	Unit	Condition		
VTH	Differential Input High Threshold	-	-	100	[mV]	VCM=1.2V		
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VCM=1.2V		
VID	Input Differential Voltage	100	400	600	[mV]			
VICM	Differential Input Common Mode Voltage	1.1	-	1.6	[V]	VTH / VTL = ±100mV		

Note: LVDS Signal Waveform.

Differential Signal



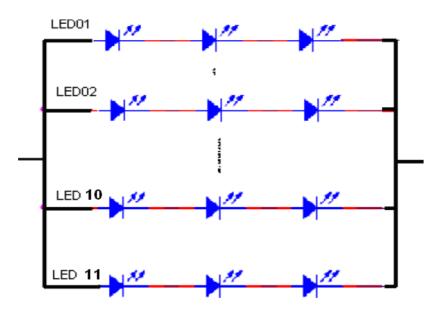
2.2 Backlight Driving Conditions

Parameter guidelines for LED light bar driver are under stable conditions at 25°C (Room Temperature):

Table 2.3: Backlight Driving Conditions								
Item	Symbol	Values Unit Condition						
		Min.	Тур.	Max.				
LED Voltage	VL	8.9		8.9	V	Note 2		
LED Current	IL	490		500	mA	Note 2		
LED Life Time	-	50,000	-	-	Hr	Note 1		

Note 1: "LED life Time" is defined as a module brightness decrease of 50% original brightness, and an ambient temperature of 25°C with typical LED current at 500mA.

Note 2: "LED Driving Condition" is defined for each LED module. (3 Serial LEDs, an LED includes 1 chip).



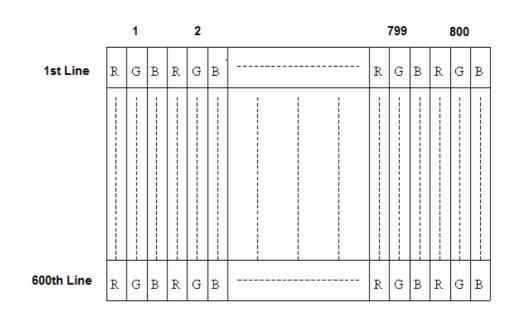
Note 3: The LED Light Bar power consumption variance is 10%. Calculator value for reference ($IL \times VL = PLED$)

Chapter

Signal Characteristics

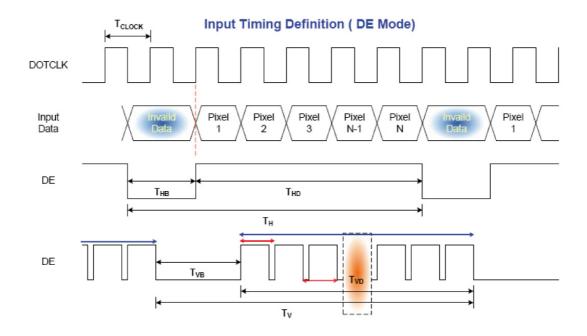
3.1 Pixel Format Image

The following figure shows the relationship between the input signal and LCD pixel format.



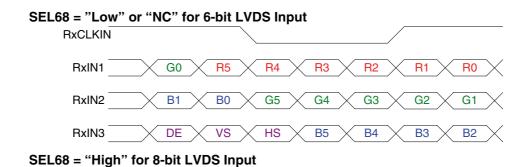
3.2 Signal Description

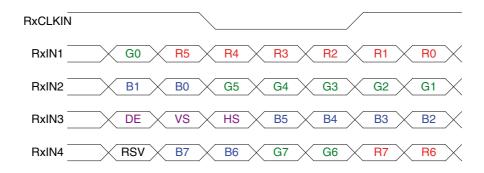
Table 3	Table 3.1: Symbol Description						
Pin No.	Symbol	Description					
1	VDD	Power Supply, 3.3V (typical)					
2	VDD	Power Supply, 3.3V (typical)					
3	UD	Vertical Reverse Scan Control. Low or NC -> Normal mode, Height -> Vertical Reverse Scan (Note)					
4	LR	Horizontal Reverse Scan Control. Low or NC -> Normal mode, Height -> Vertical Reverse Scan (Note)					
5	RxIN1-	— LVDS differential data input Pair 0					
6	RxIN1+	— LVD3 dilierential data iriput Fall 0					
7	GND	Ground					
8	RxIN2-	LVDS differential data input Dair 1					
9	RxIN2+	— LVDS differential data input Pair 1					
10	GND	Ground					
11	RxIN3-	— LVDS differential data input Pair 2					
12	RxIN3+	— LVD3 dillerential data input Fall 2					
13	GND	Ground					
14	RxCLKIN-	LVDS differential Colock input Pair					
15	RxCLKIN+	LVDS differential Colock input Pair					
16	GND	Ground					
17	SEL68	LVDS 6/8 bit select function control, Low or NC 6 Bit Input Mode. High 8-bit Input Mode (Node)					
18	NC	NC					
19	RxIN4-	LVDS differential data input Pair 3. Must be connected to Ground					
20	RxIN4+	in 6-bit input mode.					



3.3 The Input Data Format

3.3.1 **SEL68**



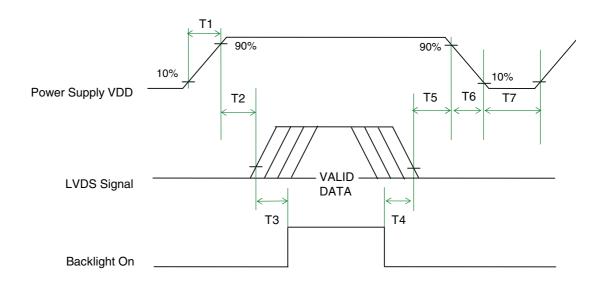


Note 1: Please follow PSWG.

Note 2: R/G/B data 7:MSB, R/G/B data 0:LSB

Signal Name	Description	Remarks
R7	Red Data 7	
R6	Red Data 6	_
R5	Red Data 5	_
R4	Red Data 4	Red-pixel Data, For 8-bit LVDS input, MSB: R5;
R3	Red Data 3	LSB:R0
R2	Red Data 2	-
R1	Red Data 1	-
R0	Red Data 0	-
G7	Green Data 7	
G6	Green Data 6	-
G5	Green Data 5	-
G4	Green Data 4	Green-pixel Data, For 8-bit LVDS input, MSB:
G3	Green Data 3	G7; LSB:G0
G2	Green Data 2	-
G1	Green Data 1	-
G0	Green Data 0	-
B7	Blue Data 7	
B6	Blue Data 6	_
B5	Blue Data 5	_
B4	Blue Data 4	Blue-pixel Data, For 8-bit LVDS input, MSB: B7;
B3	Blue Data 3	LSB:B0
B2	Blue Data 2	_
B1	Blue Data 1	_
B0	Blue Data 0	_
RxCLKIN	LVDS Data Clock	
DE	Data Enable Signal	When the signal is high, the pixel data is valid to be displayed.
VS	Vertical Synchronous Signal	
HS	Horizontal Synchro- nous Signal	

Note: Output signals from any system shall be Low or Hi-Z state when VDD is off.



3.4 Interface Timing

3.4.1 Timing Characteristics

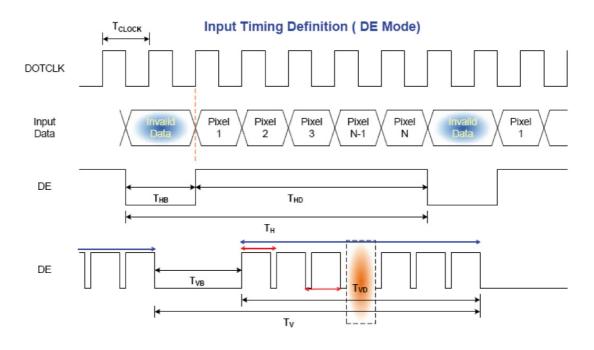
Table 3.2: Timing Characteristics								
Signal	Parameter		Symbol	Min.	Тур.	Max.		
Clock Timing	Clock frequency		1/ T _{Clock}	33.6	39.8	48.3		
Vsync Timing	Vertical Section	Period	T _V	608	628	650		
		Active	T _{VD}	600	600	600		
		Blanking	T _{VB}	8	28	50		
Hsync Timing	Horizontal Section	Period	T _H	920	1056	1024		
		Active	T _{HD}	800	800	800		
		Blanking	T _{HB}	120	256	440		

Note: Frame rate is 60 Hz.

Note: DE mode.

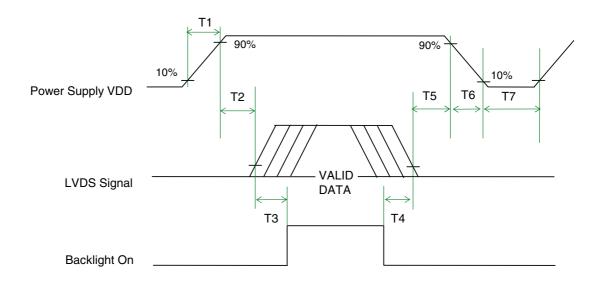
Note: Typical value refer to VESA standard

3.4.2 Input Timing Diagram



3.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power Sequence Timing

Parameter		Value			
	Min.	Тур.	Max.		
T1	0.5	-	10	[ms]	
T2	30	40	50	[ms]	
T3	200	-	-	[ms]	
T4	0.5	-	10	[ms]	
T5	10	-	-	[ms]	
T6	10	-	-	[ms]	
T7	0	-	-	[ms]	
T8	10	-	-	[ms]	
T9	-	-	10	[ms]	
T10	110	-	-	[ms]	
T11	0	16	50	[ms]	
T12	-	-	10	[ms]	
T13	1000		-	[ms]	

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

Chapter

4

Connector & Pin Assignment

4.1 TFT LCD Module

The physical interface described is for the connector on module. These connectors are capable of accommodating the following signals and consist of the following components listed.

4.1.1 Connector

Table 4.1: Connector	
Connector Name / Description	Signal Connector
Manufacturer	STM
Connector Model Number	STM-MSB24013P20HA or Compatible
Adaptable Plug	STM-P24013P20

4.1.2 Pin Assignment

Table 4.2: Pin Assignment				
Pin No.	Signal Name	Pin No.	Signal Name	
1	VDD	2	VDD	
3	UD	4	LR	
5	RxIN1-	6	RxIN1+	
7	GND	8	RxIN2-	
9	RxIN2+	10	GND	
11	RxIN3-	12	RxIN3+	
13	GND	14	RxCLKIN-	
15	RxCLKIN+	16	GND	
17	SEL68	18	NC	
19	RxIN4-	20	RxIN4+	

4.2 Backlight Unit

The physical interface described is for the connector on module. These connectors are capable of accommodating the following signals and consist of the following components listed.

Connector Name / Designation	LED Light Bar Connector / Backlight Lamp
Manufacturer	SPEEDCON
Type Part Number	WRC-1XX-DRXX00D
Mating Type Part Number	NA

4.2.1 Signal for LED light bar connector

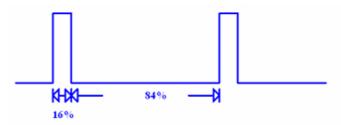
	Connector No.	Pin No.	Input	Color	Function
Lower	-CN2	1	HI 2	Red	Power supply for backlight unit
	- CINZ	2	GND 2	Black	Ground for backlight unit

Cable Length: 250mm+/-10mm

4.2.2 LED Driver Board

4.2.2.1 **Specifications:**

Table 4.3: Specifications						
Symbol	Characteristics	Condition	Min.	Тур.	Max.	Unit
	Voltage		10	12	15	V
Input	Efficiency	Vin=12V lout=500mA Vout=8.9V		85		%
	Power		0.3		20	W
	Voltage		8.9	9	10.5	V
Output	Current		100		500	mA
Output	Current Accuracy	100mA≤lout≤500m A		±5	±10	%
Protection OVP						
	Thermal Shutdown			165		°C
Environment	Operating Junction Temperature				125	°C
Environment	Operating Temperature		-20		+70	°C
	Storage Temperature		-40		+ 85	°C
	Dimmer range (Note. 1)		16		100	V
PWM Dim- mer	Dimmer VH		3.5		5.5	V
	Dimmer VL		0		2.5	V
	Dimmer Frequency		0.25	0.5	1	KHz
ON/OFF	Von		3.5		5.5	V
ON/OFF	Voff		0		2.5	



Note 1: When the input PWM signals, the high-level digital output must be greater than the total output level at 16% output.

4.2.2.2 Input Connector Pin Definitions

Table 4.4: Input Connector Pin Definition		
Pin No.	Pin Definition	
1	Vin (+12V)	
2	Vin (+12V)	
3	GND	
4	GND	
5	ON/OFF (0V: Off; +5V: On)	

Table 4.4: Input Connector Pin Definition 6 Dimming (PWM)

4.2.2.3 Output Connector Pin Definition

Table 4.5: O	Table 4.5: Output Connector Pin Definition		
Pin No.	Pin Definition		
1	VLED-		
2	VLED+		

4.2.2.4 **Dimensions**

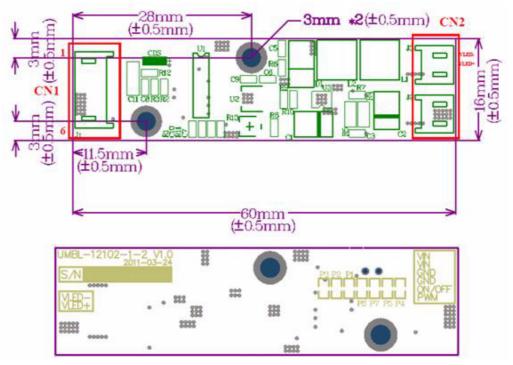


Figure 4.1 Dimensions

Chapter

Touchscreen & Touch Controller

5.1 Touchscreen (Optional: for IDK-2108R Only)

5.1.1 Touch Characteristics

The touch panel is a resistance type that the customer uses with flat display like an LCD. Once the operator touches it using a resin pen with a round end or a finger, the circuit for the touch panel sends its coordinate points to the PC reading voltages at the contact points.

5.1.2 Optical Characteristics

	Item	Specifications	Remarks
1	TRANSPARENCY	82.5% Typ. 80% Min. (Active area) (Inside of guaranteed active area)	JIS K-7105
2	HAZE	8.0% Typ. (Anti-glare)	JIS K-7105

5.1.3 Environmental Characteristics

	Item	Specifications	Remarks
1	Operational temperature	-5°C ~ 60°C	Max. wet temp is 38°C
2	Storage temperature	-30°C ~ 70°C	(No dew)
3	Operational Humidity	20% ~ 90%RH	
4	Storage temperature	10% ~ 90%RH	

5.1.4 Mechanical Characteristics

	Item	Specifications	Remarks
1	Hardness of surface	Pencil hardness 3H.	JIS K-5600-5-4 150gf, 45 degree
2	FPC peeling strength	1) 5N (5N Min.) 2) 19.6N (19.6N Min.)	1) Peeling upward by 90° 2) Peeling downward by 90°
3	Operational force	Pen 0.05N~1.96N Finger (5~200gf)	Dot-Spacer Within "guaranteed active area", but not on the age and Dot-Spacer.

5.1.5 Electronic Characteristics

	Item	Specification	Remarks
1	Rated Voltage	DC 7V max.	
2	Resistance	X axis: $200Ω \sim 1000Ω(Glass side)$	FPC connector
		Y axis: $100\Omega \sim 800\Omega$ (Film side)	
3	Linearity	±1.5%max initial value ±2.0%max (after environmental & life test)	Reference: 250gf
4	Chattering	20ms Max At connector pin	
5	Insulation Resistance	10MΩ @ (DC 25V) 10MΩ min (DC 25V)	

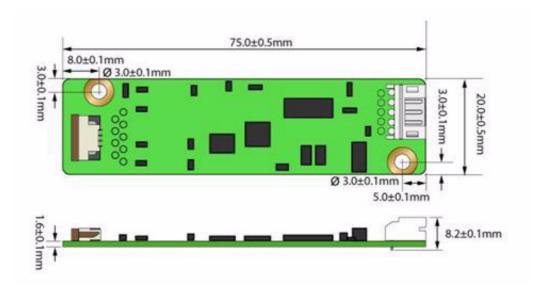
5.2 Touch Controller (Optional: for IDK-2108R Only)

5.2.1 Touch Controller Characteristics

The touch control board meets the latest Restriction of Hazardous Substances (RoHS) Directive. This touch panel controller provides optimal performance of your analog resistive touch panels for 4-wire models. It communicates with the PC system directly through a USB connector. The superior design is highly sensitive, accurate and supports a friendly operation. The touch panel driver emulates mouse left and right button functions and supports the operating systems listed.

os	Version	Interfaces
Windows Windows 7, Vista, XP/2000, ME/98 Windows XP Tablet PC edition Windows CE 2.12/3.0/4.0/5.0/6.0 Windows Embedded XP		RS-232/USB/ PS2
Windows	Windows NT4, Windows 95	RS-232/PS2
Linux	Supports kernel 2.4.x / 2.6.x with XFree 4.x / xorg 6.7 to 7.5 (Up to X server 1.6.x) The new Linux public driver supports most of the 32/64-bit Linux distributions, including: CentOS, Debian, Fedora, Gentoo, Mandrake (Mandriva), Red Hat, Ubuntu (Xubuntu), Slackware, SuSE (open SuSE) etc.	RS-232/USB/ PS2 (up to Kernel 2.6.x)
	Android 3.0 - Google Meego 1.x - Intel Nokia	RS-232/USB I2C
DOS	DOS 6.22	RS-232/PS2
Мас	Mac OS9, Mac OS X (PowerPC, Intel CPU)	USB
QNX	QNX RTOS v6.3	RS-232/USB

5.2.2 Dimensions



5.2.3 Specifications

USB Type Controller					
Circuit Board Dimensions	20mm x 75mm (0.79 inches x 2.95 inches) (4-Wire)				
Power Requirements	D.C.+5V (100mA typical, 50mV peak to peak maximum rippl and noise)				
Operating Temperature	-25 to 85 °C				
Storage Temperature	-55 to 150 °C				
Relative Humidity	95% at 60 °C				
Interface	Bi-directional RS-232 serial communications USB: 1.1 Full Speed (12Mbps)				
Protocol	No parity, 8 data bits,1 stop bit, 9600 baud (N,8,1,9600)				
Resolution	2048 x 2048 resolution				
Report Rate	·				
Response Time	Max. 20 ms				
Attached Cable	RS-232: 180cm shielded cable with 9-pin D-sub connector USB: 180cm shielded cable with USB-A connector				
Regulatory Approvals	FCC-B, CE, Unaffected by EMI from other nearly CRTs and other display devices CRTs				
EMI	Unaffected by environmental EMI				
Panel Resistance	4, 8 wire resistive model: 200 ~ 900 ohm 5 wire resistive model: 50 ~ 200 ohm				
MTBF	200,000 hrs				

5.2.4 Product Package

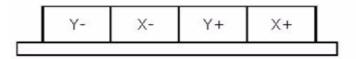
1. 96TS-CTR-4WRI02 (4 wire USB control board)



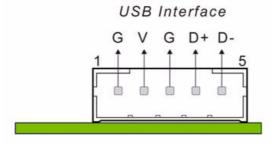
2. 1700019909 (USB cable)



3. Controller panel pin alignment



4. Controller wafer pin alignment





Appendix A

Optical Characteristics

A.1 Optical Characteristics

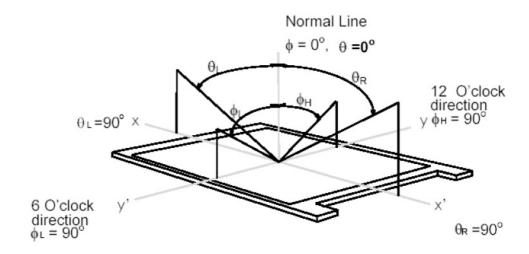
The optical characteristics are measured under stable conditions at 25°C (Room Temperature):

Table A.1: Optical Characteristics								
Item	Unit	Conditions	Min.	Тур.	Max.	Note		
Viewing Angle	[degree]	Horizontal (Right)		80		1		
		CR = 10 (Left)		80	_			
		Vertical (Upper)		80	- -	- "		
		CR = 10 (Lower)		60				
Luminance Uniformity	[%]	9 Points	80	85	-	2, 3		
Optical Response Time	[msec]	Rising	-	20	30			
		Falling	-	10	20	5		
		Rising + Falling	-	30	50	- "		
Color/Chromaticity Coordinates (CIE 1931)		White x	-	0.313	-	-4		
		White y	-	0.322	-	7		
Color Temp.	K		-	6500				
White Luminance	[cd/m ²]		1100	1200	-	4		
Contrast Ratio			-	600	-	4		

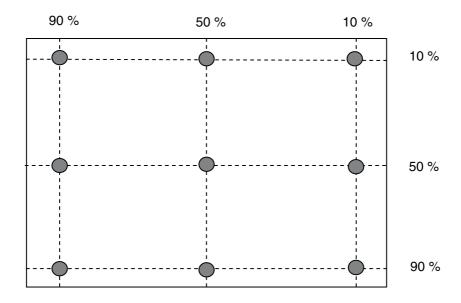
Note: Optical Equipment: BM-7, DT-101, or equivalent

Note 1: Definition of viewing angle

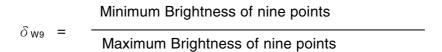
Viewing angle is the measurement of contrast ratio, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as below: 90° (θ) horizontal left and right, and 90° (Φ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.



Note 2: 9-point position

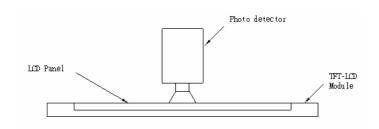


Note 3: The luminance uniformity of 9 points is defined by dividing the maximum luminance values by the minimum test point luminance



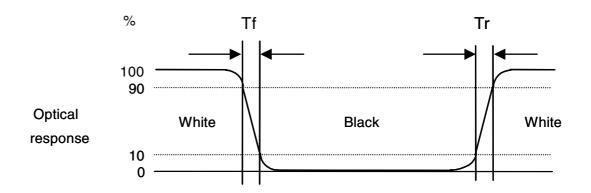
Note 4: Measurement method

The LCD module should be stabilized at a given temperature for 30 minutes to avoid abrupt temperature changes during measuring. In order to stabilize the luminance, the measurement should be executed after the backlight has been lit for 30 minutes in a stable, windless and dark room using optical equipment: DT-100 or equivalent.



Note 5: Definition of response time

The output signals of the photo detector are measured when the input signals are changed from "Full Black" to "Full White" (rising time), and from "Full White" to "Full Black" (falling time), respectively. The response time is the interval between 10% and 90% of the amplitudes. Please refer to the figure below.



Appendix B

Handling Precautions

B.1 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C (Room Temperature)

- 1. Since the front polarizer is easily damaged, pay attention not to scratch it.
- 2. Be sure to turn off the power supply when inserting or disconnecting from the input connector.
- 3. Wipe off water drops immediately. Long contact with water may cause discoloration or spots.
- 4. When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5. Since the panel is made of glass, it may break or crack if dropped or bumped on a hard surface.
- 6. Since CMOS LSI are used in this module, take care of static electricity and insure you are grounded to earth-ground when handling.
- 7. Do not open or modify the module assembly.
- 8. Do not press the reflector sheet at the back of the module in any direction.
- 9. In case a module has to be put back into the packing container after it has been removed, please press the ends of the LED light bar reflector edge softly. Otherwise the TFT module may be damaged.
- 10. At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11. After installation of the TFT Module into an enclosure, do not twist or bend the TFT Module even momentarily. When designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12. Small amounts of materials with no flammability grading are used in the LCD module. The LCD module should be supplied by power which complies with the requirements of Limited Power Source (IEC60950 or UL1950) or an exemption should be applied for.



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