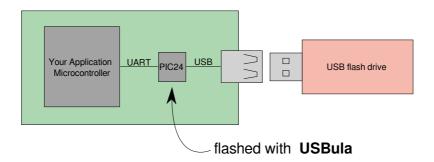
USBula – Serial to USB-Flash-Drive Bridge Firmware User Manual

USBula is a ready-to-use firmware for accessing USB flash drives from your application over a simple serial protocol. It runs on a Microchip PIC24FJ64GB002 microcontroller available in four different packages (SMD and THT). For commercial applications, there is a one-time license fee model, which limits the production costs to the actual part costs. The PIC24FJ64GB002 is available from many distributors, some with the option for pre-programming the controllers.

Features

- Connects USB memory stick to your application without USB programming
- Ready to flash binary files (HEX format, supported by most programmers)
- Runs on well available PIC24 microcontroller (on stock part from many distributors)
- Free version for evaluation and personal use
- Source code available (via license option) modify it for your needs
- +5V tolerant serial interface with simple protocol for file access
- FAT16/FAT32 is supported. 8+3 character filenames are used.

Typical Application



Examples: Data logging, load/store configurations, firmware updates

Code example

```
Serial.print("U\n");
                                 // sync (baudrate detection)
while (Serial.read() != '\n');
                                   consume response
Serial.print("01A:test.txt\n");
                                 // open file (id 1, append mode)
while (Serial.read() != '\n');
                                 // consume response
                                 // prepare write of 11 bytes
Serial.print("W10b\n");
while (Serial.read() != '\n');
                                    consume response
Serial.print("Hello World");
                                 // write data to file
while (Serial.read() != '\n');
                                   consume response
Serial.print("C1\n");
                                 // close file
while (Serial.read() != '\n');
                                 // consume response
```

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1 Integration Guide

This section describes how to integrate USBula into a customer application design.

1.1 Hardware

USBula runs on a Microchip PIC24FJ64GB002 microcontroller. The following guide is intended to help the developer integrating this controller into the customers application. This information is only a prosposal whitout any guarantee on correctness. Please consult the PIC24FJ64GB002 family datasheet for obligatory details. This datasheet also includes "Guidelines for getting started with 16-bit microcontrollers" (Section 2.0) with useful information about required external components.

PIC24FJ64GB002 product page with link to the datasheet. http://www.microchip.com/wwwproducts/Devices.aspx?product=PIC24FJ64GB002

1.1.1 Chip packages

The PIC24FJ64GB002 is available in different chip packages. The package which fits to the rest of the customers design can be used.

28-lead QFN 28-lead SOIC (0.3") 28-lead SPDIP 28-lead SSOP

These are the part numbers of the available package types.

Package Type	Shipped in Tubes	Shipped as Tape/Reel
QFN	PIC24FJ64GB002-I/ML	PIC24FJ64GB002T-I/ML
SOIC (200 mil)	PIC24FJ64GB002-I/SO	PIC24FJ64GB002T-I/SO
SPDIP	PIC24FJ64GB002-I/SP	
SSOP	PIC24FJ64GB002-I/SS	PIC24FJ64GB002T-I/SS

1.1.2 Pinout Description

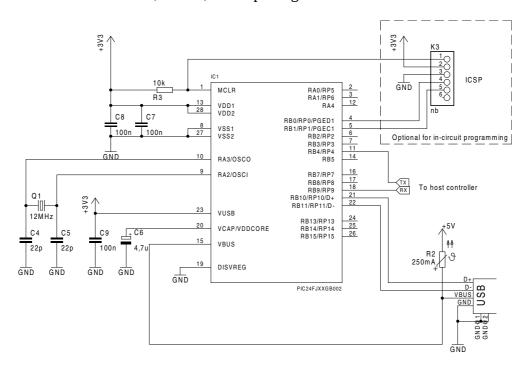
The following table lists the pins which are used by USBula. It describes which function is mapped to pin numbers of available package types.

SOIC SPDIP SSOP	QFN	Function	Description
13, 28	10, 25	VDD	+3V3 Power supply
20	17	VCAP	Voltage regulator output. Connect capacitor to this pin
23	20	VUSB	USB voltage: +3V3

15	12	VBUS		USB voltage: +5V
8, 27	5, 24	VSS		Ground supply
19	16	DISVR	EG	Disable voltage regulator. Connect to GND.
9	6	OSCI		Oscillator: 12 MHz crystal.
10	7	OSCO		
1	26	MCLR		Reset input
4	1	PGED1		Programming data (optional for in-circuit programming)
5	2	PGEC1		Programming clock (optional for in-circuit programming)
18	15	RP9	RX	UART receive signal
11	8	RP4	TX	UART transmit signal
2	27	RA0	MODE0	Internally pulled up to VDD.
3	28	RA1	MODE1	See section "Mode-Selection". For UART-Mode: leave pins open, not connected.
12	9	RA4	MODE2	For OAKI-Wode. leave pins open, not connected.
24	21	RB13	CFG0	Internally pulled up to VDD.
25	22	RB14	CFG1	See section "UART configuration". For Autobaud: leave pins open, not connected.
26	23	RB15	CFG2	Tof Autobaud. leave phils open, not connected.

1.1.3 Schematics example

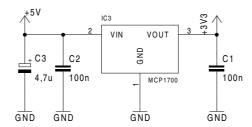
The PIC24 needs only a few external components to act as bridge between a simple serial interface and USB. This diagram shows a mimalistic circuit, running in UART mode with auto-baud detection. Pin numbers for SOIC, SPDIP, SSOP packages are shown.



1.1.4 Supply voltages: +3.3V and +5V

The USB part needs +5V. Current on the +5V line depends on the used USB flash drive (~100 mA). The maximum current should be limited (e.g. with a PTC fuse) to protect the circuit against short-circuits on the USB connector.

The microcontroller must be supplied with +3.3V (needs ~25 mA). If the target design doesn't provide this voltage yet, a voltage regulator can be used. There are out many voltage regulators for this purpose, e.g. the MCP1700 with maximum output current of 250 mA.



1.2 Load USBula HEX-File

The USBula firmware is provided in a compiled, ready-to-use binary HEX format. This format is very common for flashing software into microcontrollers. It is supported by most programmers and can be handled by most electronic manufacturing services.

1.2.1 PicKit3 and MPLAB IPE

A cost effective solution is a Microchip PicKit3 debugger/programmer (Part Number: PG164130) in combination with the free tool MPLAB IPE.

PicKit3 is a in-circuit debugger/programmer with a 2,54mm 6-pin connector. The interface is called ICSP. An 6-pole pin-header can be placed on the target circuit, so the programmer can be plugged directly on it. These connections are required for programming:

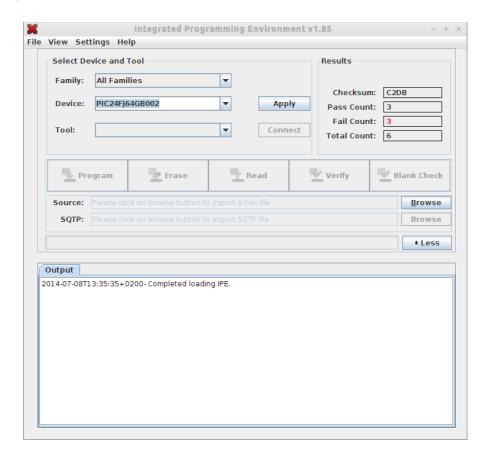
Pi	icKit3 Header	PIC24FJ pins		
		SOIC, SPDIP, SSOP QFN Function		
1	VPP/MCLR	1	26	VPP/MCLR
2	VDD	+3V3		
3	VSS	GND		
4	PGD	4	1	PGED1
5	PGC	5 2 PGEC1		
6	LVP	Do not connect		

Further details are described in the "Flash Programming Specification" downloadable from the PIC24FJ64GB002 product page:

http://www.microchip.com/wwwproducts/Devices.aspx?product=PIC24FJ64GB002

MPLAB IPE is bundled with the MPLAB X IDE, which can be downloaded for free: http://www.microchip.com/pagehandler/en-us/family/mplabx/

The IPE tool is automatically installed with the MPLAB IDE. It is available for Windows, Linux and MacOS X.



Steps for programming with IPE tool:

- 1. Select Device "PIC24FJ64GB002"
- 2. Connect PicKit3, select it as "Tool"
- 3. Open USBula-HEX-file as "Source: ... Browse"
- 4. Conenct Target with PicKit3
- 5. Press button "Program"

1.2.2 Order pre-programmed chips

The method described in the last section is applicable for prototypes and small production lots. For mass production, it can be more economically to use a programming service which ships the microcontroller already flashed packed in tubes or tape/reels. A few distributors provide such a service, even Microchip via microchipDIRECT:

http://www.microchipdirect.com/programming/

However it is recommended to integrate the ICSP connector pads to the application circuit for prototyping and debugging.

2 Serial interface

The host system communicates over the serial interface with USBula. This section describes how to

configure the interface and the serial protocol for accessing files on the USB flash drive.

2.1 Interface mode selection

The communication mode must be selected via the MODE0..2 pins. The inputs are pulled internally to VDD. So the pins must left open, if high levels are expected.

MODE2	MODE1	MODE0	Mode
Open	Open	Open	UART-Mode
All other options		ons	Reserved for future use

Currently, only the UART mode is supported by USBula.

2.2 UART communication

The UART is set up with these parameters:

8 data bits, no parity, 1 stop bit

Rx/Tx are used for communication with the host. There are no hardware handshake signals. Wiring with the host system:

 $\begin{array}{ll} \text{Host UART-Tx} & \rightarrow \text{USBula UART-Rx} \\ \text{USBula UART-Tx} & \rightarrow \text{Host UART-Rx} \end{array}$

The baudrate can be selected with pins CFG0..2. The inputs are pulled internally to VDD. If high levels are expected, the pins must be left open. If auto-baudrate is selected, the baudrate is determined automatically while sending the first command after reset. This first command must be the sync-command ('U').

CFG2	CFG1	CFG0	Baudrate (Baud)
GND	GND	GND	1200
GND	GND	Open	2400
GND	Open	GND	9600
GND	Open	Open	19200
Open	GND	GND	38400
Open	GND	Open	57600
Open	Open	GND	115200
Open	Open	Open	Auto-Baudrate

2.3 Communication protocol

The USBula acts as a slave device and sends only data in response to a request from the host. The protocol is half duplex: After transmitting a request, the host must wait for the response.

The commands are transmitted as ASCII sequences. Payload data (for read/write file) can also be binary and do not have to be in ASCII format.

Every command ends with a line break character. USBula sends LF ('\n'). When receiving it also interprets the CR ('\r') as end of line. The maximum line length including command and paramter characters is 128 bytes. The payload data for read and write command is limited to 100 bytes.

All numeric values are transmitted in hexadecimal as ASCII sequence. Example: decimal: 158 → hexadecimal: 9E

Simultanously two files can be opened (maximum files opened: 2). To specify which file is currently addressed, an identifier (called file-handler) is used.

2.4 Command description

This section describes the available serial commands. Characters sent from Host to USBula are printed in red. The opposite direction is marked with green. Line feeds are represented by "—".

2.4.1 "V" – Get Version String

Get USBula firmware version string.

Syntax	Parameters	Example
V	x.x.x = Version string	V
لx.x.xv		v1.1.0↓

2.4.2 "O" – Open file

Open file with given name. The given file-handler is used to access this file with following commands.

Syntax	Parameters	Example
Ofm:filename↓ >00↓	f: file handler m: Mode (r = read only, w =	Open file (handler 1) "test.txt": O1w:test.txt↓
	write, a = append) filename: 8+3 character name with prefixed path	>00<

2.4.3 "C" – Close file

Close file with given file handler. Pending data (buffered in RAM) are flushed to the USB memory stick.

Syntax	Parameters	Example
Cf₊	f: file handler	Close file (handler 1):
>00<		C1→
		٦00<

2.4.4 "R" - Read

Read from given file handler the given count of bytes. If the end of file is riched, the remaining

bytes are send as "filler".

Syntax	Parameters	Example
Rfssyy-J >00-J ss bytes, read from file. If end of file riched, bytes are filled with filler #rr-J	f: file handler ss: bytes to read yy: filler rr: count of read bytes (max 100)	Read 5 bytes from file (handler 1, filler: 0xFF): R105FF >00 Hello #05

2.4.5 "W" – Write

Write to given file handler.

Syntax	Parameters	Example
Wfss↓ >00↓ ss bytes, write to file #ww↓	f: file handler ss: bytes to read ww: count of bytes written (max 100)	Write "Hello world!" (=12 chars) to file (handler 1): ₩10C↓ >00↓ Hello world! #0C↓

2.4.6 "U" – Synchronize/Alive

This command is used to syncronize USBula to the used baudrate if configured in auto-baudrate mode. The command must sent first, before other commands. If USBula doesn't response, the command must be sent again after timeout. If a fix baudrate is selected via the CFG0..2 pins, this is not necessary. The "U" command can also be used to check if USBula is alive (like ping/pong).

Syntax	Parameters	Example
U-l		ΠΥ
>004		٦00<

2.5 Response codes

USBula answers to commands with a response code which indicates possible errors.

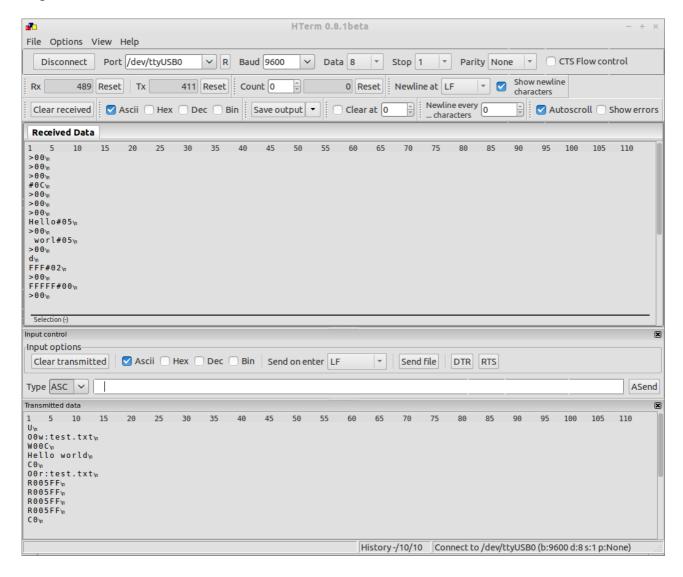
Code	Description		
>00	Successful. Everything okay!		
>01	Unknown command		
>02	Syntax error		
>03	Invalid file handler		
>04	Error while opening file		

>05	File not open
>06	Error while reading file
>07	Invalid size parameter

2.6 Example

The following screenshot shows an example communication. USBula is connected with a USB-to-serial interface to an PC running "HTerm", a terminal programm: http://www.der-hammer.info/terminal/

The commands are just typed into the input field and sent out with enter ("Send on enter LF"). The response is shown in the "Received Data" window.



3 Licensing

USBula is free for evaluation and personal use. For commercial purpose a license must be aquired. Please contact license@usbula.com.

3.1 License options and pricing

There are four different license options which depends on the purpose of use and if source code is required.

License	Price (excl. VAT)	Units	Source code	Purpose
USBula FREE	No charge	unlimited	Not included	Evaluation, personal use
USBula LE	€ 99	up to 250	Not included	Commercial
USBula LP	€ 199	unlimited	Not included	Commercial
USBula LPS	€ 399	unlimited	Included	Commercial

It is recommended to start with USBula FREE to evaluate its function and test it with the intended system components. USBula FREE has only one difference to the other license options: it automatically generates a file "usbula.txt" with license information on every USB flash drive which is plugged in.

With USBula LPS you get the source codes (MPLAB X project) and an additional documentation which describes the firmware concept, the development environment and the steps to compile the firmware project.

License upgrades are not possible. Please order a new license if the granted number of units are not enough.

3.2 License terms

Software license agreement for USBula, the Serial to USB-Flash-Drive Bridge Firmware

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