

HCS
HART® Concentrator System
HART-to-MODBUS RTU Converter



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HART® Concentrator System HART-to-MODBUS RTU Converter

Introduction

This is the user's manual for the Moore Industries HCS HART® Concentrator System. It contains all of the information needed to configure, install, operate and maintain this instrument.

About this Manual

Pay particular attention wherever you see a "**Note**", "**Caution**" or "**WARNING**".

Note— Information that is helpful for a procedure, condition or operation of the unit.

Caution— Hazardous procedure or condition that could damage or destroy the unit.

WARNING— Hazardous procedure or condition that could injure the operator.

The HCS

The HCS HART® Concentrator System converts a HART digital signal to a serial MODBUS RTU (RS-485 or RS-232, depending on Output type ordered) communication protocol. This allows HART transmitters and valves to interface directly with MODBUS-based monitoring and control systems.

Model and Serial Numbers

Moore Industries uses the model and serial numbers of our instruments to track information on each unit that we sell and service. If a problem occurs with your HCS, check for a tag affixed to the unit listing these numbers. Supply the Customer Support representative with this information when calling.

Inputs

The HCS is equipped with one input channel. This handles up to 16 HART devices in multidrop mode.

In a digital multidrop HART network, up to 16 HART instruments digitally communicate on the same wires. The HCS can be set to monitor any or all instruments and/or valves within the network. Only one MODBUS address and one communication link is needed to send the process and diagnostic data from up to 16 HART devices to a MODBUS host.

The instrument is equipped with a READY LED to indicate the health of the unit and an INPUT LED to indicate status of HART communication to the attached HART devices.

Outputs

The HCS offers a standard RS-485 or RS-232 port (depending on Output type ordered) that supports the MODBUS RTU protocol.

MB232

Allows for standard MODBUS RTU protocol interface over a RS-232 port.

MB485

Allows for standard MODBUS RTU protocol interface over a RS-485 port.

TX Power Supply

A transmitter excitation power supply (regulated 23.2Vdc \pm 3% @24mA, maximum) is standard on the HCS. You may access it at the terminals shown in Figure 3.

* HART is a registered trademark of the HART Communication Foundation

Specifications

<p>Performance Input Accuracy: Reflects the accuracy of the HART field device Input Impedance: Transmit Mode: 150 ohms; Receive Mode: Less than 5kohms Isolation: 1000Vrms between case, input, output and power terminals and will withstand 1500Vac dielectric strength test for one minute continuous with no breakdown Power Supply: 9-30DC +TX Power Supply: 23.2Vdc ±3% @ 24mA Digital Response Time: Equals the combination of the HART response time and the MODBUS response time; the HART delay is defined by the HART protocol as 500msec in normal mode and 333msec in burst mode; the MODBUS response time depends on how fast and how often a MODBUS Master requests data from the HCS; the data request to response time is 50msec Output Type: Standard MODBUS RTU protocol interface over RS-485 (parameters as specified in U.S. Standard EIA-RS485) or RS-232 (parameters as specified in U.S. Standard EIA-RS232) Output Protection: Transient protection on output</p>	<p>Performance (Continued) Address Range: Configurable from 1 to 247. Unit will assume a MODBUS address of 1 by default Baud Rate: Interface supports the following: 300, 600, 1200, 2400, 4800, 9600 and 19.2k. MODBUS interface will support even, odd and no parities. Unit will assume a baud rate of 9600 and no parity by default Transmission Range (MB485): Using 24AWG twisted pair wiring, maximum of 2 mi. (3.2km) @ 4800 baud or less; maximum of 1 mi. (1.6km) @ 9600 baud; maximum of 0.5 mi. (0.8km) @ 19200 baud Character Format: One start bit, 8 data bits and one stop bit Data Format: User-selectable Standard LSW (Least Significant Word) or Swapped MSW (Most Significant Word). Unit will assume Standard LSW by default Power Consumption: 1.5W, nominal; 2W @ 24Vdc maximum for units using transmitter excitation to supply loop power to a 2-wire instrument</p>	<p>Indicators LED Type: Dual color red/green indicate: INPUT LED: Input is present and normal (green); input signal is not found (red) READY LED: Instrument is ready for operation and configuration (green); instrument has encountered an internal problem (red)</p> <p>Ambient Conditions Operating and Storage Range: -40°C to +85°C (-40°F to +185°F) Relative Humidity: 0-95%, non-condensing RFI/EMI Immunity (Standard): 10V/m @ 80-1000MHz, 1kHz AM, when tested according to IEC61326 RFI/EMI Immunity (with -RF Option): 20V/m @ 80-1000MHz, 1kHz, when tested according to IEC61326 Noise Rejection: Common Mode: 100dB @ 50/60Hz</p> <p>Weight 290 g (10.2 oz)</p>
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Specifications and information subject to change without notice.

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Figure 1. HCS Dimensions

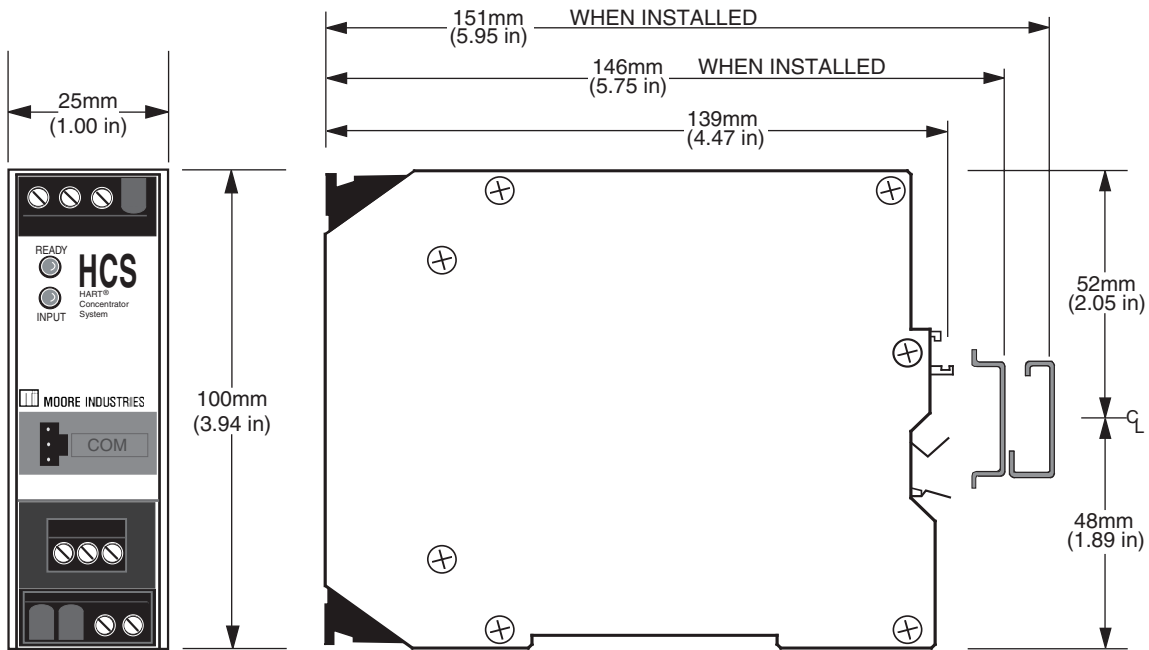
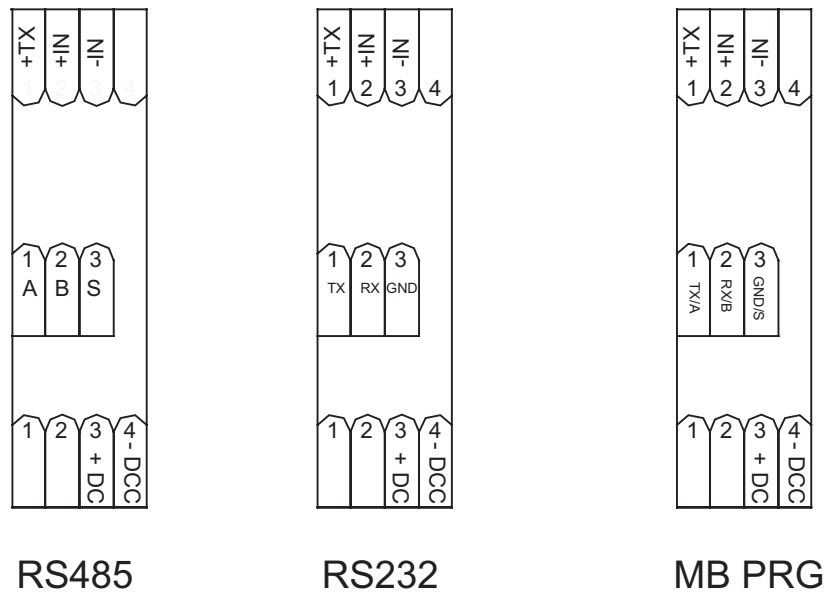


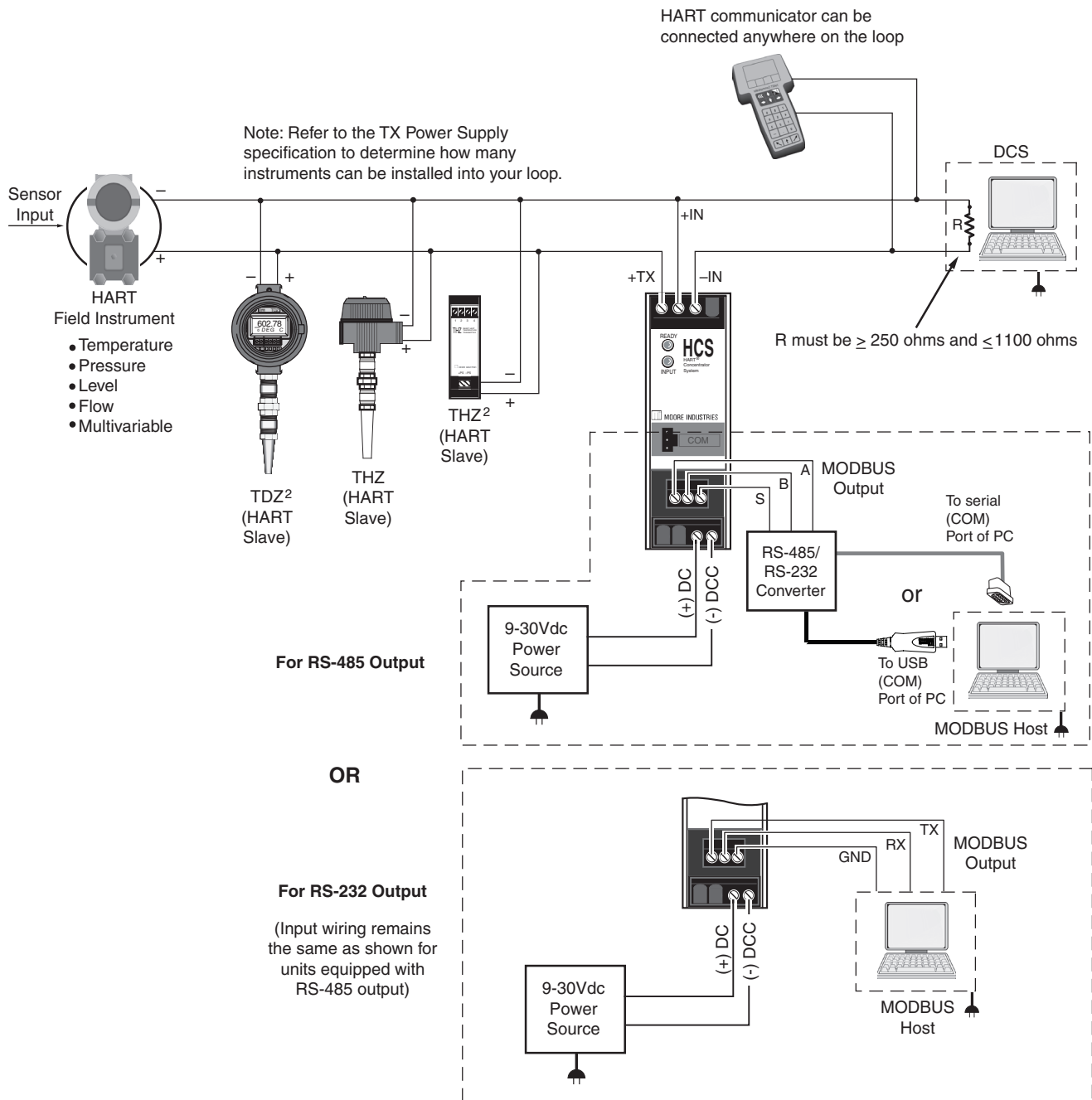
Table 1. Terminal Designations



KEY:	+TX = Power for 2-Wire transmitter	A = A MODBUS
+IN = Positive input	-IN = Negative input	B = B MODBUS
		S = S MODBUS
		(+) DC = Positive power input
		(-) DCC = Negative power input

- NOTE:**
1. Terminal blocks can accommodate 14-22 AWG solid wiring.
 2. Tighten terminals to four inch-pounds (maximum).

Figure 2. Installing the HCS Into the Loop Using the TX Power Supply to Power a Transmitter



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Figure 3. Installing the HCS Into the Loop Using an External Power Source to Power a Transmitter

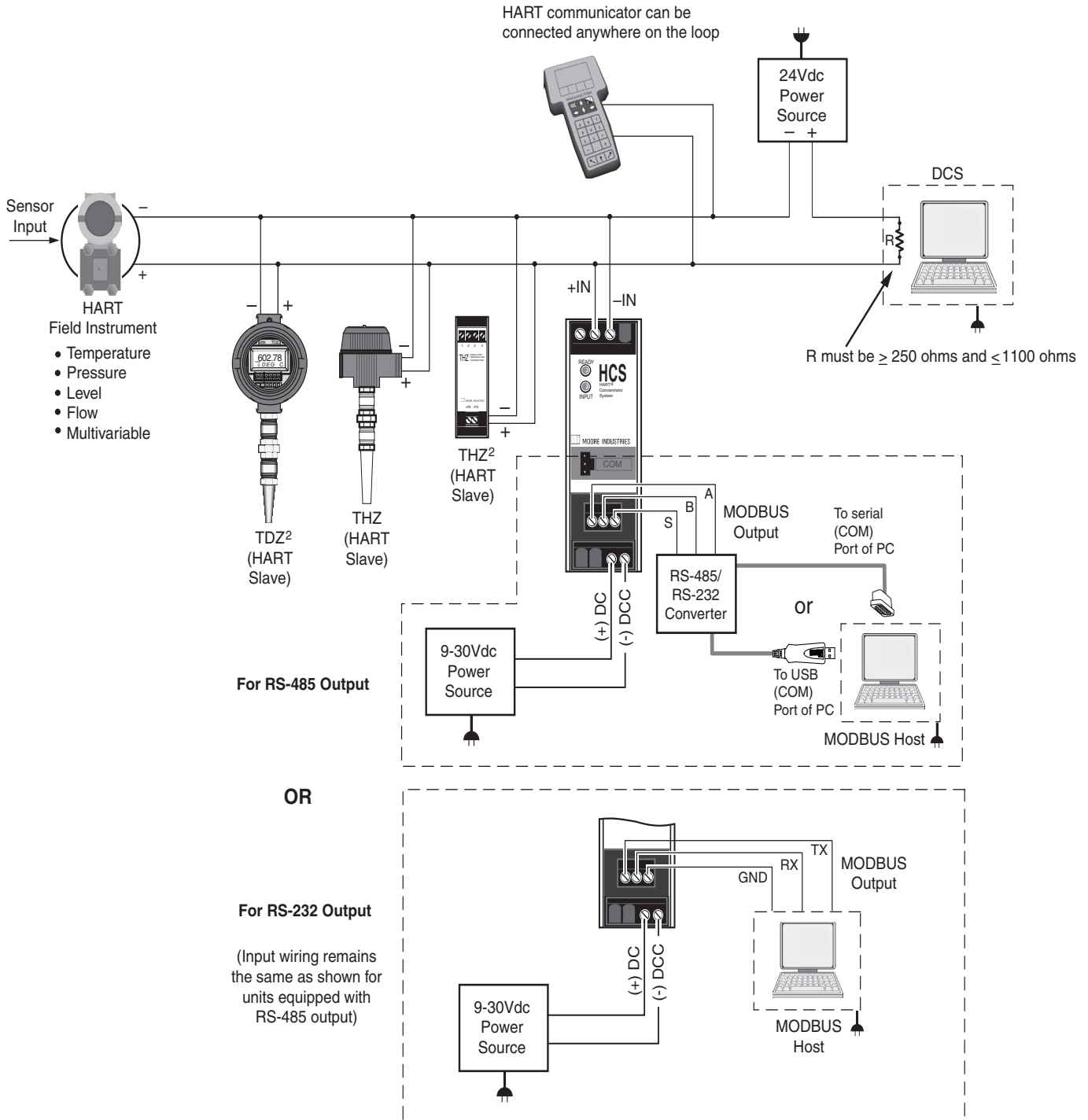
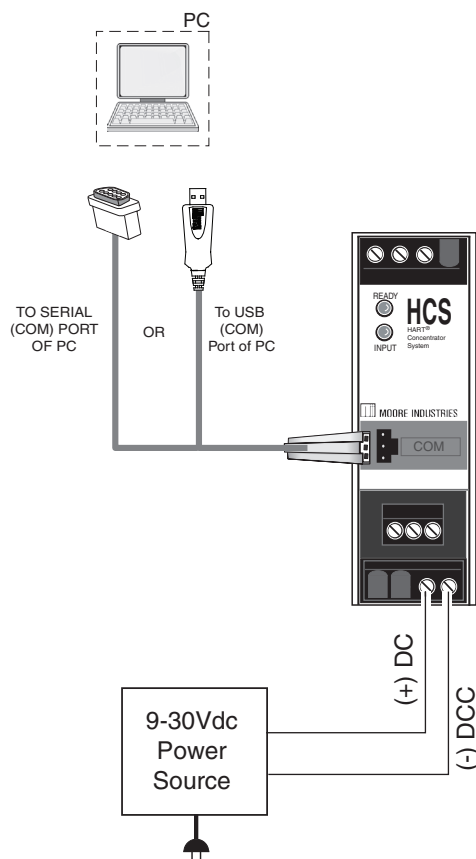


Figure 4. HCS PC Hook-Up Diagram



Default Factory Configuration for HCS

The following are the factory default configuration settings for your HCS unit:

HART Settings-

Polled HART Slave Devices: 1
HCS is a Primary Master
No of HART retries: 1

MODBUS Settings-

Address: 1
Baud Rate:..... 9600
MODBUS Parity:..... None
MODBUS Float Format:..... Standard LSW
MODBUS Register Layout:..... By Variable Type
MODBUS Failed HART Mode:..... Hold Last
Decimal Places:..... 0

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Configuring the HCS

One of the benefits of the HCS is that there are no internal or external controls to adjust or settings to change. All operating parameters are set using the PC Configuration software.

Once these software settings are made, they are downloaded to the instrument in the form of a Configuration File and stored in the unit's nonvolatile memory. You can choose to save a backup copy of the file on your PC hard drive or external media. The HCS communicates with the PC through a proprietary communications cable to the PC's serial (COM) port or optional proprietary USB cable to the PC's USB port.

Installing the Configuration Software

Refer to Table 2 for the equipment needed.

1. Insert the *Moore Industries Interface Solution PC Configuration Software* CD into the CD drive of the PC. Access the CD and open the "HCS PC Configuration Software" folder.
2. Double-click the installation program located in the folder. Follow the prompts to correctly install the program.

Once the Configuration Program is installed onto your PC, the HCS can be connected into a system and become operational.

Connecting the HCS to the PC

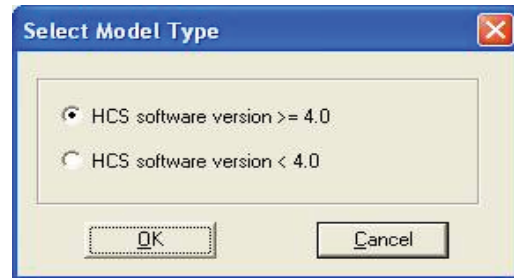
HCS can be connect to PC one of two ways:

- using the proprietary communications cable to connect to PC's serial (COM) port
- using the optional proprietary USB cable to connect to PC's USB port

See Table 2 for information on the necessary equipment.

Selecting Model Type

User must select unit type when the software is opened without a unit connected as shown below. Unit type is determined by software version. When unit is connected software will select the correct software on automatically.



Note:

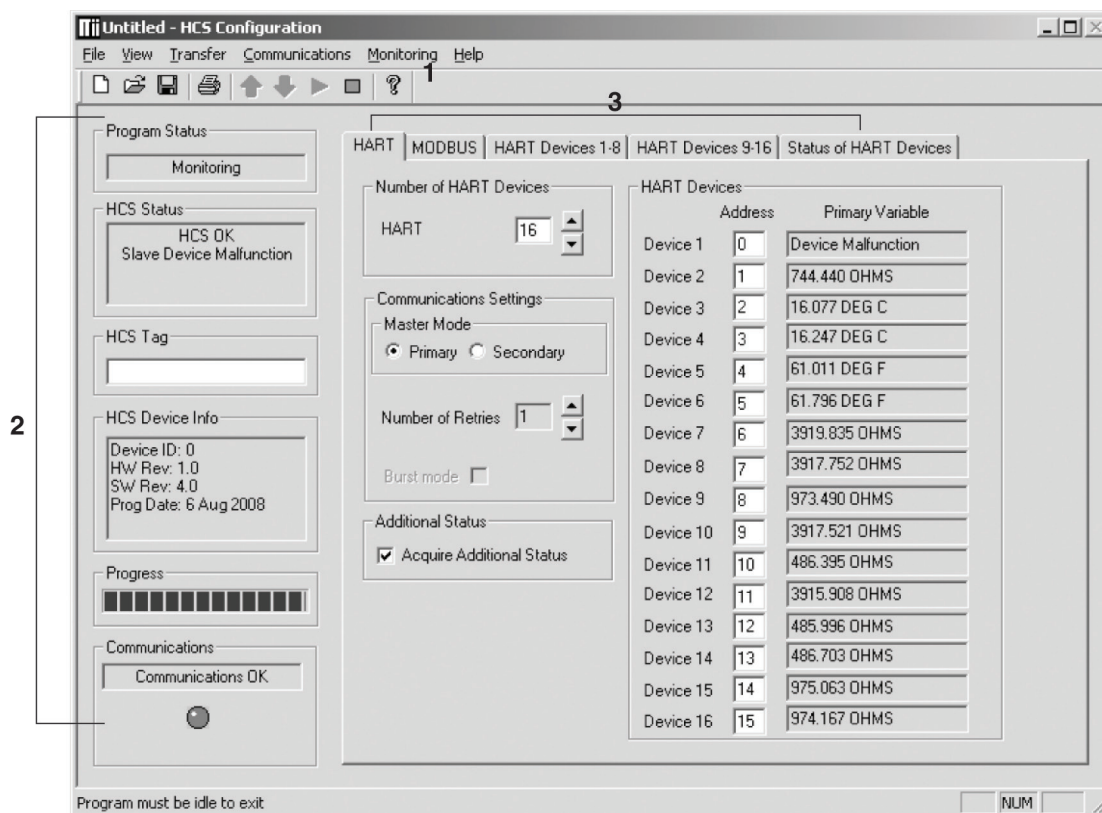
The following information applies only to units with software version 4.0 and greater, if you need information pertaining to units with a software less than 4.0 see Appendix A located at the end of this manual

Table 2. Necessary Equipment Table

Device	Specifications
Power Supply	9-30DC, ±10%
Personal Computer	Microsoft Windows based PC; 16Mb free RAM; 20MB free disk space on hard drive Microsoft Windows XP, Vista or 7 1 (one) serial port or one available USB port
Moore Industries PC Configuration Software	Version 1.0 or higher, successfully installed onto the hard drive
Communication Cable options	Serial Communications Cable (PN 803-053-26), USB Cable (PN 208-236-00) or Fuse Protected USB Cable (PN 804-030-26)

PC Configuration Software Summary

Figure 5. HCS PC Configuration Software Screen



The HCS PC Configuration Software can be used to program all of the instrument's parameters. Once the default configuration has been saved, it is safe to program other parameters.

The PC Software is composed of these sections:

1. Menu Bar/Tool Bar—Dropdown menus and corresponding icons allow you to perform various functions throughout the PC Configuration Program. *Refer to the Menu and Tool Bar Legend* section for a complete description.

2. Program Status—This portion of the program displays the activity (idle, monitoring, downloading, uploading) of the connected unit.

HCS Status—Notifies of any errors or conditions which are outside of the tolerance range. Displays *HCS OK* if the unit is operating normally.

HCS Tag—A phrase used to identify an HCS (eight alphanumeric characters, maximum).

HCS Device Info—Displays the individual characteristics of the attached HCS, such as the device ID, hardware and software revisions and the last date that the device was programmed.

Progress—This bar stays in motion any time the HCS is monitoring, uploading or downloading, to notify that a process is occurring.










Communications—Indicates current PC connection/communications status.

3. HART/MODBUS/HART Devices/Status of HART Devices Tabs—These tabs change the right side of the screen to allow you to set the appropriate part of the HCS's configuration. See corresponding sections of this manual for additional information on these tabs.

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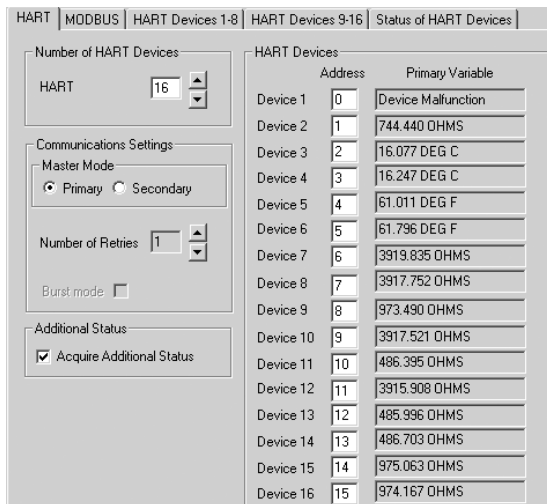
Menu and Tool Bar Legend

		Allows such functions as New, Open, Save and Print
		Controls whether Tool and Status Bars are viewed on the screen
		Allows you to Upload and Download configurations
		Select the PC Port (Com Port) that you will use
		Allows you to Monitor and Stop monitoring processes
		Displays the version of the HCS Configuration Program

Configuration Screens

HART

Figure 6. HART Tab



The screenshot shows the HART configuration interface. On the left, there are settings for the number of HART devices (set to 16), communication settings (Primary mode selected, 1 retry, burst mode unchecked), and an option to acquire additional status (checked). On the right, a table lists 16 HART devices with their addresses and primary variables.

HART Devices	Address	Primary Variable
Device 1	0	Device Malfunction
Device 2	1	744.440 OHMS
Device 3	2	16.077 DEG C
Device 4	3	16.247 DEG C
Device 5	4	61.011 DEG F
Device 6	5	61.796 DEG F
Device 7	6	3919.835 OHMS
Device 8	7	3917.752 OHMS
Device 9	8	973.490 OHMS
Device 10	9	3917.521 OHMS
Device 11	10	486.395 OHMS
Device 12	11	3915.908 OHMS
Device 13	12	485.996 OHMS
Device 14	13	486.703 OHMS
Device 15	14	975.063 OHMS
Device 16	15	974.167 OHMS

Number of HART Devices

Using the up and down arrows, or by manually entering the value from your keyboard, select the number of HART devices (16 maximum) that you will introduce into your loop. The number you have chosen will appear as enabled in the *Slave Devices* parameter.

Slave Devices

Once you have selected the number of HART slaves to be used in the loop, use this section to assign a specific address for each. Ensure that the address matches the address of the device you connected in the loop. Each must be a unique address between zero and 63. However, Address 0 is an analog address. Current readings at this address can vary from 3.6mA to 23.6mA.

Master Mode

The HART protocol allows for two communications masters on the loop: a Primary Master and a Secondary Master. Setting the HCS to function as the Primary HART Master in the application means that any other HART device in the loop must be configured either as a HART Secondary Master (1 per loop), or as a HART Slave (up to 16 per loop). Conversely, setting the HCS to function as the Secondary HART Master allows other HART devices to function either as a Primary Master, or as Slaves. Configuring more than one device on a single loop as a Primary or Secondary HART Master will cause a communications failure.

Note:

A HART hand-held communicator is typically a Secondary Master.

Number of Retries

The *Number of Retries* can be set between 1 and 3, and will determine how many times the HCS will attempt to poll the HART transmitter (without success), before it indicates a HART communication failure.

Burst Mode

Allows selection of Normal or Burst modes.

Burst mode can only be used with a single HART device (slave).

The HCS can operate in one of two modes: *Normal* or *Burst*. In each of these modes the HCS attempts to find a HART transmitter. If the designated slave (or slaves) is found, the HCS will also read the device's Tag.

In *Normal* mode, the HCS polls the HART loop for a transmitter, then polls the HART instrument twice per second, requesting the current process status and the HART instrument's diagnostic status. The HART instrument responds with the requested data.

In *Burst* mode, the monitored HART instrument continuously transmits its process variable and health status. The HCS samples the continuous HART data three times per second.

The instrument will operate in Normal Mode by default. Selecting the *Burst Mode* button will enable Burst Mode reception in the HCS. An error is declared if no burst messages are received by the HCS.

HCS Units with firmware version 4 or earlier will send commands to the slave, configuring and enabling burst mode.

HCS Units with firmware version 5 or later do not send commands to configure the slave. The slave must be configured (using a handheld configurator, or by other means) to have burst mode enabled, and to burst either HART command 1 or 3. If the HCS is able to communicate with the slave but does not receive burst messages, the Input LED will alternate red and green.

Additional Status

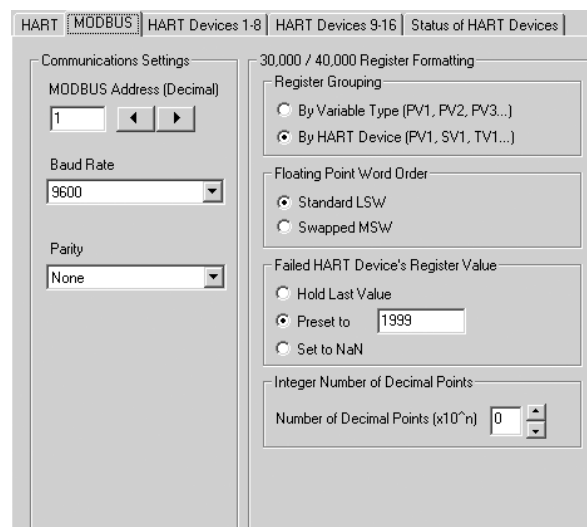
Checking the *Acquire Additional Status* box will allow *Additional Status* information to be displayed on the *Status of HART Devices* screen and also in the corresponding MODBUS register (refer to Tables 3 and 4). If the box remains unchecked, Additional Status information will be unavailable.

For software version 5.0 and greater, all 25 additional status bytes (0 to 24) are displayed and available in Modbus registers 1000-1024 (see Tables 3.1 and 4.1)

If this information is not needed, it is good practice to keep the box unchecked in order to keep polling of the additional status bytes from occurring. This will help maintain faster response times.

MODBUS

Figure 7. MODBUS Tab



The *MODBUS tab* allows you to set the MODBUS communications parameters.

Communications Settings

The Communications Settings include three areas:

MODBUS Address (Decimal)

The *MODBUS Address* is the number that the HCS monitor uses to identify itself on the MODBUS network. The MODBUS address is configurable from 1 to 247. By default, it will assume a MODBUS address of 1.

Baud Rate

The *Baud Rate* is the speed of MODBUS data transmission. It should be set to match the baud rate of the attached controller. The interface supports the following baud rates: 300, 600, 1200, 2400, 4800, 9600 and 19200.

Parity

The HART monitor supports even, odd and no *Parity*. The data format is one start bit, 8 data bits and one stop bit.

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30,000 / 40,000 Register Formatting

This section includes the following areas:

Register Grouping

This allows you to select the manner in which to group the MODBUS registers.

Selecting *By Variable Type*, the registers are grouped in order of *variables*, i.e. all primary variables (PV) are grouped together, followed by secondary variables (SV), third (TV) and then fourth (FV).

Using *By HART Slave Device* grouping places your registers in order *numerically*. It groups a HART slave device's variables in contiguous registers. For example, your first HART device's primary, secondary, third and fourth variables (PV1, SV1, TV1 and FV1) are grouped together. Next in the order are your second HART device's primary, secondary, third and fourth variables (PV2, SV2, TV2 and FV2) and so on.

Floating Point Word Order

By default, the HART Concentrator will use the *Standard LSW* (least significant word) floating point word order format. This stores the most significant bits in the second register and the least significant bits in the first register. Selecting *Swapped MSW* (*most significant word*) will reverse the order, storing the most significant bits in the first register and the least significant bits in the second register.

Failed HART Device's Register Value

You may select what would occur to a HART device's register value in the event that communication is lost with the HCS.

If selecting *Hold Last Value* and a failure is detected, the last measured value before the failure occurred is held.

Entering a user-set value in the *Preset to* text box recalls that value when a slave device failure is detected.

Selecting *NaN* (Not a Number—as put forth by the IEEE-754 standard) causes the floating point NaN value to be stored in the registers used for holding floating point values.

Integer Number of Decimal Points

The MODBUS integer registers are 16 bit signed integers and have a range of -32767 to 32768.

To obtain better resolution, the "Integer Number of Decimal Points" setting may be used. This can be set from 0-3, and will multiply the PV by 10^n where n is the number of decimal points. Care should be taken in selecting the resolution since the higher the resolution, the smaller the PV range which can be represented i.e. for n=3 the maximum range is -32.767 to +32.768. Depending on the nature of the data obtained from the slave and the required resolution, this may be unacceptable and in this case, floating point registers should be used.

The table below shows the PV range for the selected number of decimal points and a PV example.

Number of Decimals	Allowed PV Range	Displayed Integer
0	-32767 to 32768	12
1	-3276.7 to 3276.8	123
2	-327.67 to 327.68	1234
3	-32.767 to 32.768	12345
Example PV = 12.345		

HART Devices

Figure 8. HART Devices Tabs

HART Devices					
Dev Add	Tag	Primary Variable	Second Variable	Third Variable	Fourth Variable
1	0	SLAVE1	Device Malfunction	Device Malfunction	Device Malfunction
2	1	SLAVE2	744.491 OHMS	17.117 DEG C	1023.151 OHMS
3	2	SLAVE3	17.550 DEG C	17.498 DEG C	17.550 DEG C
4	3	SLAVE4	17.650 DEG C	17.706 DEG C	17.650 DEG C
5	4	SLAVE5	63.702 DEG F	64.279 DEG F	63.702 DEG F
6	5	SLAVE6	64.324 DEG F	64.364 DEG F	64.324 DEG F
7	6	SLAVE7	3919.835 OHMS	17.617 DEG C	3919.835 OHMS
8	7	SLAVE8	3917.753 OHMS	17.231 DEG C	3917.753 OHMS

This following applies to both HART Device tabs (1-8 and 9-16).

These are read-only screens that display the device number and its associated address, tag and the Primary, Second, Third and Fourth variables.

Note:

This displays the 8 character Tag and not the 32 character Long Tag.

When the HCS is in *Monitor* mode, all four variables constantly update. However, the *Tag* only updates once power is reapplied (upon a loss of power) or when communication with the HART device has been re-established.

Status of HART Devices

Figure 9. Status of HART Devices Tab

HART Devices' Status	
HART Devices	Status
Device 1	Device 1 Status
Device 2	
Device 3	Status Bytes: 0xD8
Device 4	
Device 5	ERROR - Device Malfunction
Device 6	Config Changed
Device 7	Additional Status
Device 8	Output Fixed
Device 9	
Device 10	
Device 11	
Device 12	Additional Status Bytes(15)
Device 13	0x06 88 03 03 03 00 00 00 00 00 00 00 00 3C
Device 14	
Device 15	
Device 16	
	Information
	Moore Industries
	THZ3/TDZ3
	Device Id: 2270667
	Software Revision: 1
	Hardware Revision: 0

This screen displays Status the Additional Status and Information regarding the selected device. Use the *HART Devices* selection box to choose which device to view.

For software version 4.0 and less only the first six bytes (0-5) of additional status are available. For software version 5.0 and greater, up to 25 additional status bytes (0-24) are displayed and available in Modbus registers 1000-1024 (see Tables 3.1 and 4.1). The display will only show the additional status bytes sent by the slave (from 0 to 25).

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MODBUS Register Definitions

Tables 3-6 define the MODBUS input and holding register assignment. These tables are zero based, your MODBUS host may require you to enter the MODBUS register. Often registers have an offset of “1” from the MODBUS address. For example, a MODBUS address listed below of 256 may have to be entered as 257 in your host. Please refer to your MODBUS host documentation for verification.

Note:

The following information applies only to units with software version 4.0 and greater, if you need information pertaining to units with a software less than 4.0 see Appendix A located at the end of this manual.

Table 3. Register ranges and descriptions when MODBUS registers are grouped by the “By Variable Type” parameter (MODBUS register block mode = 0) and all dynamic variables are being stored

Register Range	Description
0-15	HART Device 1-16 PV integer value x 10 ^{no} of DPs
16-31	HART Device 1-16 SV integer value x 10 ^{no} of DPs
32-47	HART Device 1-16 TV integer value x 10 ^{no} of DPs
48-63	HART Device 1-16 FV integer value x 10 ^{no} of DPs
64-79	HART Device 1-16 HART Status
80-95	HART Device 1-16 PV + SV UOM (PV UOM = MSB, SV UOM = LSB)
96-111	HART Device 1-16 TV + FV UOM (TV UOM = MSB, FV UOM = LSB)
127	HCS Status
256-287	HART Device 1-16 PV floating point value stored in 2x16-bit registers per float
288-319	HART Device 1-16 SV floating point value stored in 2x16-bit registers per float
320-351	HART Device 1-16 TV floating point value stored in 2x16-bit registers per float
352-383	HART Device 1-16 FV floating point value stored in 2x16-bit registers per float
512-517	HART Device 1 additional status byte 0 to byte 5
518-523	HART Device 2 additional status byte 0 to byte 5
524-529	HART Device 3 additional status byte 0 to byte 5
530-535	HART Device 4 additional status byte 0 to byte 5
536-541	HART Device 5 additional status byte 0 to byte 5
542-547	HART Device 6 additional status byte 0 to byte 5
548-553	HART Device 7 additional status byte 0 to byte 5
554-559	HART Device 8 additional status byte 0 to byte 5
560-565	HART Device 9 additional status byte 0 to byte 5
566-571	HART Device 10 additional status byte 0 to byte 5
572-577	HART Device 11 additional status byte 0 to byte 5
578-583	HART Device 12 additional status byte 0 to byte 5
584-589	HART Device 13 additional status byte 0 to byte 5
590-595	HART Device 14 additional status byte 0 to byte 5
596-601	HART Device 15 additional status byte 0 to byte 5
602-607	HART Device 16 additional status byte 0 to byte 5

KEY:

FV = Fourth Variable
LSB = Least Significant Bit
MSB = Most Significant Bit
PV = Primary Variable

SV = Secondary Variable
TV = Third Variable
*UOM = Unit of Measurement

*Moore Industries provides up to 169 HART Engineering Units listed in HART Communication Foundation document number: HCF-SPEC-183, Revision 14.0, Release Date 29 January 2004

Table 3.1. Additional status byte 0 to byte 24 (for software version 5.0 and greater).

Register Range	Description
1000-1024	HART Device 1 additional status byte 0 to byte 24
1025-1049	HART Device 2 additional status byte 0 to byte 24
1050-1074	HART Device 3 additional status byte 0 to byte 24
1075-1099	HART Device 4 additional status byte 0 to byte 24
1100-1124	HART Device 5 additional status byte 0 to byte 24
1125-1149	HART Device 6 additional status byte 0 to byte 24
1150-1174	HART Device 7 additional status byte 0 to byte 24
1175-1199	HART Device 8 additional status byte 0 to byte 24
1200-1224	HART Device 9 additional status byte 0 to byte 24
1225-1249	HART Device 10 additional status byte 0 to byte 24
1250-1274	HART Device 11 additional status byte 0 to byte 24
1275-1299	HART Device 12 additional status byte 0 to byte 24
1300-1324	HART Device 13 additional status byte 0 to byte 24
1325-1349	HART Device 14 additional status byte 0 to byte 24
1350-1374	HART Device 15 additional status byte 0 to byte 24
1375-1399	HART Device 16 additional status byte 0 to byte 24

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Table 4. Register ranges and descriptions when MODBUS registers are grouped by the “By HART Slave Device” parameter (MODBUS register block mode = 0) and PV, SV, TV and FV are being stored

Register Range	Description
0	HART Device 1 PV integer value x 10 ^{no} of DPs
1	HART Device 1 SV integer value x 10 ^{no} of DPs
2	HART Device 1 TV integer value x 10 ^{no} of DPs
3	HART Device 1 FV integer value x 10 ^{no} of DPs
4	HART Device 1 HART Status
5	HART Device 1 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
6	HART Device 1 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
7	Not assigned
8	HART Device 2 PV integer value x 10 ^{no} of DPs
9	HART Device 2 SV integer value x 10 ^{no} of DPs
10	HART Device 2 TV integer value x 10 ^{no} of DPs
11	HART Device 2 FV integer value x 10 ^{no} of DPs
12	HART Device 2 HART Status
13	HART Device 2 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
14	HART Device 2 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
15	Not assigned
16	HART Device 3 PV integer value x 10 ^{no} of DPs
17	HART Device 3 SV integer value x 10 ^{no} of DPs
18	HART Device 3 TV integer value x 10 ^{no} of DPs
19	HART Device 3 FV integer value x 10 ^{no} of DPs
20	HART Device 3 HART Status
21	HART Device 3 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
22	HART Device 3 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
23	Not assigned
24	HART Device 4 PV integer value x 10 ^{no} of DPs
25	HART Device 4 SV integer value x 10 ^{no} of DPs
26	HART Device 4 TV integer value x 10 ^{no} of DPs
27	HART Device 4 FV integer value x 10 ^{no} of DPs
28	HART Device 4 HART Status
29	HART Device 4 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
30	HART Device 4 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
31	Not assigned
32	HART Device 5 PV integer value x 10 ^{no} of DPs
33	HART Device 5 SV integer value x 10 ^{no} of DPs
34	HART Device 5 TV integer value x 10 ^{no} of DPs
35	HART Device 5 FV integer value x 10 ^{no} of DPs
36	HART Device 5 HART Status
37	HART Device 5 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)

Continued on next page

Table 4. Continued

Register Range	Description
38	HART Device 5 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
39	Not assigned
40	HART Device 6 PV integer value x 10 ^{no} of DPs
41	HART Device 6 SV integer value x 10 ^{no} of DPs
42	HART Device 6 TV integer value x 10 ^{no} of DPs
43	HART Device 6 FV integer value x 10 ^{no} of DPs
44	HART Device 6 HART Status
45	HART Device 6 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
46	HART Device 6 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
47	Not assigned
48	HART Device 7 PV integer value x 10 ^{no} of DPs
49	HART Device 7 SV integer value x 10 ^{no} of DPs
50	HART Device 7 TV integer value x 10 ^{no} of DPs
51	HART Device 7 FV integer value x 10 ^{no} of DPs
52	HART Device 7 HART Status
53	HART Device 7 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
54	HART Device 7 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
55	Not assigned
56	HART Device 8 PV integer value x 10 ^{no} of DPs
57	HART Device 8 SV integer value x 10 ^{no} of DPs
58	HART Device 8 TV integer value x 10 ^{no} of DPs
59	HART Device 8 FV integer value x 10 ^{no} of DPs
60	HART Device 8 HART Status
61	HART Device 8 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
62	HART Device 8 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
63	Not assigned
64	HART Device 9 PV integer value x 10 ^{no} of DPs
65	HART Device 9 SV integer value x 10 ^{no} of DPs
66	HART Device 9 TV integer value x 10 ^{no} of DPs
67	HART Device 9 FV integer value x 10 ^{no} of DPs
68	HART Device 9 HART Status
69	HART Device 9 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
70	HART Device 9 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
71	Not assigned
72	HART Device 10 PV integer value x 10 ^{no} of DPs
73	HART Device 10 SV integer value x 10 ^{no} of DPs
74	HART Device 10 TV integer value x 10 ^{no} of DPs
75	HART Device 10 FV integer value x 10 ^{no} of DPs

Continued on next page

Table 4. Continued

Register Range	Description
76	HART Device 10 HART Status
77	HART Device 10 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
78	HART Device 10 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
79	Not assigned
80	HART Device 11 PV integer value x 10 ^{no} of DPs
81	HART Device 11 SV integer value x 10 ^{no} of DPs
82	HART Device 11 TV integer value x 10 ^{no} of DPs
83	HART Device 11 FV integer value x 10 ^{no} of DPs
84	HART Device 11 HART Status
85	HART Device 11 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
86	HART Device 11 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
87	Not assigned
88	HART Device 12 PV integer value x 10 ^{no} of DPs
89	HART Device 12 SV integer value x 10 ^{no} of DPs
90	HART Device 12 TV integer value x 10 ^{no} of DPs
91	HART Device 12 FV integer value x 10 ^{no} of DPs
92	HART Device 12 HART Status
93	HART Device 12 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
94	HART Device 12 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
95	Not assigned
96	HART Device 13 PV integer value x 10 ^{no} of DPs
97	HART Device 13 SV integer value x 10 ^{no} of DPs
98	HART Device 13 TV integer value x 10 ^{no} of DPs
99	HART Device 13 FV integer value x 10 ^{no} of DPs
100	HART Device 13 HART Status
101	HART Device 13 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
102	HART Device 13 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
103	Not assigned
104	HART Device 14 PV integer value x 10 ^{no} of DPs
105	HART Device 14 SV integer value x 10 ^{no} of DPs
106	HART Device 14 TV integer value x 10 ^{no} of DPs
107	HART Device 14 FV integer value x 10 ^{no} of DPs
108	HART Device 14 HART Status
109	HART Device 14 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
110	HART Device 14 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
111	Not assigned
112	HART Device 15 PV integer value x 10 ^{no} of DPs
113	HART Device 15 SV integer value x 10 ^{no} of DPs

Continued on next page

Table 4. Continued

Register Range	Description
114	HART Device 15 TV integer value x 10 ^{no} of DPs
115	HART Device 15 FV integer value x 10 ^{no} of DPs
116	HART Device 15 HART Status
117	HART Device 15 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
118	HART Device 15 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
119	Not assigned
120	HART Device 16 PV integer value x 10 ^{no} of DPs
121	HART Device 16 SV integer value x 10 ^{no} of DPs
122	HART Device 16 TV integer value x 10 ^{no} of DPs
123	HART Device 16 FV integer value x 10 ^{no} of DPs
124	HART Device 16 HART Status
125	HART Device 16 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
126	HART Device 16 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
127	HCS Status
256-257	HART Device 1 PV floating point value stored in 2x16-bit register per float
258-259	HART Device 1 SV floating point value stored in 2x16-bit register per float
260-261	HART Device 1 TV floating point value stored in 2x16-bit register per float
262-263	HART Device 1 FV floating point value stored in 2x16-bit register per float
264-265	HART Device 2 PV floating point value stored in 2x16-bit register per float
266-267	HART Device 2 SV floating point value stored in 2x16-bit register per float
268-269	HART Device 2 TV floating point value stored in 2x16-bit register per float
270-271	HART Device 2 FV floating point value stored in 2x16-bit register per float
272-273	HART Device 3 PV floating point value stored in 2x16-bit register per float
274-275	HART Device 3 SV floating point value stored in 2x16-bit register per float
276-277	HART Device 3 TV floating point value stored in 2x16-bit register per float
278-279	HART Device 3 FV floating point value stored in 2x16-bit register per float
280-281	HART Device 4 PV floating point value stored in 2x16-bit register per float
282-283	HART Device 4 SV floating point value stored in 2x16-bit register per float
284-285	HART Device 4 TV floating point value stored in 2x16-bit register per float
286-287	HART Device 4 FV floating point value stored in 2x16-bit register per float
288-289	HART Device 5 PV floating point value stored in 2x16-bit register per float
290-291	HART Device 5 SV floating point value stored in 2x16-bit register per float
292-293	HART Device 5 TV floating point value stored in 2x16-bit register per float
294-295	HART Device 5 FV floating point value stored in 2x16-bit register per float
296-297	HART Device 6 PV floating point value stored in 2x16-bit register per float
298-299	HART Device 6 SV floating point value stored in 2x16-bit register per float
300-301	HART Device 6 TV floating point value stored in 2x16-bit register per float
302-303	HART Device 6 FV floating point value stored in 2x16-bit register per float

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Table 4. Continued

Register Range	Description
304-305	HART Device 7 PV floating point value stored in 2x16-bit register per float
306-307	HART Device 7 SV floating point value stored in 2x16-bit register per float
308-309	HART Device 7 TV floating point value stored in 2x16-bit register per float
310-311	HART Device 7 FV floating point value stored in 2x16-bit register per float
312-313	HART Device 8 PV floating point value stored in 2x16-bit register per float
314-315	HART Device 8 SV floating point value stored in 2x16-bit register per float
316-317	HART Device 8 TV floating point value stored in 2x16-bit register per float
318-319	HART Device 8 FV floating point value stored in 2x16-bit register per float
320-321	HART Device 9 PV floating point value stored in 2x16-bit register per float
322-323	HART Device 9 SV floating point value stored in 2x16-bit register per float
324-325	HART Device 9 TV floating point value stored in 2x16-bit register per float
326-327	HART Device 9 FV floating point value stored in 2x16-bit register per float
328-329	HART Device 10 PV floating point value stored in 2x16-bit register per float
330-331	HART Device 10 SV floating point value stored in 2x16-bit register per float
332-333	HART Device 10 TV floating point value stored in 2x16-bit register per float
334-335	HART Device 10 FV floating point value stored in 2x16-bit register per float
336-337	HART Device 11 PV floating point value stored in 2x16-bit register per float
338-339	HART Device 11 SV floating point value stored in 2x16-bit register per float
340-341	HART Device 11 TV floating point value stored in 2x16-bit register per float
342-343	HART Device 11 FV floating point value stored in 2x16-bit register per float
344-345	HART Device 12 PV floating point value stored in 2x16-bit register per float
346-347	HART Device 12 SV floating point value stored in 2x16-bit register per float
348-349	HART Device 12 TV floating point value stored in 2x16-bit register per float
350-351	HART Device 12 FV floating point value stored in 2x16-bit register per float
352-353	HART Device 13 PV floating point value stored in 2x16-bit register per float
354-355	HART Device 13 SV floating point value stored in 2x16-bit register per float
356-357	HART Device 13 TV floating point value stored in 2x16-bit register per float
358-359	HART Device 13 FV floating point value stored in 2x16-bit register per float
360-361	HART Device 14 PV floating point value stored in 2x16-bit register per float
362-363	HART Device 14 SV floating point value stored in 2x16-bit register per float
364-365	HART Device 14 TV floating point value stored in 2x16-bit register per float
366-367	HART Device 14 FV floating point value stored in 2x16-bit register per float
368-369	HART Device 15 PV floating point value stored in 2x16-bit register per float
370-371	HART Device 15 SV floating point value stored in 2x16-bit register per float
372-373	HART Device 15 TV floating point value stored in 2x16-bit register per float
374-375	HART Device 15 FV floating point value stored in 2x16-bit register per float
376-377	HART Device 16 PV floating point value stored in 2x16-bit register per float
378-379	HART Device 16 SV floating point value stored in 2x16-bit register per float
380-381	HART Device 16 TV floating point value stored in 2x16-bit register per float
382-383	HART Device 16 FV floating point value stored in 2x16-bit register per float

Continued on next page

Table 4. *Continued*

Register Range	Description
512-517	HART Device 1 additional status byte 0 to byte 5
518-523	HART Device 2 additional status byte 0 to byte 5
524-529	HART Device 3 additional status byte 0 to byte 5
530-535	HART Device 4 additional status byte 0 to byte 5
536-541	HART Device 5 additional status byte 0 to byte 5
542-547	HART Device 6 additional status byte 0 to byte 5
548-553	HART Device 7 additional status byte 0 to byte 5
554-559	HART Device 8 additional status byte 0 to byte 5
560-565	HART Device 9 additional status byte 0 to byte 5
566-571	HART Device 10 additional status byte 0 to byte 5
572-577	HART Device 11 additional status byte 0 to byte 5
578-583	HART Device 12 additional status byte 0 to byte 5
584-589	HART Device 13 additional status byte 0 to byte 5
590-595	HART Device 14 additional status byte 0 to byte 5
596-601	HART Device 15 additional status byte 0 to byte 5
602-607	HART Device 16 additional status byte 0 to byte 5

Table 4.1. *Additional status byte 0 to byte 24 (for software version 5.0 and greater).*

Register Range	Description
1000-1024	HART Device 1 additional status byte 0 to byte 24
1025-1049	HART Device 2 additional status byte 0 to byte 24
1050-1074	HART Device 3 additional status byte 0 to byte 24
1075-1099	HART Device 4 additional status byte 0 to byte 24
1100-1124	HART Device 5 additional status byte 0 to byte 24
1125-1149	HART Device 6 additional status byte 0 to byte 24
1150-1174	HART Device 7 additional status byte 0 to byte 24
1175-1199	HART Device 8 additional status byte 0 to byte 24
1200-1224	HART Device 9 additional status byte 0 to byte 24
1225-1249	HART Device 10 additional status byte 0 to byte 24
1250-1274	HART Device 11 additional status byte 0 to byte 24
1275-1299	HART Device 12 additional status byte 0 to byte 24
1300-1324	HART Device 13 additional status byte 0 to byte 24
1325-1349	HART Device 14 additional status byte 0 to byte 24
1350-1374	HART Device 15 additional status byte 0 to byte 24
1375-1399	HART Device 16 additional status byte 0 to byte 24

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Table 5. *HCS Status Word Bits

Status Word Bit	Type	Description
0	Error	Configuration data error
1	Error	No HART communications
2	Error	EEPROM blank
3	Error	EEPROM failure
4	Status	Slave device malfunction
5	Error	Burst mode failure
6	Error	Software watchdog failure
7	Error	COP watchdog failure
8	Status	Slave device analog output fixed
10	Error	Software fail
11	Status	Device offline
12	Not Used	N/A
13	Not Used	N/A
14	Not Used	N/A
15	Error	Configuration data area checksum error

*HCS Status is read by accessing register 127

HART Status Information

The following tables describe HART status information.

Table 6. MSB: HART First Byte

MSB	Bit/Code	Description
Bit 7 = 1 Communications Error (The byte consists of error FLAGS)	0	0 (undefined)
	1	RX buffer overflow
	2	0 (reserved)
	3	Checksum error
	4	Framing error
	5	Overrun error
	6	Parity error
Bit 7 = 0 Command Response (The byte is a numerical value representing a single error condition)	0	No error
	1	Undefined
	2	Invalid selection
	3	Passed parameter too large
	4	Passed parameter too small
	5	Too few bytes received
	6	Device specific command error
	7	In write protect mode
	8-15	Multiple meanings
	16	Access restricted
	28	Multiple meanings
	32	Device is busy
	64	Command not implemented

Table 7. LSB: HART Second Byte

MSB	Bit	Description
0 not used when a communications error exists	0	Primary variable out of limits
	1	Non primary variable out of limits
	2	Analog output saturated
	3	Analog output current fixed
	4	More status available
	5	Cold start
	6	Configuration changed
	7	Field device malfunction

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Installation

Installation consists of physically mounting the unit, grounding the instrument, and completing the electrical connections.

Mounting the HCS

The HCS is designed to snap easily onto 32mm, G-type (EN50035) or 35mm Top Hat (EN50022) DIN rails.

Making the Electrical Connections

After mounting, you are ready to connect the HCS to the loop. Each unit comes equipped with a transmitter excitation terminal which allows it to supply power to the monitored HART instrument, if necessary. Figures 2 and 3 shows the connection diagram for an HCS.

Recommended Ground Wiring Practices

Moore Industries recommends the following ground wiring practices:

- Any Moore Industries product in a metal case or housing should be grounded.
- The protective earth conductor must be connected to a system safety earth ground before making other connections.
- All input signals to, and output signals from, Moore Industries' products should be wired using a shielded, twisted pair wiring technique. Shields should be connected to an earth or safety ground.
- For the best shielding, the shield should be run all the way from the signal source to the receiving device. (see Note below)
- The maximum length of unshielded input and output signal wiring should be 2 inches.

Note:

Some of Moore Industries' instruments can be classified as receivers (IPT2, IPX2, etc.) and some can be classified as transmitters (TRX, TRY, etc.) while some are both a receiver and a transmitter (SPA2, HIM, etc). Hence, your shield ground connections should be appropriate for the type of signal line being shielded. The shield should be grounded at the receiver and not at the signal source.

CE Conformity

Installation of any Moore Industries' products that carry CE certification (Commission Electrotechnique) **must** adhere to the guidelines in *Recommended Ground Wiring Practices* (above) in order to meet the requirements set forth in applicable EMC (Electromagnetic Compatibility) directives 2004/108/EC, EN 61326. Consult the factory for the most current information on products that have been CE certified.

Power Sourcing Parameters for General Locations, Intrinsically Safe and Non-Incendive/Type N Applications

The input terminals must be connected to and/or supplied from a certified energy limiting Class 2 or a Separate Extra Low Voltage (S.E.L.V.) power supply separated from all mains by double/reinforced insulation.

Operation

Once programmed, calibrated, installed, and supplied with the correct power, the HCS begins to operate immediately. Depending upon environmental conditions, it can be expected to operate unattended for extended periods of time.

There are 2 leds which indicate the unit and input status:

INPUT LED: (Red/Green)

Green: Input is present and normal

Red: Input signal is not found

Red/Green blinking: HCS is in burst mode but the slave is not

READY LED: (Red/Green)

Green: Instrument is ready for operation and configuration

Red: Instrument has encountered an internal problem

Maintenance

Moore Industries suggests a check for terminal tightness and general unit condition every 6-8 months. Always adhere to any site requirements for programmed maintenance.

Customer Support

Moore Industries is recognized as the industry leader in delivering top quality to its customers in products and services. We perform a battery of stringent

quality assurance checks on every unit we ship. If any Moore Industries product fails to perform up to rated specifications, call us for help. Our highly skilled staff of trained technicians and engineers pride themselves on their ability to provide timely, accurate, and practical answers to your process instrumentation questions.

Factory phone numbers are listed on the back cover of this manual.

If problems involve a particular HCS, there are several pieces of information that can be gathered before you call the factory that will help our staff get the answers you need in the shortest time possible. For fastest service, gather the complete model and serial number(s) of the problem unit(s) and the job number of the original sale.

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Appendix A: Configuring the Legacy HCS

The following information applies only to units with firmware version older than 4.0.

Note:

All tables and figures within Appendix A relate only to this Appendix A unless otherwise mentioned.

Configuring the HCS

One of the benefits of the HCS is that there are no internal or external controls to adjust or settings to change. All operating parameters are set using the PC Configuration software.

Once these software settings are made, they are downloaded to the instrument in the form of a Configuration File and stored in the unit's nonvolatile memory. You can choose to save a backup copy of the file on your PC hard drive or external media. The HCS communicates with the PC through a proprietary communications cable to the PC's serial (COM) port or optional proprietary USB cable to the PC's USB port.

Installing the Configuration Software

Refer to Table 2 for the equipment needed.

1. Insert the *Moore Industries Interface Solution PC Configuration Software* CD into the CD drive of the PC. Access the CD and open the "HCS PC Configuration Software" folder.
2. Double-click the installation program located in the folder. Follow the prompts to correctly install the program.

Once the Configuration Program is installed onto your PC, the HCS can be connected into a system and become operational.

Connecting the HCS to the PC

HCS can be connect to PC one of two ways:

- using the proprietary communications cable to connect to PC's serial (COM) port
- using the optional proprietary USB cable to connect to PC's USB port

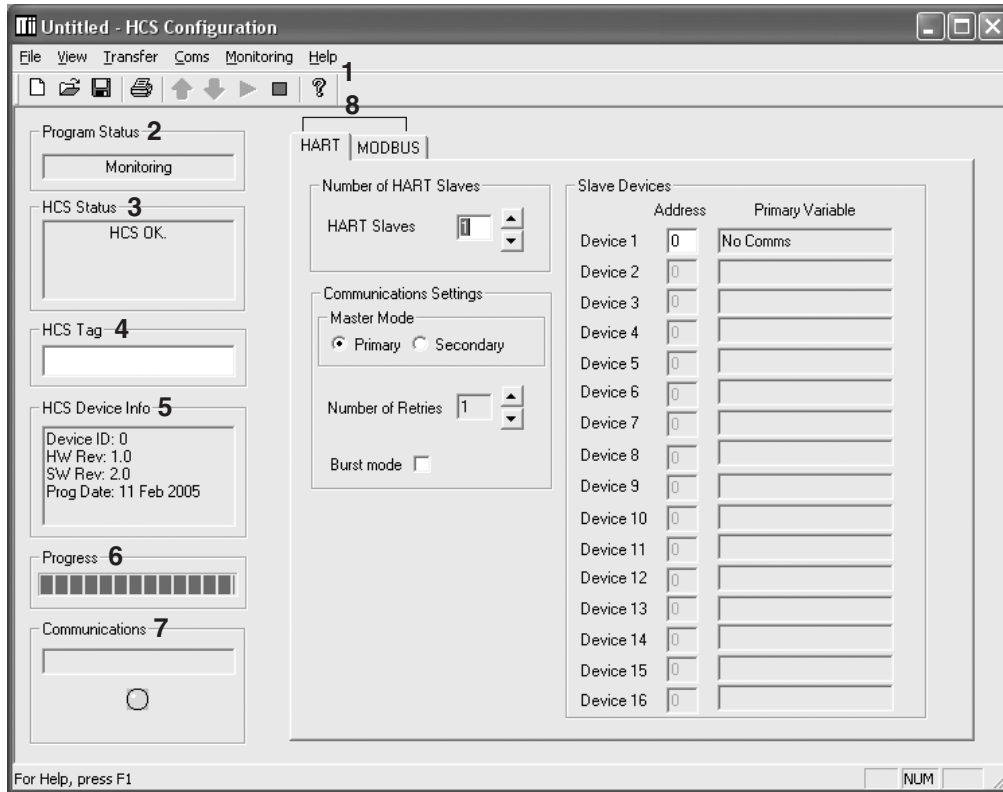
See Table 1 for information on the necessary equipment.

Table 1. Necessary Equipment Table

Device	Specifications
Power Supply	24Vdc, ±10%
Personal Computer	80386-based (or faster) IBM PC, or 100% compatible (Pentium recommended); CD Drive 4Mb free RAM; 16Mb recommended 20Mb free disk space on hard drive Microsoft Windows® 2000, NT, XP, Vista, or 7 with Internet Explorer 4.0+ or Microsoft Windows® NT with Service Pack 3 or greater 1 (one) serial port (COM 1, 2, 3 or 4) or USB port
Moore Industries PC Configuration Software	Version 1.0 or higher, successfully installed onto the hard drive
Communication Cable	Part# 803-053-26 or optional USB cable Part#208-836-00

PC Configuration Software Summary

Figure 1. HCS PC Configuration Software Screen



The HCS PC Configuration Software can be used to program all of the instrument's parameters. Once the default configuration has been saved to disk, it is safe to program other parameters.

The PC Software is composed of these sections:










- 1. Menu Bar/Tool Bar**—Dropdown menus and corresponding icons allow you to perform various functions throughout the PC Configuration Program. *Refer to the Menu and Tool Bar Legend* section for a complete description.
- 2. Program Status**—This portion of the program displays the activity (idle, monitoring, downloading, uploading) of the connected unit.
- 3. HCS Status**—Notifies of any errors or conditions which are outside of the tolerance range. Displays *HCS OK* if the unit is operating normally.

- 4. HCS Tag**—A phrase used to identify an HCS (eight alphanumeric characters, maximum).
- 5. HCS Device Info**—Displays the individual characteristics of the attached HCS, such as the device ID, hardware and software revisions and the last date that the device was programmed.
- 6. Progress**—This bar stays in motion any time the HCS is monitoring, uploading or downloading, to notify that a process is occurring.
- 7. Communications**—Indicates current PC connection/communications status.
- 8. HART/MODBUS Tabs**—These tabs change the right side of the screen to allow you to set the appropriate part of the HCS's configuration. See corresponding sections of this manual for additional information on these tabs.

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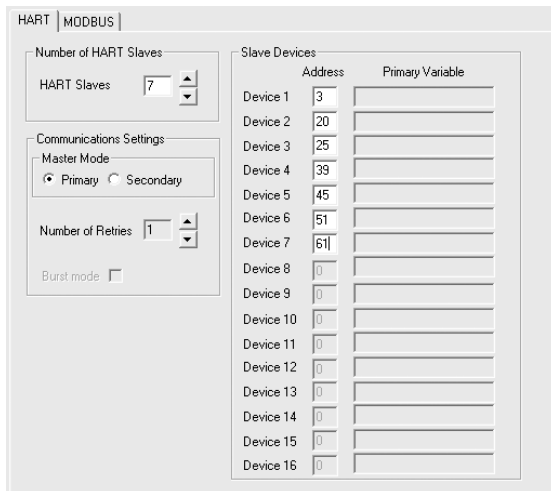
Menu and Tool Bar Legend

		Allows such functions as New, Open, Save and Print
		Controls whether Tool and Status Bars are viewed on the screen
		Allows you to Upload and Download configurations
		Select the PC Port (Com Port) that you will use
		Allows you to Monitor and Stop monitoring processes
		Displays the version of the HCS Configuration Program

Configuration Screens

HART

Figure 2. HART Tab



The screenshot shows the HART configuration interface. On the left, there are settings for 'Number of HART Slaves' (set to 7), 'Communications Settings' (Master Mode: Primary selected, Secondary unselected), 'Number of Retries' (set to 1), and 'Burst mode' (unchecked). On the right, there is a table for 'Slave Devices' with columns for 'Address' and 'Primary Variable'.

Device	Address	Primary Variable
Device 1	3	
Device 2	20	
Device 3	25	
Device 4	39	
Device 5	45	
Device 6	51	
Device 7	61	
Device 8	0	
Device 9	0	
Device 10	0	
Device 11	0	
Device 12	0	
Device 13	0	
Device 14	0	
Device 15	0	
Device 16	0	

Number of HART Slaves

Using the up and down arrows, select the number of HART slaves (16 maximum) that you will introduce into your loop. The number of slaves you have chosen will appear as enabled in the *Slave Devices* parameter.

Slave Devices

Once you have selected the number of HART slaves to be used in the loop, use this section to assign a specific address for each device. Ensure that the address matches the address of the slave you connected in the loop. Each must be a unique address between zero and 63. However, Address 0 is an analog address and is not used in HART multi-drop loops. Current readings at this address can vary from 3.6mA to 23.6mA.

Master Mode

The HART protocol allows for two communications masters on the loop: a Primary Master and a Secondary Master. Setting the HCS to function as the Primary HART Master in the application means that any other HART device in the loop must be configured either as a HART Secondary Master (1 per loop), or as a HART Slave (up to 16 per loop). Conversely, setting the HCS to function as the Secondary HART Master allows other HART devices to function either as a Primary Master, or as Slaves. Configuring more than one device on a single loop as a Primary or Secondary HART Master will cause a communications failure.

Note:

A HART hand-held communicator is typically a Secondary Master.

Number of Retries

The *Number of Retries* can be set between 1 and 3, and will determine how many times the HCS will attempt to poll the HART transmitter (without success), before it indicates a HART communication failure.

Burst Mode

Allows selection of Normal or Burst modes.

Burst mode may be enabled if there is only one slave in the loop.

The HCS can operate in one of two modes: *Normal* or *Burst*. In each of these modes the HCS attempts to find a HART transmitter.

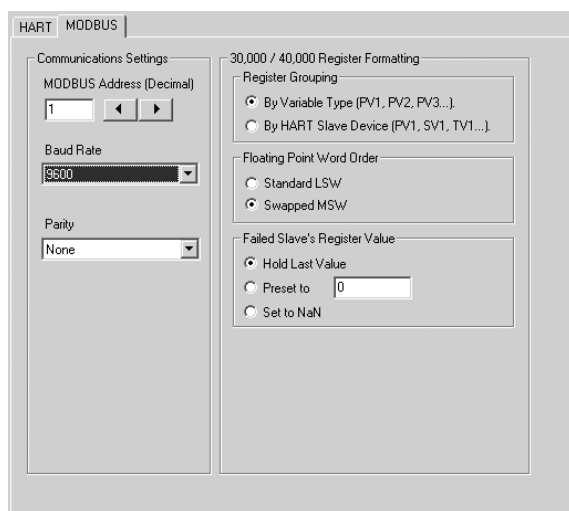
In *Normal* mode, the HCS polls the HART loop for a transmitter, then polls the HART instrument twice per second, requesting the current process status and the HART instrument's diagnostic status. The HART instrument responds with the requested data.

In *Burst* mode, the monitored HART instrument is programmed to continuously transmit its process variable and health status. The HCS samples the continuous HART data three times per second.

The instrument will operate in Normal Mode by default. Selecting the *Burst Mode* button will enable Burst Mode.

MODBUS

Figure 3. MODBUS Tab



The *MODBUS* tab allows you to set the MODBUS communications parameters.

Communications Settings

The Communications Settings include three areas:

MODBUS Address (Decimal)

The *MODBUS Address* is the number that the HCS monitor uses to identify itself on the MODBUS network. The MODBUS address is configurable from 1 to 247. By default, it will assume a MODBUS address of 1.

Baud Rate

The *Baud Rate* is the speed of MODBUS data transmission. It should be set to match the baud rate of the attached controller. The interface supports the following baud rates: 300, 600, 1200, 2400, 4800, 9600 and 19200.

Parity

The HART monitor supports even, odd and no *Parity*. The data format is one start bit, 8 data bits and one stop bit.

30,000 / 40,000 Register Formatting

This section includes the following areas:

Register Grouping

This allows you to select the manner in which to group the MODBUS registers.

Selecting *By Variable Type*, the registers are grouped in order of *variables*, i.e. all primary variables (PV) are grouped together, followed by secondary variables (SV), third (TV) and then fourth (FV).

Using *By HART Slave Device* grouping places your registers in order *numerically*. It groups a HART slave device's variables in contiguous registers. For example, your first HART device's primary, secondary, third and fourth variables (PV1, SV1, TV1 and FV1) are grouped together. Next in the order are your second HART device's primary, secondary, third and fourth variables (PV2, SV2, TV2 and FV2) and so on.

Floating Point Word Order

By default, the HART Concentrator will use the *Standard LSW* (least significant word) floating point word order format. This stores the most significant bits in the second register and the least significant bits in the first register. Selecting *Swapped MSW* (*most significant word*) will reverse the order, storing the most significant bits in the first register and the least significant bits in the second register.

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Failed Slave's Register Value

You may select what would occur to a slave device's register value in the event that communication is lost with the HCS.

If selecting *Hold Last Value* and a failure is detected, the last measured value before the failure occurred is held.

Entering a user-set value in the *Preset to* text box recalls that value when a slave device failure is detected.

Selecting *NaN* (Not a Number—as put forth by the IEEE-754 standard) causes the floating point NaN value to be stored in the registers used for holding floating point values.

MODBUS Register Definitions

Tables 2-5 define the MODBUS input and holding register assignment. These tables are zero based, your MODBUS host may require you to enter the MODBUS register. Often registers have an offset of “1” from the MODBUS address. For example, a MODBUS address listed below of 256 may have to be entered as 257 in your host. Please refer to your MODBUS host documentation for verification.

KEY:	
FV = Fourth Variable	SV = Secondary Variable
LSB = Least Significant Bit	TV = Third Variable
MSB = Most Significant Bit	*UOM = Unit of Measurement
PV = Primary Variable	

*Moore Industries provides up to 169 HART Engineering Units listed in HART Communication Foundation document number: HCF-SPEC-183, Revision 14.0, Release Date 29 January 2004

Table 2. Register ranges and descriptions when MODBUS registers are grouped by the “By Variable Type” parameter and only the PV and SV are being stored (when nine or more slaves are in the loop)

Register Range	Description
32-47	HART Device 1-16 HART Status
48-63	HART Device 1-16 PV + SV UOM (PV UOM = MSB, SV UOM = LSB)
64	HCS Status
256-287	HART Device 1-16 PV floating point value stored in 2x16-bit registers per float
288-319	HART Device 1-16 SV floating point value stored in 2x16-bit registers per float

Table 3. Register ranges and descriptions when MODBUS registers are grouped by the “By Variable Type” parameter and all dynamic variables are being stored (when eight or fewer slaves are in the loop)

Register Range	Description
32-39	HART Device 1-8 HART Status
40-47	HART Device 1-8 PV + SV UOM (PV UOM = MSB, SV UOM = LSB)
48-55	HART Device 1-8 TV + FV UOM (TV UOM = MSB, FV UOM = LSB)
64	HCS Status
256-271	HART Device 1-8 PV floating point value stored in 2x16-bit registers per float
272-287	HART Device 1-8 SV floating point value stored in 2x16-bit registers per float
288-303	HART Device 1-8 TV floating point value stored in 2x16-bit registers per float
304-319	HART Device 1-8 FV floating point value stored in 2x16-bit registers per float

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Table 4. Register ranges and descriptions when MODBUS registers are grouped by the “By HART Slave Device” parameter (when nine or more slaves are in the loop)

Register Range	Description
2	HART Device 1 HART Status
3	HART Device 1 UOM (PV UOM = MSB, SV UOM = LSB)
6	HART Device 2 HART Status
7	HART Device 2 UOM (PV UOM = MSB, SV UOM = LSB)
10	HART Device 3 HART Status
11	HART Device 3 UOM (PV UOM = MSB, SV UOM = LSB)
14	HART Device 4 HART Status
15	HART Device 4 UOM (PV UOM = MSB, SV UOM = LSB)
18	HART Device 5 HART Status
19	HART Device 5 UOM (PV UOM = MSB, SV UOM = LSB)
22	HART Device 6 HART Status
23	HART Device 6 UOM (PV UOM = MSB, SV UOM = LSB)
26	HART Device 7 HART Status
27	HART Device 7 UOM (PV UOM = MSB, SV UOM = LSB)
30	HART Device 8 HART Status
31	HART Device 8 UOM (PV UOM = MSB, SV UOM = LSB)
34	HART Device 9 HART Status
35	HART Device 9 UOM (PV UOM = MSB, SV UOM = LSB)
38	HART Device 10 HART Status
39	HART Device 10 UOM (PV UOM = MSB, SV UOM = LSB)
42	HART Device 11 HART Status
43	HART Device 11 UOM (PV UOM = MSB, SV UOM = LSB)
46	HART Device 12 HART Status
47	HART Device 12 UOM (PV UOM = MSB, SV UOM = LSB)
50	HART Device 13 HART Status
51	HART Device 13 UOM (PV UOM = MSB, SV UOM = LSB)
54	HART Device 14 HART Status
55	HART Device 14 UOM (PV UOM = MSB, SV UOM = LSB)
58	HART Device 15 HART Status
59	HART Device 15 UOM (PV UOM = MSB, SV UOM = LSB)
62	HART Device 16 HART Status
63	HART Device 16 UOM (PV UOM = MSB, SV UOM = LSB)
64	HCS Status
256-257	HART Device 1 PV floating point value stored in 2x16-bit register per float
258-259	HART Device 1 SV floating point value stored in 2x16-bit register per float
260-261	HART Device 2 PV floating point value stored in 2x16-bit register per float
262-263	HART Device 2 SV floating point value stored in 2x16-bit register per float
264-265	HART Device 3 PV floating point value stored in 2x16-bit register per float

Continued on next page

Table 4. Continued

266-267	HART Device 3 SV floating point value stored in 2x16-bit register per float
268-269	HART Device 4 PV floating point value stored in 2x16-bit register per float
270-271	HART Device 4 SV floating point value stored in 2x16-bit register per float
272-273	HART Device 5 PV floating point value stored in 2x16-bit register per float
274-275	HART Device 5 SV floating point value stored in 2x16-bit register per float
276-277	HART Device 6 PV floating point value stored in 2x16-bit register per float
278-279	HART Device 6 SV floating point value stored in 2x16-bit register per float
280-281	HART Device 7 PV floating point value stored in 2x16-bit register per float
282-283	HART Device 7 SV floating point value stored in 2x16-bit register per float
284-285	HART Device 8 PV floating point value stored in 2x16-bit register per float
286-287	HART Device 8 SV floating point value stored in 2x16-bit register per float
288-289	HART Device 9 PV floating point value stored in 2x16-bit register per float
290-291	HART Device 9 SV floating point value stored in 2x16-bit register per float
292-293	HART Device 10 PV floating point value stored in 2x16-bit register per float
294-295	HART Device 10 SV floating point value stored in 2x16-bit register per float
296-297	HART Device 11 PV floating point value stored in 2x16-bit register per float
298-299	HART Device 11 SV floating point value stored in 2x16-bit register per float
300-301	HART Device 12 PV floating point value stored in 2x16-bit register per float
302-303	HART Device 12 SV floating point value stored in 2x16-bit register per float
304-305	HART Device 13 PV floating point value stored in 2x16-bit register per float
306-307	HART Device 13 SV floating point value stored in 2x16-bit register per float
308-309	HART Device 14 PV floating point value stored in 2x16-bit register per float
310-311	HART Device 14 SV floating point value stored in 2x16-bit register per float
312-313	HART Device 15 PV floating point value stored in 2x16-bit register per float
314-315	HART Device 15 SV floating point value stored in 2x16-bit register per float
316-317	HART Device 16 PV floating point value stored in 2x16-bit register per float
318-319	HART Device 16 SV floating point value stored in 2x16-bit register per float

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Table 5. Register ranges and descriptions when MODBUS registers are grouped by the “By HART Slave Device” parameter and PV, SV, TV and FV are being stored (when eight or fewer slaves are in the loop)

Register Range	Description
4	HART Device 1 HART Status
5	HART Device 1 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
6	HART Device 1 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
7	Not assigned
12	HART Device 2 HART Status
13	HART Device 2 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
14	HART Device 2 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
15	Not assigned
20	HART Device 3 HART Status
21	HART Device 3 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
22	HART Device 3 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
23	Not assigned
28	HART Device 4 HART Status
29	HART Device 4 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
30	HART Device 4 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
31	Not assigned
36	HART Device 5 HART Status
37	HART Device 5 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
38	HART Device 5 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
39	Not assigned
44	HART Device 6 HART Status
45	HART Device 6 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
46	HART Device 6 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
47	Not assigned
52	HART Device 7 HART Status
53	HART Device 7 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
54	HART Device 7 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
55	Not assigned
60	HART Device 8 HART Status
61	HART Device 8 PV & SV UOM (PV UOM = MSB, SV UOM = LSB)
62	HART Device 8 TV & FV UOM (TV UOM = MSB, FV UOM = LSB)
63	Not assigned
64	HCS Status
256-257	HART Device 1 PV floating point value stored in 2x16-bit register per float
258-259	HART Device 1 SV floating point value stored in 2x16-bit register per float
260-261	HART Device 1 TV floating point value stored in 2x16-bit register per float
262-263	HART Device 1 FV floating point value stored in 2x16-bit register per float
264-265	HART Device 2 PV floating point value stored in 2x16-bit register per float

Continued on next page

Table 5. Continued

266-267	HART Device 2 SV floating point value stored in 2x16-bit register per float
268-269	HART Device 2 TV floating point value stored in 2x16-bit register per float
270-271	HART Device 2 FV floating point value stored in 2x16-bit register per float
272-273	HART Device 3 PV floating point value stored in 2x16-bit register per float
274-275	HART Device 3 SV floating point value stored in 2x16-bit register per float
276-277	HART Device 3 TV floating point value stored in 2x16-bit register per float
278-279	HART Device 3 FV floating point value stored in 2x16-bit register per float
280-281	HART Device 4 PV floating point value stored in 2x16-bit register per float
282-283	HART Device 4 SV floating point value stored in 2x16-bit register per float
284-285	HART Device 4 TV floating point value stored in 2x16-bit register per float
286-287	HART Device 4 FV floating point value stored in 2x16-bit register per float
288-289	HART Device 5 PV floating point value stored in 2x16-bit register per float
290-291	HART Device 5 SV floating point value stored in 2x16-bit register per float
292-293	HART Device 5 TV floating point value stored in 2x16-bit register per float
294-295	HART Device 5 FV floating point value stored in 2x16-bit register per float
296-297	HART Device 6 PV floating point value stored in 2x16-bit register per float
298-299	HART Device 6 SV floating point value stored in 2x16-bit register per float
300-301	HART Device 6 TV floating point value stored in 2x16-bit register per float
302-303	HART Device 6 FV floating point value stored in 2x16-bit register per float
304-305	HART Device 7 PV floating point value stored in 2x16-bit register per float
306-307	HART Device 7 SV floating point value stored in 2x16-bit register per float
308-309	HART Device 7 TV floating point value stored in 2x16-bit register per float
310-311	HART Device 7 FV floating point value stored in 2x16-bit register per float
312-313	HART Device 8 PV floating point value stored in 2x16-bit register per float
314-315	HART Device 8 SV floating point value stored in 2x16-bit register per float
316-317	HART Device 8 TV floating point value stored in 2x16-bit register per float
318-319	HART Device 8 FV floating point value stored in 2x16-bit register per float

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Table 6. HCS Status Word Bits

Status Word Bit	Type	Description
0	Error	Configuration data error
1	Error	No HART communications
2	Error	EEPROM blank
3	Error	EEPROM failure
4	Status	Slave device malfunction
5	Error	Burst mode failure
6	Error	Software watchdog failure
7	Error	COP watchdog failure
8	Status	Slave device analog output fixed
10	Error	Software fail
11	Status	Device offline
12	Not Used	N/A
13	Not Used	N/A
14	Not Used	N/A
15	Error	Configuration data area checksum error

Table 7. Slave Device Status

Status Bit	Description
0	Primary variable out of limits
1	Non-Primary variable out of limits
2	Analog output #1 saturated
3	Analog output #1 fixed
4	More status available
5	Cold start
6	Configuration changed
7	Field device malfunction

EC Declaration of Conformity

Moore Industries-International, Inc.
16650 Schoenborn Street
North Hills, CA 91343-6196 U.S.A.

Date Issued: 04 Dec. 2013
No. 100-100-247 Rev. B
Page 1 of 1

Equipment Description:

HART Concentrator System (HART-to-MODBUS RTU Converter)
Model HCS / * / * / * / * / *

* Indicates any input, output, power, options and housing as stated in the product data sheet.

Directive:

2004/108/EC (EMC)

Specifications Conformed To:

EN 61326-1:2006 Electrical equipment for measurement, control and laboratory use - EMC requirements

On Behalf of Moore Industries-International, Inc., I declare that, on the date the equipment accompanied by this declaration is placed on the market, the equipment conforms with all technical and regulatory requirements of the above listed directives.

Signature:



Deanna Esterwold, Quality Manager

RETURN PROCEDURES

To return equipment to Moore Industries for repair, follow these four steps:

1. Call Moore Industries and request a Returned Material Authorization (RMA) number.

Warranty Repair –

If you are unsure if your unit is still under warranty, we can use the unit's serial number to verify the warranty status for you over the phone. Be sure to include the RMA number on all documentation.

Non-Warranty Repair –

If your unit is out of warranty, be prepared to give us a Purchase Order number when you call. In most cases, we will be able to quote you the repair costs at that time. The repair price you are quoted will be a "Not To Exceed" price, which means that the actual repair costs may be less than the quote. Be sure to include the RMA number on all documentation.

2. Provide us with the following documentation:
 - a) A note listing the symptoms that indicate the unit needs repair
 - b) Complete shipping information for return of the equipment after repair
 - c) The name and phone number of the person to contact if questions arise at the factory
3. Use sufficient packing material and carefully pack the equipment in a sturdy shipping container.
4. Ship the equipment to the Moore Industries location nearest you.

The returned equipment will be inspected and tested at the factory. A Moore Industries representative will contact the person designated on your documentation if more information is needed. The repaired equipment, or its replacement, will be returned to you in accordance with the shipping instructions furnished in your documentation.

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RETURN POLICY

For a period of thirty-six (36) months from the date of shipment, and under normal conditions of use and service, Moore Industries ("The Company") will at its option replace, repair or refund the purchase price for any of its manufactured products found, upon return to the Company (transportation charges prepaid and otherwise in accordance with the return procedures established by The Company), to be defective in material or workmanship. This policy extends to the original Buyer only and not to Buyer's customers or the users of Buyer's products, unless Buyer is an engineering contractor in which case the policy shall extend to Buyer's immediate customer only. This policy shall not apply if the product has been subject to alteration, misuse, accident, neglect or improper application, installation, or operation. THE COMPANY SHALL IN NO EVENT BE LIABLE FOR ANY INCIDENTAL OR CONSEQUENTIAL DAMAGES.



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