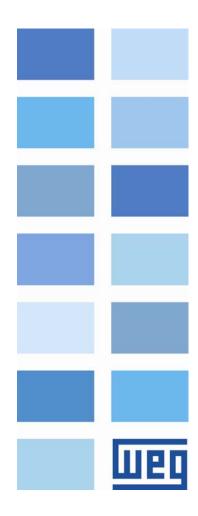
Modbus RTU

CFW100

User's Manual





Modbus RTU User's Manual

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ABOUT THIS MANUAL

This manual supplies the necessary information for the operation of the CFW100 frequency inverter using the Modbus RTU protocol. This manual must be used together with the CFW100 user manual.

ABBREVIATIONS AND DEFINITIONS

ASCII American Standard Code for Information Interchange

CRC Cycling Redundancy Check EIA Electronic Industries Alliance

TIA Telecommunications Industry Association

RTU Remote Terminal Unit

NUMERICAL REPRESENTATION

Decimal numbers are represented by means of digits without suffix. Hexadecimal numbers are represented with the letter 'h' after the number. Binary numbers are represented with the letter 'b' after the number.

DOCUMENTS

The Modbus RTU protocol was developed based on the following specifications and documents:

Document	Version	Source
MODBUS Application Protocol Specification, December	V1.1b	MODBUS.ORG
28th 2006.		
MODBUS Protocol Reference Guide, June 1996.	Rev. J	MODICON
MODBUS over Serial Line, December 20th 2006.	V1.02	MODBUS.ORG

In order to obtain this documentation, consult MODBUS.ORG, which is nowadays the organization that keeps, publishes and updates the information related to the Modbus protocol.



1 INTRODUCTION TO SERIAL COMMUNICATION

In a serial interface the data bits are sent sequentially through a communication channel or bus. Several technologies use the serial communication for data transfer, including the RS232 and RS485 interfaces.

The directions that specify the RS232 and RS485 standards, however, do neither specify the character format, nor its sequence for the data transmission and reception. Therefore, besides the interface, it is also necessary to identify the protocol used for the communication. Among the several existent protocols, one used a lot in the industry is the Modbus RTU protocol.

In the sequence the characteristics of the RS485 and USB serial interfaces available for the product will be presented, as well as the Modbus RTU protocol for the use of this interface.



2 INTERFACE DESCRIPTION

The interfaces for serial communication RS485 or USB available for the CFW100 frequency inverter depend on the selected communication module for the product. Following are presented information about the connection and installation of the equipment, using different communication modules.

2.1 RS485 COMMUNICATION MODULE (CFW100-CRS485)



Figure 2.1: Module with RS485 interface

This plug-in module for the CFW100 frequency inverter has one RS485 interface. This standard RS485 interface has two functions:

- Impoint to Point Connection with remote keypad, via mini USB¹ connector.
- Monnection via RS485 for network operation, via terminals.



NOTE!

Although RS485 communication signal is available on both connectors – mini USB and control terminal – these signals are the same (internally). For this reason, it is not possible to use RS485 interface as command source or reference source and remote keypad at the same time.

2.1.1 RS485 module's connector

The RS485 interface connections are available via control terminal using the following pin assignment:

Table 2.1: RS485 connector pinout for the module (CFW100-CRS485)

Pin	Name	Description				
7	RS485 - B	RS485 (Terminal B)				
6	RS485 - A	RS485 (Terminal A)				
8	0 V	Reference 0 V				
9	PE	Grounding connection				
10	-					

Table 2.2: Configuration of the switches to configure the RS485

Switch Setting	Option		
S1.1 = OFF e S1.2 = OFF	RS485 Termination off		
S1.1 = ON e S1.2 = ON	RS485 Termination on		
S1.1 = OFF e S1.2 = ON	This complimation is not		
S1.1 = ON e S1.2 = OFF	This combination is not allowed		
S1.1 = ON e S1.2 = OFF	allowed		

2.1.2 RS485 Interface Characteristics

- The interface follows the EIA/TIA-485 standard.
- It allows communication baud rates from 9600 up to 38400 Kbit/s.
- The interface is electrically isolated and with differential signal, which grants more robustness against electromagnetic interference.
- It allows the connection of up to 32 devices to the same segment. More devices can be connected by using repeaters².
- A maximum bus length of 1000 meters.

¹ For connections that require distances greater than 3 meters, use remote keypad connection via control terminal.

² The limit of devices that can be connected on the network depends on the protocol used.



2.1.3 Terminating resistor

It is necessary to enable a terminating resistor at both ends of the main bus for each segment of the RS485 network. If the equipment located at both ends of the bus does not have termination resistors, use active terminating to enable these resistors.

2.1.4 Indications

Details on the alarms, communications failures and communication states are made through the keypad (HMI) and product parameters.

2.1.5 Connection with the RS485 Network

The following points must be observed for the connection of the device using the RS485 interface:

- It is recommended the use of a shielded cable with a twisted pair of wires.
- It is also recommended that the cable has one more wire for the connection of the reference signal (GND). In case the cable does not have the additional wire, then the GND signal must be left disconnected.
- The cable must be laid separately (and far away if possible) from the power cables.
- All the network devices must be properly grounded, preferably at the same ground connection. The cable shield must also be grounded.
- Enable the termination resistors only at two points, at the extremes of the main bus, even if there are derivations from the bus.

2.2 USB COMMUNICATION MODULE (CFW100-CUSB)



Figure 2.2: Module with USB connection

For this module, a USB interface with mini-USB conector is available. When connecting the USB interface, it will be recognized as a USB to serial converter, and a virtual COM port will be created³. Thus communication is made with the drive via this COM port.

The USB accessory also provides the connection to the remote keypad via standard RS485 interface.

2.2.1 Indications

Details on the alarms, communications failures and communication states are made through the keypad (HMI) and product parameters.

 $^{^{3}}$ It is necessary to install the USB driver on the CD-ROM supplied with the product. The COM port number created depends on the availability in the operating system and, once connected, consult the hardware resources of the system to identify this port.



3 PROGRAMMING

Next, the CFW100 frequency inverter parameters related to the Modbus RTU communication will be presented.

3.1 SYMBOLS FOR THE PROPERTIES DESCRIPTION

RO Reading only parameter

CFG Parameter that can be changed only with a stopped motor.

P0105 - 1ST/2ND RAMP SELECTION

P0220 - LOCAL/REMOTE SELECTION SOURCE

P0221 - SPEED REFERENCE SELECTION - LOCAL SITUATION

P0222 - SPEED REFERENCE SELECTION - REMOTE SITUATION

P0223 - FORWARD/REVERSE SELECTION - LOCAL SITUATION

P0224 - RUN/STOP SELECTION - LOCAL SITUATION

P0225 – JOG SELECTION – LOCAL SITUATION

P0226 - FORWARD/REVERSE SELECTION - REMOTE SITUATION

P0227 - RUN/STOP SELECTION - REMOTE SITUATION

P0228 – JOG SELECTION – REMOTE SITUATION

These parameters are used in the configuration of the command source for the CFW100 frequency inverter local and remote situations. In order that the device be controlled through the Modbus RTU interface, the options 'serial' available in these parameters, must be selected.

The detailed description of these parameters is found in the CFW100 programming manual.

P0308 - SERIAL ADDRESS

Range: 1 to 247

Properties: CFG

Default: 1

Description:

It allows programming the address used for the inverter serial communication. It is necessary that each device in the network has an address different from all the others.

P0310 - SERIAL BAUD RATE

Range: 0 = 9600 bits/s **Default:** 0

1 = 19200 bits/s2 = 38400 bits/s

Properties: CFG

Description:

It allows programming the baud rate for the serial communication interface, in bits per second. This baud rate must be the same for all the devices connected to the network.



P0311 – SERIAL INTERFACE BYTE CONFIGURATION

Range: 0 = 8 data bits, no parity, 1 stop bit **Default:** 1

1 = 8 data bits, even parity, 1 stop bit 2 = 8 data bits, odd parity, 1 stop bit 3 = 8 data bits, no parity, 2 stop bits 4 = 8 data bits, even parity, 2 stop bits

5 = 8 data bits, odd parity, 2 stop bits

Properties: CFG

Description:

It allows programming the number of data bits, parity and stop bits of the serial interface bytes. This configuration must be identical for all the devices connected to the network.

P0313 - COMMUNICATION ERROR ACTION

Range: 0 = Inactive Default: 1

1 = Disable via Run/Stop2 = Disable via General Enable

3 = Change to Local

4 = Change to Local keeping commands and reference

5 = Causes a Fault

Properties: CFG

Description:

It allows the selection of the action to be executed by the device, if it is controlled via network and a communication error is detected.

Table 3.1: P0313 options

Options	Description				
0 = Inactive	No action is taken and the drive remains in the existing status.				
1 = Disable via Run/Stop	A stop command with deceleration ramp is executed and the motor stops according to the programmed deceleration ramp.				
2 = Disable via General Enable	The drive is disabled by removing the General Enabling and the motor coasts to stop.				
3 = Change to Local	The drive commands change to Local.				
4 = Change to Local keeping commands and reference	The drive commands change to Local, but the status of the enabling and speed reference commands received via network are kept, providing that the drive has been programmed to use in Local mode the commands via HMI.				
5 = Causes a Fault	Instead of an alarm, the communication error causes a drive fault, so that a drive fault reset becomes necessary in order to restore normal operation.				

The following events are considered communication errors:

Serial communication (RS485):

A128 alarm/F228 fault: Serial communication timeout

The actions described in this parameter are executed by means of the automatic writing of the selected actions in the respective bits of the interface control words. Therefore, in order that the commands written in this parameter be effective, it is necessary that the device be programmed to be controlled via the used network interface (with exception of option "Causes a Fault", which blocks the equipment even if it is not controlled by network). This programming is achieved by means of parameters P0220 to P0228.



P0314 - SERIAL WATCHDOG

Range: 0.0 to 999.0s **Default:** 0.0

Properties: CFG

Description:

It allows programming a time limit for the detection of serial interface communication error. If the frequency inverter remains without receiving valid telegrams longer than the time programmed in this parameter, it will be considered that a communication error has occurred, the alarm A128 will be showed on the HMI and the option programmed in P0313 will be executed.

After being powered up, the frequency inverter starts counting this time from the first received valid telegram. The value 0.0 disables this function.

P0316 - SERIAL INTERFACE STATUS

Range: 0 = Inactive Default: -

1 = Active

2 = Watchdog error

Properties: RO

Description:

It allows identifying whether the RS232 or RS485 serial interface board is properly installed, and whether the serial communication presents errors.

Table 3.2: P0316 options

Options	Description					
0 = Inactive	Inactive serial interface. It occurs when the device does not have the RS232 or RS485 board installed					
1 = Active	Installed and acknowledged RS232 or RS485 interface board.					
2 = Watchdog error	The serial interface is active, but a serial communication error has been detected - A128 alarm/F228 fault.					

P0680 – STATUS WORD

Range: 0000h to FFFFh Default: -

Properties: RO

Description:

It allows the device status monitoring. Each bit represents a specific status:

Bits	15	14	13	12	11	10	9	8	7	6	5	4 to 0
Function	Fault condition	Reserved	Undervoltage	LOC/REM	906	Speed direction	Active General Enable	Motor Running	Alarm condition	In configuration mode	Second ramp	Reserved



Table 3.3: P0680 parameter bit functions

Bits	Values
Bits 0 to 4	Reserved.
Bit 5 Second ramp	O: The drive is configured to use the first ramp values, programmed in P0100 and P0101, as the motor acceleration and deceleration ramp times. 1: The drive is configured to use the second ramp values, programmed in P0102 and P0103, as the motor acceleration and deceleration ramp times.
Bit 6 In configuration mode	O: The drive is operating normally. 1: The drive is in the configuration mode. It indicates a special condition during which the drive cannot be enabled: Executing the self-tuning routine Executing the oriented start-up routine Executing the HMI copy function Executing the flash memory card self-guided routine There is a parameter setting incompatibility There is no power at the drive power section
Bit 7 Alarm condition	0: The drive is not in alarm condition. 1: The drive is in alarm condition. Note: The alarm number can be read by means of the parameter P0048 – Present Alarm.
Bit 8 Motor Running	O: The motor is stopped. 1: The drive is running the motor at the set point speed, or executing either the acceleration or the deceleration ramp.
Bit 9 Active General Enable	O: General Enable is not active. 1: General Enable is active and the drive is ready to run the motor.
Bit 10 Speed direction	O: The motor is running in the reverse direction. 1: The motor is running in the forward direction.
Bit 11 JOG	O: Inactive JOG function. 1: Active JOG function.
Bit 12 LOC/REM	O: Drive in Local mode. 1: Drive in Remote mode.
Bit 13 Undervoltage	0: No Undervoltage. 1: With Undervoltage.
Bit 14	Reserved.
Bit 15 Fault condition	0: The drive is not in a fault condition. 1: The drive has detected a fault. Note: The fault number can be read by means of the parameter P0049 – Present Fault.

P0681 - MOTOR SPEED IN 13 BITS

Range: - 32768 to 32767 **Default:** - **Properties:** RO

Description:

It allows monitoring the motor speed. This word uses 13-bit resolution with signal to represent the motor rated frequency (P0403):

■ P0681 = 0000h (0 decimal) \rightarrow motor speed = 0

■ P0681 = 2000h (8192 decimal) → motor speed = rated frequency

Intermediate or higher speed values in rpm can be obtained by using this scale. E.g.60Hz rated frequency if the value read is 2048 (0800h), then, to obtain the speed in Hz one must calculate:

8192 => 60 Hz 2048 => Frequency in Hz

Frequency in Hz = $\frac{60 \times 2048}{8192}$

Frequency in Hz = 15 Hz

Negative values in this parameter indicate that the motor is running in the reverse direction.



P0682 - SERIAL CONTROL WORD

Range: 0000h to FFFFh Default: 0000h

Properties:

Description:

It is the device Modbus RTU interface control word. This parameter can only be changed via serial interface. For the other sources (HMI, etc.) it behaves like a read-only parameter.

In order to have those commands executed, it is necessary to program the equipment to be controlled via serial. This programming is achieved by means of parameters P0105 and P0220 to P0228.

Each bit of this word represents a command that can be executed.

Bits	15 to 8	7	6	5	4	3	2	1	0
Function	Reserved	Fault reset	Reserved	Second ramp	LOC/REM	906	Speed direction	General enable	Run/Stop

Table 3.4: P0682 parameter bit functions

Bits	Values					
Bit 0	0: It stops the motor with deceleration ramp.					
Run/Stop	1: The motor runs according to the acceleration ramp until reaching the speed reference value.					
Bit 1	0: It disables the drive, interrupting the supply for the motor.					
General enable	1: It enables the drive allowing the motor operation.					
Bit 2	0: To run the motor in a direction opposed to the speed reference.					
Speed direction	1: To run the motor in the direction indicated by the speed reference.					
Bit 3	0: It disables the JOG function.					
JOG	1: It enables the JOG function.					
Bit 4	0: The drive goes to the Local mode.					
LOC/REM	1: The drive goes to the Remote mode.					
	0: The drive uses the first ramp values, programmed in P0100 and P0101, as the motor acceleration and					
Bit 5	deceleration ramp times.					
Second ramp	1: The drive is configured to use the second ramp values, programmed in P0102 and P0103, as the motor					
	acceleration and deceleration ramp times.					
Bit 6	Reserved.					
Bit 7	0: No function.					
Fault reset	1: If in a fault condition, then it executes the reset.					
Bits 8 to 15	Reserved.					

P0683 - SERIAL SPEED REFERENCE

Range: -32768 to 32767 **Default:** 0 **Properties:** -

Description:

It allows programming the motor speed reference via the Modbus RTU interface. This parameter can only be changed via serial interface. For the other sources (HMI, etc.) it behaves like a read-only parameter.

In order that the reference written in this parameter be used, it is necessary that the drive be programmed to use the speed reference via serial. This programming is achieved by means of parameters P0221 and P0222.

This word uses a 13-bit resolution with signal to represent the motor rated frequency (P0403).

■ P0683 = 0000h (0 decimal) \rightarrow speed reference = 0

P0683 = 2000h (8192 decimal) → speed reference = rated frequency (P0403)

Intermediate or higher reference values can be programmed by using this scale. E.g.60Hz rated frequency, to obtain a speed reference of 30 Hz one must calculate:



60 Hz => 8192 30 Hz => 13 bit reference

13 bit reference = 30×8192 60

13 bit reference = 4096

=> Value corresponding to 30 Hz in a 13 bit scale

This parameter also accepts negative values to revert the motor speed direction. The reference speed direction, however, depends also on the control word - P0682 - bit 2 setting:

- Bit 2 = 1 and P0683 > 0: reference for forward direction
- Bit 2 = 1 and P0683 < 0: reference for reverse direction
- Bit 2 = 0 and P0683 > 0: reference for reverse direction
- Bit 2 = 0 and P0683 < 0: reference for forward direction



4 MODBUS RTU PROTOCOL

The Modbus RTU protocol was initially developed in 1979. Nowadays, it is a widely spread open protocol, used by several manufactures in many equipments.

4.1 TRANSMISSION MODES

Two transmission modes are defined in the protocol specification: ASCII and RTU. The modes define the way the message bytes are transmitted. It is not possible to use the two transmission modes in the same network.

The CFW100 frequency inverter uses only the RTU mode for the telegram transmission. The bytes are transmitted in hexadecimal format and its configuration depends on the programming done by means of P0311.

4.2 MESSAGE STRUCTURE FOR RTU MODE

The Modbus RTU structure uses a master-slave system for message exchange. It allows up to 247 slaves, but only one master. Every communication begins with the master making a request to a slave, which answers to the master what has been asked. In both telegrams (request and answer), the used structure is the same: Address, Function Code, Data and CRC. Only the data field can have a variable size, depending on what is being requested.

Master (request telegram):

Ado	lress Fu	nction Rec	uest Data	CRC
(1 k	oyte) (1	byte) (r	n bytes)	(2 bytes)

Slave (response telegram):

۵,,,,,			
Address	Function	Response Data	CRC
(1 byte)	(1 byte)	(n bytes)	(2 bytes)

4.2.1 Address

The master initiates the communication sending a byte with the address of the slave to which the message is destined. When sending the answer, the slave also initiates the telegram with its own address. The master can also send a message to the address 0 (zero), which means that the message is destined to all the slaves in the network (broadcast). In that case, no slave will answer to the master.

4.2.2 Function Code

This field also contains a single byte, where the master specifies the kind of service or function requested to the slave (reading, writing, etc.). According to the protocol, each function is used to access a specific type of data.

For the available list of supported functions, refer to item 5.

4.2.3 Data Field

It is a variable size field. The format and contents of this field depend on the used function and the transmitted value. This field is described together with the function description (refer to item 5).

4.2.4 CRC

The last part of the telegram is the field for checking the transmission errors. The used method is the CRC-16 (Cycling Redundancy Check). This field is formed by two bytes; where first the least significant byte is transmitted (CRC-), and then the most significant (CRC+). The CRC calculation form is described in the protocol specification; however, information for its implementation is also supplied in the Apêndice B.

4.2.5 Time Between Messages

In the RTU mode there is no specific character that indicates the beginning or the end of a telegram. The indication of when a new message begins or when it ends is done by the absence of data transmission in the network, for a minimum period of 3.5 times the transmission time of a data byte (11 bits). Thus, in case a telegram has initiated after the elapsing of this minimum time, the network elements will assume that the first



received character represents the beginning of a new telegram. And in the same manner, the network elements will assume that the telegram has reached its end when after receiving the telegram elements, this time has elapsed again.

If during the transmission of a telegram the time between the bytes is longer than this minimum time, the telegram will be considered invalid because the frequency inverter will discard the bytes already received and will mount a new telegram with the bytes that were being transmitted.

For communication rates higher than 19200 bits/s, the used times are the same as for that rate. The next table shows us the times for different communication transmission rates:

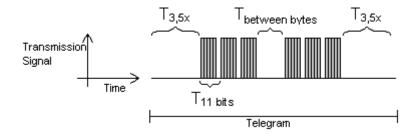


Table 4.1: Communication rates and the time periods involved in the telegram transmission

Baud rate	T11 bits	T3,5x	
1200 bits/s	9.167 ms	32.083 ms	
2400 bits/s	4.583 ms	16.042 ms	
4800 bits/s	2.292 ms	8.021 ms	
9600 bits/s	1.146 ms	4.010 ms	
19200 bits/s	573 µs	2.005 ms	
38400 bits/s	573 µs	2.005 ms	
57600 bits/s	573 µs	2.005 ms	

 \blacksquare T_{11 bits} = Time for transmitting one byte of the telegram.

 \blacksquare T_{between bytes} = Time between bytes.

 $T_{3,5x}$ = Minimum interval to indicated beginning and end of a telegram (3.5 x T11bits).



5 OPERATION IN THE MODBUS RTU NETWORK - SLAVE MODE

The CFW100 frequency inverter has the following characteristics when operated in Modbus RTU network:

- Network connection via RS485 serial interface.
- Address, communication rate and byte format defined by means of parameters.
- It allows the device programming and control via the access to parameters.

5.1 AVAILABLE FUNCTIONS AND RESPONSE TIMES

In the Modbus RTU specification are defined the functions used to access different types of data. In the CFW100 the parameters have been defined as being holding type registers. In order to access those data the following services (or functions) have been made available:

Read Holding Registers

Description: reading of register blocks of the holding register type.

Function code: 03.

Read Input Registers Erro! Indicador não definido.

Description: reading of register blocks of the input register type.

Function code: 04.

Write Single Register

Description: writing in a single register of the holding type.

Function code: 06.

Write Multiple Registers

Description: writing in register blocks of the holding register type.

Function code: 16.

Read Device Identification

Description: identification of the device model.

Function code: 43.

The response time, from the end of transmission of the master until the response of the slave, ranges from 2 to 10 ms for any of the functions above.

5.2 MEMORY MAP

The CFW100 Modbus communication is based on the reading/writing of the equipment parameters. All the drive parameters list is made available as holding type 16-bit registers. The data addressing is done with the offset equal to zero, which means that the parameter number corresponds to the register number. The following table illustrates the parameters addressing, which can be accessed as holding type register.

 Modbus data address

 Decimal
 Hexadecimal

 P0000
 0
 0000h

 P0001
 1
 0001h

 :
 :
 :

 P0100
 100
 0064h

 :
 :
 :

Table 5.1: Modbus RTU Memory Map

It is necessary to know the inverter list of parameters to be able to operate the equipment. Thus, it is possible to identify what data are needed for the status monitoring and the control of the functions. The main parameters are:

Monitoring (reading):



- P0680 (holding register 680): Status word
- P0681 (holding register 681): Motor speed

Command (writing):

- P0682 (holding register 682): Command Word
- P0683 (holding register 683): Speed Reference

Refer to the Programming Manual for a complete parameter list of the equipment.



NOTE!

- All the parameters are treated as holding type registers. Depending on the master that is used, those registers are referenced starting from the base address 40000 or 4x. In this case, the address that must be programmed in the master for a parameter is the address showed in the table above added to the base address. Refer to the master documentation to find out how to access holding type registers.
- It should be noted that read-only parameters can only be read from the equipment, while other parameters can be read and written through the network.



6 DETAILED DESCRIPTION OF THE FUNCTIONS

A detailed description of the functions available in the CFW100 frequency inverter for the Modbus RTU is provided in this section. In order to elaborate the telegrams it is important to observe the following:

- The values are always transmitted in hexadecimal.
- The address of a datum, the number of data and the value of registers are always represented in 16 bits. Therefore, it is necessary to transmit those fields using two bytes superior (high) and inferior (low).
- The telegrams for request, as well as for response, cannot exceed 64 bytes.
- The transmitted values are always integer, regardless of having a representation with decimal point. Thus, the value 9.5 would be transmitted via serial as being 95 (5Fh). Refer to the CFW100 parameter list to obtain the resolution used for each parameter.

6.1 FUNCTION 03 - READ HOLDING REGISTER

It reads the content of a group of registers that must be necessarily in a numerical sequence. This function has the following structure for the request and response telegrams (each field represents a byte):

Request (Master)	Response (Slave)
Slave Address	Slave Address
Function	Function
Address of the initial register (high byte)	Byte count
Address of the initial register (low byte)	Datum 1 (high)
Number of registers (high byte)	Datum 1 (low)
Number of registers (low byte)	Datum 2 (high)
CRC-	Datum 2 (low)
CRC+	etc
	CRC-
	CRC+

Example: reading of the motor speed (P0002) and the motor current (P0003) of slave at address 1 (assuming that P0002 = 30 Hz and P0003 = 1.5 A).

- Address: 1 = 01h (1 byte)
- Initial register address: 2 = 0002h (2 bytes)
- Value of the fist parameter: 30 = 001Eh (2 bytes)
- Value of the second parameter: 15 = 000Fh (2 bytes)

Request (Master)		Response (Slave)	
Field	Value	Field	Value
Slave Address	01h	Slave Address	01h
Function	03h	Function	03h
Initial register (high)	00h	Byte count	04h
Initial register (low)	02h	P0002 (high)	00h
Number of registers (high)	00h	P0002 (low)	1Eh
Number of registers (low)	02h	P0003 (high)	00h
CRC-	65h	P0003 (low)	0Fh
CRC+	CBh	CRC-	DAh
·		CRC+	31h

6.2 FUNCTION 06 - WRITE SINGLE REGISTER

This function is used to write a value for a single register. It has the following structure (each field represents a byte):

Request (Master)	Response (Slave)
Slave Address	Slave Address
Function	Function
Register address (high byte)	Register address (high byte)
Register address (low byte)	Register address (low byte)
Value for the register (high byte)	Value for the register (high byte)
Value for the register (low byte)	Value for the register (low byte)
CRC-	CRC-
CRC+	CRC+



Example: writing of 30 Hz as the speed reference (P0683) (assuming a motor frequency of 60 Hz) for the slave at address 3.

Address: 3 = 03h (1 byte)

Initial register address: 683 = 02ABh (2 bytes)

Value for parameter: 1000h (2 bytes)

Request (Master)		Response (Slave)	
Field	Value	Field	Value
Slave Address	03h	Slave Address	03h
Function	06h	Function	06h
Register (high)	02h	Register (high)	02h
Register (low)	ABh	Register (low)	ABh
Value (high)	10h	Value (high)	10h
Value (low)	00h	Value (low)	00h
CRC-	F5h	CRC-	F5h
CRC+	B0h	CRC+	B0h

Note that for this function the slave response is an identical copy of the request made by the master.

6.3 FUNCTION 16 - WRITE MULTIPLE REGISTERS

This function allows writing values for a group of registers, which must be in a numerical sequence. It can also be used to write in a single register (each field represents a byte):

Request (Master)	Response (Slave)
Slave Address	Slave Address
Function	Function
Initial register address (high byte)	Initial register address (high byte)
Initial register address (low byte)	Initial register address (low byte)
Number of registers (high byte)	Number of registers (high byte)
Number of registers (low byte)	Number of registers (low byte)
Byte count (number of data bytes)	CRC-
Datum 1 (high)	CRC+
Datum 1 (low)	
Datum 2 (high)	
Datum 2 (low)	
etc	
CRC-	
CRC+	

Example: writing of the acceleration time (P0100) equal to 1.0 s and the deceleration time (P0101) equal to 2.0 s, of a slave at address 15.

Address: 15 = 0Fh (1 byte)

Initial register address: 100 = 0064h (2 bytes)

Value for the fist parameter: 10 = 000Ah (2 bytes)

Value for the second parameter: 20 = 0014h (2 bytes)

Request (Master)		Response (Slave)	
Field	Value	Field	Value
Slave Address	0Fh	Slave Address	0Fh
Function	10h	Function	10h
Initial register (high)	00h	Initial register (high)	00h
Initial register (low)	64h	Initial register (low)	64h
Number of registers (high)	00h	Number of registers (high)	00h
Number of registers (low)	02h	Number of registers (low)	02h
Byte count	04h	CRC-	01h
P0100 (high)	00h	CRC+	39h
P0100 (low)	0Ah		
P0101 (high)	00h		
P0101 (low)	14h		
CRC-	E0h		
CRC+	91h		



6.4 FUNCTION 43 - READ DEVICE IDENTIFICATION

It is an auxiliary function that allows the reading of the product manufacturer, model and firmware version. It has the following structure:

Request (Master)	Response (Slave)
Slave Address	Slave Address
Function	Function
MEI Type	MEI Type
Reading code	Conformity Level
Object number	More Follows
CRC-	Next object
CRC+	Number of objects
	Code of the first object
	Size of the first object
	Value of the first object (n bytes)
	Code of the second object
	Size of the second object
	Value of the second object (n bytes)
	etc
	CRC-
	CRC+

This function allows the reading of three information categories: Basic, Regular and Extended, and each category is formed by a group of objects. Each object is formed by a sequence of ASCII characters. For the CFW100 frequency inverter, only basic information formed by three objects is available:

- Objeto 00h VendorName: represents the product manufacturer.
- Objeto 01h ProductCode: formed by the product code (CFW100).
- Objeto 02h MajorMinorRevision: it indicates the product firmware version, in the format 'VX.XX'.

The reading code indicates what information categories are read, and if the objects are accessed in sequence or individually. The CFW100 supports the codes 01 (basic information in sequence) and 04 (individual access to the objects). The other fields are specified by the protocol, and for the CFW100 they have fixed values.

Example: reading of basic information in sequence, starting from the object 02h, from a CFW100 at address 1:

Request (Master)		Response (Slave)	
Field	Value	Field	Value
Slave Address	01h	Slave Address	01h
Function	2Bh	Function	2Bh
MEI Type	0Eh	MEI Type	0Eh
Reading code	01h	Reading code	01h
Object number	02h	Conformity Level	81h
CRC-	70h	More Follows	00h
CRC+	77h	Next object	00h
		Number of objects	01h
		Object code	02h
		Object size	05h
		Object value	'V1.00'
		CRC-	3Ch
		CRC+	53h

In this example the value of the objects was not represented in hexadecimal, but using the corresponding ASCII characters instead. E.g.: for the object 02h, the value 'V1.00' was transmitted as being five ASCII characters, which in hexadecimal have the values 56h ('V'), 31h ('1'), 2Eh ('.'), 30h ('0') and 30h ('0').

6.5 COMMUNICATION ERRORS

Communication errors may occur in the transmission of telegrams, as well as in the contents of the transmitted telegrams. Depending on the type of error, the slave may or not send a response to the master.



When the master sends a message for an inverter configured in a specific network address, the product will not respond to the master if the following occurs:

- Parity bit error.
- CRC error.
- *Timeout* between the transmitted bytes (3.5 times the transmission time of a byte).

In those cases, the master must detect the occurrence of the error by means of the timeout while waiting for the slave response. In the event of a successful reception, during the treatment of the telegram, the slave may detect problems and send an error message, indicating the kind of problem found:

- Invalid function (Error code = 1): The requested function has not been implemented for the equipment.
- Invalid datum address (Error code = 2): the datum address does not exist.
- Invalid datum value (Error code = 3): It occurs in the following situations:
 - The value is out of the permitted range.
 - An attempt to write in a datum that cannot be changed (reading only register/bit).



NOTE!

It is important that it be possible to identify at the master what type of error occurred, in order to be able to diagnose problems during the communication.

In the event of any of those errors, the slave must send a message to the master indicating the type of error that occurred. The error messages sent by the slave have the following structure:

Request (Master)	Response (Slave)
Slave Address	Slave Address
Function	Function (with the most significant bit in 1)
Data	Error code
CRC-	CRC-
CRC+	CRC+

Example: the master requests to the slave at the address 1 the writing in the register 2900 (nonexistent register):

Request (Master)		Response (Slave)	
Field	Value	Field	Value
Slave Address	01h	Slave Address	01h
Function	06h	Function	86h
Register (high)	0Bh	Error code	02h
Register (low)	54h	CRC-	C3h
Value (high)	00h	CRC+	A1h
Value (low)	00h		
CRC-	CAh		
CRC+	3Eh		



7 FAULTS AND ALARMS RELATED TO THE MODBUS RTU COMMUNICATION

A128/F228 - TIMEOUT FOR SERIAL COMMUNICATION

Description:

It is the only alarm/fault related to the serial communication indicates that the equipment stopped receiving valid serial telegrams for a period longer than the one programmed in P0314.

Operation:

The parameter P0314 allows programming a period of time during which the equipment must receive at least one valid telegram via the RS485 serial interface – with address and error-checking field correct – otherwise, it will be considered that there was any problem in the serial communication. The time counting initiates after the reception of the first valid telegram. This function can be used by any serial protocol supported by the equipment.

After the serial communication timeout has been identified, the A128 alarm or F228 fault message will be showed on the HMI, depending on the P0313 programming. For alarms, if the communication is reestablished and new valid telegrams are received, the alarm indication will be removed from the HMI.

Possible Causes/Correction:

- Verify factors that could cause failures in the communication (cables, installation, and grounding).
- Make sure that the master sends telegrams to the equipment in intervals shorter than the programmed in P0314.
- Disable this function at P0314.



I. APPENDICES

APÊNDICE A. ASCII TABLE

Table I.1: ASCII Characters

Dec	Hex	Chr		Dec	Hex	Chr	Dec	Hex	Chr	Dec	Hex	Chr
0	00	NUL	(Null char.)	32	20	Sp	64	40	@	96	60	*
1	01	SOH	(Start of Header)	33	21	!	65	41	А	97	61	a
2	02	STX	(Start of Text)	34	22	п	66	42	В	98	62	b
3	03	ETX	(End of Text)	35	23	#	67	43	C	99	63	C
4	04	EOT	(End of Transmission)	36	24	\$	68	44	D	100	64	d
5	05	ENQ	(Enquiry)	37	25	%	69	45	E	101	65	е
6	06	ACK	(Acknowledgment)	38	26	&	70	46	F	102	66	f
7	07	BEL	(Bell)	39	27	1	71	47	G	103	67	g
8	08	BS	(Backspace)	40	28	(72	48	Н	104	68	h
9	09	HT	(Horizontal Tab)	41	29)	73	49	I	105	69	i
10	0A	LF	(Line Feed)	42	2A	*	74	4A	J	106	6A	j
11	0B	VT	(Vertical Tab)	43	2B	+	75	4B	K	107	6B	k
12	0C	FF	(Form Feed)	44	2C	,	76	4C	L	108	6C	1
13	0D	CR	(Carriage Return)	45	2D	_	77	4D	M	109	6D	m
14	0E	SO	(Shift Out)	46	2E		78	4E	N	110	6E	n
15	0F	SI	(Shift In)	47	2F	/	79	4F	0	111	6F	0
16	10	DLE	(Data Link Escape)	48	30	0	80	50	P	112	70	р
17	11	DC1	(Device Control 1)	49	31	1	81	51	Q	113	71	q
18	12	DC2	(Device Control 2)	50	32	2	82	52	R	114	72	r
19	13	DC3	(Device Control 3)	51	33	3	83	53	S	115	73	s
20	14	DC4	(Device Control 4)	52	34	4	84	54	Т	116	74	t
21	15	NAK	(Negative Acknowledgement)	53	35	5	85	55	U	117	75	u
22	16	SYN	(Synchronous Idle)	54	36	6	86	56	V	118	76	v
23	17	ETB	(End of Trans. Block)	55	37	7	87	57	W	119	77	W
24	18	CAN	(Cancel)	56	38	8	88	58	Х	120	78	x
25	19	EM	(End of Medium)	57	39	9	89	59	Y	121	79	У
26	1A	SUB	(Substitute)	58	3A	:	90	5A	Z	122	7A	Z
27	1B	ESC	(Escape)	59	3В	;	91	5B	[123	7в	{
28	1C	FS	(File Separator)	60	3C	<	92	5C	\	124	7C	
29	1D	GS	(Group Separator)	61	3D	=	93	5D]	125	7D	}
30	1E	RS	(Record Separator)	62	3E	>	94	5E	^	126	7E	~
31	1F	US	(Unit Separator)	63	3F	?	95	5F		127	7F	DEL



APÊNDICE B. CRC CALCULATION USING TABLES

Next, a function using programming language "C" is presented, which implements the CRC calculation for the Modbus RTU protocol. The calculation uses two tables to supply pre-calculated values of the necessary displacement for the calculation.

```
/* Table of CRC values for high-order byte */
 static unsigned char auchCRCHi[] = {
 0 \times 000, \ 0 \times C1, \ 0 \times 81, \ 0 \times 40, \ 0 \times 01, \ 0 \times C0, \ 0 \times 80, \ 0 \times 41, \ 0 \times C0, \ 0 \times C0, \ 0 \times 80, \ 0 \times 41, \ 0 \times C0, \ 0 \times C1, \ 0 \times 81, \ 0 \times 40, \ 0 \times C1, \ 0 \times
 0x01,\ 0xC0,\ 0x80,\ 0x41,\ 0x00,\ 0xC1,\ 0x81,\ 0x40,\ 0x00,\ 0xC1,\ 0x81,\ 0x40,\ 0x01,\ 0xC0,\ 0x80,\ 0x41,
 0x01, \ 0xC0, \ 0x80, \ 0x41, \ 0x00, \ 0xC1, \ 0x81, \ 0x40, \ 0x00, \ 0xC1, \ 0x81, \ 0x40, \ 0x01, \ 0xC0, \ 0x81, \ 0x41, \ 0x60, \ 0x81, \ 0x81
 0 \times 00, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times 40, \quad 0 \times 01, \quad 0 \times C0, \quad 0 \times 80, \quad 0 \times 41, \quad 0 \times C0, \quad 0 \times 80, \quad 0 \times 41, \quad 0 \times 00, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times C1, \quad 0 \times 
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               0x40,
 0 \times 01, \quad 0 \times C0, \quad 0 \times 80, \quad 0 \times 41, \quad 0 \times 00, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times 40, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times 40, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times 40, \quad 0 \times C1, \quad 0 \times 
 0 \times 000, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times 40, \quad 0 \times 01, \quad 0 \times C0, \quad 0 \times 80, \quad 0 \times 41, \quad 0 \times 01, \quad 0 \times C0, \quad 0 \times 80, \quad 0 \times 41, \quad 0 \times 00, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times 40, \quad 0 \times 60, \quad 0 \times
 0 \times 00, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times 40, \quad 0 \times 01, \quad 0 \times C0, \quad 0 \times 80, \quad 0 \times 41, \quad 0 \times 01, \quad 0 \times C0, \quad 0 \times 80, \quad 0 \times 41, \quad 0 \times 00, \quad 0 \times C1, \quad 0 \times 81, \quad 0 \times 40, \quad 0 \times 60, \quad 0 \times 
 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   0x81, 0x40, 0x01, 0xC0, 0x80,
  0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
                                                                                                                                                                                                                                                                                                                                                                                                              0x01, 0xC0,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    0x80, 0x41, 0x00, 0xC1, 0x81,
 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       0x80,
 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
                                                                                                                                                                                                                                                                                                                                                                                                              0x00, 0xC1,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    0x81, 0x40, 0x01, 0xC0,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               0 \times 41.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          0x81,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   0x80, 0x41,
 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0x00, 0xC1,
 0 \times 01, \ 0 \times 20, \ 0 \times 80, \ 0 \times 41, \ 0 \times 00, \ 0 \times C1, \ 0 \times 81, \ 0 \times 40, \ 0 \times 00, \ 0 \times C1, \ 0 \times 81, \ 0 \times 40, \ 0 \times 01, \ 0 \times 20, \ 0 \times 80, \ 0 \times 41, \ 0 \times 10, \ 0 \times 
 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40 };
  /* Table of CRC values for low-order byte */
 static char auchCRCLo[] = {
 0x00, 0xC0,
                                                                                                   0xC1, 0x01, 0xC3, 0xO3, 0x02, 0xC2, 0xC6, 0x06, 0x07, 0xC7, 0xC5, 0xC5, 0xC4, 0x04,
 0xCC, 0x0C,
                                                                                                   0x0D, 0xCD, 0x0F, 0xCF, 0xCE, 0x0E, 0x0A, 0xCA,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    0xCB,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      0 \times 0 B,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        0xC9, 0x09, 0x08,
 0xD8, 0x18, 0x19, 0xD9, 0x1B, 0xDB, 0xDA, 0x1A, 0x1E, 0xDE, 0xDF, 0x1F,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0xDD, 0x1D, 0x1C,
 0x14, 0xD4, 0xD5, 0x15, 0xD7, 0x17, 0x16, 0xD6, 0xD2, 0x12,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        0x11,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   0x13, 0xD3,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0xD1, 0xD0,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               0 \times 10.
 0xF0, 0x30, 0x31, 0xF1, 0x33, 0xF3, 0xF2, 0x32, 0x36, 0xF6,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   0xF7, 0x37, 0xF5, 0x35, 0x34,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               0xF4.
 0x3C, 0xFC, 0xFD, 0x3D, 0xFF, 0x3F, 0x3E, 0xFE, 0xFA, 0x3A, 0x3B, 0xFB, 0x39, 0xF9, 0xF8,
  0x28, 0xE8, 0xE9, 0x29, 0xEB, 0x2B, 0x2A, 0xEA, 0xEE, 0x2E, 0x2F, 0xEF, 0x2D, 0xED, 0xEC,
 0 \times E4 \,, \quad 0 \times 24 \,, \quad 0 \times 25 \,, \quad 0 \times E5 \,, \quad 0 \times 27 \,, \quad 0 \times E7 \,, \quad 0 \times E6 \,, \quad 0 \times 26 \,, \quad 0 \times 22 \,, \quad 0 \times E2 \,, \quad 0 \times E3 \,, \quad 0 \times E3 \,, \quad 0 \times E1 \,, \quad 0 \times 21 \,, \quad 0 \times 20 \,, \quad 0 \times E1 \,, \quad 0 \times 
 0xA0, 0x60, 0x61, 0xA1, 0x63, 0xA3, 0xA2, 0x62, 0x66, 0xA6, 0xA7, 0x67, 0xA5, 0x65, 0x64,
 0x6C, 0xAC,
                                                                                                   0xAD, 0x6D,
                                                                                                                                                                                                        0xAF, 0x6F, 0x6E, 0xAE,
                                                                                                                                                                                                                                                                                                                                                                                                              0xAA,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                   0x6A,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    0x6B,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      0xAB,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0x69,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0xA9,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            0xA8,
 0x78, 0xB8, 0xB9, 0x79, 0xBB, 0x7B, 0x7A, 0xBA, 0xBE, 0x7E,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    0x7F,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      0xBF,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0x7D, 0xBD, 0xBC,
 0xB4, 0x74, 0x75, 0xB5, 0x77, 0xB7, 0xB6, 0x76,
                                                                                                                                                                                                                                                                                                                                                                                                               0x72, 0xB2,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    0xB3,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      0x73,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0xB1, 0x71, 0x70,
 0x50, 0x90, 0x91, 0x51, 0x93, 0x53, 0x52, 0x92,
                                                                                                                                                                                                                                                                                                                                                                                                              0x96, 0x56,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    0x57,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      0x97,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0x55,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          0x95, 0x94,
 0x9C, 0x5C, 0x5D, 0x9D, 0x5F, 0x9F, 0x9E, 0x5E, 0x5A, 0x9A, 0x9B, 0x5B,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0x99, 0x59, 0x58,
 0x88, 0x48, 0x49, 0x89, 0x4B, 0x8B, 0x8A, 0x4A, 0x4E, 0x8E, 0x8F,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  0x4F,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0x8D, 0x4D, 0x4C,
 0x44, 0x84, 0x85, 0x45, 0x87, 0x47, 0x46, 0x86, 0x82, 0x42, 0x43, 0x83, 0x41, 0x81, 0x80, 0x40 };
  /* The function returns the CRC as a unsigned short type */
 unsigned short CRC16(puchMsg, usDataLen)
unsigned char *puchMsg;
                                                                                                                                                                                                                                                                                                                                    /* message to calculate CRC upon
 unsigned short usDataLen;
                                                                                                                                                                                                                                                                                                                                   /* quantity of bytes in message
                               unsigned char uchCRCHi = 0xFF;
                                                                                                                                                                                                                                                                                                                                    /* high byte of CRC initialized
                               unsigned char uchCRCLo = 0xFF;
                                                                                                                                                                                                                                                                                                                                   /* low byte of CRC initialized
                                                                                                                                                                                                                                                                                                                                   /* will index into CRC lookup table
                               unsigned uIndex;
                                                                                                                                                                                                                                                                                                                                   /* pass through message buffer
                               while (usDataLen--)
                                                               uIndex = uchCRCLo ^ *puchMsgg++; /* calculate the CRC
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               * /
                                                               uchCRCLo = uchCRCHi ^ auchCRCHi[uIndex];
                                                              uchCRCHi = auchCRCLo[uIndex];
                               return (uchCRCHi << 8 | uchCRCLo);
 }
```