

# **Laser Deflection Cube User Instruction Manual**

Rev 1.2 WiM Hardware Rev 1.2 / Firmware Rev 2.03



# **Fraunhofer Institute for Photonic Microsystems**

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# **Table of Contents**



# <span id="page-4-0"></span>**1 Safety Instructions**

### **Laser Safety**



**Laser class II** implemented  $\lambda = 650$  nm **P < 1 mW** 

Laser emits towards the back-side of scanner mirror.

- Do not disassemble scan head.
- Do not use with missing scanner mirror.
- No consumer product
- Operation by trained personnel only.

### **High Voltage**



## **200 V / 2.5 mA**

Do not touch electronic board, if powered. The maximum amount of energy stored in the converter is less than 50 mJ below any hazardous levels.

**ESD** 



The LDC scan head as well as the driver electronics, uses semiconductors that can be damaged by electrostatic discharge (ESD). When handling, care must be taken so that the devices are not damaged.

The following precautions must be taken:

- Do not open the protective conductive packaging until being at an approved anti-static work station.
- Use a conductive wrist strap attached to a good earth ground.
- Always discharge yourself by touching a grounded bare metal surface or approved anti-static mat before picking up an ESD - sensitive electronic component.
- Use an approved anti-static mat to cover your work surface.

# <span id="page-5-0"></span>**2 Preparation for use**

## <span id="page-5-1"></span>**2.1 Delivery Contents**

- Scan head
- Driving electronics
- Power adaptor
- PICKit<sup>™</sup> Serial Analyzer SPI-USB interface
- USB cable
- CD with API and application software

# <span id="page-5-2"></span>**2.2 Unpacking**

The scan head has to be handled carefully.

The scan head as well as the electronic board has to be handled solely in an ESD qualified environment.

## <span id="page-5-3"></span>**2.3 Connecting scan head with driver electronics**

To realize a very small form factor of the scan head, the connection to the driver electronic board is implemented as a plug-less FPC (Flexible Printed Circuit) connection with 1 mm pitch. The connecting needs caution especially while inserting the flex tail into the jack.

To avoid damaging of the flex tail and the pins, pull the lever approx. 1 mm out of the jack until a mechanical stop is noticeable. After, insert the flex tail with pins downwards into the jack and carefully close the lever.

Never bend the flex tail with a radius smaller than 5 mm!

# <span id="page-5-4"></span>**2.4 Start-up procedure**

The LDC module driving electronics is factory configured to start the scanner at the specified frequency and amplitude after powering-up by connecting the power supply.

The wire of electronic board is unmistakably reverse polarity protected matching to the delivered power supply.

To change the scan amplitude the PC interface (see chapter [5](#page-13-0)) can be used.

# <span id="page-6-0"></span>**3 Scan Head**

## <span id="page-6-1"></span>**3.1 Layout**



*Figure 1: Scan head layout* 



*Figure 2: Drawing LDC scan head* 

# <span id="page-6-2"></span>**3.2 Resonant Micro Scanning Mirror Theory**

The scan head contains as the key component a MEMS micro scanning mirror, designed for periodical deflection of light. The scanner chip ([Figure 3](#page-7-0)) is fabricated at the Fraunhofer IPMS using CMOS compatible technology. It consists of a 30 to 75 μm thick plate, suspended by two torsional springs. The optical reflection coefficient is enhanced by a thin layer of aluminum. The edges between fixed frame and moving mirror plate are formed as comb-electrodes. The membrane with the mechanical active structures is carried by a patterned bulk silicone.



<span id="page-7-0"></span>*Figure 3: Exemplary design of a micro scanner mirror* 

The vertical sides of the comb like driving electrodes and of the mirror plate form a variable capacitance. The oscillation of the plate is excited resonantly with the double mechanical oscillation frequency. An applied voltage creates an electrostatic torque which accelerates the plate towards its rest position. To prevent a deceleration of the plate's movement after passing the rest position the voltage between the electrodes must be switched to zero at zero crossing. At the maximum deflection angle the voltage is switched on again. A typical resonance curve is shown in figure 3. The asymmetric behavior is explained by the deceleration and therewith the collapse of oscillation due to a remaining electrostatic torque after passing rest position. This always occurs for driving frequencies smaller than the double mechanical resonance frequency. The resonance curve shows a hysteresis. Therefore, different parts of the curve are obtained dependent from the direction of the frequency sweep. The largest deflection angle will be achieved, applying a frequency sweep starting with a frequency higher than a type-specific frequency f<sub>2</sub> down to a frequency slightly higher than the resonance frequency f<sub>1</sub>. This start-up procedure is already implemented in the firmware of the driving electronics.

Due to the in-plane electrode configuration this scanner design is not suitable to achieve a static deflection.



*Figure 4: MEMS micro scanner resonance behavior* 

After the start-up procedure, the amplitude of the scanner can be controlled theoretically by both, the voltage and the frequency of the drive signal. Using the frequency as controlled parameter, one has to take care that the frequency always has to be larger than the resonance frequency, otherwise the oscillation aborts. To avoid this state, especially in view of the fact that the resonance frequency can be slightly shifting under changing environmental conditions, normally the phase of the mirror oscillation is controlled instead. Utilizing a phase controlled operation; the coupling efficiency for the driving signal is maximal at a minimal phase (running near the mechanical resonance of the scanner). This causes the lowest possible driving voltage and thereby the lowest power consumption of the driving circuit. On the other hand, the oscillation frequency wills then slightly changing depending on the environmental conditions. Due to a steep rise of the frequency response curve near resonance, changing conditions effects also a drift of the deflection amplitude.

To obtain a certain deflection in practice, the amplitude is always controlled by the driving voltage. The phase control can be used additionally to reduce the necessary driving voltage. All these driving theory aspects are implemented in the delivered driving electronics.

## <span id="page-8-0"></span>**3.2.1 Position Feedback Sensor Theory**

To control amplitude and/or phase of the MEMS scanner mirror device, an optical position feedback sensor sub-module is implemented in the scan head. The position sensor consists of a red laser diode as light source and two photo diodes as detectors. The laser emits a beam towards the back-side of the scanner mirror. This beam is reflected by the moving scanner mirror. The two photo diodes are arrange in a way, that the laser beam hits one photo diode at zero deflection crossing, the other at a certain angular deflection. Using the timing of the trigger signals, generated by the photo diodes and be pre-processed by a logic circuit, the phase and the amplitude of the scanner can be evaluated [\(Figure 5](#page-9-0)).

The amplitude is calculated from the AMPL signal according

$$
A = \frac{\Theta_{\text{sensor}}}{\sin\left[\pi\left(\frac{1}{2} - \frac{t_A}{T}\right)\right]}
$$
(1)

where  $\Theta_{\text{sensor}}$  is the angular position of the photo diode and *T* is the cycle duration of the scanner oscillation.

The phase is calculated from the REF and the TRIG signal. The REF signal is the master clock reference for the scanner drive. Ideally, the length of high and low level of the TRIG signal should be equal. Due to fabrication inaccuracies regarding the position of the photo diode relative to the scanner die position, the two times are not completely matching. To compensate the offset, a mathematical correction has to be accomplished:

$$
\varphi = \left[ \frac{2 \left( t_{phase1} + t_{phase2} \right) - T}{2 \ T} \pi \right] \tag{2}
$$

On-board, the sensor signals are analyzed by a time-to-digital converter and are also available at the I/O connector. The phase and the amplitude calculation are already implemented in the firmware of the controller and can be read out using the SPI interface.

[Figure 5](#page-9-0) shows the relations between the signals and the timing. Note, that due to the not determined direction of transient oscillation the mirror oscillation can also be 180° phase shifted.



<span id="page-9-0"></span>*Figure 5: Sensor Signal Timing Characteristic Diagram* 

*Table1: Position feedback sensor characteristic* 



# <span id="page-10-0"></span>**4 Driving Electronics**

The driving electronics is designed to run the LDC module either independently or under permanent data exchange with a controlling PC.

# <span id="page-10-1"></span>**4.1 Layout**



<span id="page-10-3"></span>*Figure 6: Electronic board layout (top view)* 

# <span id="page-10-2"></span>**4.2 I/O Connector**

The power supply input as well all provided I/O pins are connected to a 18-pin 2.54 mm pitch header ([Figure 6\)](#page-10-3). The I/O port includes a set of digital state signals, the raw and pre-processed position feedback sensor signals and a SPI interface.

### *Table 2: Pin out I/O Connector*



# <span id="page-11-0"></span>**4.3 State Indicators Leeds**



*Figure 7: State indicator Leeds* 

## <span id="page-12-0"></span>**4.4 Absolute Maximum Ratings**

Stresses above those listed can cause permanent damage to the device. This is a stress rating only, and functional operation of the device at those or any other conditions above those indicated in the operation listings of this specification is not implied. Exposure to maximum rating conditions for extended periods can affect device reliability.

#### **Power Supply**

VDD Supply Voltage vs. GND....................................................................................................6 V

#### **5V Logic**



#### **3V Logic (Output only)**



## <span id="page-12-1"></span>**4.5 DC Voltage Specifications**





*Table 4: DC voltage characteristic* 



# <span id="page-13-0"></span>**5 PC Interface**

The LDC module driving electronics can be controlled optionally from a PC running on Windows XP or later.

In delivery there is included a SPI – USB adapter, which is directly connectable to LDC module driving electronics, an USB cable, an interface API and a simple application which can control the LDC module easily.

## <span id="page-13-1"></span>**5.1 System requirements**

- Windows XP, Vista, 7
- .NET framework 2.0 (2.0.50727) or later
- USB 2.0
- For LDC interface API: Software development environment supporting .NET like Visual Studio 2005 (Express) or later, LabView 8 or later)

## <span id="page-13-2"></span>**5.2 SPI-USB adapter PICkit™ Serial analyzer**



3 - Lanyard Connection 5 - Pin 1 Marker 1 - Status LEDs 2 - Push Button 4 - USB Port Connection 6 - Communications Connector

*Figure 8: PICkit™ Serial Analyzer – Layout and description* 

The PICkit™ serial analyzer provides access to the onboard SPI interface of the LDC module driving electronics. It can be directly connected to the header of electronic board. The marked pin 1  $(\circled{S})$  has to be match to pin 1 of the multi-purpose header of the LDC module driving electronics. ([Figure 9](#page-14-1))



<span id="page-14-1"></span>*Figure 9: Wiring of PICkit™ serial anlyzer* 

## <span id="page-14-0"></span>**5.3 Graphical User Interface**

The control panel allows controlling the scanner with a minimum of functionality getting started with PC interfacing.

The GUI is stored on the CD as IPMS\_LDC\_Control\_Panel.exe.

The Software allows to set the scanning amplitude (mechanical scan amplitude, half angle), to start and stop the scanner and provides some status information.

After starting the software directly from CD or a copy from a PC drive, the window opens in its start-up appearance [\(Figure 10](#page-14-2)).



*Figure 10: Graphical user interface in start-up appearance* 

<span id="page-14-2"></span>The connect button establishes a permanent communication with the LDC module. In case, the LDC module driving electronics is not connected or powered an error message is generated. Further, the connection is checked for errors always it was started once. If the connection breaks, the control panel generates an error message and falls back to start-up behavior.

In command frame [\(Figure 11](#page-15-1)) now the scan angle (mechanical scan half angle) can be set in the predefined scan angle range displayed is status frame. *On* starts a sweep to the set scan angle and keeps that angle constant. *Off* stops the scanner oscillation.

In the status window further following parameters are displayed:

- Serial number of electronics and scan head
- Current scan angle
- Current mechanical oscillation frequency
- Current scanner drive voltage
- Current scanner oscillation phase



*Figure 11: Graphical user interface* 

<span id="page-15-1"></span>The control panel can be closed with keeping the LDC module in the last state, this means the scanner oscillation continues (*Quit and Keep Running*) or with stopping the mirror oscillation before quitting (*Halt and Quit*).

Controls and settings much more than these can be programmed using the LDC Interface API and a common programming language.

## <span id="page-15-0"></span>**5.4 LDC Interface API**

The API does not require any driver or software installation.

To use the API it is only necessary to refer the .NET class library *LDCInterfaceAPI.dll* from CD within the software project of the target application.

The class library *LDCInterfaceAPI.dll* contains two classes *LDCInterfaceAPI* and *PICkitS*. The *PICkitS* class provides all functionality of the PICkit<sup>™</sup> Serial analyzer. Low level access to the SPI layer of the *PICkitS* is completely encapsulated by *LDCInterfaceAPI* class.

**Direct calling of** *PICkitS* **functions may damage the LDC module.** 

## <span id="page-16-0"></span>**5.4.1 Command reference**

The commands are a one by one translation of the SPI commands described in chapter [SPI](#page-26-0)  [Interface](#page-26-0) [\(Table 9](#page-31-1)). Additional the command list contains functions to initialize and clean up the USB-SPI interface hardware.

### <span id="page-16-1"></span>**5.4.1.1 SPI initialization / Clean up**

static int **Init**(double dBitate)

This function initializes the PICkit serial analyzer and configures the interface as SPI master **Init must be performed prior any other communication.** 

```
Parameters:
```
*dBitrate*: data transmission bandwidth in kHz (0.61...1250 kHz)

Returns:

error code, see [Table 5](#page-24-1) 

static int **CleanUp**()

Shuts down communication threads and closes file handles. **CleanUp must be performed prior to closing host application.** 

> Returns: error code, see [Table 5](#page-24-1)

### **5.4.1.2 Module Activation**

static int **EnableModule**(byte chParameter)

Enables or disables the resonant scanner. Enable starts the scanner with a frequency sweep and the predefined start-up parameters. After start-up the scanner control is switched to the configured operation state.

Parameters: *chParameter*: 0: disable / stop 1: enable / start

Returns:

error code, see [Table 5](#page-24-1) 

static int **EnableExtern**(byte chParameter)

EnableExtern switches the activation behavior between software and hardware control.

Parameters:

*chParameter*: 0: hardware pin is ignored – software control 1: Scanner module is enabled with hardware pin at low

Returns:

error code, see [Table 5](#page-24-1) 

### **5.4.1.3 Resonance scanner parameters**

```
static int SetAmplitude(float fValue) 
static int GetAmplitude(byte chParameter, out float fValue)
```
Mechanical scan amplitude (MSA) in degree. The SetAmplitude function sets the target for the amplitude control. The GetAmplitude function returns the target or the current value depending on the parameter.

Note: Feedback sensor must be switched on and amplitude control must be enabled. Otherwise the amplitude will not be controlled.

Parameters:



static int **GetAmplitudeLimits**(out float fMinValue, out float fMaxValue)

Returns the factory-defined range for amplitude values, the minimum and the maximum. The limits represents the parameter input range and the range of a internal watchdog. Electronics switches to an error state if the limit is achieved.

Returns:

error code, see [Table 5](#page-24-1)  *fMinValue*: minimum *fMaxValue*: maximum

static int **SetPhase**(float fValue) static int **GetPhase**(byte chParameter, out float fValue)

Phase of the scanner oscillation respective to the drive signal. The SetPhase function sets the target for the phase control. The GetPhase function returns the target or the current value depending on the parameter.

Note: Feedback sensor must be switched on and amplitude control must be enabled. Otherwise the amplitude will not be controlled.



static int **GetPhaseLimits**(out float fMinValue, out float fMaxValue)

Returns the factory-defined range for phase values, the minimum and the maximum. The limits represents the parameter input range and the range of a internal watchdog. Electronics switches to an error state if the limit is achieved.

Returns:

 error code, see [Table 5](#page-24-1)  *fMinValue*: minimum *fMaxValue*: maximum

static int **SetVoltage**(float fValue) static int **GetVoltage**(out float fValue)

Peak to peak voltage of the resonance scanner drive signal.

Note: The Voltage is not stored in EEPROM. On Power-up the scanner starts with the preconfigured start-up voltage.

A voltage, set in idle mode (module disabled), is overwritten by pre-defined start-up parameters if the scanner is enabled.

Setting voltage during scanner start-up is denied and returns illegal command.

Setting voltage directly in amplitude and phase control mode is not interlocked but strictly not recommended.

SetVoltage can be inactivated in the some customized module configuration.

Parameters: Returns:

*fValue:* voltage in V

 error code, see [Table 5](#page-24-1)   *fValue* (out): current voltage in V

static int **GetVoltageLimits**(out float fMinValue, out float fMaxValue)

Returns the factory-defined range for voltage values, the minimum and the maximum [V]. The limits define the parameter input range. Furthermore the internal amplitude control (control variable: voltage) is limited to this range.

Returns:

 error code, see [Table 5](#page-24-1)  *fMinValue*: minimum *fMaxValue*: maximum

static int **SetFrequency**(float fValue) static int **GetFrequency**(out float fValue)

Sets and returns the frequency of scanner drive signal in Hz. The drive frequency is double the mechanical oscillation frequency. This frequency setting is only possible if phase control is disabled and amplitude control is in voltage mode or disabled.

**Note:** The frequency is not stored in EEPROM. On power-up the scanner starts with the preconfigured start-up frequency.

A frequency, set in idle mode (module disabled), is overwritten by pre-defined start-up parameters if the scanner is enabled.

Setting frequency during scanner start-up is denied and returns illegal command.

Setting frequency directly in amplitude and phase control mode is not interlocked but strictly not recommended.

SetFrequency can be inactivated in the some customized module configuration. Parameters:

*Value:* frequency

Returns:

 error code, see [Table 5](#page-24-1)  *fValue* (out): current frequency of resonance scanner drive

static int **GetFrequencyLimits**(out float fMinValue, out float fMaxValue)

Returns the defined range for frequency values, the minimum and the maximum in Hz.

Returns:

 error code, see [Table 5](#page-24-1)  *fMinValue*: minimum *fMaxValue*: maximum

#### static int **GetResonanceFrequency**(out float fValue)

Returns the resonance frequency of the MEMS scanner, stored in EEPROM [Hz]. The limits define the parameter input range.

Returns:

 error code, see [Table 5](#page-24-1)  *fValue*: eesonance frequency

#### **5.4.1.4 Optical feedback sensor and control**

static int **EnableSensor**(byte chParameter)

Switches the scan head internal optical feedback sensor. With activated sensor amplitude and phase of scanner oscillation can be set and measured. Further sensor activation is necessary for amplitude and phase control.

Function EnableSensor can be inactivated in the some customized module configuration. Parameters:

> *chParameter*: 0: Sensor disabled and laser off 1: Sensor

Returns:

error code, see [Table 5](#page-24-1) 

static int **EnableAmplitudeControl**(byte chParameter)

Enables / disables the scanner amplitude control using the feedback sensor signal. Disabling amplitude control can be prohibited in the some customized module configuration.

Parameters:

*chParameter*: 0: amplitude control disabled, amplitude regulation via voltage or frequency

1: amplitude control is enabled

Returns:

error code, see [Table 5](#page-24-1) 

#### static int **SetAmplitudeControlMode**(byte chParameter) static int **GetAmplitudeControlMode**(out byte chValue)

AmplitudeControlMode mode selects the parameter, the amplitude is controlled with. This function can be inactivated in the some customized module configuration.



#### static int **EnablePhaseControl**(byte chParameter)

Enables / disables the scanner phase control using the feedback sensor signal. Disabling phase control can be prohibited in the customized module configuration.

Parameters:

*chParameter*: 0: phase control disabled 1: phase control is enabled

Returns:

error code, see [Table 5](#page-24-1) 

#### static int **EnableAutoRestart**(byte chParameter)

Defines the behavior, if the feedback sensor detects an error state, mainly if after start-up or while operation the sensor detects no more oscillation. Independent from setting of EnableAutoRestart, the auto-restart functionality is only possible if the feedback sensor is activated.

Parameters:

*chParameter*: 0: The current operation of the scanner is continued and an error is signaled

1: The module is re-set and re-started

Returns:

error code, see [Table 5](#page-24-1) 

static int **GetModuleConfiguration**(out UInt16 nValue)

Returns the settings of the module configuration registers for module enabling, sensor enabling and control modes as 2-byte value. The bit mapping is shown in



Table 10.

Returns:

error code, see [Table 5](#page-24-1) 

nValue*: see* 

Table 10

#### static int **GetControlState**(out byte chValue)

Returns in the state of the scanner oscillation, amplitude control and phase control. If a value (phase or amplitude) is changed, the assigned bit is set to 0. If the scanner oscillation is on target the bit is set to 1.

Returns:

 error code, see [Table 5](#page-24-1)  *chValue*: see [Table 11](#page-34-0) 

## **5.4.1.5 Diagnostics**

static int **GetSerial**(out UInt32 nValue)

Returns a unique serial number of the LDC-Module.

Returns:

 error code, see [Table 5](#page-24-1)   *nValue:* serial number

static int **GetVersion**(out string szValue)

Returns the firmware version of the LDC module driving electronics.

Returns:

 error code, see see [Table 5](#page-24-1)   *szValue:* string with the firmware version

static int **GetState**(out byte chValue)

Returns the current state of the internal state machine.

Returns:

error code, see [Table 5](#page-24-1) 

*chValue: state, see* 

*Table 10.* 

static int **GetErrorCode**(out byte chValue)

Returns the error code of the last error detected by the LDC driving electronics

Returns:

 error code of the function, see [Table 5](#page-24-1)   *chValue:* see [Table 12](#page-34-1) 

## **5.4.1.6 Storing of settings**

```
static int EEPROM(byte chParameter)
```
Stores and loads the configuration to or from the electronic internal EEPROM.

Parameters: *chParameter*: 0: Store 1: Load

Returns:

error code of the function, see [Table 5](#page-24-1) 

## **5.4.1.7 Low-level functions**

Low level fuctions are not needed to call directly for a communication with the LDC module.

```
static int TransmitFrame(byte chOpCode, byte[] chTXData, out byte[] 
chRXData)
```
Low level function to transmit data frames to the LDC module. The function is not necessary for the user to communicate with the LDC module. It is called from the SetCommand/GetCommand function and can be used for own low-level access to the SPI protocol.

Parameters:



error code of the function, see [Table 5](#page-24-1) 

*Note*: Data length of the arrays must be match with the transmitted data type (table 6, Manual Laser Deflection Cube)

```
static int GetCommand(byte chOpCode,out…) 
static int SetCommand(byte bOpCode,…)
```
The functions GetCommand / Set Command can be used to call a function via its OpCode. Types of parameter are identical to upper description.

Parameters: *chOpCode*: SPI command code, see [Table 9](#page-31-1)  Returns: error code of the function, see [Table 5](#page-24-1) 

# <span id="page-24-0"></span>**5.4.2 LDC Interface API Error Codes**



<span id="page-24-1"></span>

## <span id="page-25-0"></span>**5.4.3 Code sample**

The following code (C#, Microsoft Visual Studio 2005) initializes the PICkit and the SPI Interface, activates the scanner and reads out the actual voltage. Last, the PICkit interface is removed from memory.



*Figure 12: Implementation of LDC Interface API as class library in Microsoft Visual Studio 2005* 

```
using LDCInterfaceAPI; 
private void LDCFunction() 
{
   int ret; 
   ret = LDCInterface.Init(1000);
  if (ret!=0) MessageBox.Show("Error initializing PICKit Serial. Error "+ret); 
   ret = LDCInterface.EnableModule(1); 
   if (ret!=0) 
      MessageBox.Show("Error enabling scanner. Error "+ret); 
   float Voltage; 
   ret = LDCInterface.GetVoltage(out Voltage); 
  if (ret!=0) MessageBox.Show("Error reading voltage. Error "+ret); 
   MessageBox.Show("Current Voltage: "+Voltage); 
   LDCInterface.CleanUp; 
}
```
# <span id="page-26-0"></span>**6 SPI Interface**

# <span id="page-26-1"></span>**6.1 Interface Hardware Description**

The LDC electronic board provides a Serial Peripheral Interface (SPI) interface to configure the control of the micro scanner device and to receive operational status information of the scanner and the position feedback sensor by a superordinated system controller.

The SPI interface of the on-board digital signal controller is electrically connected directly to the edge connector of the board.

The SPI interface is configured to run in slave mode. This means, that both, the Serial Clock (SCK) and the transmission request, controlled by Slave Select (SS) have to be provided by the SPI master device.

To initiate a data transmission or reception, the SS signal must be driven low. The interface is further configured that the data transmission occurs at the rising edge of the high-active Serial Clock input signal (SCK). The word length of data is determined to one byte. A command frame embodies multiple bytes, so the SS command must be hold on low until the complete sequence is transmitted.

To transmit data to the LDC board, the data must be provided at Serial Data In (SDI). Data can be read from the LDC board using the Serial Data Out Signal (SDO). Both data transmission lines are served simultaneously with the serial clock.

[Figure 13](#page-26-2) shows the timing diagram of the serial communication.



<span id="page-26-2"></span>*Figure 13: SPI Timing Diagram* 



# <span id="page-27-0"></span>**6.2 Timing Characteristic**

*Figure 14: SPI Timing Characteristic Diagram* 

		Table 6: Timing Requirements
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<span id="page-28-1"></span>*Figure 15: Load Conditions for Device Timing Requirements* 

## <span id="page-28-0"></span>**6.3 Transmission Protocol**

The communication protocol of the SPI interface is based on a fixed data frame architecture ([Table 7](#page-28-2)). Each data frame, initialized by the master device, contains a command sequence followed by a data block with data to be transmitted to the LDC board as well a command response followed by a data block for receiving data from LDC board. Each data block is secured by a length byte and a checksum byte.

Considering the SPI physical layer definition, with each byte transmitted in one direction a byte is received vice versa. To ensure a failure-free data transmission, an empty byte token (*0xFF*) has to be transmitted to the slave while receiving data from slave. Likewise, the slave transmits the empty byte token (*0xFF*) during receiving data from master. Further, each transmission has to be finalized with an end token (*0xFF*).

After a data request by the master, the slave usually needs some calculations until the response is ready for transmission. If the master continues querying the slave by serving the serial clock (SCK) signal before the data are available, the slave transmits an empty byte token until calculation is finished. The protocol ensures that each response after the pause is different to the empty byte token (*0xFF*). So a byte different to the empty byte token (*0xFF*) can be interpreted as the continuation of the frame after the pause.



<span id="page-28-2"></span>*Table 7: SPI Data Transmission Frame* 

Command



Data Length



Data

Depends on command

 $CRC$ 



End Token



Empty Byte Token



Response



*Table 8: SPI Protocol characteristic* 

<span id="page-29-2"></span>

## <span id="page-29-0"></span>**6.4 Error Handling**

At each time, the transmission is initiated by enabling the Slave Select (SS) signal, the interface expects a sequence with the command code, regardless in which state the previous transmission was interrupted. This precaution obtains a determined data stream also in the case, a transmission fails or is not completed by the master.

Transmission errors can be identified by the data length, and the evaluation of the CRC. Commands responded by the slave with at least one error flag in the response byte are not executed and can be repeated immediately.

After an elapsed timeout (see [Table 8\)](#page-29-2) a reset of the SPI interface of the LDC board is performed. The timeout event arises regardless a serial clock (SCK) is provided by the master or not.

<span id="page-29-1"></span><sup>-</sup><sup>1</sup> If the specified time is elapsed, data from LDC board can be expected not anymore.

# <span id="page-30-0"></span>**6.5 Data Formats**

In present data transmission protocol multi-byte parameters are always sent least significant byte first (little endian). This is compliant e.g. to Intel x86- or x64- based machines.

Note the inverse *bit* endiness in data transmission with the most significant bit first (see [Figure](#page-30-1)  [17](#page-30-1)).

The floating point data format (single precision), used for several parameters is compliant to the IEEE-754 standard. The format and an example are shown in [Figure 16](#page-30-2). The 4-byte word is transmitted also in little endiness byte order, least significant byte first and the most significant *bit* first, see [Figure 17](#page-30-1).



<span id="page-30-2"></span>*Figure 16: IEEE-754 compilant float data format (single precision); s=sign* 

Memory data format (little endian)



SPI transmission bit order

Clock   0   1   2   3   4   5   6   7   8   9  10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 28 30 31																	

<span id="page-30-1"></span>*Figure 17: Examples for bit/ byte order of values (little endian) and during data transmission via SPI* 

# <span id="page-31-0"></span>**6.6 SPI Command List**

*Table 9: SPI Command List* 

<span id="page-31-1"></span>







*Table 10: Bit allocation of module configuration* 

### *Table 11: Return codes of internal state machine*

<span id="page-34-0"></span>

State	<b>Description</b>
$\Omega$	Booting hardware
	Module disabled
$\mathcal{P}$	Initializing hardware
3	Starting scanner (frequency sweep)
4	Waiting for sensor signals
5	Scanner oscillation with enabled control
6	Error
	Hardware calibration mode (can not occur in normal operation)

*Table 12: Bit allocation of the control state* 

<span id="page-34-1"></span>

## *Table 13: Error Codes*

<span id="page-35-0"></span>

# **NOTES**