

TSW5005 – TI WiMAX 5.6-GHz Transceiver

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1 Introduction

This user's manual describes the operation of the Texas Instruments TI WiMAX 5.6-GHz Transceiver Board and Adapter Board. The boards are supplied with a parallel port interface cable and power cable. Operation of the board requires an external dual-power supply and a personal computer.

2 Release Inventory

The release package contains:

1. TI WiMAX 5.6-GHz Transceiver Board
2. Adapter board
3. Parallel port interface cable
4. Power cable
5. Documentation compact disc (CD)

3 System Requirements

1. +7.5-V power supply, 1 A
2. +4.1-V power supply, 1 A
3. +5-V power supply, 100 mA
4. Personal computer running Windows™ 98, 2000, or XP
5. Typical test equipment used for system measurement:
 - a. Signal generator: Agilent ESG Series (with baseband I/Q modulation option for 802.16x modulated testing) or equivalent
 - b. Spectrum analyzer: Agilent PSA Series (with phase noise option) or equivalent
 - c. Vector signal analyzer: Agilent 89600 Series for 802.16x modulated EVM testing or equivalent

Windows is a trademark of Microsoft Corporation.
LabVIEW is a trademark of National Instruments Corporation.

4 Operating Procedure

4.1 TX Operation

1. Connect the RF board and adapter board together via the 70-pin I/O connector as shown in [Figure 1](#). Connect power, serial interface, TX I/Q baseband signals, and RF output as shown in [Figure 1](#).

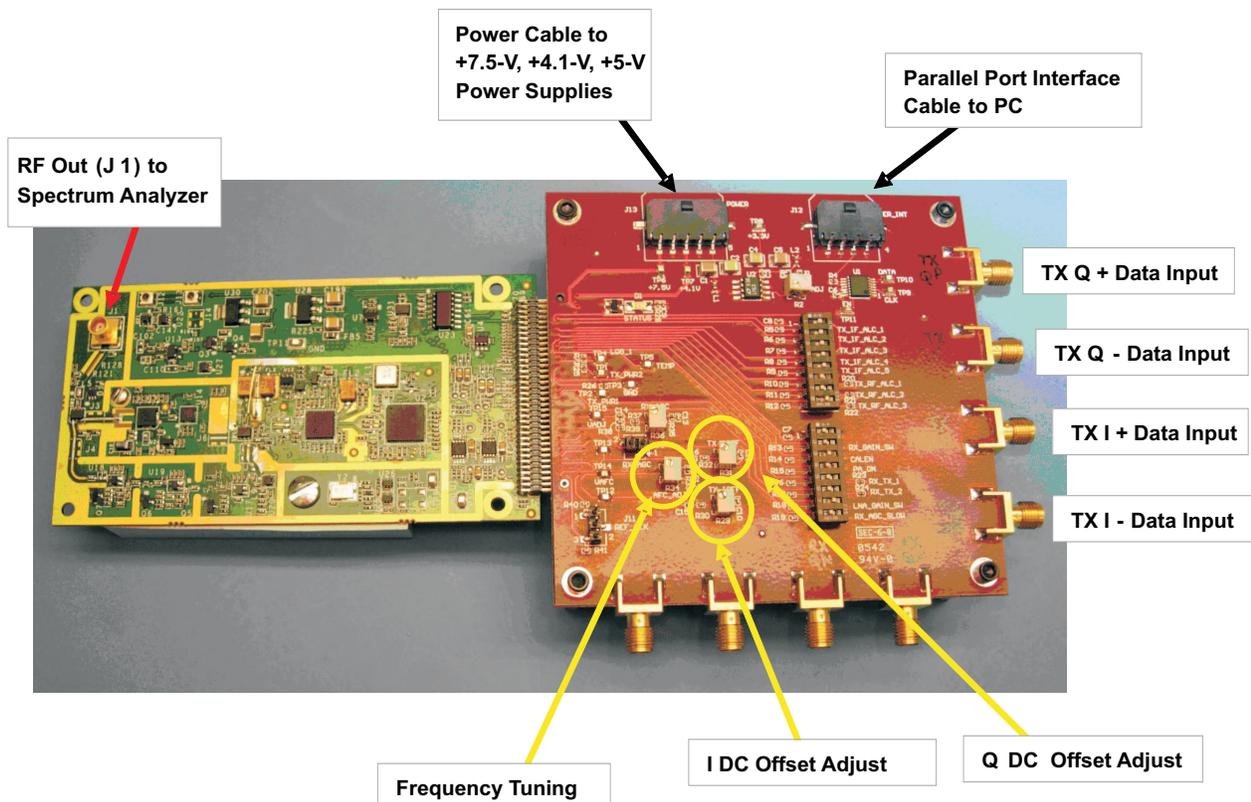


Figure 1. Connections for TX Operation

2. Set DIP switches on the adapter board as follows:
 - TX_IF_ALC[1:5] = X**
 - TX_RF_ALC1 = 0**
 - TX_RF_ALC2 = X**
 - TX_RF_ALC3 = 1**
 - RX_GAIN_SW = X**
 - CAL_EN = X**
 - PA_ON = 1**
 - RX_TX_1 = 1**
 - RX_TX_2 = 1**
 - LNA_GAIN_SW = 0**
 - RX_AGC_SLOW = 0**
3. Turn on power supplies
4. On the control GUI, click on *Configure Par Port* and verify that CLOCK = bit 0, DATA = bit 1, and LE = bit 2. Change settings if required (see Appendix A).
5. Program DUT to ACTIVE mode on the control GUI.
6. Program IF VCO and RF VCO frequencies as desired on the control GUI. Nominal IF VCO frequency = 398 MHz. Nominal RF VCO frequency = 2998 MHz (for 5600 MHz RF output).
7. Click on *Write Registers* on the control GUI.
8. Observe RF output on spectrum analyzer or VSA.
9. Adjust TX I/Q input drive, TX Output Power Control setting (on control GUI), and TX_RF_ALC1 DIP

- switch (on adapter board) as required for desired TX output power level.
10. Adjust I Offset (R29) and Q Offset (R31) using potentiometers on adapter board to null carrier (for single tone IQ input) or minimize I/Q offset error (for modulated IQ input).
 11. Adjust AFC_ADJ (R34) on adapter board as required to minimize frequency error.

Note: For optimum EVM performance, I/Q quadrature of the TX I and TX Q inputs must be adjusted to minimize quadrature error. I/Q crossover resistors R234, R235, R258, and R259 can be used to add a fixed quadrature offset to correct for I/Q non-idealities. Contact TI for further information.

4.2 RX Operation

1. Connect the RF board and adapter board together via the 70-pin I/O connector as shown in [Figure 2](#). Connect power, serial interface, RX I/Q baseband signals, and RF input as shown in [Figure 1](#).
2. Set DIP switches on the adapter board as follows:
 - TX_IF_ALC[1:5] = X**
 - TX_RF_ALC1 = 0**
 - TX_RF_ALC2 = X**
 - TX_RF_ALC3 = 1**
 - RX_GAIN_SW = X**
 - CAL_EN = 1**
 - PA_ON = 0**
 - RX_TX_1 = 0**
 - RX_TX_2 = 0**
 - LNA_GAIN_SW = 1**
 - RX_AGC_SLOW = 0**
3. Verify that RF Signal Source at J1 input is OFF.
4. Turn on power supplies
5. On the control GUI, click on *Configure Par Port* and verify that CLOCK = Bit 0, DATA = Bit 1, and LE = Bit 2. Change settings if required (see Appendix A).
6. Program DUT to ACTIVE mode on the control GUI.
7. Program IF VCO and RF VCO frequencies as desired on the control GUI. Nominal IF VCO frequency = 398 MHz. Nominal RF VCO frequency = 2998 MHz (for 5600 MHz RF output).
8. Click on *Write Registers* on the control GUI.
9. STATUS light on adapter board should be OFF.
10. Toggle the CAL_EN DIP switch on the adapter board first LOW, then HIGH.
11. STATUS light on adapter board should be ON. Once the STATUS light is ON, the internal DC offset calibration is active.
12. Set RF Signal Source to desired power level (< -50 dBm) and frequency, and turn ON.
13. Observe baseband I/Q output either on spectrum analyzer (DC coupled) or VSA.
14. Adjust AGC gain using the RX_AGC potentiometer (R36) on the adapter board for desired baseband output level. For high level RF input power (-50 dBm to -20 dBm), use the LNA_GAIN_SW and RX_AGC_SLOW DIP switches to further reduce gain.
15. Adjust AFC_ADJ (R34) on adapter board as required to minimize frequency error.

Note: For optimum EVM performance, I/Q quadrature of the RF input source must be adjusted to minimize quadrature error. (No quadrature correction is available in the RX path on the Transceiver Board).

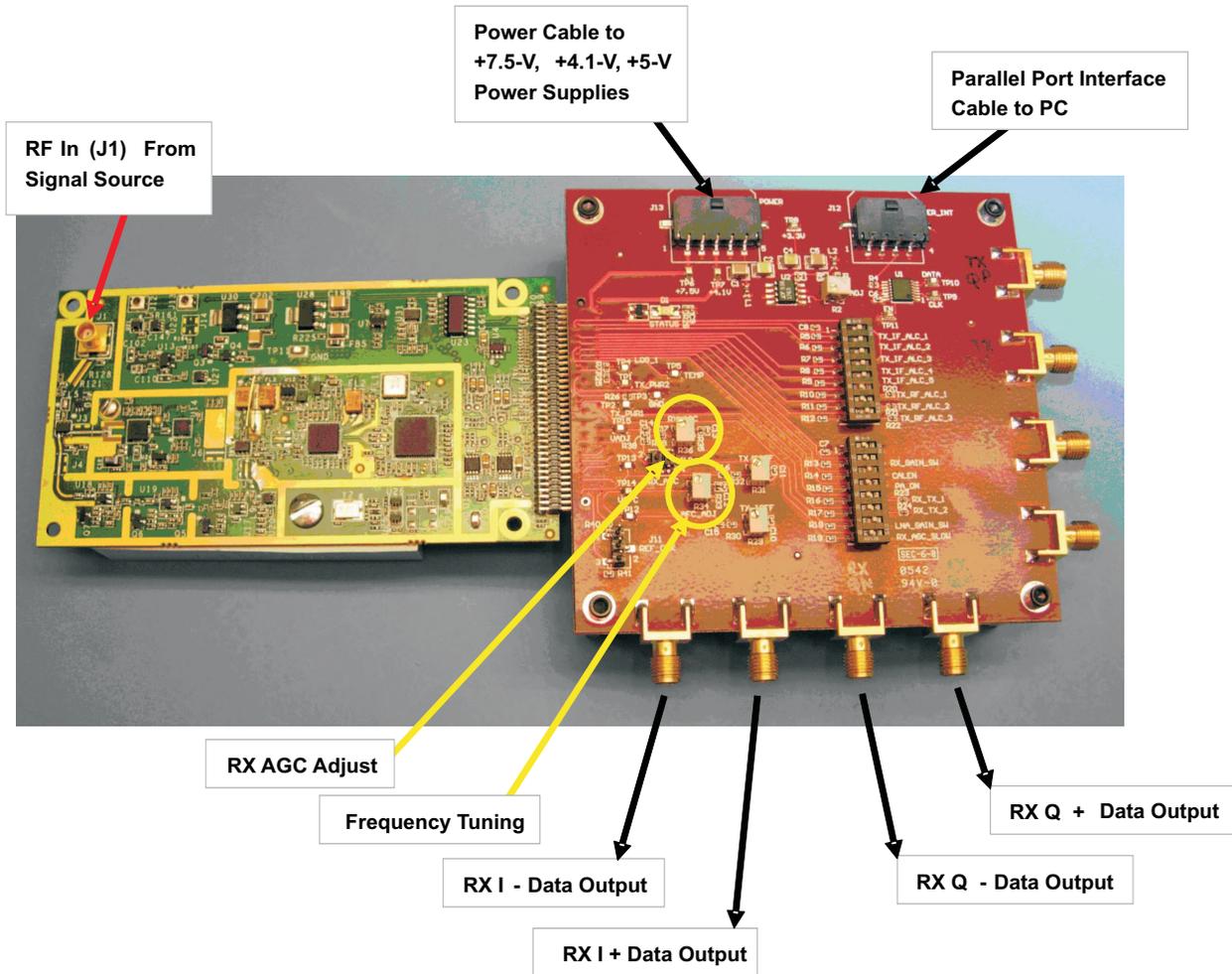


Figure 2. Connections for RX Operation

Appendix A Programming the TI WiMAX 5.6-GHz Transceiver Board

Programming control of the TI WiMAX 5.6-GHz Transceiver Board is performed via J12 on the adapter board. An interface cable is provided with the board to transition from J12 on the reference board to a standard DB-25 style connector. This should be connected to the PC parallel port via a standard 1:1 wired DB-25 cable.

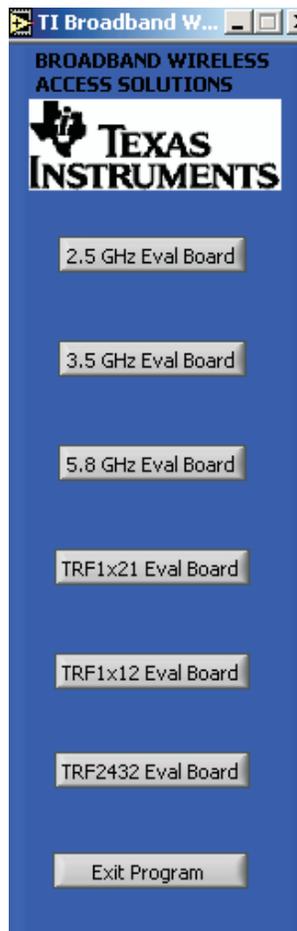
The pin assignments for the reference board are shown in [Table A-1](#):

Table A-1. Reference Board Pin Assignments

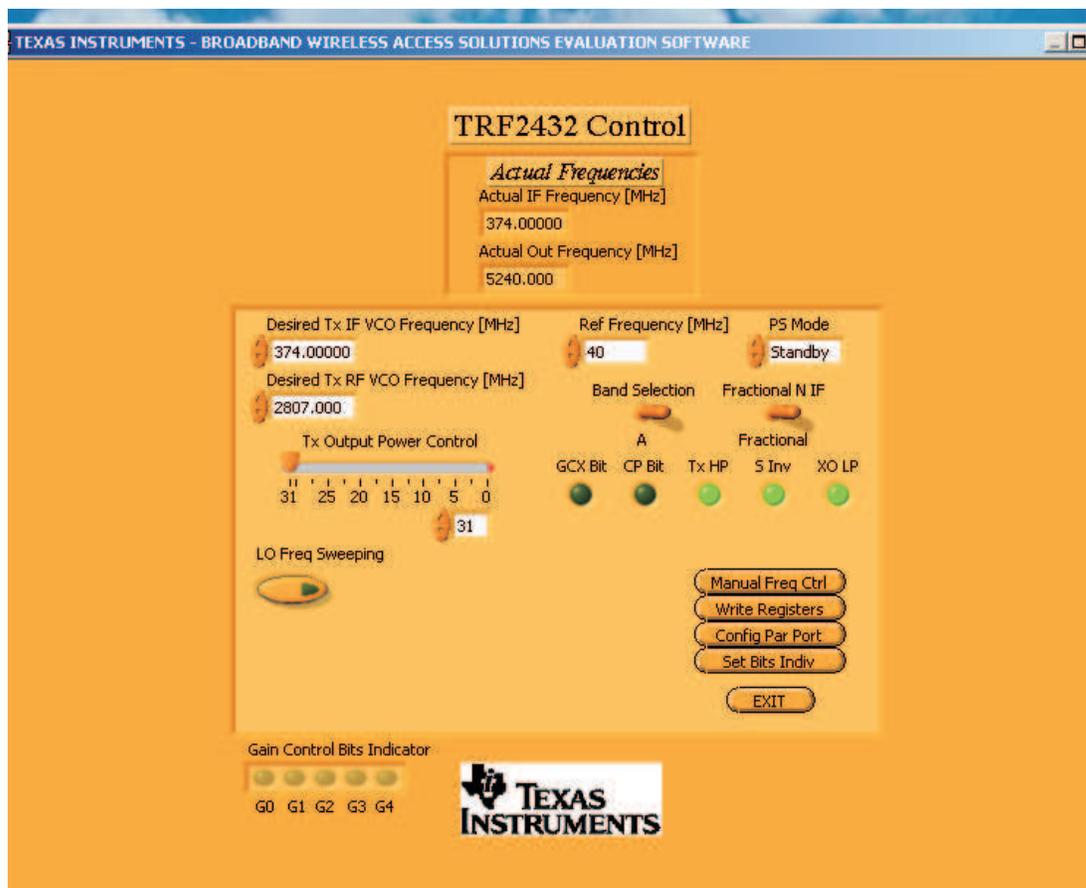
Data Bit	Signal Name	PC DB25 Pin	Ref Board J1 Pin
0	CLK	2	1
1	DATA	3	2
2	LE	4	3
—	GROUND	18	4

A CD is provided which contains the setup.exe file. This is a LabVIEW™-based stand-alone executable program for control of the TI WiMAX 5.6-GHz Transceiver Board. See the **TI BWA Labview GUI Installation Instructions.doc** file on the CD for installation instructions.

After installation, the following main menu should appear after clicking START → PROGRAMS → TI_BWA:



Click on the 5.8 GHz Eval Board button. The following control GUI should appear:



All controls needed to program the TI WiMAX 5.6-GHz Transceiver Board are available on the main screen (identical controls as the TRF2432 chip). See the TRF2432 data sheet ([SLWS177](#)) for explanation of controls. The Actual Output Frequency is shown for convenience only and is the calculated RF frequency that is generated.

A.1 Parallel Port Configuration

The parallel port address and pin assignments can be modified by clicking on the **Configure Par Port** button. The default parallel port address is 378, and the pin assignments must be set to match [Table A-1](#). To ensure proper operation, ensure that the bit settings are set as follows:

- Bit for CLK = 0
- Bit for DATA = 1
- Bit for LE = 2

A.2 Manual Register Control

Manual bit-by-bit control of all programming registers is available by clicking on the **Set Bits Individually** button. The register/bit assignments are in the same order as [Table 16](#) of the data sheet.

After modifying bit settings, press the **SEND** button next to a register to write the new values to the TRF2432. Any changes made to the bit settings on this panel are reflected on the main panel when the manual panel is closed.

CAUTION

Manually modifying bit settings can potentially send invalid control values. Use at your own risk.

A.3 Manual Frequency Control

The **Manual Freq Control** panel allows input of IF/RF register values and any reference frequency. It calculates the resulting IF VCO and RF VCO frequencies. Pressing the **SEND** button writes the new register values to the TRF2432. This panel is intended for use in cases where an external reference frequency other than 40 MHz or 44 MHz is used.

CAUTION

Manually modifying frequency control registers can potentially send invalid control values. Use at your own risk.

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EVM WARNINGS AND RESTRICTIONS

It is important to operate this EVM within the input voltage range of 0 V to 3.3 V and the output voltage range of 0 V to 3.3 V.

Exceeding the specified input range may cause unexpected operation and/or irreversible damage to the EVM. If there are questions concerning the input range, please contact a TI field representative prior to connecting the input power.

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During normal operation, some circuit components may have case temperatures greater than 85°C. The EVM is designed to operate properly with certain components above 85°C as long as the input and output ranges are maintained. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors. These types of devices can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during operation, please be aware that these devices may be very warm to the touch.

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