

Develop a Dallas 1-Wire[®] Master Using the Z8F1680 Series of MCUs

AN033101-0412

Abstract

This Application Note describes how to interface the Dallas 1-Wire bus with Zilog's Z8F1680 Series of MCUs as master devices. The Z8F0880, Z8F1680 and Z8F2480 devices that comprise Zilog's Z8F1680 Series features multiple general-purpose input and output pins which can be easily configured to interface to the Maxim/Dallas Semiconductor 1-Wire device. Because there is no hardware support for the 1-Wire protocol¹, the Z8F1680 MCU can communicate over the 1-Wire bus via software control (i.e., bit banging). For this application, a DS18S20 temperature sensor is used as a demonstration of the 1-Wire protocol through a single slave configuration.

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- **Note:** The source code file associated with this application note, [AN0331-SC01.zip](#), is available free for download from the Zilog website. This source code has been tested with version 5.0.0 of Zilog Developer Studio (ZDSII) for Z8 Encore! XP MCUs. Subsequent releases of ZDSII may require you to modify the code supplied with this application.
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Overview of the 1-Wire Bus

1-Wire products provide a combination of memory, mixed signal and secure authentication functions via a single contact serial interface. With both power and communication delivered via serial protocol, 1-Wire devices are unmatched in their ability to provide key functions to systems where interconnects must be minimized.

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- **Note:** For more information about the Maxim/Dallas 1-Wire Interface, visit <http://www.maxim-ic.com/products/1-wire/>.
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The 1-Wire bus employs a single wire/data line to receive and transmit data; its architecture requires a pull-up resistor to pull up the voltage on the data line at the Master side. This pull-up resistor is used to power the 1-Wire device during idle time.

The 1-Wire bus also communicates via half-duplex transmission, in which the Master and Slave can transmit and receive commands one at a time; the Master initiates and controls all 1-Wire operations. The 1-Wire bus operates in a three-phase transaction scenario which includes a reset sequence, an 8-bit ROM command sequence and an 8-bit function command sequence. Additionally, the 1-Wire bus uses conventional CMOS/TTL logic and operates at 2.8V to 6.0V.

1. The 1-Wire protocol is a registered trademark of Maxim/Dallas Semiconductor.

Theory of Operation

The 1-Wire bus protocol is called *1-Wire* because it only uses one data line to send and receive data from the 1-Wire device. Communication between the Z8F1680 MCU and the 1-Wire device is set up in a Master-Slave interface.

The 1-Wire Bus Connection to the Z8F1680 Series MCU

The entire Z8 Encore! XP Family of MCUs can interface as masters with the 1-Wire bus interface; the 1-Wire device can be connected as a slave to any of the general-purpose input/output Z8 Encore! XP MCU pins. The DS18S20 temperature sensor can be powered by an external supply on the V_{CC} pin, or it can operate under *parasite power*, which allows the DS18S20 to function without an external supply. However, parasite power is not recommended for use under extreme high temperatures due to potentially unstable communications resulting from high leakage currents that could occur.

The advantage of a conventional power supply method over the parasite power method is that an external MOSFET pull-up is not required. As a result, the 1-Wire bus is free from transaction overhead associated with the use of parasite power. Figure 1 shows a schematic diagram of the 1-Wire bus with an external pull-up resistor that is used to pull the data line to logic High using an external power supply.

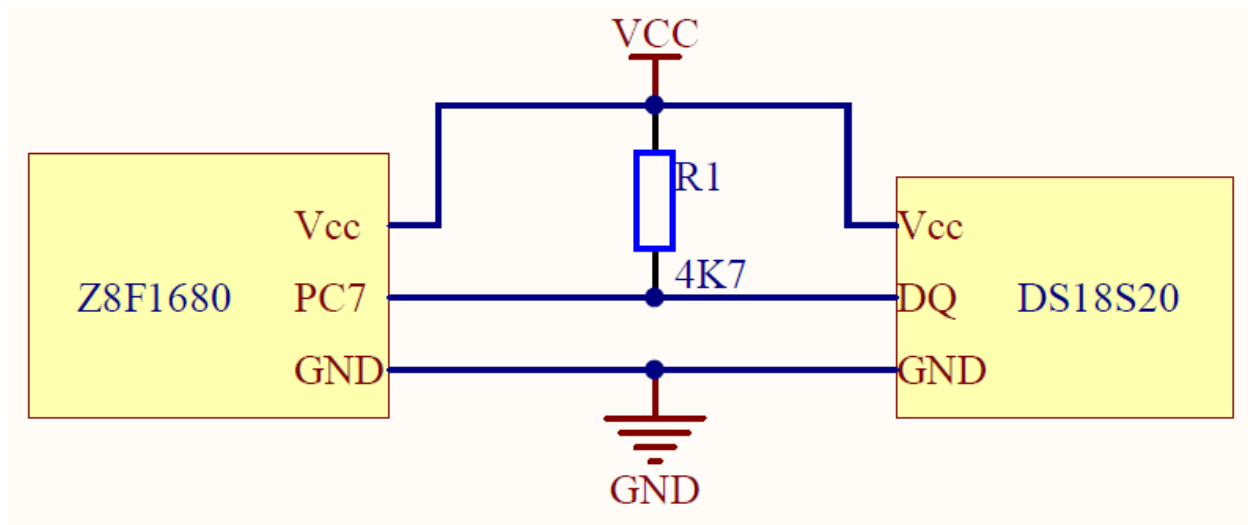


Figure 1. Schematic Diagram of the 1-Wire Interface with a Z8F1680 Series MCU

Discussion: 1-Wire Bus Operation

The 1-Wire bus consists of four basic operations: Reset, Write 0 Bit, Write 1 Bit and Read Bit. This section discusses these four operations wherein the activity of the 1-Wire protocol occurs.

Reset

The following events occur when a Reset command is issued over the 1-Wire bus; see Figure 2.

- The pull-up resistor pulls the data line High at all times.
- The 1-Wire Master initiates the data line Low for at least $480\mu\text{s}$ and not more than $640\mu\text{s}$ during its reset sequence, then sets the data line to *open drain* or *high impedance*.
- The 1-Wire Slave sets a presence pulse by triggering the data line to Low after $15\text{--}60\mu\text{s}$ from the time the Master sets the data line to *open drain*.
- The Master checks the data line for this presence pulse approximately $60\text{--}240\mu\text{s}$ from the time the data line is set to *open drain*.
- The 1-Wire Slave undergoes a recovery period of at least $1\mu\text{s}$.

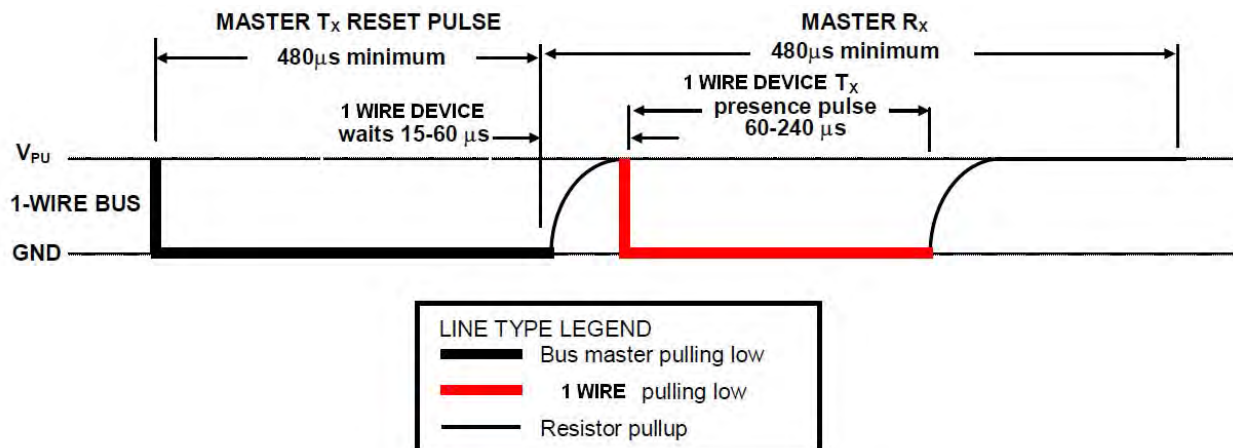


Figure 2. Reset Timing Diagram

Write 0

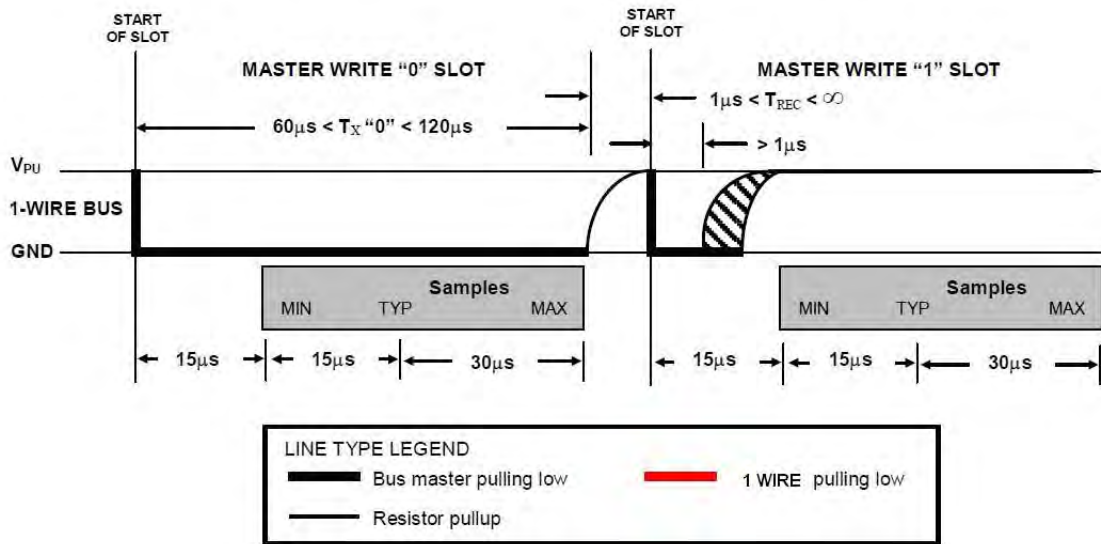
The following sequence of events occurs when a Write0 command is issued over the 1-Wire bus; see Figure 3.

1. The Master writes to bit 0 by pulling the data line Low for at least $60\mu\text{s}$, then releasing the data line as *open drain*.
2. The Master waits for a minimum recovery period of $1\mu\text{s}$ before sending the next bit.

Write 1

The following sequence of events occurs when a Write1 command is issued over the 1-Wire bus; see Figure 3.

1. The Master writes to bit 1 by pulling the data line Low within 15 μ s, then releasing it by setting the data line to *open drain*.
2. The Master waits for a minimum recovery period of 1 μ s before sending the next bit.



Read

The following sequence of events occurs when a Read command is issued over the 1-Wire bus:

1. The Master pulls the data line Low for at least 1 μ s, then releases the bus.
2. After releasing the data line, the Master waits for a period of 15 μ s, then checks the data line.
3. The Master reads logic 0 if the data line is Low within 15 μ s; otherwise, the Master reads logic 1 if the data line is High.

A read/write timing diagram of these events is shown in Figure 4.

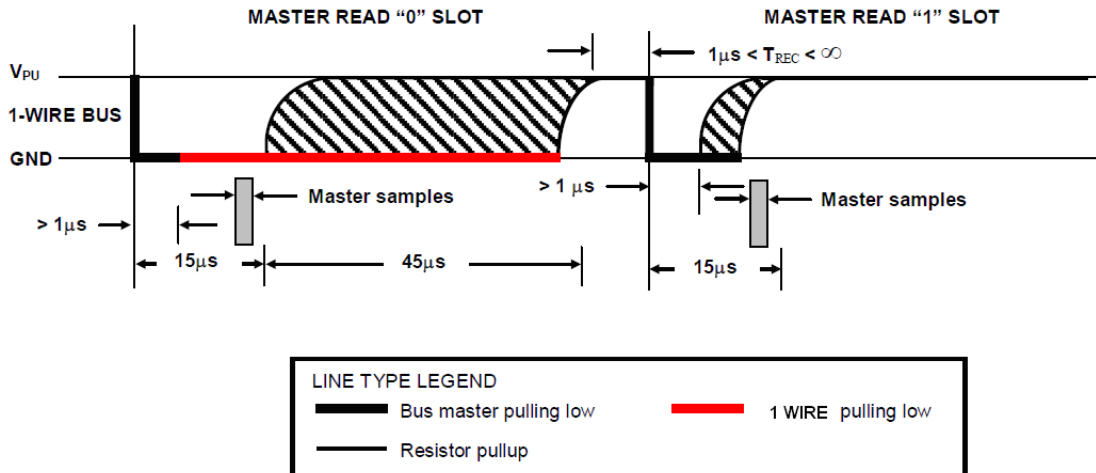


Figure 4. Read Timing Diagram

Software Implementation

1-Wire communication consists of a three-phase operation sequence that includes a Reset sequence, a ROM sequence and a function sequence. The Write 0, Write 1, and Read bits are the basic building blocks of the ROM and function command sequences. All timing requirements for the Maxim/Dallas 1-Wire bus are catered by the Z8F1680 MCU's timers via interrupts. An API library has been created with the 1-Wire function; this library can be modified by the user depending on the GPIO pin to be used with the 1-Wire interface.

There are items to consider regarding the usage of Timer0 and Timer1 to meet the timing requirement range imposed by the Maxim/Dallas 1-Wire protocol, as the following discussion shows.

Timer0

- Required time interval: 1 ms
- Prescale = 4
- Mode = CONTINUOUS
- Timer Clock Frequency = 20MHz

The equation below calculates the required CONTINUOUS Mode time-out interval.

$$\text{CONTINUOUS_Mode_Time-Out_Period(s)} = \frac{\text{reloadValue} * \text{prescale}}{\text{TimerClockFrequency (Hz)}}$$

Plugging these values into the above equation to achieve a CONTINUOUS Mode time-out interval of 1 ms solves for the Timer0 reload value, as follows:

$$1 \text{ msec} = \frac{\text{reloadValue} * 4}{20,000,000\text{Hz}}$$

$$\text{reloadValue} = 5000 \text{ or } 0x1388$$

Timer1

- Required time interval: 10.4 μ s
- Prescale = 4
- Mode = CONTINUOUS
- Timer Clock Frequency = 20MHz

Using the same equation used to calculate for Timer0, we can calculate the Timer1 reload value:

$$10.4\mu\text{sec} = \frac{\text{reloadValue} * 4}{20,000,000\text{Hz}}$$

$$\text{reloadValue} = 52 \text{ or } 0x34$$

► **Note:** Additional delay time can be factored in with an assembly instruction called NOP `asm("nop")`. The NOP instruction is used as a special delay mechanism to achieve a 1-Wire timing requirement of less than 10.4 μ s; the Z8F1680 MCU's timer is not able to provide this delay time. However, a NOP instruction requires only 1 instruction cycle to complete and meets this 1-Wire timing restriction.

Reset Function

The Master initiates communication via a system reset function (i.e., Reset). In this Reset function, the Master sets the port pin to output and sets the data line to Low for at least 480 μ s, then releases the data line, sets it to input and, 15–60 μ s after it releases the data line, the Master checks the data line for a presence pulse from the 1-Wire device.

The presence pulse is used to determine if a 1-Wire device is present. This presence pulse exists if the data line is pulled down by the 1-Wire device between 60-240 μ s after the Master has released the data line as open drain.

The following routine resets all 1-Wire devices.

```
void Reset_Function(void)
{
    output_TX();                // initialize ports as OUTPUT
    ONEWIRE_OUT = ~ONEWIRE_PIN; // Pull LOW
    delay_us(TIME_480to640µs); // delay 480µs Rx
    input_RX();                // initialize ports as INPUT and
                                // presence pulse will appear
    delay_us(TIME_470µs);      // rest of 480 µs slot Tx
}
```

Write 0 Function

In the routine that follows, the Master sets the port pin to output and pulls the data line by sending logic Low within 60-120µs. The port pin is next set to input to release the data line; a recovery period of 1µs ensues on the 1-Wire device.

```
void Write_Zero_Bit(void)
{
    output_TX();                // set port to OUTPUT
    ONEWIRE_OUT = ~ONEWIRE_PIN; // LOGIC LOW
    delay_us(TIME_60to120µs);   // time slot at 60-120µs
    input_RX();                // pull up (release) SET AS INPUT
                                // to draw pull-up
    delay_us(TIME_RECOVERY);    // recovery time from 1µs to
                                // infinity
}
```

Write 1 Function

In the following routine, the Master sets the port pin to output and pulls the data line by sending a logic Low within 15µs. Next, the port pin is set to input to release the data line of the 1-Wire device.

```
void Write_One_Bit(void)
{
    output_TX();                // set port output
    ONEWIRE_OUT = ~ONEWIRE_PIN; // pull logic low
    asm("nop");                 // delay within 1-15µs
    input_RX();                // pull up (release)
    delay_us(TIME_RECOVERY);    // recovery time
}
```

Read Function

In this routine, the Master sets the port pin to output and pulls the data line by sending a logic Low for a minimum of 1µs, then releasing the bus. The 1-Wire device begins transmitting a 1 or 0 on the bus.

```
UINT8 Read_Bit(void)
{
    output_TX();                // set as output to pull logic Low
    ONEWIRE_OUT &= ~ONEWIRE_PIN;
    asm("nop");
    input_RX();                 // set as input for pull-up
    asm("nop");
    if(ONEWIRE_IN&ONEWIRE_PIN)  // checks the bits of data if the
                                // data is in the specified bus
    {
        return 1;    // reads as 1
    }
    else
    {
        return 0;    // reads as 0
    }
}
```

Transmit Byte Function

In the following routine, the transmit byte function is used to transmit a byte of data to the 1-Wire device. These 8 bits of data are interpreted as Write0 and Write1, with the least significant bit sent first.

```
void onewire_CMD(UINT8 data)
{
    UINT8 i=0;
    for (i=0;i<8;i++)
    {
        if (data & (0x01 << i))    // check every bit to send to
                                    // DS18S20
        {
            Write_One_Bit();        // write 1 into 1-Wire device
        }
        else
        {
            Write_Zero_Bit();       // write 0 into 1-Wire device
        }
    }
}
```

Receive Byte Function

In the routine that follows, the received byte function interprets the received bits of data on the data line. This function is also used as an indicator of the maximum number of bytes that the device is expected to receive.


```
UINT8 ONEWIRE_Receive(void)
{
  UINT8 value=0;
  UINT8 i=0;
  UINT8 bit=0;
  for(i=0;i<8;i++) // receives the data byte (8 bits)
  {
    bit=read_bit();
    if(bit) value|=(1<<i); //
    delay_us(TIME_50to60µs); // 50µs delay for data integrity
  }
  return(value);
}
```

DS18S20 Temperature Sensor Operation with the 1-Wire Device

The 1-Wire interface application focuses on the `void Read_Temperature(void)` function. With this function, the DS18S20 sensor follows a transaction sequence (i.e., the Reset, ROM and Function sequences) of the 1-Wire interface, as indicated in Table 1.

Table 1. DS18S20 Temperature Sensor Functions

Function	Description
<code>Reset_Function();</code>	The Z8F1680 MCU issues reset pulse and check if DS18S20 responds with a presence pulse.
<code>onewire_CMD(SKIP_ROM);</code>	The Z8F1680 MCU issues Skip ROM command to DS18S20.
<code>onewire_CMD(CONVERT_T);</code>	The Z8F1680 MCU issues Convert T command in which DS18S20 converts temperature.
<code>Reset_Function();</code>	The Z8F1680 MCU issues reset pulse.
<code>onewire_CMD(SKIP_ROM);</code>	The Z8F1680 MCU issues Skip ROM Command.
<code>onewire_CMD(RD_SCRPAD);</code>	The Z8F1680 MCU issues Read Scratchpad to DS18S20.
<code>ONEWIRE_Receive();</code>	The Z8F1680 MCU reads entire scratchpad including the generated CRC byte made by the DS18S20.

After the scratchpad data is gathered, Byte 0 and Byte 1 — which are the LSB and MSB of the temperature data, respectively — are fetched by the CPU. This temperature data is factored into an actual temperature reading that is displayed in degrees Centigrade and Fahrenheit.

Testing/Demonstrating the Application

This 1-Wire application is comprised of the following hardware and software elements, plus documentation.

Hardware

- USB Smart Cable
- USB to RS-232 cable
- 4.7 K Ω resistor (labeled R1 in [Figure 1](#) on page 2)
- 1-Wire Temperature Sensor (DS18S20)
- Z8F1680 Development Board with external 20 MHz crystal

Software

- ZDS II version 5.0.0 for Z8 Encore! XP MCUs
- [AN0331-SC01](#) zip file containing project and source files
- BAUD_57600 HyperTerminal file

Documentation

- [Z8 Encore! XP F1680 Series \(28-Pin\) Development Kit User Manual \(UM0203\)](#)

Connect the Hardware

Observe the following procedure to connect all hardware elements.

1. Connect the 1-Wire Temperature Sensor (DS18S20) to V_{CC} (pin 3), GND (pin 5) and DQ (1-Wire bus). The DQ pin is connected through a 4.7 K Ω pull-up resistor to Port C Pin 7 (Pin 13 of JP2) of the Z8F1680 Development Board; see the schematic diagram presented in [Figure 1](#) on page 2.
2. Connect the USB to RS-232 cable to the PC, and connect the USB Smart Cable to the Z8F1680 Development Board.
3. Connect the power cable to apply power to the Z8F1680 Development Board.

Configure the Software

Observe the following procedure to configure the software.

1. Open the BAUD_57600 HyperTerminal file located in the AN0331-SC01.zip file. Configure the settings in HyperTerminal to:
 - Baud = 57600
 - Data Bits = 8
 - Parity = None
 - Stop Bits = 1
 - Flow Control = None

2. Open ZDSII, then navigate via the **File** menu in ZDSII to open the project file labeled AN0331 - Develop 1-Wire interface using Z8F1680.zdsproj.

► **Note:** The project is set to an external clock frequency of 20MHz.

3. Compile and download the program to the Z8F1680 Development Board.
4. You should see the HyperTerminal display shown in Figure 5; the Serial ID may vary depending on the type of 1-Wire device you are using. If you do not see this display, verify your Configure Target settings in the ZDSII project settings (**Project** → **Settings...** → **Debugger** → **Setup button**) to ensure that Clock Source is set to **External** and that Clock Frequency (MHz) is set to **20.00000**.

```
232 - HyperTerminal
File Edit View Call Transfer Help
=====
Welcome 1 Wire Interface using Z8F1680
=====

Device Serial ID = 0x10 0x58 0xd 0x32 0x12 0x8 0x0 0xee
Device is DS18S20 1-Wire Temperature Sensor
_ TempC= 26 degrees C || TempF= 78 degrees F

Connected 1:01:11      ANSIW      57600 8-N-1      SCROLL  CAPE  NUM  Capture  Print
```

Figure 5. 1-Wire Interface Output to HyperTerminal

Results

The temperature readings, as displayed in HyperTerminal, indicate that the firmware developed for the Z8F1680 and DS18S20 devices satisfies the the timing requirements of the 1 Wire interface. The Device ID of the 1-Wire temperature sensor is detected and displayed as well.

Summary

This application note discusses the implementation of a Dallas 1-Wire interface using Zilog's Z8F1680 Series microcontrollers. The application passes the timing requirements of the 1-Wire interface, and successfully implements the 1-Wire interface with a 1-Wire temperature sensor. The firmware described herein can be used with other types of 1-Wire devices, which can be determined by means of identifying a device's family code.

References

The following document describes the functional specifications of the Z8F1680 MCU, which is a member of Zilog's Z8 Encore! XP Series of MCUs.

- [Z8 Encore! XP F1680 Series Product Specification \(PS0250\)](#)

The following documents can be found on <http://www.maxim-ic.com>.

- [1-Wire Devices](#)
- [1-Wire Tutorial](#)
- [1-Wire Parasite-Power Digital Thermometer](#)

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