

Hipot (Withstanding Voltage) & Insulation Resistance Tester

# **TOS5300 Series**

The PWM amp system provides highly-stable output 5 kV/100 mA (500VA) AC Hipot (withstanding voltage) test 6 kV/maximum output 50W DC Hipot (withstanding voltage) tester (TOS5301) 25 V-1000 V (7 steps), 500V or greater, up to  $5.00 \text{G}\Omega$  Insulation Resistance test High-precision measurement  $\pm 1.5\%$  of reading (with voltmeter 500V or higher, Ammeter 1mA or higher) Rise time/Fall time control Key lock function and Protection cover for key operation Equipped with USB interface



# **Highly stable**

Newly developed, high-efficiency PWM switching amplifier!

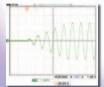


Providing a stable output of high voltage without being affected by AC line variation. Ensure the user to perform highly reliable testing with confidence, even in regions with large voltage variations. (Input voltage fluctuation rate: ±0.3%)

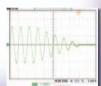
# Rise/Fall Time control function of the applied voltage

Equipped with a Rise time/ Fall time control function

Prevents from an excessive stress applied to the EUT or for standard tests.







▲ Fall time control wave

# Supporting the World-wide input voltage

Reducing

the tact time

Increasing the productivity!

Capable of setting the test time from 0.1s

# Usable globally!

Usable in any country without changing the input power supply.

# Selectable output frequency!

The instrument not rely on the input power environment. Supplying the stable test voltage with 50/60 Hz frequencies.

# A new standard for & (epater publication) to qilt gnitest esmaticer nottaluanl epatev (or bellog) epatev fugni

# Lightweight & Compact design

### Increasing your work efficiency!

Weighs Approx. 15 kg 40% lighter than conventional models \*Compared to TOS5300 and TOS8870A

# Pursuing usability and safety

# All new design of the control panel and output terminals!

Eliminates the projected components of output terminals, and equips with a new type of the LOW terminal. Pursuing the improvement of safety and a convenience in production line, such as providing the protection cover for the front panel.



▲ Output terminal Left : HIGH (red) Right : LOW (black, with lock function)



▲ View with the protection cover removed from front panel

# 200mA & 500VA!



Hipot (Withstand-Voltage) & Insulation Resistance Tester

# TOS5300 Series

The "TOS5300 Series" is a series of test instruments used in Hipot (withstanding voltage) tests and insulation resistance tests, two of the four tests regarded as necessary for ensuring the safety of electrical products. With an output of 5 kV/100 mA (AC) and 6 kV/10 mA (DC), the series can be used in Hipot (withstanding voltage) & insulation resistance testing of electronic equipment and electronic parts, based on the requirements of IEC, EN, UL, VDE, JIS, and other international safety standards and the Electrical Appliance and Material Safety Law. Also, the test voltage stability is improved with the adoption of a newly developed switching amplifier. Since the output voltage can be kept constant even when the AC line voltage or frequency changes, consistent testing can be performed, even when the power supply environment is in an unstable region. The TOS5300 is also equipped with a number of features that are capable of meeting a variety of test needs. It is a new low-cost standard model that provides thorough operability, reliability and safety.

Applied test / Model	TOS5300	TOS5301	TOS5302
AC Hipot (Withstanding voltage) test (ACW)	V	V	V
DC Hipot (Withstanding voltage) test (DCW)		V	
Insulation Resistance test (IR)			<b>V</b>

Features and Functions

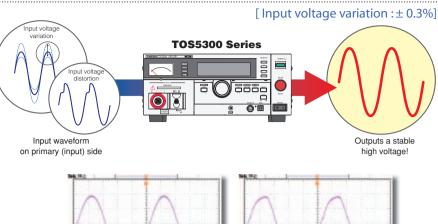
NEW

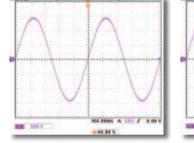
● ACW: 5 kV/100 mA; DCW: 6 kV/50W ● IR: 25-1000V (7 steps)/500V or greater, up to 5.00 GΩ ● High-precision measurement  $\pm$  1.5% of reading (Voltage: 500V or greater; Current: 1 mA or more) ● Rise time / Fall time control function ● Discharge function ● World-wide input voltage ● AUTO function ● USB interface ● Panel memory function (3 sets) ● Key lock and Protection cover for panel operation

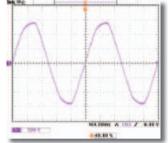
# The achievement of AC Hipot (Withstanding voltage) testing with a constant stable output!

The output waveform is essential factor in Hipot (Withstanding voltage) testing!

A conventional Hipot (Withstanding voltage) tester boosts and outputs the AC line's input voltage through the use of a slide transformer. With this slide transformer system, input voltage fluctuations will affect the output, preventing tests from being performed properly. At times, the application of distortion voltage applied to the EUT may cause a failure of new product (accelerating a deterioration of components). Since the TOS5300 Series equips with a high-efficient PWM amplifier that can output a stable high-voltage without being affected by the variation of AC power line, users can perform "safe", "stable", and highly "reliable" tests with confidence, even in regions with large voltage variations.





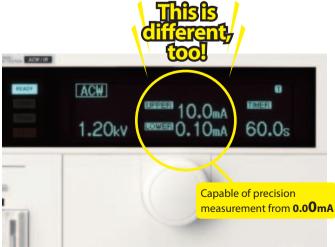


▲ AC output waveform of the TOS5300 Series

▲ AC output waveform of the slide transformer system

# Realizing high-precision measurement with high-resolution and high-speed judgement

Equipped with a high-accuracy, high-resolution of True RMS measurement circuit, including a Voltmeter with  $\pm 1.5\%$  of reading (500V or greater) / minimum resolution of 1V, and an Ammeter with  $\pm 1.5\%$  of reading (1 mA or more) / minimum resolution of 1µA. In addition, it is also equipped with an Auto range function, with achieving a judgment accuracy of  $\pm 1.5\%$  of reading. The Lower limit judgment accuracy achieves a level of performance equivalent to the Upper limit judgment accuracy that enables to detect for such a poor contact or disconnections of test leads. Moreover, it realizes the fast judgment by the test time of 0.1 second, while reliable testing can be performed, thanks to high-precision, high-resolution, high-speed measurement and the judgment functions.



▲ AC Hipot (Withstanding voltage) test settings display (example)

# Supporting the World-wide input voltage



Usable in any country, without changing the input power supply.

Selectable output frequency!

The instrument not rely on the input power environment. Supplying the stable test voltage with 50/60 Hz frequencies.

# Reducing the tact time

Reduction of the tact time leads to improve the productivity. However, it has been an issue that reducing the tact time may cause to worsen the measurement accuracy when the test time is faster than the measuring response speed. The TOS5300 series has been achieved to set the test time from 0.1s.

(Model TOS5301)

# 6kV/50WDC Hipot (Withstanding voltage) test

Capable to perform DC Hipot (Withstanding voltage) test up to 6 kV. (Model TOS5301) Equipped with a stable DC/DC converter with a low-ripple and the load variation of 3% or less.

# Insulation resistance test for 25V to 1000V\*

The TOS5302 is equipped with an insulation resistance tester. The test voltages can be set from 25V, 50V, 100V, 125V, 250V, 500V and 1000V. And for setting at 500V and above, it can perform the insulation resistance test up to  $5.00~\rm G\Omega$ .

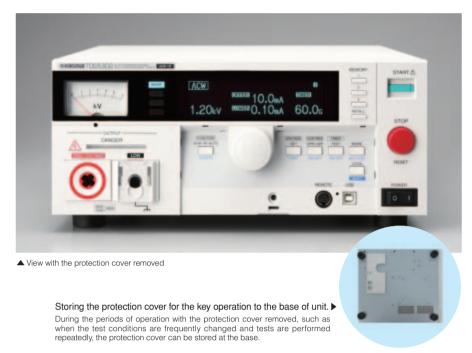
\*At 500V and above, measurements up to 5.00  $G\Omega$  are possible.

# Protection cover prevents physical operation error in the production site

Prevents a physical operation error by installing the protection cover on the control keys.



In many cases, workers on electronic equipment production lines and inspection lines are not technical experts. Therefore, it is possible that the operators may change setting conditions and make operation errors. In order to prevent from such cases, the TOS5300 is equipped with a key lock function and a protection cover to disable a physical key operation from the front panel.



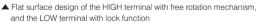
# New design of output terminal improves safety and functionality

# Eliminates the projected components from the front panel. The new design of LOW terminal.

In consideration of safety for the operator and the environment, the output terminal of HIGH-side has been placed in the most distant location from the control area. The free rotation machanisim protects from twisting (or breaking) of the cable. Also, with having the lock function for the LOW terminal on the main unit, the metal plate is no longer attached to the test lead of LOW-side, and it makes to resist damage to the test lead. Because of elimination of these projected components, the TOS5300 can avoid from unexpected accidents such as when the unit travels to other location. And also when the test lead is snagged on something, or unexpected stress is applied on the test lead, the High (High-voltage) test lead is designed to disconnect easily, but the Low (ground) test lead is designed to resist disconnection.

In order to prevent the insertion error, the color coding of the cable are classified to HIGH (red) and LOW (black) , and the plug shape of terminal are also different design.





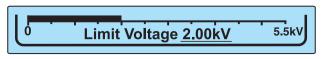


It could be a cause of defect if the cable are incorrectly wired for the HIGH (High-voltage) side and LOW (Low-voltage) side. In order to prevent the insertion error, the protection plate is attached to the Low-voltage (Black) test lead.

# Monitoring the output voltage and protection when applying an overvoltage by the operation error

# LIMIT VOLTAGE function

Prevents the user from setting a test voltage that exceeds the preset voltage.



▲ LIMIT VOLTAGE setting (example)

# Monitoring output voltage function

If the output voltage exceeds the setting voltage of  $(\pm\ 350V)$ , it turns off the output and the system switches to PROTECTION mode. In order to handle kilo's of high voltage when the Hipot (Withstand voltage) and insulation tests are conducted, there are number of safety measures are required to take place. Having with these functions improve, the operational safety and the protection for the EUT.

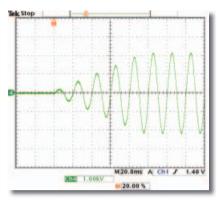
# Rise time / Fall time control function

# Rise time control function

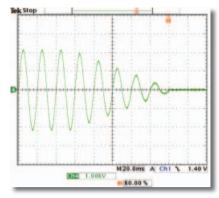
The Rise time control function enables you to increases the test voltage gradually to reach the setting voltage while the AC Hipot (Withstanding voltage) test is conducted. The voltage rise time can be set from 0.1s to 10.0s at a resolution of 0.1s.

### Fall time control function

The Fall time control function enables you to decrease the test voltage gradually when the PASS judgment is made at the AC Hipot (Withstanding voltage) test. The voltage fall time is fixed at 0.1 s. (OFF is also selectable).







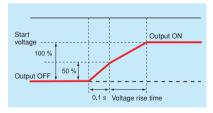
▲ Fall time control waveform (example)



The rise time control function is to prevent the EUT (test object) from being exposed to stress that exceeds the required amount. The Hipot (Withstanding voltage) test is conducted to verify the safety performance of the EUT and which test voltage for Hipot (Withstanding voltage) test is applied approximately five to ten times greater than the voltage that handles by the EUT. If a high voltage is applied rapidly with no rise time, the transitional large voltage (current) will be occurred, and it may cause a damage to the EUT. If, as a result of the test, the EUT is suffered of the insulation (dielectric) breakdown, the failure of defect can easily be identified without any problem. However, when breakage is occurred partially, it becomes hard to identify the problem.

In other words there are cases in which "at a glance, a Hipot (Withstanding voltage) test appears to have been successfully passed, however, the fact is found that the insulation performance has degraded." In such cases there is a potential risk of danger that the insulation failure will occur after the EUT has been released into the market as a commercial product. The result of testing performed to confirm safety may cause the loss of product's safety. For this reason, safety standards stipulate the procedure of Hipot (Withstanding voltage) test, and the test voltage must be gradually increased to the specified voltage when the test is performed. The rise time control function adopted in the in the TOS5300 Series can set the voltage rise time from 0.1s to 10.0s (at a resolution

of 0.1 s) and also it is capable to set the 50% (fixed) of the applied test voltage. In addition, the fall time control function enables to decrease the test voltage gradually after the completion of a PASS judgement. The voltage fall time is fixed at 0.1s (OFF is also selectable).



▲ Start voltage can be set at 50% of the test voltage.

### **Examples of Safety Standards** (Routine Tests)

## IEC60950/J60950

### - Information processing equipment

The test voltage applied to the insulation part should be increased gradually from zero to the specified voltage, then hold at the specified voltage for 60 seconds.

# • *IEC60335/J0335* - Home appliances

The initial test voltage should be applied less than half of the specified voltage, then gradually increase to the specified voltage.

### • *IEC60065/J0065* - Audio/video

The initial test voltage should be applied less than half of the specified voltage, then rapidly increase to the specified voltage and hold for 1 minute.

# • IEC61010/JIS C 1010

#### -Measurement equipments

Avoids any detectable transient phenomenon, the test voltage should be increased gradually to the specified voltage within 5 seconds, then hold at the specified voltage for 5 seconds.



### Q.What is a Hipot (Withstanding voltage) test?

**A.** Withstanding test also called a dielectric strength test or Hipot test, a withstanding voltage test is intended to verify whether an electrical product or part has sufficient dielectric strength with respect to the voltage being handled.

### Q.What is PASS / FAIL criteria?

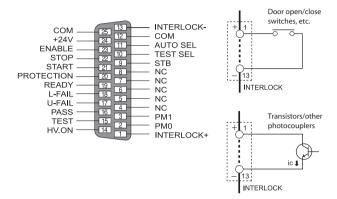
**A.** It is considered as "Electrical breakdown" when the current exceeds the limit value flowing through the insulated section during a test. If "the Electrical breakdown" does not occur, the insulator is determined to have sufficient insulating strength.

#### Q.How is the test conducted?

A. Apply the voltage with much higher stress than it would normally be applied to the insulated section for the specified time period. While testing, it evaluates to verify whether any insulation breakdown has occurred on the insulator.

# Interlock feature

The product is equipped with an interlock function that operates together with external devices to interrupt output. To ensure the safe operation of tester, the interlock function activates when the SIGNAL I/O connector pins number 1 and 13 are opened, and when they are short-circuited, the interlock function is released.





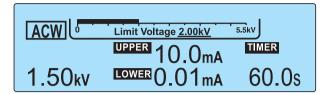
▲ The picture shown above indicates the caution sign of " HI VOLTAGE DANGER" with a chain surrounding the test site.

# Discharge feature (Model TOS5301 / TOS5302)

Equipped with a forced discharge function that forcibly discharge the electricity which has been charged in the EUT after the completion of DC Hipot (Withstanding voltage) test or insulation resistance test.

# **Upper limits / Lower limits setting function**

It automatically detects connector lead breaks and disconnections of wiring by measuring extremely small amounts of current that flows when voltage is applied to the EUT.



▲ Example setting display of Upper limit, Lower limit, and Test time



Normally, even with a good-quality EUT, a certain degree of leakage current flows. If the current value is set at slightly smaller than the specific range of the EUT, it is useful in detecting breaks of the test lead and faulty connections, which enables tests to be performed with even higher reliability. You can perform testing effectively if you set the lower limit value with LOWER ON during Hipot (Withstanding voltage) test, and the upper limit value with UPPER ON during insulation resistance test.

# **Calibration due notice and Warning function**

To assure the traceability of periodic calibration of the product, this function gives a notice of calibration due managed by the built-in real-time clock. Even if the due data has elapsed, it is possible to avoid the oversight of operator with limiting the operation with a display of warning message.



▲ Example setting display of Calibration due







#### Q.What is an insulation resistance test?

A. An insulation resistance test is to measure the resistance value of insulator and verify that whether the insulator has a sufficient performance. It is similar to the Hipot (Withstanding voltage) test that confirms the function or performance of an insulator, and it should be the required conditions to prevent the accidents from an electrical shock and fire.

### Q.What is the procedure of testing?

**A.** In many cases, after moisture is absorbed (or is not absorbed in some cases), 500 volts or other specified value of DC voltage is applied, and the resistance value is measured from the current flowing.

# Q.What is the difference between an insulation resistance test and a Hipot test?

**A.**The Hipot (Withstanding voltage) test detects a faulty insulation whether insulation breakdown occurs. In contrast, the insulation resistance test detects faulty insulation by measuring the resistance value.

# Easy setting of test conditions with panel memory feature!

MEMORY

2

3

RECALL

To set the test conditions such as test voltage, judgment value and test duration, simply press a key and turn the knob on the front panel. The test conditions can be saved in the panel memory (3 sets).

■ Panel memory setting key

(Model TOS5302)

# **AUTO TEST feature for consecutive testing**

The TOS5302 can perform an AC Hipot (Withstanding voltage) test and an insulation resistance test consecutively.

Either of the following can be conducted:

Insulation resistance test  $\rightarrow$  AC Hipot (Withstanding voltage) test, or AC Hipot (Withstanding voltage) test  $\rightarrow$  Insulation resistance test.

AUTO				
FUNC	VOLTAGE	UPPER	LOWER	TIMER
IR	25V	OFF	OFF	0.3s
ACW	1.50kV	10.0mA	OFF	60.0s

▲ Insulation resistance test → AC withstand-voltage test

AUTO				
FUNC	VOLTAGE	UPPER	LOWER	TIMER
ACW	1.50kV	0.02mA	OFF	60.0s
IR	25V	OFF	OFF	0.3s

▲ AC withstand-voltage test → Insulation resistance test

# **REMOTE connector & USB interface**

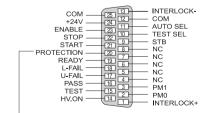


Equipped with the REMOTE connector and USB interface on the front panel are exclusive use for the options. Easy connection with the PC.

# **SIGNAL I/O Connector**

The rear panel is equipped with SIGNAL I/O that provides functions such as start and stop operation and signal output.

TEST SEL   I   NA   NA   Selection signal   Selection of single test with AUTO SEL   L: ACW   H: DCW   Selection of AUTO test with AUTO SEL   L: ACW   H: DCW   Selection of AUTO test with AUTO SEL   L: ACW   H: DCW   Selection of AUTO test with AUTO SEL   L: ACW   H: DCW   Selection of AUTO test with AUTO SEL   L: ACW   AUTO SEL   L: Single test   L: Single test   AUTO SEL   L: Single test   L: Single test   AUTO SEL   AUTO SEL	PIN No	Signal name	I/O	TOS5300	TOS53	01			TOS5302
Open: Terminal-to-terminal resistance is 1.2 kG or more Short: Terminal-to-terminal resistance is 1.2 kG or more Short: Terminal-to-terminal resistance is 1.2 kG or less									utput is interrupted
When input strobe signal starts up latch selection signal call panel memory "Memory selection prioritizes from TEST SEL, AUTO SEL   H   H   Memory 1	1	INTERLOCK+	ı	Open: Terminal-to-term	ninal re	sistar	ice is	1.2 kΩ	
PMI   1   up latch selection signal call panel memory   Memory selection prioritizes   H   L   Memory 2   H   L   L   Enables TEST SEL and AUTO SEL selection   H   L   L   Enables TEST SEL and AUTO SEL selection   H   L   L   Enables TEST SEL and AUTO SEL selection   L   L   Enables TEST SEL and AUTO SEL selection   L   L   Enables TEST SEL and AUTO SEL selection   L   L   Enables TEST SEL and AUTO SEL selection   L   L   Enables TEST SEL and AUTO SEL selection   L   L   Enables TEST SEL and AUTO SEL selection   L   L   L   L   L   L   L   L   L	2	PM0	1			PM0	PM1	Called	Panel Memory Number
panel memory y Memory selection prioritizes from TEST SEL, AUTO SEL  4	3	PM1	1			_			•
Test selection of Auto Selection				panel memory		_			,
5 NC — — — — — — — — — — — — — — — — — —						_	_		
6 NC	4	NC	_			_	-		
7 NC	5	NC	_			_	-		
8 NC — — — — — — — — — — — — — — — — — —	6	NC	_			_	-		
9   STB	7	NC	_			_	-		
Single/Independent test's selection signal/ AUTO Test's sequence selection signal selection of single test with AUTO SEL L: ACW H: DCW Selection of AUTO test with AUTO SEL L: ACW → IR H: IR → ACW Selection of AUTO test with AUTO SEL L: ACW → IR H: IR → ACW Selection of AUTO test with AUTO SEL L: ACW → IR H: IR → ACW Selection of AUTO test with AUTO SEL L: ACW → IR H: IR → ACW Selection of AUTO test with AUTO SEL L: ACW → IR H: IR → ACW Selection of AUTO test with AUTO SEL L: ACW → IR H: IR → ACW Selection of AUTO test with AUTO SEL L: ACW → IR H: IR → ACW Selection of AUTO test L: Single test test Single test test Single test L: Single tes	8	NC	_			_	-		
TEST SEL I NA Selection signal Selection of single test with AUTO SEL L: ACW + IP DCW Selection of AUTO test with AUTO SEL L: ACW → IR H: IR → ACW Selection of AUTO test with AUTO SEL L: Single test L: Single test L: Single test H: AUTO test Selection of AUTO test/single test L: Single test H: AUTO test Short: Terminal and - terminal are opened, output is interrupted and the system shifts to the Protection status. Open: Terminal-to-terminal resistance is 1.2 kΩ or more Short: Terminal-to-terminal resistance is 1.2 kΩ or less Short: Terminal-to-terminal resistance is 1.2 kΩ or less ON during test and while voltage remains between output terminals with substance is 1.2 kΩ or less ON during test, while voltage remains between output terminals of the voltage remains between output terminals of time) ON during test (except when voltage is rising or falling)  15 TEST O ON during test (except when voltage is rising or falling)  16 PASS O ON for at least 0.2 sec. when PASS has been determined (PASS HOLD time) Continuously ON when PASS HOLD time is set for HOLD on time) Continuously ON when value over acceptable maximum is detected and UPPER FAIL is determined  18 L-FAIL O Continuously ON when value under acceptable minimum is detected, and LOWER FAIL is determined  19 READY O ON during standby (READY status)  20 PROTECTION O ON while protection function is activated (PROTECTION ON)  21 START I Start signal input terminal  22 STOP I Stop signal input terminal  23 ENABLE I Start signal input terminal	9	STB	1	Panel men	nory's s	trobe	signa	al inpi	ut terminal
11 AUTO SEL I NA NA LEST/Single test L: Single test L: Single test L: Single test H: AUTO test  12 COM — Circuit's common terminal  When + terminal and - terminal are opened, output is interrupted and the system shifts to the Protection status. Open: Terminal-to-terminal resistance is 1.2 kΩ or more Short: Terminal-to-terminal resistance is 1.2 kΩ or more Short: Terminal-to-terminal resistance is 1.2 kΩ or less  14 HV.ON O ON during test and while voltage remains between output terminals and during automatic test (AUTOTEST)  15 TEST O ON during test (except when voltage is rising or falling)  16 PASS O ON for at least 0.2 sec. when PASS has been determined (PASS HOLD time) Continuously ON when PASS HOLD time is set for HOLD  17 U-FAIL O Continuously ON when value over acceptable maximum is detected and UPPER FAIL is determined  18 L-FAIL O Continuously ON when value under acceptable minimum is detected, and LOWER FAIL is determined  19 READY O ON during standby (READY status)  20 PROTECTION O ON while protection function is activated (PROTECTION ON)  21 START I Start signal input terminal  22 STOP I Stop signal input terminal  Shifts to the Protection status when the ENABLE signal changes	10	TEST SEL	ı	NA	selecti L: ACW	on sig	gnal		test's selection signal/ AUTO Test's sequence selection signal Selection of single test with AUTO SEL L: ACW; H: DCW Selection of AUTO test with AUTO SEL L: ACW → IR
When + terminal and - terminal are opened, output is interrupted and the system shifts to the Protection status. Open: Terminal-to-terminal resistance is 1.2 kΩ or more Short: Terminal-to-terminal resistance is 1.2 kΩ or more Short: Terminal-to-terminal resistance is 1.2 kΩ or less	11	AUTO SEL	ı	NA	NA				test/single test L: Single test
13 INTERLOCK- I Open: Terminal-to-terminal resistance is 1.2 kΩ or more Short: Terminal-to-terminal resistance is 1.2 kΩ or less  14 HV.ON O ON during test and while voltage remains between output terminals etween output terminals with the voltage remains between output terminals and during automatic test (AUTOTEST)  15 TEST O ON during test (except when voltage is rising or falling)  16 PASS O ON for at least 0.2 sec. when PASS has been determined (PASS HOLD time) Continuously ON when PASS HOLD time is set for HOLD  17 U-FAIL O Continuously ON when value over acceptable maximum is detected and UPPER FAIL is determined  18 L-FAIL O Continuously ON when value under acceptable minimum is detected, and LOWER FAIL is determined  19 READY O ON during standby (READY status)  20 PROTECTION O ON while protection function is activated (PROTECTION ON)  21 START I Start signal input terminal  22 STOP I Stop signal input terminal  23 ENABLE I Start signal is ENABLE signal input terminal: Shifts to the Protection status when the ENABLE signal changes	12	СОМ	_	Circuit's common term	inal				
14	13	INTERLOCK-	I	and the system shifts to Open: Terminal-to-term	o the Pr ninal re	otect sistar	ion st	atus. 1.2 kΩ	Ω or more
16 PASS O ON for at least 0.2 sec. when PASS has been determined (PASS HOLD time) Continuously ON when PASS HOLD time is set for HOLD On time) Continuously ON when value over acceptable maximum is detected and UPPER FAIL is determined  18 L-FAIL O Continuously ON when value under acceptable minimum is detected, and LOWER FAIL is determined  19 READY O ON during standby (READY status)  20 PROTECTION O ON while protection function is activated (PROTECTION ON)  21 START I Start signal input terminal  22 STOP I Stop signal input terminal  23 ENABLE I Start signal's ENABLE signal input terminal:  Shifts to the Protection status when the ENABLE signal changes	14	HV.ON	0			age re	emain	S	voltage remains between output terminals, and during automatic test
17 U-FAIL O Continuously ON when PASS HOLD time is set for HOLD  18 L-FAIL O Continuously ON when value over acceptable maximum is detected and UPPER FAIL is determined  18 L-FAIL O Continuously ON when value under acceptable minimum is detected, and LOWER FAIL is determined  19 READY O ON during standby (READY status)  20 PROTECTION O ON while protection function is activated (PROTECTION ON)  21 START I Start signal input terminal  22 STOP I Stop signal input terminal  23 ENABLE I Start signal's ENABLE signal input terminal:  Shifts to the Protection status when the ENABLE signal changes	15	TEST	0	ON during test (except	when v	/oltag	ge is ri	ising (	or falling)
and UPPER FAIL is determined  18 L-FAIL O Continuously ON when value under acceptable minimum is detected, and LOWER FAIL is determined  19 READY O ON during standby (READY status)  20 PROTECTION O ON while protection function is activated (PROTECTION ON)  21 START I Start signal input terminal  22 STOP I Stop signal input terminal  23 ENABLE I Start signal's ENABLE signal input terminal:  Shifts to the Protection status when the ENABLE signal changes	16	PASS	0						
19 READY O ON during standby (READY status) 20 PROTECTION O ON while protection function is activated (PROTECTION ON) 21 START I Start signal input terminal 22 STOP I Stop signal input terminal 23 ENABLE I Start signal's ENABLE signal input terminal: Shifts to the Protection status when the ENABLE signal changes	17	U-FAIL	0				accep	table	maximum is detected,
20 PROTECTION O ON while protection function is activated (PROTECTION ON) 21 START I Start signal input terminal 22 STOP I Stop signal input terminal 23 ENABLE I Start signal's ENABLE signal input terminal: Shifts to the Protection status when the ENABLE signal changes	18	L-FAIL	0						
21 START   I Start signal input terminal	19	READY	0	ON during standby (READY status)					
22 STOP I Stop signal input terminal  23 ENABLE I Start signal's ENABLE signal input terminal: Shifts to the Protection status when the ENABLE signal changes	20	PROTECTION	0	ON while protection function is activated (PROTECTION ON)					
23 ENABLE I Start signal's ENABLE signal input terminal: Shifts to the Protection status when the ENABLE signal changes	21	START	Ι	Start signal input terminal					
23 ENABLE I Shifts to the Protection status when the ENABLE signal changes	22	STOP	Ī	Stop signal input terminal					
24 +24V — +24V internal power supply output terminal: Maximum output current 100 m.	23	ENABLE	ı						
	24	+24V	_	+24V internal power supply output terminal: Maximum output current 100 mA					
25 COM — Circuit's common terminal	25	COM	_						





# Specifications - Withstanding voltage tester

- Unless specified otherwise, the specifications are for the following settings and conditions.

   The warm-up time is 30 minutes.

   TYP:These are typical values. These values do not guarantee the performance of the product.

   rdng: Indicates the readout value.
- set: Indicates a setting.
  f.s: Indicates full scale.

				TOS5300	то	S5301	TOS5302				
	Output rang	e			0.05 kV	/ to 5.00 kV					
		Accuracy			±(2 % of set + 20 V) w	hen no load is connected					
		Setting range			0.00 kV	/ to 5.50 kV					
		Resolution		10 V steps							
	Max. rated o	utput *1		500 VA (5 kV/100 mA)							
	Max. rated v	oltage		5 kV							
	Max. rated c	urrent	100 mA (when the output voltage is 0.5 kV or greater)								
AC output	Transformer	rating			5	00 VA					
section	Output volta	ige waveform *2		Sine							
		Distortion		If the output voltage is 0.5 kV or more: 3 % or less (when no load or a pure resistive load is connected).							
	Frequency			50 Hz or 60 Hz							
		Accuracy		±0.5 % (excluding during voltage rise time)							
	Voltage regu	lation		10 % o	r less (when changing fro	om maximum rated load to	no load)				
	Input voltag	e variation		±0.3 % (5 kV	when no load is connec	ted; power supply voltage	: 90 V to 250 V)				
	Short-circuit	current		200	mA or more (when the o	utput voltage is 1.0 kV or g	reater)				
	Output meth	nod			PWM	switching					
	Output rang	e			0.05 kV	/ to 6.00 kV					
		Accuracy			,	of set + 20 V) ad is connected					
		Setting range			0.00 kV	/ to 6.20 kV					
		Resolution			10	V STEP					
	Max. rated o	utput *1			50 W (5	kV / 10 mA)					
	Max. rated v	oltage				6 kV					
DC output	Max. rated c	urrent		_	1	0 mA	_				
section	Rinnle (TYP)	5 kV when no load is connected			50	) Vp-p					
	pp.c ( ,	Max. rated load			10	0 Vp-p					
	Voltage regu	lation			3% or less (When c	hanging from maximum d to no load))					
	Short-circuit	current (TYP)			40 mA (when ger	neration 6 kV output)					
	Discharge fe	ature		Forced discharge after test co (discharge resistance: 12							
Start Voltage	!			The voltage at the start of withstanding voltage tests can be set to 50% of the test voltage.							
Limit Voltage	<u> </u>			The test voltage upper limit can be set . AC: 0.00 kV to 5.50 kV, DC: 0.00 kV to 6.20 kV							
Output volta	ge monitor fea	ature		If output voltage exceeds the specified value + 350 V or is lower than the specified value - 350 V, output is turned off, and protective features are activated.							
		Scale	output is turned on, and protective reatures are activated.  6 kV AC/DC f.s								
	Analog	Accuracy				5 % f.s					
	Analog	Indication				response/rms scale					
					Average value	response/ims scale					
Voltmeter		Measurement range			0.000 kV to	6.500 kV AC/DC					
		Display				□□□ kV					
	Digital	Accuracy		V <	500 V: ±(1.5 % of rdng +	20 V); V ≥ 500 V: ±1.5 % of	rdng				
		Response				oonse time: 50 ms)					
	Hold feature			After a test is finished,	the measured voltage is	retained until the PASS or	FAIL judgment is cleared.				
	Measurement		4.5.0	00 4: 440 4	AC: 0.00 r	nA to 110 mA	45.000 4.440 4				
		range	i = measured cu	.00 mA to 110 mA	DC: 0.00	mA to 11 mA	AC: 0.00 mA to 110 mA				
				i < 1 mA	1 mA ≤ i < 10 mA	10 mA ≤ i < 100 mA	100 mA ≤ i				
Ammeter	Digital	Display				□□.□□ mA	□□□. □ mA				
, annietei	Signal			шшш μл							
		Accuracy *3		1.00 m	ıA ≤ i: ±(1.5 % of rdng); i <	< 1.00 mA: ±(1.5 % of rdng	+ 30 μA)				
		Response	True rms (response time: 50 ms)								
		Hold feature		After a test is finish	ed, the measured voltag	e is retained until the PASS	judgment is cleared.				
	1		After a test is finished, the measured voltage is retained until the PASS judgment is cleared.								

# Specifications -Withstanding voltage tester

				TOS5300 TOS5301			TOS5302		
				Judgment	Judgr	ment method	Display	Buzzer	SIGNAL I/O
			UPPER FAIL	detected, the output is turn turned off, and an UPPER FAIL rise time (Rise Time) of DC with	than or equal to the upper limit is ed off, and an UPPER the output is judgment occurs. During the voltage istanding voltage tests, an UPPER FAIL is a problem with the voltage rise ratio.	FAIL LED lights OVER is displayed on the screen	ON	Generates a U-FAIL signal	
	Judgment me judgment ope			LOWER FAIL	the output is turned off, an This judgment is not perfor	requal to the lower limit is detected, d a LOWER FAIL judgment occurs. med during voltage rise time (Rise the voltage fall time (Fall Time) of AC	FAIL LED lights UNDER is displayed on the screen	ON	Generates a L-FAIL signal
udgment eature				PASS	If the specified time elapses turned off, and a PASS judgm	without any problems, the output is ent occurs.	PASS LED lights	ON	Generates a PASS signal
			• The U • The F • For P	<ul> <li>If PASS HOLD is enabled, the PASS signal is generated continuously until the TOS5300 Series receives a STOP signal.</li> <li>The UPPER FAIL and LOWER FAIL signals are generated continuously until the TOS5300 Series receives a STOP signal.</li> <li>The FAIL and PASS buzzer volume levels can be changed.</li> <li>For PASS judgments, the length of time that the buzzer sounds for is fixed to 0.2 seconds.</li> <li>Even if PASS HOLD is enabled, the buzzer turns off after 0.2 seconds.</li> </ul>					
	Upper limit se	etting		AC: 0.0	01 mA to 110 mA		AC: 0.0	01 mA to 110 mA	
	Lower limit se	etting		AC: 0.01	mA to 110 mA / OFF	AC: 0.01 mA to 110 mA / OF DC: 0.01 mA to 11 mA / OF		AC: 0.01 n	nA to 110 mA / OFF
	Judgment acc	curacy *3			1.00 m/	$A \le i: \pm (1.5 \% \text{ of set}), i < 1.00 \text{ mA}: \pm (1.5 \% \text{ of set})$	5 % of set + 30 μA)		
	Current detec	tion method			Calculates the curre	nt's true rms value and compares this	value with the refer	ence valu	e
	Calibration		Calibrated with the rms of a sine wave using a pure resistive load						
	Voltage rise ti		0.1 s to 10.0 s						
	Resolution 0.1 s  Voltage fall time 0.1 s / OFF (only enabled when a PASS judgment occurs)								
ime									
-	Test time		0.1 s to 999 s, can be turned off (TIMER OFF)						
		Resolution				0.1 s to 99.9 s: 0.1 s. 100 s to 999			
	Accuracy					±(100 ppm + 20 ms) excluding Fa	ıll Time		

#### \*1. Regarding the output time limits:

Taking size, weight, and cost into consideration, the heat dissipation capability of the voltage generator that is used for withstanding voltage tests has been designed to be one half that of the rated output. Use the TOS5300 Series within the following limits. If you use the product in a manner that exceeds these limits, the output section may overheat, and the internal protection circuits may be activated. If this happens, stop testing, and wait until the TOS5300 Series returns to its normal temperature.

Ambient temperature	Upper limit		Pause time	Output time	
	AC	$50 \text{ mA} < i \le 110 \text{ mA}$ Greater than or equal to the output time		30 min. max.	
t < 40 °C	AC	i ≤ 50 mA	Not necessary	Continuous output possible	
t≤40 C	D.C.	5 mA < i ≤ 11 mA	Greater than or equal to the output time	1 min. max.	
	DC	$i \le 5 \text{ mA}$ Greater than or equal to the wait time (WAIT TIME)		Continuous output possible	

(Output time = voltage rise time + test time + voltage fall time)

#### \*2. Regarding the test voltage waveform:

Waveform distortions may occur if an DUT whose capacitance is dependent on voltage (for example, an DUT that consists of ceramic capacitors) is connected as the load. However, if the test voltage is 1.5 kV, the effect of a capacitance of 1000 pF or less can be ignored. Because the product's high-voltage power supply uses the PWM switching method, if the test voltage is 500 V or less, the switching and spike noise proportions are large. The lower the test voltage, the greater the waveform is distorted.

#### \*3. Regarding ammeter and judgment accuracy:

During AC withstanding voltage tests, current also flows in the stray capacitance of items such as the measurement leads and jigs. This current that flows in the stray capacitances is added to the current that flows in the DUT, and the sum of these currents is measured. Especially if you want to perform judgments with high sensitivity and accuracy, it is necessary to consider methods to limit the current that flows in these stray capacitances, such as by adding upper and lower limits.

Output voltage	1 kV	2 kV	3 kV	4 kV	5 kV
When using 350 mm long test leads that are suspended in air (TYP)	2 μΑ	4 μΑ	6 μΑ	8 μΑ	10 μΑ
When using the accessory, high test lead TL31-TOS (TYP)	16 μΑ	32 μΑ	48 μΑ	64 μΑ	80 μΑ

# Specifications -Insulation resistance test section

			TOS5302									
	Output voltag	ge	25 V, 50 V, 100 V, 125 V, 250 V, 500 V, 1000 VDC (negative)									
		Accuracy					-0 %, -	+5 %				
	Max. rated loa	ad		1 W (-1000 V DC / 1 mA)								
	Max. rated cu	rrent					1 m	A				
		1000 V when no					21/					
Output	Ripple	load is connected					2 Vp-p c	or less				
section		Max. rated load					10 Vp-p	or less				
	Voltage regula				1 % c	or less (when c	hanging from r		ed load to	no load)		
	Short-circuit						12 mA c					
	Discharge fea	ture					est completion					
	Limit voltage					J 11				0 V, 500 V, 1000 V		
	Output voitag	ge monitor feature Scale	if output voit	age exceeds	10 % or set + 10	or is lower			itput is tu	rned off, and protecti	ve reatures	are activated.
	Analog	Accuracy	6 kV AC/DC f.s ± 5 % f.s									
	Allalog	Indication				Λν.	erage value resi		ulo.			
						AV	erage value resp	ponse/mis sca	ile			
Volt- meter		Measurement range					0 V to -1	200 V				
	Digital	Display		Measure		V < 1			V < 1000		$V \le V$	
			L	Disp	olay		V		V		V	
		Accuracy					± (1 % of rd	lng + 1 V)				
		25 V				25 N	$\leq R \leq 25 \text{ M}\Omega / \pm 0$ $M\Omega < R \leq 125 \text{ M}$ $M\Omega < R \leq 250 \text{ M}$	$\Omega$ / ±5 % of ro	dng			
							≤ R ≤ 50 MΩ / ±					
		50 V					$M\Omega < R \le 250 \text{ M} \Omega$		-			
							$M\Omega < R \le 500 \text{ M}$					
						0.100	MΩ ≤ R ≤ 100 I	MΩ / ±2 % of	rdng			
		100 V					$M\Omega < R \le 500 \text{ N}$		-			
	Measurement			$500 \text{ M}\Omega < R \le 1 \text{ G}\Omega / \pm 10 \% \text{ of rdng}$								
	range / measurement			$0.125  M\Omega \le R \le 125  M\Omega / \pm 2 \%$ of rdng								
Resistance	accuracy	125 V	$125 \text{ M}\Omega < R \le 625 \text{ M}\Omega / \pm 5\% \text{ of rdng}$									
meter	*4*5		625 MΩ < R ≤ 1.25 GΩ / ±10 % of rdng									
		250 V		$0.250 \text{M}\Omega \leq R \leq 250 \text{M}\Omega / \pm 2\% \text{ of rdng}$ $250 \text{M}\Omega < R \leq 1.25 \text{G}\Omega / \pm 5\% \text{ of rdng}$								
		250 0		1.25 G $\Omega$ < R ≤ 2.5 G $\Omega$ / ±10 % of rdng								
			$0.50 \text{ M}\Omega \le R \le 500 \text{ M}\Omega / \pm 2\% \text{ of rdng}$									
		500 V	$500 \text{ M}\Omega < R \le 2.5 \text{ G}\Omega / \pm 5 \% \text{ of rdng}$									
			$2.5  \text{G}\Omega < \text{R} \le 5  \text{G}\Omega / \pm 10  \%  \text{of rdng}$									
		1000 V	1 MΩ ≤ R < 1 GΩ / ±2 % of rdng 1 GΩ ≤ R ≤ 5 GΩ / ±5 % of rdng									
				I GL2 ≤ R ≤ 5 GL2 / ±5 % of rang								
	Display *5		25 kΩ ≤ R ⋅	< 1.00 MΩ		R < 10.0 MΩ	10.0 MΩ ≤ R		100.0 N	$M\Omega \le R < 1.00 \text{ G}\Omega$	1.00 GΩ :	≤ R ≤ 9.99 GΩ
	D.Spiay 5			kΩ		□ MΩ		ΜΩ	[	□□ ΜΩ	□.	□□ GΩ
Hold feat	ure			Afte	er a test is finish	ned, the measu	red resistance i	is retained un	til the PA	SS judgment is cleare	d.	
Current d	etection respo	nse speed				Can be switch	hed between th	ree levels: Fa	st, Mid, SI	ow		
			Judgment			Judgment				Display	Buzzer	SIGNAL I/O
			Judgment	If a recistan	so that is great			r limit is data	ctod the	Display FAIL LED lights;	buzzei	SIGNAL I/O
			UPPER FAIL							OVER is displayed	ON	Generates
				not perforn	ned during volt	age rise time (	Rise Time).			on the screen		a U-FAIL signal
										FAIL LED lights;		Generates
	ludament me	thad and	LOWER FAIL		curs during the OWER FAIL judg		ime (Rise Time	), the output	is turned	UNDER is displayed on the screen	ON	a L-FAIL signal
	Judgment me judgment ope				, ,		11 4		1 66	on the sereen		<i>c</i> .
			PASS		fied time elapse judgment occu		problems, the o	output is turn	еа оп,	PASS LED lights	ON	Generates a PASS signal
Judgment			• If PASS HOLD is	enabled, the	PASS signal is	generated con	tinuously until	the TOS5300	Series red	ceives a STOP signal.		
feature							ntinuously unti	il the TOS5300	) Series re	eceives a STOP signal.		
			• The FAIL and P.				ounds for is five	d to 0.2 secon	nds			
			• For PASS judgments, the length of time that the buzzer sounds for is fixed to 0.2 seconds.  Even if PASS HOLD is enabled, the buzzer turns off after 0.2 seconds.									
	Upper limit se	etting range	0.03 MΩ to 5.00	GΩ								
	Lower limit se	etting range	0.03 MΩ to 5.00	GΩ								
			Measurement a	ccuracy + 2 d	igits							
	Judgment acc	uracy	Humidity: 20 %i						leads or o	other problems.		
		JPPER and LOWER)	For judgments of						is necess:	arv.		
			If the current detection response speed is set to Mid, a test time of at least 0.3 seconds is necessary.  If the current detection response speed is set to Slow, a test time of at least 0.5 seconds is necessary.									
	Voltage rise ti	me	10 ms (TYP)		•	-						
Time	Test Time		0.1 s to 999 s, ca	an be turned	off (TIMER OFF)	<u> </u>						
Time		Resolution	0.1 s to 99.9 s: 0	.1 s. 100 s to	999 s:1 s.							
	Accuracy		± (100 ppm + 2	20 ms)								
			± (100 ppm + 20 m3)									

<sup>\*4.</sup> Humidity: 20 %rh to 70 %rh (no condensation). No bends in the test leads. \*5. R = measured insulation resistance

# Specifications - Other features / Interfaces

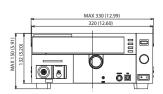
		TOS5300	TOS5301	TOS5302			
Double action feature		Tests can only be started by pressing and	releasing STOP and then pressing START withi	n 0.5 seconds of releasing the STOP switch.			
Length of time to main	tain a PASS judgment result	You can set the length of tim	You can set the length of time to maintain a PASS judgment: 50 ms, 100 ms, 200 ms, 1 s, 2 s,5 s, or HOLD.				
Momentary feature		Tests ar	e only executed while the START switch is hel	d down.			
Fail mode feature		This feature enables you to prevent rem	otely transmitted stop signals from clearing F	AIL judgments and PROTECTION modes.			
Timer feature		This fe	eature finishes tests when the specified time e	lapses.			
Output voltage monito	r feature		ge exceeds "setting + 350 V" or is lower than " tches to PROTECTION mode, output is turned				
Memory		Up to t	hree sets of test conditions can be saved to m	nemory.			
Key lock		Lo	cks panel key operations (settings and change	es).			
Protective features			the TOS5300 Series switches to the PROTECTI stops testing. A message is displayed on the so				
Interlock Pro	tection		An interlock signal has been detected.				
Power Suppl	y Protection		An error was detected in the power supply.				
Volt Error Pro	otection		While monitoring the output voltage, a voltage outside of the rated limits was detected.  AC or DC withstanding voltage tests: $\pm 350  \text{V}$ Insulation resistance test: $\pm (10  \% \text{ of set} + 10  \text{V})$				
Over Load Pr	otection		During a withstanding voltage test, a value that is greater than or equal to the output limit power was specified.  AC withstanding voltage test: 550 VA. DC withstanding voltage test: 55 VA.				
Over Heat Pr	otection	The internal temperature of the TOS5300 Series became too high.					
Over Rating I	Protection	During a withstanding voltage test, th	e output current was generated for a length o	f time that exceeds the regulated time.			
Calibration P	rotection		The specified calibration period has elapsed.				
Remote Prote	ection	A connection to or di	sconnection from the front-panel REMOTE co	nnector was detected.			
SIGNAL I/O P	rotection	The rear-p	anel SIGNAL I/O connector's ENABLE signal ha	s changed.			
USB Protection	on	The USB connector has been discor	nected while the TOS5300 Series was being c	ontrolled through the USB interface.			
System clock		Set in the fo	ollowing format: year/month/day hour/minut	tes/seconds.			
Calibration d	ate		Set when the TOS5300 Series is calibrated.				
Calibration pe	eriod setting	Sets the period before the next calibration is necessary.					
Notification of period elapse	of when the calibration es	Sets the operation that is performed when the specified calibration period elapses. When the TOS5300 Series turns on, it can display a notification or switch to the protection mode and disable testing.					
	USB		USB Specification 2.0				
Interfaces REMOTE		Front-panel 9-pin MINI DIN connector. By connecting an optional device to this connector, you can control the starting and stopping of tests remotely					
	SIGNAL I/O	Rear-panel D-sub 25-pin connector					

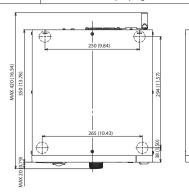
# Specifications - General

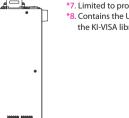
			TOS5300	TOS5301	TOS5302				
Display				VFD: $256 \times 64$ dots + 4 status indicators					
Backup b	attery life		3 years (at 25 °C or 77 °F)						
	Installation	location		Indoors, at a height of up to 2000 m					
	Spec guarante	ed Temperature		5 °C to 35 °C (41 °F to 95 °F)					
ļ	range	Humidity		20 %rh to 80 %rh (no condensation)					
Environ- ment	Operating	Temperature		0 °C to 40 °C (32 °F to 104 °F)					
lilicit	range	Humidity		20 %rh to 80 %rh (no condensation)					
	Ctorago rar	Temperature		-20 °C to 70 °C (-4 °F to 158 °F)					
	Storage rar	Humidity		90 %rh or less (no condensation)					
		oltage range voltage range)		100 VAC to 240 VAC (90 VAC to 250 VAC)					
Power supply	Power	When no load is connected (READY)		100 VA or less					
supply	consumptio	When rated load isconnected	800 VA max.						
	Allowable	frequency range	47 Hz to 63 Hz						
Insulation	resistance (be	tween AC LINE and the chassis)	30 MΩ or more (500 VDC)						
Withstand	ing voltage (b	etween AC LINE and the chassis)		1390 VAC, 2 seconds, 20 mA or less					
Earth con	tinuity			25 AAC, 0.1 Ω or less					
Safety *6			Complies with the requirements of the following	g directive and standard. Low Voltage Directive 2	006/95/EC, EN 61010-1 Class I Pollution degree 2				
Electromagnetic compatibility (EMC) *6 *7			Applicable under the following cor	ng directive and standard. EMC Directive 2004/1 Iditions The maximum length of all cabling ar st lead TL31-TOS is being used. Electrical discl					
Dimensio	ns		See "Outline drawing."						
Weight			Approx. 14 kg (30.9 lb.)	Approx. 15 kg (33.1 lb.)	Approx. 14 kg (30.9 lb.)				
Accessories			Power cord: 1pc. / High test lead (TL31-TOS): 1set (1 red wire and 1 black wire, each with alligator clips); 1.5 m / D-sub 25-pin plug: 1set; assembly type / High-voltage warning sticker: 1pc. / User's manual: 1pc. / CD-R: 1pc. *8						

# Outline drawing

Unit: mm (inch)







- \*6. Does not apply to specially ordered or modified TOS5300 Series testers.
  \*7. Limited to products that have the CE mark on their panels.
  \*8. Contains the User's Manual, the Communication Interface Manual, the KI-VISA library, and the Safety evalution test.

# **Ordering information**

Product Name	Model	Remarks
AC Hipot (Withstanding voltage) & Insulation Resistance tester (ACW/IR)	TOS5302	Hipot (Withstanding voltage) test: AC 5 kV/100 mA Insulation Resistance test: 25V - 1000V
AC/DC Hipot (Withstanding voltage) tester (ACW/DCW)	TOS5301	Hipot (Withstanding voltage) test: AC 5 kV/100 mA, DC 6 kV/50 W
AC Hipot (Withstanding voltage) tester (ACW)	TOS5300	Hipot (Withstanding voltage) test: AC 5 kV/100 mA

# **Options**

#### **Test Lead**

TL31-TOS (Max.AC5kV/1.5m)

\*Standardly attached to main unit in TOS5300 Series.

TL32-TOS (Max.AC5kV/3m)



#### **Test Probe**

HP01A-TOS\* (Max.AC4kV · DC5kV / 1.8m)
HP02A-TOS\* (Max.AC4kV · DC5kV / 3.5m)



#### Remote Control Box

RC01-TOS\* (For single-handed operation / 1.5 m)

RC02-TOS\* (For two-handed operation / 1.5 m)





#### **DIN Cable**

DD-5P/9P conversion adapter (DIN - MIni DIN)



\*The remote control conversion cable [DD-5P/9P] is required when used with TOS5300 Series.

### Warning Light Unit

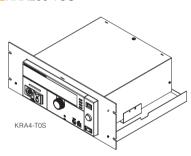
PL02-TOS (for DC24V)



#### Rack Mount Adapter

KRA4-TOS (inch rack for EIA standards)

KRA200-TOS (millimeter rack for JIS standards)



# **TOS Series** Electrical safety testers developed under the quality brand of KIKUSUI!!

Withstanding Voltage & Insulation Tester

### TOS9200 Series



Insulation Tester
TOS7200



Hipot (Withstanding Voltage) Tester

# **TOS5000A Series**



Earth Continuity Tester **TOS6210 / 6200** 







Leakage Current Tester

**TOS3200** 





#### KIKUSUI ELECTRONICS CORPORATION

1-1-3, Higashiyamata, Tsuzuki-ku, Yokohama, 224-0023, Japan Phone: (+81) 45-593-7570, Facsimile: (+81) 45-593-7571, www.kikusui.co.jp

KIKUSUI AMERICA, INC.1-877-876-2807 www.kikusuiamerica.com

530 Lakeside Drive, Suite#180, Sunnyvale, CA 94085, U.S.A.
Phone: 408-733-3432 Facsimile: 408-733-1814

KIKUSUI TRADING (SHANGHAI) Co., Ltd. www.kikusui.cn

Room, D-01,11F, Majesty Bld, No.138, Pudong Ave, Shanghai City
Phone: 021-5887-9067 Facsimile: 021-5887-9069

For our local sales distributors and representatives, please refer to "sales network" of our website.

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Carl-Zeiss-Straße 43 63322 Rödermark / Germany fon + 49 6074 / 91 99 08 - 0 fax + 49 6074 / 91 99 08 - 17 web www.dhs-tools.de mail info@dhs-tools.de

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