ZK-S12-A Starter Kit for Freescale HCS12(X) Family (112-Pin LQFP ZIF Socket)

User's Manual

1. Introduction

Overview

The ZK-S12-A Starter Kit has been designed for the evaluation of the 112-pin LQFP Freescale HCS12(X) family and the debugging of user applications. The ZK-S12-A Starter Kit can be used as a standalone application, via its built-in USB to BDM interface, or together with an external debugger through a BDM-compatible connection.

Starter Kit Features

The ZK-S12-A Starter Kit features the following sections.

- An "MCU" section containing:
 - An MC9S12DP256B microcontroller (in 112-pin LQFP package, already programmed with a demo application—in addition, you can also use any other pin-to-pin-compatible device);
 - ZIF socket for the microcontroller;



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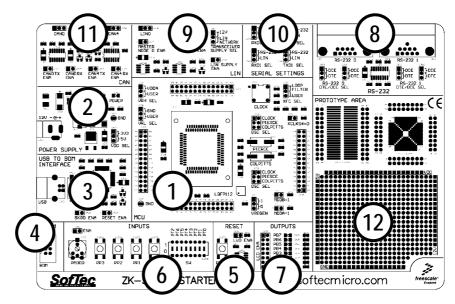
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- A connector area to access the I/O pins of the microcontroller for expansion prototyping;
- Three clock sources: a provision for a clock module, a 16 MHz crystal in Pierce configuration and a 16 MHz crystal in Colpitts configuration, selectable via the "OSC SEL" and "XCLK#=0" jumpers;
- A jumper ("XFC SEL") allowing either the provided RC loop filter (needed for the microcontroller's internal PLL) or a usermade RC loop filter to be enabled;
- Two jumpers ("VRH SEL" and "VRL SEL") which allow the high and low voltage reference for the MCU internal A/D converter to be defined;
- A "VREGEN" jumper allowing the on-chip voltage regulator to be enabled or disabled;
- Two jumpers ("MODA=1" and "MODB=1") which allow the MCU operating mode upon reset to be defined.
- 2. A "POWER SUPPLY" section which accepts a 12 V DC voltage (used for the LIN and CAN transceivers) and, thanks to the built-in switching power supply, provides a regulated VDD voltage for the rest of the board. A jumper ("VDD SEL") allows two different microcontroller VDD voltages (3.3 V or 5.0 V) to be selected. An additional linear power supply regulator provides the 5.0 V voltage required by the "USB TO BDM INTERFACE" section.
- 3. A built-in "USB TO BDM INTERFACE" section which allows the host PC to communicate with the microcontroller through a standard USB interface. USB 2.0 is fully supported. When using an external incircuit debugger (via the BDM connector), the "USB TO BDM INTERFACE" circuitry must be bypassed by removing the "RESET ENA" and "BKGD ENA" jumpers.
- 4. A standard BDM connector for in-circuit debugging/programming;
- A "RESET" section containing the reset supervisor circuitry and a
 push-button connected to the MCU Reset pin. The reset supervisor
 circuitry (enabled by default) can be disabled by removing the "LVD
 ENA" jumper.
- 6. An "INPUTS" section containing:
 - Four push-buttons, connected to PP0, PP1, PP2 and PP3;
 - Eight general-purpose DIP-switches connected to Port T;
 - A potentiometer, together with a jumper to connect/disconnect to/from PAD00.
- An "OUTPUTS" section containing eight high-efficiency (low-current) LEDs connected to Port B, together with eight jumpers to connect/disconnect each of the eight LEDs to/from their respective Port B pins.
- 8. A "RS-232" section providing two RS-232 channels connected to the microcontroller's SCI0 and SCI1 serial communication interfaces. Each RS-232 channel can be configured as DTE (Data Transmission Equipment) or DCE (Data Communication Equipment) via the relative jumper ("RS-232 x DTE/DCE SEL"). The microcontroller's RX and TX lines used by the RS-232 transceivers are shared with the LIN trasceivers' RX and TX lines, respectively. Use the "RXDx SEL" and "TXDx SEL" jumpers in the "SERIAL"



- SETTINGS" section of the board to select whether to use the RS-232, LIN, or to free the microcontroller's RX and TX lines. Two 9-pin, D-Sub female connectors are provided for each RS-232 channel.
- The "LIN" section contains two LIN transceivers, each capable of a speed of up to 100 Kbps in fast mode. Every LIN node can be configured as a master node via its respective "MASTER NODEx **ENA**" jumper, which inserts a 900 Ohm resistor between the LIN bus line and the LIN transceiver power supply line. The LIN transceivers can be powered either by the Starter Kit's internal 12 V DC reference, or by the LIN network itself, via the "TRANSCEIVER SUPPLY SEL" jumper. Analogously, the LIN network can be supplied by the Starter Kit's internal 12 V DC reference via the "LIN SUPPLY ENA" jumper. The microcontroller's RX and TX lines used by the LIN transceivers are shared with the RS-232 transceivers' RX and TX lines, respectively. Use the "RXDx SEL" and "TXDx SEL" jumpers in the "SERIAL SETTINGS" section of the board to select whether to use the RS-232, LIN, or to free the microcontroller's RX and TX lines associated with these nodes. Two 3x1 male header connectors are provided to interface to an external LIN bus.
- 10. The "SERIAL SETTINGS" section has four jumpers that allow the uses of the SCI0 and SCI1 peripherals of the microcontroller to be chosen. The SCI0 peripheral can be connected to the LIN0 node or to the RS-232 channel 0, or can be freed by removing all jumpers. The SCI1 peripheral can be connected to the LIN1 node or to the RS-232 channel 1, or can be freed by removing all jumpers.
- 11. The "CAN" section contains two fault-tolerant (up to 125 Kbaud) CAN transceivers. The TX and RX signals of CAN nodes CAN0 and CAN4 can be disconnected (by removing the respective "CANx TX ENA" and "CANx RX ENA" jumpers) from the microcontroller's respective pins. Two 3x1 male header connectors are provided to interface to an external CAN bus.
- A prototype area features both a standard, thru-hole area (for mounting traditional components) and a SMD area (for soldering SMD components). Additionally, all of the board's supply lines (12 V, 5 V, VDD and GND lines) are provided.



The ZK-S12-A Starter Kit



Supported Devices

The ZK-S12-A Starter Kit supports the following devices:

- MC9S12A family;
- MC9S12B family;
- MC9S12D family;
- MC9S12DB/DG/DJ/DP/DT family;
- MC9S12KC/KG/KL/KT family;
- MC9S12XDP/DT family (S12X core);
- And any future pin-to-pin compatible device.

Note: the MC9S12XDP/DT family (S12X core) requires a special version of CodeWarrior Development Studio (CodeWarrior Development Studio for HC9S12X/XGATE).

Recommended Reading

- Freescale HCS12 microcontroller-specific datasheets;
- SK-S12(X) and ZK-S12(X) Series Starter Kit User's Manual;
- ZK-S12-A Schematic.

2. Device Configuration

Oscillator Configuration

Thanks to the 112-pin LQFP ZIF socket, the ZK-S12-A Starter Kit can support different devices, as detailed in the table below.

All of the jumpers in the "MCU" section of the board must be set according to the specific microcontroller installed. Particular care must be taken in order to correctly set the oscillator configuration.

Device	Pierce Configuration		Colpitts Co	nfiguration	Clo	Clock	
	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	
MC9S12A64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12A128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12A256	-	-	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12A512	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12B64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12B128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12D64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DB128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DG128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DG256	-	-	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DJ64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DJ128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DJ256	-	-	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DJ512	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DP256	-	-	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DP512	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DT128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DT256	-	-	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12DT512	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed	
MC9S12KG128	"PIERCE"	Installed	-	-	"CLOCK"	Installed	



Device	Pierce Configuration		Colpitts Configuration		Clock	
	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	"OSC SEL" Jumpers	"XCLKS#=0" Jumper
MC9S12KT256	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12XDP512	"PIERCE"	Installed	-	-	"CLOCK"	Installed

Peripherals

Some devices do not feature the same number of CAN and SCI peripherals. Please refer to the table below for details.

Device	CAN0	CAN1	CAN2	CAN3	CAN4	SCI0	SCI1
MC9S12A64						✓	✓
MC9S12A128						✓	✓
MC9S12A256						✓	✓
MC9S12A512						✓	✓
MC9S12B64	✓					✓	✓
MC9S12B128	✓					✓	✓
MC9S12D64	✓					✓	✓
MC9S12DB128	✓				✓	✓	✓
MC9S12DG128	✓				✓	✓	✓
MC9S12DG256	✓				✓	✓	✓
MC9S12DJ64	✓					✓	✓
MC9S12DJ128	✓				✓	✓	✓
MC9S12DJ256	✓				✓	✓	✓
MC9S12DJ512	✓				✓	✓	✓
MC9S12DP256	✓	✓	✓	✓	✓	✓	✓
MC9S12DP512	✓	✓	✓	✓	✓	✓	✓
MC9S12DT128	✓	✓			✓	✓	✓
MC9S12DT256	✓	✓			✓	✓	✓
MC9S12DT512	✓	✓			✓	✓	✓
MC9S12KG128	✓				✓	✓	✓
MC9S12KT256	✓	✓			✓	✓	✓
MC9S12XDP512	✓	✓	✓	✓	✓	✓	✓

3. Summary of Jumper and Connector Settings

Jumpers Summary

Name	Reference	Description/Pi	nout
J105	1	VRH SELECTI	ON
	2	1-2 (VDDA)	VRH tied to VDDA (default)
	3	2-3 (USER)	VRH connected to the J103 connector
J106	1	VRL SELECTION	ON
	2	1-2 (GND)	VRL tied to ground (default)
	3	2-3 (USER)	VRL connected to the J104 connector
J107	1 2	XFC SELECTION	ON
		1-2 (LOOP FIL	TER)
	3		The on-board RC PLL loop filter
			is selected (default)
		2-3 (USER)	The XFC PLL loop filter pin is connected to signal pin connector



Name	Reference	Description/Pind	out
J109	1	VOLTAGE REGU	JLATOR ENABLE
	2 3	1-2 (1)	Microcontroller internal voltage regulator enabled (default)
		2-3 (0)	Microcontroller internal voltage regulator disabled. VDD1, VDD2 and VDDPLL must be supplied externally.
J110		MODA=1	•
		Installed Not Installed	MODA line pulled to VDD MODA line tied to ground by internal pull down (default)
J111		MODB=1	
		Installed Not Installed	MODB line pulled to VDD MODB line tied to ground by internal pull down (default)
J113	1 2	OSCILLATOR SE	ELECTION
	3	1-2 (CLOCK) 3-4 (PIERCE) 5-6 (COLPITTS)	Clock selected Pierce crystal configuration selected Colpitts crystal configuration selected (default)
J114	1 2	OSCILLATOR SE	<u>'</u>
	3 4 6	1-2 (CLOCK) 3-4 (PIERCE) 5-6 (COLPITTS)	Clock selected Pierce crystal configuration selected Colpitts crystal configuration selected (default)
J115		XCLKS#=0	,
		Installed Not Installed	XCLKS# pin pushed to ground. Full- swing Pierce crystal configuration or clock selected. XCLKS# pulled to VDD. Loop controlled Colpitts crystal configuration selected. (default)
J202	1	VDD SELECTION	
	2 0	1-2 (3∀3) 2-3 (5∀)	VDD = 3.3 V VDD = 5 V (default)
J204	1	LED ENABLE	(and)
		Installed Not Installed	The LEDs are connected to the PB[70] port of the microcontroller (default) The LEDs are not connected to the microcontroller.
J205		POTENTIOMETE	R ENABLE
		Installed:	The potentiometer is connected to the microcontroller's AN00 pin (default)
		Not Installed:	The potentiometer is not connected to the microcontroller



Name	Reference	Description/Pin	out
J206		LVD ENABLE	
		Installed:	The LVD circuit is connected to microcontroller's RESET line (default)
		Not Installed:	The LVD circuit is not connected to RESET line of the microcontroller
J303	1	RS-232 0 DTE/D	DCE SELECTION
	3	1-2 (DCE)	The RS-232 channel 0 is configured as DCE (default)
		2-3 (DTE)	The RS-232 channel 0 is configured as DTE
J304	1	RS-232 0 DTE/D	OCE SELECTION
	3	1-2 (DCE)	The RS-232 channel 0 is configured as DCE (default)
		2-3 (DTE)	The RS-232 channel 0 is configured as DTE
J305	1	RS-232 1 DTE/D	DCE SELECTION
	3	1-2 (DCE)	The RS-232 channel 1 is configured as DCE (default)
		2-3 (DTE)	The RS-232 channel 1 is configured as DTE
J306	1	RS-232 1 DTE/D	OCE SELECTION
	3	1-2 (DCE)	The RS-232 channel 1 is configured as DCE (default)
		2-3 (DTE)	The RS-232 channel 1 is configured as DTE
J307		MASTER NODE	0 ENABLE
		Installed Not Installed	LIN0 master node (default) LIN0 slave node
J309		MASTER NODE	1 ENABLE
		Installed Not Installed	LIN1 master node (default) LIN1 slave node
J311	1		VER SUPPLY SELECTION
	2	1-2 (12V)	LIN transceivers are supplied by the Starter Kit's internal 12 V DC voltage (default)
		2-3 (LIN NETWO	DRK)
			LIN transceivers supplied by the LIN bus' VBAT line
J312		LIN SUPPLY EN	NABLE
		Installed	LIN bus is powered by the Starter Kit's internal 12 V DC voltage (default)
10.17		Not Installed	LIN bus is self-powered
J313		CAN0 TX ENAB	ile
		Installed	Microcontroller's PM1/TXCAN0 pin connected to CAN0
		Not Installed	transceiver (default) Microcontroller's PM1/TXCAN0 pin floating



Name	Reference	Description/Pind	out
J314		CANO RX ENAB	LE
		Installed	Microcontroller's PM0/RXCAN0 pin connected to CAN0 transceiver (default)
		Not Installed	Microcontroller's PM0/RXCAN0 pin floating
J316		CAN4 TX ENABI	_E
		Installed	Microcontroller's PJ7/TXCAN4 pin connected to CAN4 transceiver (default)
		Not Installed	Microcontroller's PJ7/TXCAN4 pin floating
J317		CAN4 RX ENAB	LE
		Installed	Microcontroller's PJ6/RXCAN4 pin connected to CAN4 transceiver (default)
		Not Installed	Microcontroller's PJ6/RXCAN4 pin floating
J319	1	RXD0 SELECTION	
	2	1-2 (RS-232)	Microcontroller's PS0/RXD0 pin connected to RS-232 0 transceiver (default)
		2-3 (LIN)	Microcontroller's PS0/RXD0 pin connected to LIN0 transceiver
J320	1	TXD0 SELECTIO	
	2	1-2 (RS-232)	Microcontroller's PS1/TXD0 pin connected to RS-232 0 transceiver (default)
		2-3 (LIN)	Microcontroller's PS1/TXD0 pin connected to LIN0 transceiver
J321	1	RXD1 SELECTION	
	2	1-2 (RS-232)	Microcontroller's PS2/RXD1 pin connected to RS-232 1 transceiver (default)
		2-3 (LIN)	Microcontroller's PS2/RXD1 pin connected to LIN1 transceiver
J322	1	TXD1 SELECTIO	
	2	1-2 (RS-232)	Microcontroller's PS3/TXD1 pin connected to RS-232 1
		2-3 (LIN)	transceiver (default) Microcontroller's PS3/TXD1 pin connected to LIN1 transceiver
J402		BKGD ENABLE	The state of the s
		Installed	Microcontroller's BKGD line connected to the "USB TO BDM
		Not Installed	INTERFACE" (default) Microcontroller's BKGD line not connected to the "USB TO BDM INTERFACE"
J403		RESET ENABLE	
		Installed	Microcontroller's RESET# line connected to the "USB TO BDM INTERFACE" (default)
		Not Installed	Microcontroller's RESET# line not connected to the "USB TO BDM INTERFACE"



Connectors Summary/Pinout

Name	Reference	Description/Pinout
J101,		MCU I/O Connectors
J102,		
J103,		See schematic for pin explanation
J104 J108		Ground Test Point
3100		Ground rest Foint
	W	
J112	1—	BDM Connector
		4 DVCD
		1. BKGD 2. GND
		3. N.C.
		4. RESET#
		5. N.C.
		6. VDD
J201	2-(•)-1	12 V DC Power Supply Input Connector
		1. 12 V DC
		2. GND
J203		Ground Test Point
1001	W	
J301		RS-232 0 Connector
		1. N.C.
		2. TX or RX (see J303/J304 jumpers)
		3. RX or TX (see J303/J304 jumpers)
	9 6	4. N.C.
	5 1	5. GND
		6. N.C. 7. N.C.
		8. N.C.
		9. N.C.
J302		RS-232 1 Connector
		1. N.C.
		TX or RX (see J305/J306 jumpers) RX or TX (see J305/J306 jumpers)
		4. N.C.
	9 6 5 1	5. GND
	,	6. N.C.
		7. N.C.
		8. N.C.
J308	1 3	9. N.C. LINO Connector
JSU8		LINU Connector
	000	1. VBAT – LIN Bus Power Supply
		2. LIN – LIN Signal
		3. GND
J310	1 3	LIN1 Connector
	000	VBAT – LIN Bus Power Supply
		2. LIN – LIN Signal
		3. GND
J315	1 3	CAN0 Connector
	000	
		CANH – CAN Differential Bus Line CND
		GND CANL – CAN Differential Bus Line
		O. STATE OFFICIONICAL DUS LINE



Name	Reference	Description/Pinout	
J318	1 3	CAN4 Connector	
	000	CANH – CAN Differential Bus Line GND CANL – CAN Differential Bus Line	
J401		USB Connector	
		 5 V DC USB Bus Power Supply Line USB D- USB D+ GND 	

