

S5207

# Revision 1.0

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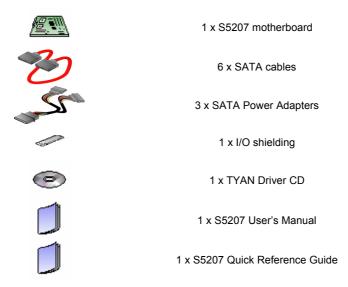
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# Before you begin...

# Check the box contents!

The retail motherboard package should contain the following:



If any of these items are missing, please contact your vendor/dealer for replacement before continuing with the installation process.  $\frac{1}{2} \int_{-\infty}^{\infty} \frac{1}{2} \left( \frac{1}{2} \int_{-\infty}^{\infty}$ 

# **Chapter 1: Introduction**

#### 1.00 - Congratulations!

You are now the owner of the S5207 low-power Intel® processor solution. The S5207 is a flexible Intel® platform for multiple applications, based on the Intel® 3100 chipset. This motherboard is designed to support the Intel® Core™ Duo and Intel® Celeron M processor (Yonah); Intel ® Core™ 2 Duo (Merom) processor and up to 4GB of registered DDR2-400 DIMM. This motherboard features a Flex ATX form factor, 4 USB ports, Dual Gigabit Ethernet port, one video port, 6 SATA ports. The S5207 offers exceptional performance and versatility for all your server platform needs.

Remember to visit TYAN's Website at <a href="http://www.tyan.com">http://www.tyan.com</a>. There you can find information on all of TYAN's products with FAQs, distributors list and BIOS setting explanations.

#### 1.01 - Hardware Specifications

#### Processor

- One mPGA479 socket
- Supports Intel ® Core™ Duo and Intel® Celeron M processor (Yonah); Intel ® Core™ 2 Duo Processor (Merom) in FC-PGA (Flip-Chip Pin Grid Array) package
- 533/667 MHz FSB support
- Onboard IMVP-IV CPU voltage regulation

#### Chipset

- Intel® 3100 chipset
- Super I/O chip: SMSC SCH5017

#### Memory

- Two DDR2-400 DIMM sockets
- Up to 4GB of Reg/ECC DDR2-400 memory modules

#### **Expansion Slots**

- (2) PCI Express x8 slots (w/ x4 signals)

#### Integrated I/O

- Two USB 2.0 (vertical) ports
- Six integrated SATA ports up to 1.5GB/s
- One 18-pin TYFP header.
- One 9-pin serial port (pin header)

# Back Panel I/O ports

- One PS/2 keyboard & mouse port
- Two stacked USB 2.0 ports
- One 9-pin UART serial port
- One 15-pin VGA port
- Two RJ45 10/100/1000 Base-T ports w/ activity LED

# **Integrated Video Controller**

- ATI<sup>®</sup> ES1000
- 32-bit PCI Interface
- 32MB frame buffer memory

#### **Integrated LAN Controllers**

- Two Intel 82573L 10/100/1000 Gigabit LAN
- Two side-by-side RJ45 with LEDs

#### **System Management**

- Integrated H/W monitoring functions in Super I/O chip
- One 4-pin CPU fan header with auto fan control and two 4-pin system fan headers.
- Temperature and voltage monitoring
- Watchdog timer

#### **BIOS**

- AMI BIOS® on 8Mb Flash ROM
- Supports ACPI
- Auto detection of memory size
- Auto-configuration of hard disk types
- Multiple boot options including PXE
- Power Management:S1, S3, S4 and S5.

# Form Factor

- Flex ATX (9" x 7.5")
- ATX (20-pin + 4-pin) power connectors

# Integrated SATA (From Intel 3100)

- Six integrated SATA ports up to 1.5Gbit/s
- Supports AHCI mode

# Regulatory

- FCC Class B (DoC)
- European Community CE (Declaration of Conformity)

# **Software Specifications**

# **OS (Operating System) Support**

Windows XP Pro 32-bit + Sp2 Windows XP Pro 64-bit + Sp1 Windows Server 2003 32-bit + Sp1 Windows Server 2003 64-bit + R2 Vista Home Basic and Home Premium 32-bit Vista Business 32-bit Vista Ultimate 32-bit

TYAN reserves the right to add support or discontinue support for any OS with or without notice.

# **Chapter 2: Board Installation**

# Installation

You are now ready to install your motherboard. The mounting-hole pattern of the S5207 matches the Flex ATX specification. Before continuing with installation, confirm that your chassis supports the Flex ATX motherboard.

# How to install our products properly.... the first time!

The first thing you should do is to read this user's manual because the important information contained will make configuration and setup much easier. Here are some precautions you should take when installing your motherboard:

- (1) Ground yourself properly before removing your motherboard from the antistatic bag. Unplug the power from your computer power supply and then touch a safely grounded object to release static charge (i.e. power supply case). For the safest conditions, Tyan recommends wearing a static safety wrist strap.
- (2) Hold the motherboard by its edges and do not touch the bottom of the board, or flex the board in any way.
- (3) Avoid touching the motherboard components, IC chips, connectors, memory modules, and leads.
- (4) Place the motherboard on a grounded antistatic surface or on the antistatic bag that the board was shipped in.
- (5) Inspect the board for damage.

The following pages include details on how to install your motherboard into your chassis, as well as installing the processor, memory, disk drives and cables.

NOTE

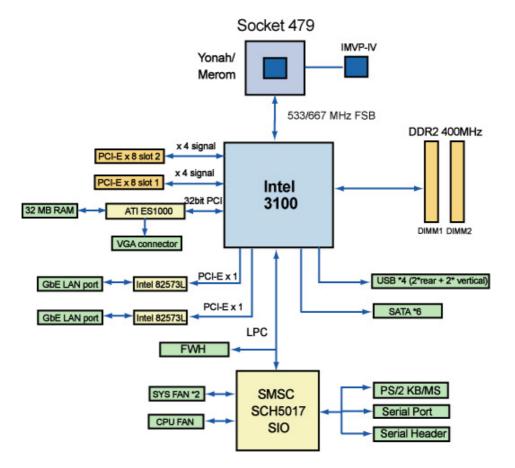
DO NOT APPLY POWER TO THE BOARD IF IT HAS BEEN DAMAGED

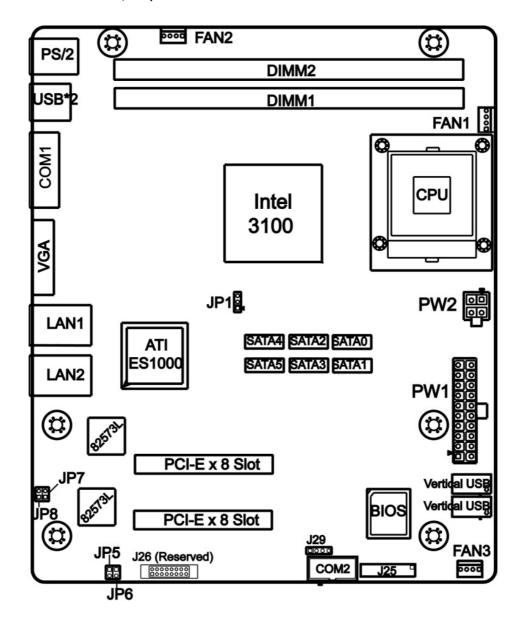


This picture is representative of the latest board revision available at the time of publishing. The board you receive may or may not look exactly like the picture above.

The following page includes details on the vital components of this motherboard.

# S5207 Block Diagram





This jumper diagram is representative of the latest board revision available at the time of publishing. The board you receive may not look exactly like the diagram above.

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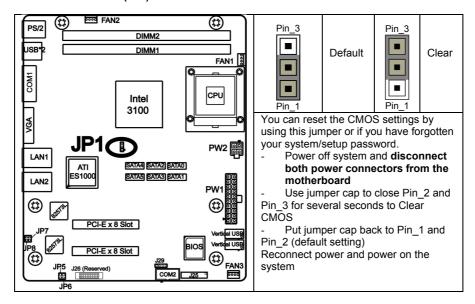
# 2.03 - Jumper Settings

Jumper	Function	Settings
JP1	Clear CMOS	See Section 2.04
J15	COM2 Header	See Section 2.05
J25	Front Panel Header	See Section 2.06
J29	System Speaker Header	See Section 2.07
JP5,JP6, JP7,JP8	LAN LED Header	See Section 2.08
FAN1,2,3	FAN Headers	See Section 2.09

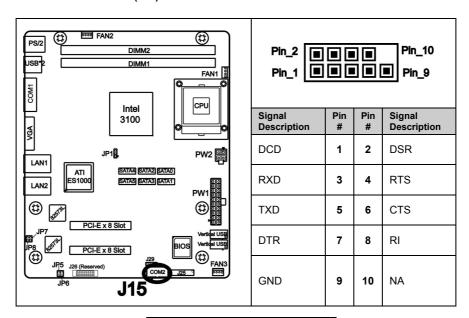
# Jumper Legend

	OPEN - Jumper OFF	Without jumper cover		
	CLOSED - Jumper ON	With jumper cover		
↑ Pin-1	To indicate the location of pin-1			
↑ Pin-1	To indicate the location of	f pin-1		

# 2.04 - Clear CMOS (JP1)

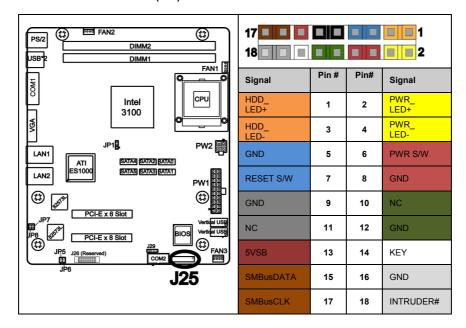


#### 2.05 - COM2 Header (J15)

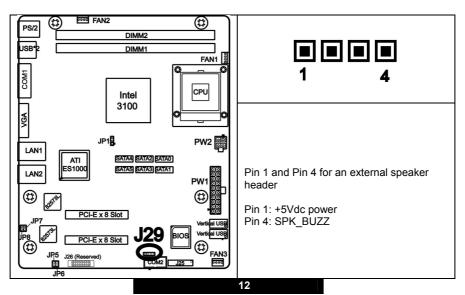


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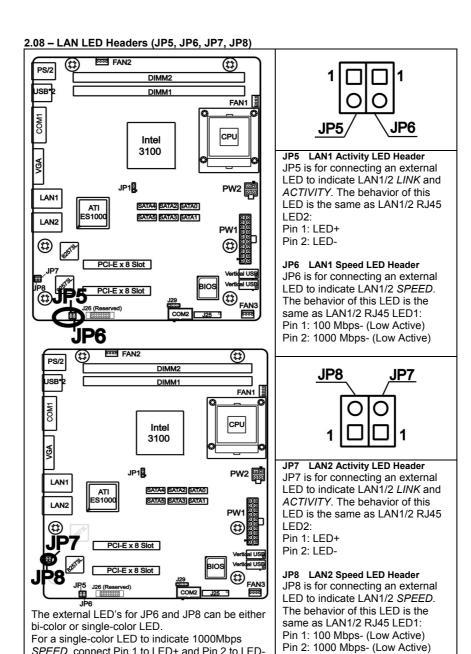
# 2.06 - Front Panel Header (J25)



# 2.07- System Speaker Header (J29)

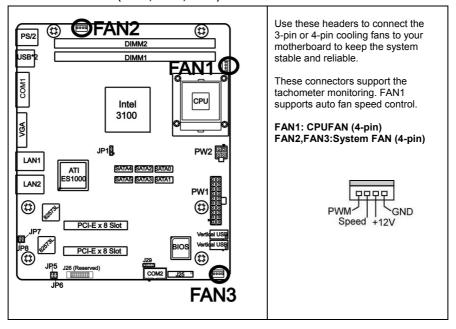


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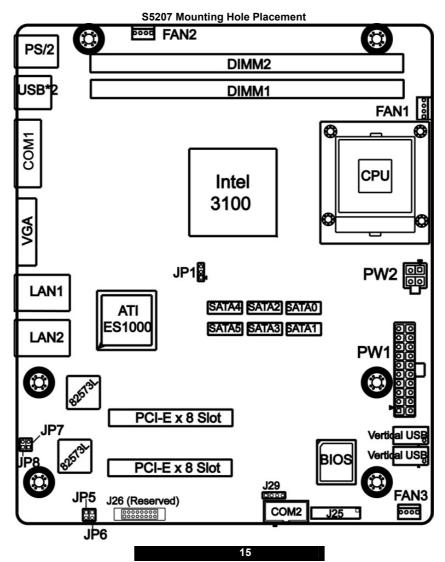
SPEED, connect Pin 1 to LED+ and Pin 2 to LED-

# 2.09 - FAN Headers (FAN1, FAN2, FAN3)



#### 2.10 - Tips on Installing the Motherboard in Chassis

Before installing your motherboard, make sure your chassis has the necessary motherboard support studs installed. These studs are usually metal and are gold in color. Usually, the chassis manufacturer will pre-install the support studs. If you're unsure of stud placement, simply lay the motherboard inside the chassis and align the screw holes of the motherboard to the studs inside the case. If there are any studs missing, you will know right away since the motherboard will not be able to be securely installed.

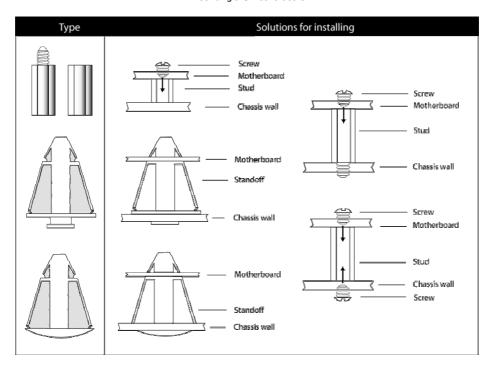


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Some chassis include plastic studs instead of metal. Although the plastic studs are usable, Tyan recommends using metal studs with screws that will fasten the motherboard more securely in place.

Below is a chart detailing what the most common motherboard studs look like and how they should be installed.

# Mounting the Motherboard



 $\textbf{TIP:} \ \ \text{Use metal studs if possible, as they hold the motherboard into place more securely than plastic standoffs.}$ 

#### 2.11 - Installing the Processor

Your S5207 supports Intel ® Core<sup>™</sup> Duo and Intel® Celeron M processor (Yonah); Intel ® Core<sup>™</sup> 2 Duo processor (Merom) in FC-PGA (Flip-Chip Pin Grid Array) package. Only Intel ® processors are certified and supported with this motherboard. Refer to the Tyan website for further details: www.tyan.com

<u>Processor Installation</u>
The processor should be installed carefully. Make sure you are wearing an antistatic strap and handle the processor as little as possible.

Follow these instructions to install your processor

For Pin Grid Array (PGA) sockets, look at the bottom of the processor to locate which corner of the processor does not have a PIN in it.





In order to insert the processor into the socket, line up the corner that does not 2. have a pin and insert the processor into the socket.





Do NOT force the pins of the processor into the socket, as it may cause damage to the processor. Insertion of the processor should be smooth and gentle, when aligned correctly.

 Hold down the processor with your finger and use a small flat head screw driver to turn the locking screw clockwise 180 degrees, to the locked position. Next to the sides of the screw on the socket, there are diagrams of a closed lock and an open lock indicating if the socket is locked or unlocked.





# 2.12 Heatsink Installation Procedures

After you have installed the processor, you should proceed to installing the heatsink. Heatsink will ensure that the processor does not overheat and continues to operate at maximum performance for as long as you own it.

Mount the self-adhesive square metal bracket on the bottom of the board underneath
the processor socket. This bracket lines up with the four holes making a square
around the processor and provides the nut for the fan screws to plug into.

**Note:** This CPU back bracket usually comes with CPU fan/heatsink. Both bracket and heatsink are not included in this motherboard accessories.

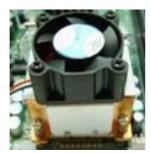




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2. Place the fan on top of the processor and screw it into the bracket as shown in





# Finishing Installing the Heatsink

After you finish installing the heatsink onto the processor and socket, attach the end wire of the fan (which should already be attached to the heatsink) to the motherboard.

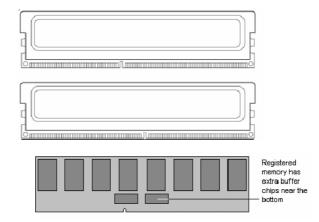


After you're finished installing all the fans you can connect your drives (hard drives, CD-ROM drives, etc.) to your motherboard.

#### 2.13 - Installing the Memory

Before attempting to install any memory, make sure that the memory you have is compatible with the motherboard as well as the processor. A Critical aspect to any system build is whether you're using the recommended memory for your specific motherboard. For compatibility information, please check Tyan's web site at: <a href="www.tyan.com">www.tyan.com</a>

The following diagram shows common types of DDR2 RAM modules:



Here are a few key points to note before installing memory into your S5207:

- Only Reg/ECC DDR2 400 MHz memory modules are supported.
- DIMM2 (the DIMM connector closest to the edge of the board, furthest from the CPU socket) must be installed for the system to turn on and POST (power on self test).
- ECC support.
- S5207 supports up to 4GB.

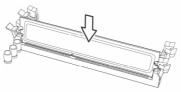
#### **Memory Installation Procedure**

When you install the memory modules, make sure the module aligns properly with the memory slot. The modules are keyed to ensure that it is inserted only one way. The method of installing memory modules are detailed by the following diagrams.

1. Press the locking levers in the direction shown in the following illustration.



Align the memory module with the socket. The memory module is keyed to fit only one way in the socket.



3. Seat the module firmly into the socket by gently pressing down until it sits flush with the socket. The locking levers pop up into place.



To remove the memory module, simply push the latches outwards until the memory module pops up.

**TIP:** When installing memory, a module may require a considerable amount of force to seat properly, although this is very rare. To avoid bending and damaging your motherboard, place it on its anti-static bag and onto a flat surface, and then proceed with memory installation.

NOTE

**YOU MUST ALWAYS** unplug the power connector to the motherboard before performing system hardware changes, to avoid damaging the board or expansion device.

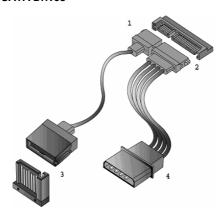
# 2.14 - Attaching Drive Cables

# **SATA Drives**

The S5207 is equipped with 6 Serial ATA (SATA) connectors. There is no need to set Master/Slave jumpers on SATA drives. Tyan has supplied six SATA cables and three SATA power adapters. If you did not receive the proper cables or power adapters for your particular product SKU, please contact your local vendor/distributor.

The following picture illustrates how to connect a SATA drive and a SAS drive respectively.

# **SATA Drives**



- SATA drive cable connection
- SATA drive power connection
- SATA cable motherboard connector
- 4. SATA drive power adapter

# 2.15 - Installing Add-In Cards

Before installing add-in cards, it's helpful to know if they are fully compatible with your motherboard. For this reason, we've provided the diagrams below, showing the compatible slots that may appear on your motherboard. Not all of the slots shown will necessarily appear on your motherboard, however, there will be combinations of what you see here.



Simply find the appropriate slot for your add-in card and insert the card firmly. Do not force any add-in cards (or anything else) into any slots if they won't seat in place. It's better to try another slot or return the faulty card rather than damaging both the motherboard and the add-in card.

**TIP:** It's good practice to install add-in cards in a staggered manner, rather than directly adjacent to each other. This allows air to circulate within the chassis more easily, providing improved cooling for all installed devices.

NOTE

**YOU MUST ALWAYS** unplug the power connector to the motherboard before performing system hardware changes, to avoid damaging the board or expansion device.

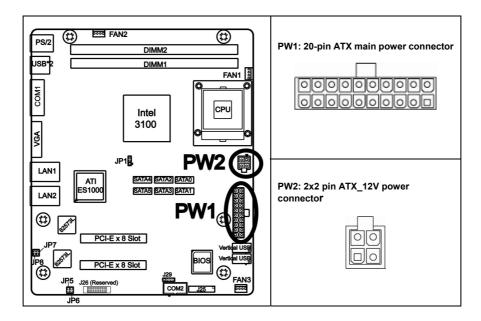
# 2.16 - Connecting External Devices

The following diagrams will detail the rear port stack for this S5207 motherboard:

# PS/2 Mouse/Keyboard Stacked USB2.0 Ports Serial Port VGA Port Gigabit Ethernet

This picture is representative of the latest board revision available at the time of publishing. The board you receive may or may not look exactly like the above picture.

Peripheral devices can be plugged straight into any of these ports but software may be required to complete the installation.



We suggest using a standard off-the-shelf ATX power supply with a power rating of 350 W. Look to the <a href="https://www.tyan.com">www.tyan.com</a> website for further information.

**NOTE:** The S5207 peripheral drive power connector must be independent of any other devices. A device such as a DVD/CD-ROM drive, hard drive, or any other devices cannot be attached onto the same power line. If connected, system stability is compromised.

# 2.18 - Finishing Up

Congratulations on making it this far! You're finished setting up the hardware aspect of your computer. Before closing up your chassis, make sure that all cables and wires are connected properly, especially IDE cables and most importantly, jumpers. You may have difficulty powering on your system if the motherboard jumpers are not set correctly.

In the rare circumstance that you have experienced difficulty, you can find help by asking your vendor for assistance. If they are not available for assistance, please find setup information and documentation online at our website or by **calling your vendor's support line.** 

# **Chapter 3: BIOS Setup**

# 3.1. About the BIOS

The BIOS is the basic input/output system, the firmware on the motherboard that enables your hardware to interface with your software. The BIOS determines what a computer can do without accessing programs from a disk. The BIOS contains all the code required to control the keyboard, display screen, disk drives, serial communications, and a number of miscellaneous functions. This chapter describes the various BIOS settings that can be used to configure your system.

The BIOS section of this manual is subject to change without notice and is provided for reference purposes only. The settings and configurations of the BIOS are current at the time of print and are subject to change, and therefore may not match exactly what is displayed on screen.

This section describes the BIOS setup program. The setup program lets you modify basic configuration settings. The settings are then stored in a dedicated, battery-backed memory (called NVRAM) that retains the information even when the power is turned off.

To start the BIOS setup utility:

- 1. Turn on or reboot your system.
- 2. Press <Del> during POST (F4 on remote console) to start the BIOS setup utility.

#### 3.1.1 Setup Basics

The table below shows how to navigate in the setup program using the keyboard.

Key	Function	
Tab	Moves from one selection to the next	
Left/Right Arrow Keys	Changes from one menu to the next	
Up/Down Arrow Keys	Moves between selections	
Enter	Opens highlighted section	
PgUp/PgDn Keys	Changes settings.	

#### 3.1.2 Getting Help

Pressing [F1] will display a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window, press [ESC] or the [F1] key again.

#### 3.1.3 In Case of Problems

If you have trouble booting your computer after making and saving the changes with the BIOS setup program, you can restart the computer by holding the power button down until the computer shuts off (usually within 4 seconds); resetting by pressing CTRL-ALT-DEL; or clearing the CMOS.

The best advice is to only alter settings that you thoroughly understand. In particular, do not change settings in the Chipset section unless you are absolutely sure of what you are doing. The Chipset defaults have been carefully chosen either by TYAN or your system manufacturer for best performance and reliability. Even a seemingly small change to the Chipset setup options may cause the system to become unstable or unusable.

#### 3.1.4 Setup Variations

Not all systems have the same BIOS setup layout or options. While the basic look and function of the BIOS setup remains more or less the same for most systems, the appearance of your Setup screen may differ from the charts shown in this section. Each system design and chipset combination requires a custom configuration. In addition, the final appearance of the Setup program depends on the system designer. Your system designer may decide that certain items should not be available for user configuration, and remove them from the BIOS setup program.

**NOTE**: The following pages provide the details of BIOS menu. Please be noticed that the BIOS menu are continually changing due to the BIOS updating. The BIOS menu provided are the most updated ones when this manual is written. Please visit Tyan's website at <a href="http://www.tyan.com">http://www.tyan.com</a> for the information of BIOS updating.

# 3.2 BIOS Main Menu

In this section, you can alter general features such as the date and time. Note that the options listed below are for options that can directly be changed within the Main Setup screen.

		BIOS Set	up Utility				
Main	Advanced	PCIPnP	Boot	Security	Chipset	Exit	
System Ove	System Overview				Use [Enter], [TAB] or [SHIFT-TAB] to select a		
AMI BIOS Version: Build Date: ID:	xx.xx.xx xx/xx/xx xxxxxxxx			field.	or [-] to confi		
Processor							
Speed: Count:				_	Select Screer ect Item	1	
System Mer Size:	mory			TAB S	Change Field Select Field General Help		
System Tim System Dat			H:MM:SS] DD/YYYY]	F9 S	Save and Ex etup Defaults Exit		

# **BIOS Version**

This displays the version of BIOS.

# **BIOS Build Date**

This displays the date of BIOS build.

# System Time / Date setup

System Time: Adjusts the system clock.
HH Hours (24hr. format)
MM Monutes SS Seconds System Date: Adjusts the system date.

MM Months

DD Days

YYYY Years

# **Processor**

This displays the CPU model and frequency.

**System Memory**This displays the Memory volume.

# 3.3 BIOS Advanced Menu

This section facilitates configuring advanced BIOS options for your system.

Main	Advanced	BIOS Setu PCIPnP	up Utility Boot	Socurity	Chipset	Exit
iviairi	Auvanceu	PCIPIIP	DOOL	Security	Chipset	EXIL
Advanced S	Settings			Configu	Configure CPU	
WARNING	: Setting wrong way cause sys	alues in below s tem to malfuncti				
➤ IDE Con ➤ Super IC ➤ ACPI Co ➤ AHCI Co ➤ APM Co ➤ Event Lo ➤ Hardwar ➤ PCI Expi	O Configuration on figuration on figuration on figuration of Configuration of Configuration ress Configuration Access Configuration	on		↑↓ Sel Enter G F1 ( F10	Select Screen lect Item o to Sub Scre General Help Save and Exi Exit	een

**3.3.1 Advanced CPU Configuration**This section allows you to fine-tune the processor options.

his section allows you to line-tune the pi	occasor options.	
Advanced		1
Configure advanced CPU settings Module Version: xx.xx		This should be enabled in order to enable or disable
Manufacturer: Intel		the Hardware Prefetcher Disable Feature.
Frequency: FSB Speed: Cache L1: Cache L2: Ratio Actual Value:		
Max CPUID Value Limit Vanderpool Technology Execute Disable Bit Core Multi-Processing DTS-based Thermal Management DTS Calibration Intel® SpeedStep™ tech. Intel ® C-State tech. C1 Config.	[Disabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Disabled] [Disabled]	← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit
C2 Config. C3 Config. C4 Config.	[Standard] [Standard] [Disabled]	

# Core Multi-Processing

When disabled, diable one execution core.

# **DTS-based Thermal Management**

Enable/Disable Thermal Management utilizing the CPU's Digital Thermal Sensor.

#### Intel® SpeedStep tech

Maximum: CPU speed is set to maximum. Minimum: CPU speed is set to minimum.

Automatic: CPU speed controlled by operating system.

Disabled: Default CPU speed.

# 3.3.2 Advanced IDE Configuration

Advanced		I	
IDE Configuration	Select whether the IDE		
ATA/IDE Configuration Configure SATA as	[Enhanced] [IDE]	<ul> <li>channels should be initialized in Compatible or Enhanced mode of operation.</li> </ul>	
<ul><li>▶ Primary IDE Master</li><li>▶ Primary IDE Slave</li><li>▶ Secondary IDE Master</li><li>▶ Secondary IDE Slave</li></ul>	[Not Detected] [Not Detected] [Not Detected] [Not Detected]		
Hard Disk Write Protect IDE Detect Time Out	[Disabled] [35]	← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit	

#### **ATA/IDE Configuration**

This defines the operation mode of IDE channels.
Disabled/ Compatible/ Enhanced

# Configure SATA as

Select legacy IDE or AHCI as the SATA interface.

# **Hard Disk Write Protect**

This feature is used to Disable/Enable device write protection. This will be effective only if device is accessed through BIOS.

Disabled/ Enabled

#### **IDE Detect Time Out (Sec)**

Select the time out value for detecting ATA/ATAPI device(s).

# 3.3.3 Super I/O Configuration

This setting allows you to configure Serial Port1 Base Addresses. Disabled/ 3F8/IRQ4 / 3E8/IRQ4 / 2E8/IRQ3

This setting allows you to configure Serial Port2 Base Addresses. Disabled/ 2F8/IRQ3 / 3E8/IRQ4 / 2E8/IRQ3

Advanced			
Configure SCH5017 Super IO C	Allow BIOS to Select Serial Port1 Base Addresses.		
Serial Port1 Address [3F8/IRQ4] Serial Port2 Address [2F8/IRQ3]		Totti base Addresses.	
		← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit	

# 3.3.4 ACPI Configuration

Advanced	
ACPI Settings	General ACPI Configuration settings
► General ACPI Configuration ► Advanced ACPI Configuration ► Chipset ACPI Configuration	Comiguration Sciengs
	← Select Screen  ↑↓ Select Item  Enter Go to Sub Screen.  F1 General Help  F10 Save and Exit  Esc Exit

# 3.3.4.1 General ACPI Configuration

Advanced		I	
General ACPI Configuration		Select the ACPI state used for System Suspend.	
Suspend mode Repost Video on S3 Resume	[Auto] [No]	ioi dystem daspena.	
		← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit	

Suspend mode: S1 (POS)/ S3(STR)/Auto Repost Video on S3 Resume: Determines whether to invoke VGA BIOS post on S3/STR

resume.

# 3.3.4.2 Advanced ACPI Configuration

Advanced		I
Advanced ACPI Configuration	Enable RSDP pointers to 64-bit Fixed System Description Tables. Different ACPI version has some addition.	
ACPI Version Features [ACPI v2.0] ACPI APIC support [Enabled] AMI OEMB table [Enabled] Headless mode [Disabled]		
		← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit

# 3.3.4.3 Chipset ACPI Configuration

Advanced		l	
South Bridge ACPI configuration		Enable/Disable APIC ACPI SCI IRQ.	
APIC ACPI SCI IRQ USB Device Wakeup From S3/S4	[Disabled] [Enabled]		

3.3.5 AHCI Configuration

Advanced		1	
AHCI Settings		Some SATA CD/DVD in	
AHCI CD/DVD Boot Time out	[15]	AHCI mode need to wait ready longer.	
►AHCI Port0 [Not Detected]  ►AHCI Port1 [Not Detected]  ►AHCI Port2 [Not Detected]  ►AHCI Port3 [Not Detected]  ►AHCI Port4 [Not Detected]  ►AHCI Port5 [Not Detected]		← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit	

3.3.6 APM Configuration

Advanced APM Configuration		Enable/Disable APM
Power Management/APM Video Power Down Mode Hard Disk Power Down Mode Suspend Time Out	[Enabled] [Suspend] [Suspend] [Disabled]	Enable/Bladde / II III
Throttle Slow Clock Ratio Keyboard & PS/2 Mouse Power Button Mode	[50%] [Monitor] [On/Off]	<ul> <li>← Select Screen</li> <li>↑↓ Select Item</li> <li>+- Change Option</li> <li>F1 General Help</li> </ul>
Advance Resume Event Controls Resume On RTC Alarm	[Disabled]	F10 Save and Exit Esc Exit

# **Video Power Down Mode**

Power Down Video in Suspend or Standby Mode.

# Hard Disk Power Down Mode

Power Down Hard Disk in Suspend or Standby Mode.

# **Suspend Time Out**

Go into Suspend in the specified Time.

Throttle Slow Clock Ratio Select the duty cycle in throttle mode.

# **Power Button Mode**

Go into on/off, or Suspend when Power Button is pressed.

# Resume on PME#

Disable/Enable PME to generate a wake Event.

# Resume on RTC Alarm

Disable/Enable RTC to generate a wake Event.

# 3.3.7 Event Log Configuration

Advanced		
Event Logging details	View all unread events on the Event Log.	
View Event Log Mark all events as read Clear Event Log		
	← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit	

# 3.3.8 Hardware Health Configuration

This displays critical system parameters like CPU speed, fan speeds, voltage levels and CPU temperature.

Advanced		İ	
Hardware Health Configuration		Enables Hardware Health	
H/W Health Function Auto FAN Power Control PWM Minimal Duty Cycle  Hardware Health Event Monitoring  Mainboard Voltages Report CPU Temperature: Ambient (Near BIOS chip) Temp: Ambient (WhitmoreLake) Temp: FAN1 Speed FAN2 Speed FAN3 Speed	[Enabled] [Enabled] [30% Duty Cycle]	← ↑↓ +- F1 F10 Esc	Save and Exit

3.3.9 PCI Express Configuration

Advanced		İ	
PCI Express Configuration		Enable/Disable PCI Express L0s and L1 link power states.	
Active State Power-Management PCle Slot 1 (x4) PCle Slot 2 (x4) PCle Jitter Tolerance PCle Compliance mode  LAN1 LAN2	[Disabled] [Enabled] [Enabled] [Enabled] [Disabled]  [Auto]	← ↑↓ +- F1 F10 Esc	Select Screen Select Item Change Option General Help Save and Exit Exit

## 3.3.10 Remote Access Configuration

Advanced	
Configure Remote Access type and parameters	Select Remote Access type.
Remote Access [Disabled]	type.
	← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit

3.3.11 USB Configuration

Advanced			
USB Configuration			le USB host ollers
Module Version		COITE	Ulicis
USB Device Enabled:			
USB Function Legacy USB support USB 2.0 Controller USB 2.0 Controller mode BIOS EHCI Hand-off  VUSB Mass Storage Device Configura	[4 USB Ports]     [Enabled]     [Enabled]     [HiSpeed]     [Enabled]	← ↑↓ +- F1 F10 Esc	Select Screen Select Item Change Option General Help Save and Exit Exit

**Legacy USB Support**Enables support for legacy USB. Auto option disables legacy support if no USB Devices are connected.

# **USB 2.0 Controller Mode**

Configure the USB 2.0 Controller in HiSpeed(480Mbps) or FullSpeed (12Mbps)

# **BIOS EHCI Hand-off**

This is a workaround for OSes without EHCl hand-off support. The EHCl ownership change should claim by EHCl driver.

## 3.3.11.1 USB Mass Storage Device Configuration

Advanced	1		
USB Mass Storage Device Configuration	Number of Seconds POST		
USB Mass Storage Reset Delay [20 sec]	<ul> <li>waits for the USB mass storage device after start unit command.</li> </ul>		
Device #1 ATP ATP Petito UFD  Emulation Type Device #2 ATP ATP Petito UFD Emulation Type  [Auto]	← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit		

Emulation Type [Auto]
If Auto, USB device less than 530MB will be emulated as Floppy and remaining as hard drive. Forced FDD option can be used to force a HDD formatted drive to boot as FDD (Ex.ZIP drive)

## 3.4 PCI/PnP

		BIOS S	Setup Utility			
Main	Advanced	PCIPnP	Boot	Security	Chipset	Exit
	d PCI/PnP Settin G: Setting wrong may cause sy	Clear N System	IVRAM durir 1 Boot.	ng		
	ay O/S ncy Timer RQ TO PCI VGA	<b>.</b>	[No] [No] [64] [Yes] [Disabled]	↑↓ Se Enter G F1 F10	Select Scree elect Item Go to Sub Sc General Hel Save and Ex Exit	reen p

## Plug & Play O/S

No: Lets the BIOS configure all the devices in the system.

Yes: Lets the operating system configure Plug and Play (PnP) devices not required for boot if your system has a Plug and Play operating system

# **PCI Latency Timer**

Value in units of PCI clocks for PCI device latency timer register.

# Allocate IRQ to PCI VGA

Yes: Assign IRQ to PCI VGA card if card requests IRQ.

No: Does not assign IRQ to PCI VGA card even if card requests an IRQ.

## **Palette Snooping**

Enabled: informs the PCI devices that an ISA graphics device is installed in the system so the card will function correctly.

## 3.5 Boot

	BIOS Se	tup Utility			
Main Advanced	PCIPnP	Boot	Security	Chipset	Exit
Boot Settings			Configu System	re Settings d	uring
▶Boot Settings Config	uration		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
1 <sup>st</sup> Boot Device 2 <sup>nd</sup> Boot Device 3 <sup>rd</sup> Boot Device 4 <sup>th</sup> Boot Device	[USB:ATP [Network:	ATP Petito] ATP Petito] IBA GE slo] IBA GE slo]	↑↓ Sel Enter G F1	Select Screer lect Item o to Sub Scr General Help Save and Ex Exit	een

3.5.1 Boot Settings Configuration

5.5. I BOOL Settings Configuration		
Boot		
Boot Settings Configuration	Allow BIOS to SKIP certain	
Quick Boot Quiet Boot AddOn ROM Display Mode Bootup Num-Lock PS/2 Mouse Support Wait for F1 if Error Hit "DEL" Message Display Interrupt 19 Capture Endless Boot Auto Eject Function	[Disabled] [Disabled] [Force BIOS] [On] [Auto] [Enabled] [Enabled] [Enabled] [Disabled] [Disabled]	tests while booting. This will decrease the time needed to boot the system.  ← Select Screen ↑↓ Select Item +- Change Option F1 General Help F10 Save and Exit Esc Exit

# **Quiet Boot**

Disabled: Displays normal POST messages.
Enabled: Displays OEM Logo instead of POST messages.

**AddOn ROM Display Mode** Force BIOS/ Keep Current Set display mode for Option ROM.

Interrupt 19 Capture
Enabled: Allows option roms to trap interrupt 19.

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# 3.6 Security

			BIOS Se	etun U	Itility					
Main	Advanced	PCIF		•	oot	S	ecurity	,	Chipset	Exit
Security Settings							Install passw		change the	•
Supervisor Password: Not Installed User Password: Not Installed										
Change Supervisor Password Change User Password						1 🖤	elec	lect Scree of Item to Sub Sci		
Boot Sec	tor Virus Protecti	on	[Disab	oled]			F1 F10 Esc	Sa	eneral Help ave and Ex iit	

# 3.7 Chipset

		BIOS S	etup Utility				
Main	Advanced	PCIPnP	Boot	S	ecurity	Chipset	Exit
Advance	d Chipset Settings	3			Config	ure South Bri	dge
WARNIN	IG: Setting wrong may cause sy				routure		
1	Bridge Configuration, Spread Spectr		oled]		↑↓ Se Enter 0 F1 F10	Select Screen elect Item Go to Sub Scr General Help Save and Ex Exit	reen

# 3.7.1 Chipset

	set		
South Bridge Chipset Configuration	ı	Option	าร
SMBUS Controller	[Enabled]	Enable Disable	•
SLP_S4# Min. Assertion Width Restore on AC Power Loss	[1 to 2 seconds] [Power On]		
Watchdog Mode	[Disabled]	$\uparrow\downarrow$	Select Screen Select Item Change Option General Help Save and Exit Exit

Watchdog Mode
Post: BIOS post Watchdog, timer counting start at PowerOn, Stop at OS boot
OS: OS boot Watchdog, Start at OS boot
PowerOn: Start at PowerOn.

### 3.8 Exit

	Exit
Exit Options	Exit system setup after saving the changes.
Save Changes and Exit Discard Changes and Exit Discard Changes	F10 key ca be used for this operation.
Load Optimal Defaults Load Failsafe Defaults	← Select Screen  ↑↓ Select Item  +- Change Option  F1 General Help  F10 Save and Exit  Esc Exit

Save Changes and Exit
Exit system setup after saving the changes F10key can be used for this operation.

**Discard Changes and Exit**Exit system setup without saving any changes
ESC key can be used for this operation.

**Discard Changes**Discards all changes made without exiting BIOS setup.

F7 key can be used for this operation.

**Load Optimal Defaults**Loads Optimal Default values for all the setup questions.
F9 key can be used for this operation.

Load Failsafe Defaults
Loads Failsafe Default values for all the setup questions.
F8 key can be used for this opera

# **Chapter 4: Diagnostics**

Note: if you experience problems with setting up your system, always check the following things in the following order:

### CPU, Memory, Video

By checking these items, you will most likely find out what the problem might have been when setting up your system. For more information on troubleshooting, check the Tyan website at: <a href="http://www.tyan.com">http://www.tyan.com</a>.

### 4.0 Beep Codes

Fatal errors which halt the boot process are communicated through a series of audible beeps.

- (1) Memory module initialization failed
  - (a) memory modules might not be plugged in correct configuration
  - (b) wrong type of memory
  - (c) bad memory modules
- (2) Graphics initialization failed

Before contacting your vendor or Tyan Technical Support, be sure that you note as much as you can about the beep code length and order that you experience. Also, be ready with information regarding add-in cards, drives and O/S to speed the support process and come to a quicker solution.

### 4.1 Flash Utility

Every BIOS file is unique for the motherboard it was designed for. For Flash Utilities, BIOS downloads, and information on how to properly use the Flash Utility with your motherboard, please check the Tyan web site: <a href="http://www.tyan.com">http://www.tyan.com</a>

NOTE

Please be aware that by flashing your BIOS, you agree that in the event of a BIOS flash failure, you must contact your dealer for a replacement BIOS. There are no exceptions. Tyan does not have a policy for replacing BIOS chips directly with end users. In no event will Tyan be held responsible for damages done by the end user.

# **Appendix: Glossary**

**ACPI (Advanced Configuration and Power Interface):** a power management specification that allows the operating system to control the amount of power distributed to the computer's devices. Devices not in use can be turned off, reducing unnecessary power expenditure.

**AGP** (Accelerated Graphics Port): a PCI-based interface which was designed specifically for demands of 3D graphics applications. The 32-bit AGP channel directly links the graphics controller to the main memory. While the channel runs only at 66 MHz, it supports data transmission during both the rising and falling ends of the clock cycle, yielding an effective speed of 133 MHz.

**ATAPI (AT Attachment Packet Interface):** also known as IDE or ATA; a drive implementation that includes the disk controller on the device itself. It allows CD-ROMs and tape drives to be configured as master or slave devices, just like HDDs.

**ATX**: the form factor designed to replace the AT form factor. It improves on the AT design by rotating the board 90 degrees, so that the IDE connectors are closer to the drive bays, and the CPU is closer to the power supply and cooling fan. The keyboard, mouse, USB, serial, and parallel ports are built-in.

**Bandwidth:** refers to carrying capacity. The greater the bandwidth, the more data the bus, phone line, or other electrical path can carry. Greater bandwidth results in greater speed.

**BBS (BIOS Boot Specification):** a feature within the BIOS that creates, prioritizes, and maintains a list of all Initial Program Load (IPL) devices, and then stores that list in NVRAM. IPL devices have the ability to load and execute an OS, as well as provide the ability to return to the BIOS if the OS load process fails. At that point, the next IPL device is called upon to attempt loading of the OS.

**BIOS (Basic Input/Output System):** the program that resides in the ROM chip, which provides the basic instructions for controlling your computer's hardware. Both the operating system and application software use BIOS routines to ensure compatibility.

**Buffer:** a portion of RAM which is used to temporarily store data; usually from an application though it is also used when printing and in most keyboard drivers. The CPU can manipulate data in a buffer before copying it to a disk drive. While this improves system performance (reading to or writing from a disk drive a single time is much faster than doing so repeatedly) there is the possibility of losing your data should the system crash. Information in a buffer is temporarily stored, not permanently saved.

**Bus:** a data pathway. The term is used especially to refer to the connection between the processor and system memory, and between the processor and PCI or ISA local buses.

**Bus mastering:** allows peripheral devices and IDEs to access the system memory without going through the CPU (similar to DMA channels).

**Cache:** a temporary storage area for data that will be needed often by an application. Using a cache lowers data access times since the information is stored in SRAM instead of slower DRAM. Note that the cache is also much smaller than your regular memory: a typical cache size is 512KB, while you may have as much as 4GB of regular memory.

**Closed and open jumpers:** jumpers and jumper pins are active when they are "on" or "closed", and inactive when they are "off" or "open".

**CMOS (Complementary Metal-Oxide Semiconductors):** chips that hold the basic startup information for the BIOS.

**COM port:** another name for the serial port, which is called as such because it transmits the eight bits of a byte of data along one wire, and receives data on another single wire (that is, the data is transmitted in serial form, one bit after another). Parallel ports transmit the bits of a byte on eight different wires at the same time (that is, in parallel form, eight bits at the same time).

**FBDIMM (Double Data Rate):** a technology designed to double the clock speed of the memory. It activates output on both the rising and falling edge of the system clock rather than on just the rising edge, potentially doubling output.

**DIMM (Dual In-line Memory Module):** faster and more capacious form of RAM than SIMMs, and do not need to be installed in pairs.

**DIMM bank:** sometimes called DIMM socket because the physical slot and the logical unit are the same. That is, one DIMM module fits into one DIMM socket, which is capable of acting as a memory bank

**DMA (Direct Memory Access):** channels that are similar to IRQs. DMA channels allow hardware devices (like soundcards or keyboards) to access the main memory without involving the CPU. This frees up CPU resources for other tasks. As with IRQs, it is vital that you do not double up devices on a single line. Plug-n-Play devices will take care of this for you.

**DRAM (Dynamic RAM):** widely available, very affordable form of RAM which looses data if it is not recharged regularly (every few milliseconds). This refresh requirement makes DRAM three to ten times slower than non-recharged RAM such as SRAM.

**ECC** (Error Correction Code or Error Checking and Correcting): allows data to be checked for errors during run-time. Errors can subsequently be corrected at the same time that they're found.

**EEPROM (Electrically Erasable Programmable ROM):** also called Flash BIOS, it is a ROM chip which can, unlike normal ROM, be updated. This allows you to keep up with changes in the BIOS programs without having to buy a new chip. TYAN's BIOS updates can be found at http://www.tvan.com

**ESCD (Extended System Configuration Data):** a format for storing information about Plug-n-Play devices in the system BIOS. This information helps properly configure the system each time it hoots

Firmware: low-level software that controls the system hardware.

**Form factor:** an industry term for the size, shape, power supply type, and external connector type of the Personal Computer Board (PCB) or motherboard. The standard form factors are the AT and ATX

Global timer: onboard hardware timer, such as the Real-Time Clock (RTC).

HDD: stands for Hard Disk Drive, a type of fixed drive.

H-SYNC: controls the horizontal synchronization/properties of the monitor.

**HyperThreading**<sup>™</sup>: a high speed, low latency, scalable point-to-point link for interconnecting ICs on boards. It can be significantly faster than a PCI bus for an equivalent number of pins. It provides the bandwidth and flexibility critical for today's networking and computing platforms while retaining

the fundamental programming model of PCI.

IC (Integrated Circuit): the formal name for the computer chip.

**IDE** (Integrated Device/Drive Electronics): a simple, self-contained HDD interface. It can handle drives up to 8.4 GB in size. Almost all IDEs sold now are in fact Enhanced IDEs (EIDEs), with maximum capacity determined by the hardware controller.

IDE INT (IDE Interrupt): a hardware interrupt signal that goes to the IDE.

I/O (Input/Output): the connection between your computer and another piece of hardware (mouse, keyboard, etc.)

**IRQ (Interrupt Request):** an electronic request that runs from a hardware device to the CPU. The interrupt controller assigns priorities to incoming requests and delivers them to the CPU. It is important that there is only one device hooked up to each IRQ line; doubling up devices on IRQ lines can lock up your system. Plug-n-Play operating systems can take care of these details for you.

**Latency:** the amount of time that one part of a system spends waiting for another part to catch up. This occurs most commonly when the system sends data out to a peripheral device and has to wait for the peripheral to spread (peripherals tend to be slower than onboard system components).

**NVRAM:** ROM and EEPROM are both examples of Non-Volatile RAM, memory that holds its data without power. DRAM, in contrast, is volatile.

Parallel port: transmits the bits of a byte on eight different wires at the same time.

**PCI** (Peripheral Component Interconnect): a 32 or 64-bit local bus (data pathway) which is faster than the ISA bus. Local buses are those which operate within a single system (as opposed to a network bus, which connects multiple systems).

**PCI PIO (PCI Programmable Input/Output) modes:** the data transfer modes used by IDE drives. These modes use the CPU for data transfer (in contrast, DMA channels do not). PCI refers to the type of bus used by these modes to communicate with the CPU.

PCI-to-PCI bridge: allows you to connect multiple PCI devices onto one PCI slot.

**Pipeline burst SRAM:** a fast secondary cache. It is used as a secondary cache because SRAM is slower than SDRAM, but usually larger. Data is cached first to the faster primary cache, and then, when the primary cache is full, to the slower secondary cache.

**PnP** (**Plug-n-Play**): a design standard that has become ascendant in the industry. Plug-n-Play devices require little set-up to use. Devices and operating systems that are not Plug-n-Play require you to reconfigure your system each time you add or change any part of your hardware.

**PXE** (**Preboot Execution Environment**): one of four components that together make up the Wired for Management 2.0 baseline specification. PXE was designed to define a standard set of preboot protocol services within a client with the goal of allowing networked-based booting to boot using industry standard protocols.

RAID (Redundant Array of Independent Disks): a way for the same data to be stored in different places on many hard drives. By using this method, the data is stored redundantly and multiple hard drives will appear as a single drive to the operating system. RAID level 0 is known as striping, where data is striped (or overlapped) across multiple hard drives, but offers no fault-tolerance. RAID level 1 is known as mirroring, which stores the data within at least two hard drives, but does not stripe. RAID level 1 also allows for faster access time and fault-tolerance, since either hard drive can be

read at the same time. RAID level 0+1 is both striping and mirroring, providing fault-tolerance, striping, and faster access all at the same time.

RAIDIOS: RAID I/O Steering (Intel)

**RAM (Random Access Memory):** technically refers to a type of memory where any byte can be accessed without touching the adjacent data and is often referred to the system's main memory. This memory is available to any program running on the computer.

**ROM** (Read-Only Memory): a storage chip which contains the BIOS; the basic instructions required to boot the computer and start up the operating system.

**SDRAM (Synchronous Dynamic RAM):** called as such because it can keep two sets of memory aFBDIMMesses open simultaneously. By transferring data alternately from one set of aFBDIMMesses and then the other, SDRAM cuts down on the delays associated with non-synchronous RAM, which must close one aFBDIMMess bank before opening the next.

**Serial port:** called as such because it transmits the eight bits of a byte of data along one wire, and receives data on another single wire (that is, the data is transmitted in serial form, one bit after another).

SCSI Interrupt Steering Logic (SISL): Architecture that allows a RAID controller, such as AcceleRAID 150, 200 or 250, to implement RAID on a system board-embedded SCSI bus or a set of SCSI busses. SISL: SCSI Interrupt Steering Logic (LSI) (only on LSI SCSI boards)

Sleep/Suspend mode: in this mode, all devices except the CPU shut down.

**SDRAM (Static RAM):** unlike DRAM, this type of RAM does not need to be refreshed in order to prevent data loss. Thus, it is faster and more expensive.

**Standby mode:** in this mode, the video and hard drives shut down; all other devices continue to operate normally.

**UltraDMA-33/66/100:** a fast version of the old DMA channel. UltraDMA is also called UltraATA. Without a proper UltraDMA controller, your system cannot take advantage of higher data transfer rates of the new UltraDMA/UltraATA hard drives.

**USB** (Universal Serial Bus): a versatile port. This one port type can function as a serial, parallel, mouse, keyboard or joystick port. It is fast enough to support video transfer, and is capable of supporting up to 127 daisy-chained peripheral devices.

VGA (Video Graphics Array): the PC video display standard

V-SYNC: controls the vertical scanning properties of the monitor.

**ZCR (Zero Channel RAID):** PCI card that allows a RAID card to use the onboard SCSI chip, thus lowering cost of RAID solution

**ZIF Socket (Zero Insertion Force socket):** these sockets make it possible to insert CPUs without damaging the sensitive CPU pins. The CPU is lightly placed in an open ZIF socket, and a lever is pulled down. This shifts the processor over and down, guiding it into the board and locking it into place.

# **Technical Support**

If a problem arises with your system, you should first turn to your dealer for direct support. Your system has most likely been configured or designed by them and they should have the best idea of what hardware and software your system contains. Hence, they should be of the most assitance for you. Furthermore, if you purchased your system from a dealer near you, take the system to them directly to have it serviced instead of attempting to do so yourself (which can have expensive consequences).

If these options are not available for you then Tyan Computer Corporation can help. Besides designing innovative and quality products for over a decade, Tyan has continuously offered customers service beyond their expectations. Tyan's website (<a href="www.tyan.com">www.tyan.com</a>) provides easy-to-access resources such as in-depth Linux Online Support sections with downloadable Linux drivers and comprehensive compatibility reports for chassis, memory and much more. With all these convenient resources just a few keystrokes away, users can easily find the latest software and operating system components to keep their systems running as powerful and productive as possible. Tyan also ranks high for its commitment to fast and friendly customer support through email. By offering plenty of options for users, Tyan serves multiple market segments with the industry's most competitive services to support them.

"Tyan's tech support is some of the most impressive we've seen, with great response time and exceptional organization in general" - Anandtech.com

Please feel free to contact us directly for this service at techsupport@tyan.com

Help Resources:

- 1. See the beep codes section of this manual.
- 2. See the TYAN website for FAQ's, bulletins, driver updates, and other information: <a href="http://www.tyan.com">http://www.tyan.com</a>
- 3. Contact your dealer for help BEFORE calling TYAN.
- 4. Check the TYAN user group: alt.comp.periphs.mainboard.TYAN

## **Returning Merchandise for Service**

During the warranty period, contact your distributor or system vendor FIRST for any product problems. This warranty only covers normal customer use and does not cover damages incurred during shipping or failure due to the alteration, misuse, abuse, or improper maintenance of products.

**NOTE:** A receipt or copy of your invoice marked with the date of purchase is required before any warranty service can be rendered. You may obtain service by calling the manufacturer for a Return Merchandise Authorization (RMA) number. The RMA number should be prominently displayed on the outside of the shipping carton and the package should be mailed prepaid. TYAN will pay to have the board shipped back to you



### Notice for the USA

Compliance Information Statement (Declaration of Conformity Procedure) DoC FCC Part 15: This device complies with part 15 of the FCC Rules

Operation is subject to the following conditions:

This device may not cause harmful interference, and

This device must accept any interference received including interference that may cause undesired operation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try one or more of the following measures:

Reorient or relocate the receiving antenna.

Increase the separation between the equipment and the receiver. Plug the equipment into an outlet on a circuit different from that of the receiver. Consult the dealer on an experienced radio/television technician for help.

### Notice for Canada

This apparatus complies with the Class B limits for radio interference as specified in the Canadian Department of Communications Radio Interference Regulations. (Cet appareil est conforme aux norms de Classe B d'interference radio tel que specifie par le Ministere Canadien des Communications dans les reglements d'ineteference radio.)



Notice for Europe (CE Mark)
This product is in conformity with the Council Directive 89/336/EEC, 92/31/EEC (EMC)

CAUTION: Lithium battery included with this board. Do not puncture, mutilate, or dispose of battery in fire. Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by manufacturer. Dispose of used battery according to manufacturer instructions and in accordance with your local regulations.

Document #: D1793-100

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