

RL78 Family EEPROM Emulation Library Pack02 Ver.1.01 for the CC-RL compiler

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Release Note

Thank you for using the RL78 Family EEPROM Emulation Library Pack02 Ver.1.01 for the CC-RL compiler.

This document contains notes and points for caution on using the EEPROM Emulation Library Pack02 Ver.1.01 for the CC-RL compiler. Please read this document before use.

Contents

1.	Target Product	.2
	User's Manual	
3.	Revisions	.2
4.	Supported Tools	.3
5.	Installation	.4
5.1		
5.2	2 Uninstallation	. 4
5.3	B File Configuration	. 5
6.	How to Debug a Program	.6
6.1	Notes on Debugging	.6
7.	How to Allocate Sections	.6



1. Target Product

The target product of this release note is as follows:

Product Name	Ver.	ZIP File Name	Zip Ver.
RL78 Family	Ver.1.01	JP_R_EEL_RL78_P02_V1.01_CCRL_A_E	V1.01A
EEPROM Emulation Library Pack02			
for the CC-RL compiler			

2. User's Manual

The user's manual and "difference document" listed below are available for this version. Please read both documents before using this product.

Title	Document Number
RL78 Family EEPROM Emulation Library Pack02 User's Manual	R01US0068EJ0100
RL78 Family EEPROM Emulation Library Pack02 Differences between the CA78K0R compiler version and the CC-RL compiler version	R20UT3486EJ0100

3. Revisions

There is no correction item.



4. Supported Tools

Use the following tool version when using the EEPROM Emulation Library Pack02 Ver. 1.01 for the CC-RL compiler.

Tool Name	Version
Integrated development environment CS+	V3.01.00 or later



5. Installation

This chapter describes how to install and uninstall the EEPROM Emulation Library Pack02 Ver. 1.01.

5.1 Installation

Install the EEPROM Emulation Library Pack02 by using the following procedure:

- (1) Start Windows.
- (2) Decompress the folder that contains the EEPROM Emulation Library Pack02 files and place the extracted folder at any location chosen by the user.

5.2 Uninstallation

Uninstall the EEPROM Emulation Library Pack02 by using the following procedure:

- (1) Start Windows.
- (2) Delete the folder that contains the EEPROM Emulation Library Pack02 files and was placed at the location chosen by the user.



5.3 File Configuration

The file configuration after decompressing the folder that contains the EEPROM Emulation Library Pack02 for the CC-RL compiler files is shown below.

Installation folder						
EELRL78 Pack02						
L Vx.xx						
- Read me	— Read me					
L TERMS AND CONDITIONS FOR USIN	L TERMS AND CONDITIONS FOR USING THE SOFTWARE.pdf :					
Terms and conditions for using the software						
r01us0068ejxxxx_rl78.pdf	: User's Manual					
- r20ut3485ejxxxx_rl78.pdf	: Release Note (this document)					
r20ut3486ejxxxx_rl78.pdf	: Difference document					
	: EEPROM emulation library (EEL)					
fdl.lib	: Data flash library (FDL)					
— incri78						
eel.h	: EEL header file for C program					
eel.inc	: EEL header file for assembler					
eel_types.h	: EEL header file that specifies definitions for C program					
eel_types.inc	: EEL header file that specifies definitions for assembler					
	: FDL header file for C program					
fdl.inc	: FDL header file for assembler					
fdl_types.h	: FDL header file that specifies definitions for C program					
smprl78						
asm						
eel_descriptor.inc	: EEL descriptor header file					
eel_descriptor.asm	: EEL descriptor source file					
fdl_descriptor.inc	: FDL descriptor header file					
fdl_descriptor.asm	: FDL descriptor source file					
L _c						
eel_descriptor.h	: EEL descriptor header file					
eel_descriptor.c	: EEL descriptor source file					
eel_user_types.h	: EEL user-defined header file					
fdl_descriptor.h	: FDL descriptor header file					
fdl_descriptor.c	: FDL descriptor source file					

Notes1: Since "x" in this table means a version number or revision number, these numbers are omitted.



6. How to Debug a Program

For details on how to perform debugging by using IECUBE or the on-chip debugging emulator E1 or E20, see the following document:

Title	
CS+ Integrated Development Environment User's Manual: RL78 Debug Tool ^{Note1}	

Note1: You can download this document from the "CS+ Integrated Development Environment" of the Renesas Electronics website.

6.1 Notes on Debugging

(1)The EEPROM Emulation Library Pack02 cannot be debugged by a simulator. To perform debugging, either use the on-chip debugging function of the RL78 microcontroller or prepare the IECUBE.

7. How to Allocate Sections

The CC-RL compiler has a -start option that is used to allocate sections to desired locations.

-start = [Section name]/Address

Note that the sizes of the sections are not specifiable. $^{\mbox{\tiny Note2}}$

Use the -start option to specify all sections for which settings are required by EEPROM Emulation Library Pack02.

While the CA78K0R compiler reserves the self-RAM as an area not for use by the user program, the CC-RL compiler does not require designation of the self-RAM because there is no need to make settings for addresses in unused areas.

Note2: For details on the methods of defining and allocating all data, refer to the user's manual for CS+. Reference to the map file (*.map) generated in building is required to confirm the state of allocation.



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