

# Allegro to PADS<sup>®</sup> Translator User's Guide

PADS 9.3.1

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The Allegro to PADS Layout translator is one of a group of translators included with the PADS PCB Design Tools. It is installed with PADS Layout.

You can use this translator to migrate Cadence Allegro designs to PADS Layout.

**Tip:** To find the version number of the translator, click the translator icon in the title bar o, and select About Translator.

## Restrictions

- Allegro designs must be prepared for translation using the Cadence Allegro PCB Editor in at least the XL version.
- Only the "electrical" type data is translated.

## **Supported Versions**

The translator supports up to version 16.3 of Allegro PCB.

# **Migrating Allegro Designs to PADS Layout**

To perform the migration, you must have access both to PADS Layout and to the Cadence Allegro PCB Editor in at least the XL version. If both applications reside on the same machine, use Procedure 1 to migrate the design. If they are on different machines, use Procedure 2.

## **Procedure 1—PADS and Allegro on the Same Machine**

Perform the following steps to migrate an Allegro design to PADS Layout when PADS Layout and Allegro PCB Editor reside on the same machine:

- 1. Prepare the Allegro design(s) for migration, as follows:
  - a. Copy the contents of the *<PADS install dir>\SDD\_HOME\translators\skill\_scripts* folder to the \$HOME\pcbenv folder.
  - b. For each design you want to translate:
    - i. Create a new folder (...myboards\_<*boardID*>), for example), and copy into it the Allegro design (.brd) file you want to migrate.

ii. Open the design file, and in the Allegro command prompt window enter these command lines:

Command> skill load "dfl\_main" (include quotes) Command> main out

- iii. In the Allegro to Expedition Translator dialog box, click the **Start One Way Translation** button.
- iv. When the SKILL script has completed, if any errors were found, fix them and rerun the SKILL script. The migration will not complete correctly if all errors are not fixed.
- 2. Migrate the prepared Allegro design(s) to PADS Layout.

**Tip:** If you're migrating only a single design including attributes, use the File > Import command in PADS Layout instead of the following procedure. After the design is migrated, it is automatically opened in PADS Layout. If you are migrating multiple designs, or want to choose whether to translate attributes or not, perform the following steps:

- a. From the Start Menu, run the Allegro to PADS Translator.
- b. In the Allegro Designs Translator dialog box:
  - i. Identify the location where you want the translated files placed.
  - ii. Use the Add button to specify the files you want to translate.
  - iii. Specify whether or not to translate attributes.
  - iv. Click the **Translate** button. The output filename(s) will be in the format

design\_#########.pcb.

## **Procedure 2—PADS and Allegro on Different Machines**

Perform the following steps to migrate an Allegro design to PADS Layout when PADS Layout and Allegro PCB Editor reside on different machines:

- 1. On the Allegro machine, prepare the Allegro design(s) for migration, as follows:
  - a. Copy the contents of the *<PADS install dir>\SDD\_HOME\translators\skill\_scripts* folder on the PADS machine to the \$HOME\pcbenv folder.
  - b. Copy *<PADS install dir>\SDD\_HOME\translators\win32\bin\tech\_translator.exe* on the PADS machine to the \$HOME\pcbenv folder.
  - c. Set the AEX\_BIN\_ROOT environment variable to point to \$HOME\pcbenv.
  - d. For each design you want to translate:

- i. Create a new folder (...myboards\_<*boardID*>), for example), and copy into it the Allegro design (.brd) file you want to migrate.
- ii. Open the design file, and in the Allegro command prompt window enter these command lines:

Command> skill load "dfl\_main" (include quotes) Command> main out

- iii. In the Allegro to Expedition Translator dialog box, click the **Start One Way Translation** button.
- iv. When the SKILL script has completed, if any errors were found, fix them and rerun the SKILL script. The migration will not complete correctly if all errors are not fixed.
- e. Copy all the prepared design folders (...myboards\_<*board*\*>) to the PADS machine.
- 2. On the PADS machine, migrate the prepared Allegro design(s) to PADS Layout.

**Tip:** If you're migrating only a single design and including attributes, use the File > Import command in PADS Layout instead of the following procedure. After the design is migrated, it is automatically opened in PADS Layout. If you are migrating multiple designs, or want to choose whether to translate attributes or not, perform the following steps:

- a. From the Start Menu, run the Allegro to PADS Translator.
- b. In the Allegro Designs Translator dialog box:
  - i. Identify the location where you want the translated files placed.
  - ii. Use the Add button to specify the files you want to translate.
  - iii. Specify whether or not to translate attributes.
  - iv. Click the **Translate** button. The output filename(s) will be in the format design\_##########.pcb.

### **Related Topics**

Error Codes and Log Messages

This chapter describes differences between Allegro file content and PADS Layout file content, and possible discrepancies that can occur in translation.

# Zero Size Pads

Allegro zero size pads are not supported by the translator. As a workaround, you can resize them to make them as small as possible (1 DBU), and put a message in the log file stating what these pads are.

# **Decals and Design Graphics**

Decals generated during the translation do NOT contain all the pertinent information that existed in Allegro (for example, notes, some graphics, etc). Therefore check the translated design closely.

## **Board Outline**

Board outlines must be contiguous in Allegro to successfully translate into PADS. Check the board outline in Allegro to ensure that there are no gaps or opens.

# **SMD Edge Connectors**

An outstanding bug exists when sometimes the translator does not handle double-sided SMD edge connectors correctly. If the design has one of these connectors please double-check it after the translation in PADS. This problem typically occurs on designs that have an AGP / PCI type connector.

## Lack of Memory

Due to Windows OS limitations, the translator cannot allocate more than 2GB of memory. If you are translating a very large design, the translator may run out of memory and display an "Unable to allocate enough memory to translate current design" message.

The suggested workaround is to configure Windows to provide 3GB virtual address space. To do this:

- 1. Add /3GB to the Windows boot.ini file as described at http://www.microsoft.com/whdc/system/platform/server/PAE/PAEmem.mspx.
- 2. Reboot your computer and start the translation again.

# **Create\_devices Failure**

SKILL scripts use the Allegro create\_devices command to export device files to ...\devices in the design directory. If this command fails, data export cannot complete successfully. To complete the data export process, you must fix the design board to make the create\_devices command work.

One example of this problem is Allegro device names containing an equals sign (=). When the SKILL script encounters such a name, it removes the equals sign, which causes the Allegro create\_devices command to fail. To resolve this problem, you must do one of the following:

- Edit the device name in the Allegro schematic and forward annotate it to the board.
- Remove the device from the Allegro design.

Another example is device names longer than 32 characters. This causes a problem with exporting devices. If your job has device names that are longer than 32 characters, set the Allegro environment variable ALLEGRO\_LONG\_PACKAGE\_NAME to TRUE.

# Exclamation mark (!) in the Design Board Path

SKILL scripts create a temporary Allegro macro file with commands exporting part of the data. If the design board path contains an exclamation mark (!), the temporary macro file name also contains an exclamation mark; this causes the SKILL script to try to execute the temporary macro file with the replay command.

# Disabling Board Refresh while Exporting Data with SKILL Scripts

For better performance, design board refresh is disabled while exporting data from Allegro with SKILL scripts.

# Allegro to PADS Mapping Table

The following table shows the translation mapping of Allegro elements to PADS.

From Allegro Element:	To PADS Element:
Layers	
Assy_Line	Not Supported
Board Outline	Board Outline object
Break Outs	Traces & Vias (Fanouts)
Component_Body Outline_1	User layer (Component Body Outline_1)
Component_Body Outline_2	User layer (Component Body Outline_2)
DAM	Not Supported
Dielectric	Not Supported
Dimension_Keepout	Not Supported
Drill	Not Supported
Error	Not Supported
Fixture_Outline	Not Supported
Force	Not Supported
Glue_Mask_1	Not Supported
Glue_Mask_2	Not Supported
Head_Insertion	Not Supported
Insertion_Machine_Keepout	User layer (Insertion)
Milling	Not Supported
One-Way_Region	User layer (One Way Region)
Panel_Outline	Not Supported
Paste_Mask_1	Paste Mask Top
Paste_Mask_2	Paste Mask Bottom
Place_1	Layer 20 (Placement Outline)
Place_2	Layer 20 (Placement Outline)
Placement_Keepout_1	User layer
Placement_Keepout_2	User layer

From Allegro Element: To PADS Element:	
Placement_Region_1	User layer
Placement_Region_2	User layer
Power	•
	Plane Layer
Prepreg	Not Supported
Probe_1 and Probe_2	Not Supported
Probe_Area	Not Supported
Probe_Symbol	Not Supported
Routing_Keepout	Keepout (Trace & Via)
Shape_Edit	Not Supported
Sheet_Dielectric	Not Supported
Signal_X	Layer (Signal)
Silkscreen_1	Silkscreen Outline Top
Silkscreen_2	Silkscreen Outline Bottom
Solder_Paste_1	Paste Mask Top
Solder_Paste_2	Paste Mask Bottom
Soldermask_1	Soldermask Top
Soldermask_2	Soldermask Bottom
Test Points	Test Points
TestPoint_Keepout	Not Supported
TestPoint_Outline	Not Supported
TestPoint_Reference	Not Supported
Thermal	Supported
Trace Keepout	Keepout (Trace)
User Defined Layer	User layer
Via Keepout	Keepout (Via)
Fabrication	
Aperture Table	Not Supported
Artwork Format	Not Supported
Artwork Simulation	Not Supported
Artwork Stackup	Not Supported

From Allegro Element: To PADS Element:	
Drill Data	Not Supported
Drill Simulation	Not Supported
Drill Table	Not Supported
Mill Table	Not Supported
Fills, Keepouts, Planes	
Area Fills	Copper Pour or Plane Area
Area Fills (Protected)	Copper Pour or Plane Area
Area Fill Tie Bar and Cutouts	Not Supported
Classic Area Fills	Copper Pour or Plane Area
Cutouts	Copper Keepout
Cutouts (Protected)	Copper Keepout
No Connect Rules	Not supported
Placement Outline	Not Supported
Power Fill	Plane Layer
Route Keepout	Keepout (Trace)
Signal Layer	Electrical Layer
Split Power Planes	Plane Layer
Trace Keepout	Keepout (Trace)
Via Keepout	Keepout (Via)
Visual-Text, Shapes, etc.	
Color map settings	Not Supported
Component Ref. Des. (Top)	Ref. Des. on Silkscreen Top
Component Ref. Des. (Bottom)	Ref. Des. on Silkscreen Bottom
Dimensioning	Not Supported
Drill Symbols	Supported
Guides	Not supported
Net Type Patterns	Not Supported
Part Name	Not Supported
Pin Ref. Des.	Pin Numbers
Shapes on User Defined Layers	Shapes on User Layers

Shapes on Route LayersShapes on Routing LayersTargets/Registration MarksGraphics on User LayersText on User Defined LayerText on User LayerText on Route LayerText on User LayerParts, Traces & ViasSMD PadstacksBlind PadstacksSMD PadstacksBlind/Buried ViasBuried ViasBreak OutsTraces & ViasBreak Outs (Protected)Buried ViasBreak Outs (Component)TracesBuried PadstacksOnly for 2 pin parts, maps to Buried PartComponent Type RulesNot SupportedComponentsPartsComponents (Fixed)PartsDrilled HolesPartsFixed ComponentsPartsJumpersPartsMicroVias (Protected)Vias (small diameter)Mounting HolesAdditional pins in decalNo Cleanup ViasViasPins (Protected)PartsReuse Block (Replica linked)Components and tracesReuse Block (Smashed linked)Cromponents and tracesReuse Block (Smashed linked)Treated as editable components & traces	From Allegro Element: To PADS Element:	
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Reuse Block (Primitive)Treated as single componentReuse Block (Replica linked)Components and tracesReuse Block (Replica un-linked)Treated as editable components & tracesReuse Block (Smashed linked)Components and tracesReuse Block (Smashed linked)Treated as editable components & tracesReuse Block (Smashed un-linked)Treated as editable components & traces	No Cleanup Vias	Vias
Reuse Block (Replica linked)Components and tracesReuse Block (Replica un-linked)Treated as editable components & tracesReuse Block (Smashed linked)Components and tracesReuse Block (Smashed un-linked)Treated as editable components & traces	Pins (Protected)	Parts
Reuse Block (Replica un-linked)Treated as editable components & tracesReuse Block (Smashed linked)Components and tracesReuse Block (Smashed un-linked)Treated as editable components & traces	Reuse Block (Primitive)	Treated as single component
Reuse Block (Smashed linked)Components and tracesReuse Block (Smashed un-linked)Treated as editable components & traces	Reuse Block (Replica linked)	Components and traces
Reuse Block (Smashed un-linked) Treated as editable components & traces	Reuse Block (Replica un-linked)	Treated as editable components & traces
Reuse Block (Smashed un-linked) Treated as editable components & traces	Reuse Block (Smashed linked)	Components and traces
		-
Snield Trace Traces	Shield Trace	Traces

From Allegro Element:	To PADS Element:
Shield Trace (Protected)	Traces
Teardrops	Not Supported
Test Coupon	Not Supported
Test Points	Test Points
Through Padstacks	Through Padstacks
Through Vias	Through Vias
Through Vias (Protected)	Through Vias
Traces	Traces
Traces (Protected)	Traces
Traces in Cell	Not supported
Other	
Component to Component Rules	Supported
Component Type Rules	Not Supported
Highspeed Rules	Not supported
Hybird Objects	Not Supported
MCM Objects	Not Supported
Net Types	Net Classes
Pin Type, SLT	Not Supported
RF Objects	Not Supported
Thermal	Not Supported
Variants	Not Supported

Table 3-1 is a list of error codes and translator log messages.

Error code	Explanation
[F]	A fatal error. The translator will terminate. A file was not created due to a problem, setting, or switch that would not allow the file to be translated. A translated file is not created due to the fatal error that was encountered.
[E]	A serious, but non-fatal error. The translator will continue, but you must resolve the problem, and the problem encountered might prevent the file from being useable by Expedition.
[I]	Current status information.
[W]	A warning.

## Table 3-1. Error Codes and Log Messages

## **Third-Party Information**

This section provides information on open source and third-party software that may be included in the Allegro to PADS Translator product.

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## **End-User License Agreement**

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