



**SBC84825 Series  
Intel® Atom™ Based  
Capa Board with LVDS  
User's Manual**



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## **Caution**

If you replace wrong batteries, it causes the danger of explosion. It is recommended by the manufacturer that you follow the manufacturer's instructions to only replace the same or equivalent type of battery, and dispose of used ones.

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## ESD Precautions

Computer boards have integrated circuits sensitive to static electricity. To prevent chipsets from electrostatic discharge damage, please take care of the following jobs with precautions:

- Do not remove boards or integrated circuits from their anti-static packaging until you are ready to install them.
- Before holding the board or integrated circuit, touch an unpainted portion of the system unit chassis for a few seconds. It discharges static electricity from your body.
- Wear a wrist-grounding strap, available from most electronic component stores, when handling boards and components.

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## **CHAPTER 1**

### **INTRODUCTION**

The **SBC84825** is a Capa board with support for Intel® ATOM™ processor Z510P/Z530P, and integrates chipset Intel® System Controller Hub US15W that deliver outstanding system performance through high-bandwidth interfaces, multiple I/O functions for interactive applications and various embedded computing solutions. The board has one 200-pin unbuffered SODIMM socket for DDR2 400/533 MHz SO-DIMM memory, maximum memory capacity up to 2GB. It also features dual-display by VGA and LVDS, one Gigabit Ethernet, one serial ATA port, six USB 2.0 high speed compliant, built-in high definition audio codec that can achieve the best stability and reliability for industrial applications. It provides you with unique embedded features, such as four serial ports (3x RS-232 and 1x RS-232/422/485) and 3.5" form factor that applies an extensive array of PC peripherals.

## 1.1 Specifications

- **CPU**
  - Intel® ATOM™ processor Z510P/Z530P
- **System Chipset**
  - Intel® System Controller Hub US15WP
- **BIOS**
  - Phoenix-Award BIOS, Y2K compliant
  - 8Mbit FWH Flash, DMI, Plug and Play
  - SmartView for multiple LCD type selection, display mode option and application extension features
  - "Load Optimized Default" to backup customized setting in the BIOS flash chip to prevent from CMOS battery fail
- **System Memory**
  - One x 200-pin unbuffered DDR2 SODIMM socket
  - Maximum to 2GB DDR2 400/533 MHz memory
- **Onboard Multi I/O**
  - Controller: Winbond W83627DHG-PT
  - Serial Ports: 3 ports for RS-232 (COM2~4) and 1 port for RS-232/422/485 (COM1).
- **CompactFlash™ Socket**
  - One CompactFlash™ Type II low profile slot
- **USB Interface**
  - Six USB ports with fuse protection and complies with USB Spec. Rev. 2.0
- **Display**
  - One 2 x 20-pin LVDS connector, one 7-pin wafer connector for inverter control
  - One D-sub 15-pin slim type connector
- **Watchdog Timer**
  - 1~255 seconds; up to 255 levels



- **Ethernet**
  - One port with Realtek RTL8111DL for Gigabit/Fast Ethernet
  - One RJ-45 connector
- **Audio**
  - HD Audio compliant via ALC662.
  - Amplifier for speaker out via LM1877.
  - Supports multi-channel audio stream, 32-bit sample depth, and sample rate up to 192KHz
  - MIC-in, Line-in, Line-out/Speaker-out (jumper selectable)
- **Power Management**
  - ACPI (Advanced Configuration and Power Interface)
- **Form Factor**
  - 3.5" form factor



**NOTE** *All specifications and images are subject to change without notice.*

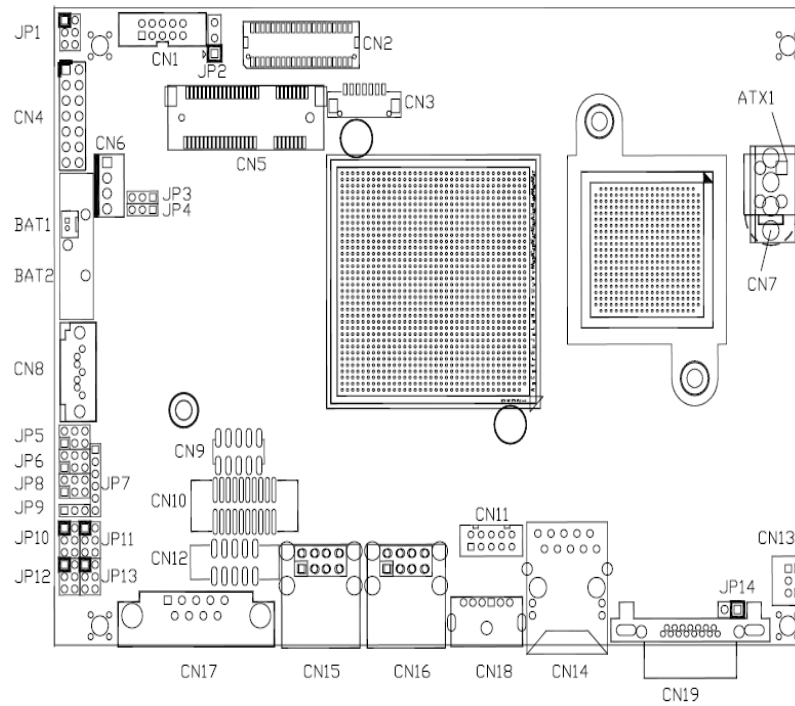
## **1.2 Utilities Supported**

- Chipset Driver
- Ethernet Driver
- Graphic Drivers
- Audio Drivers

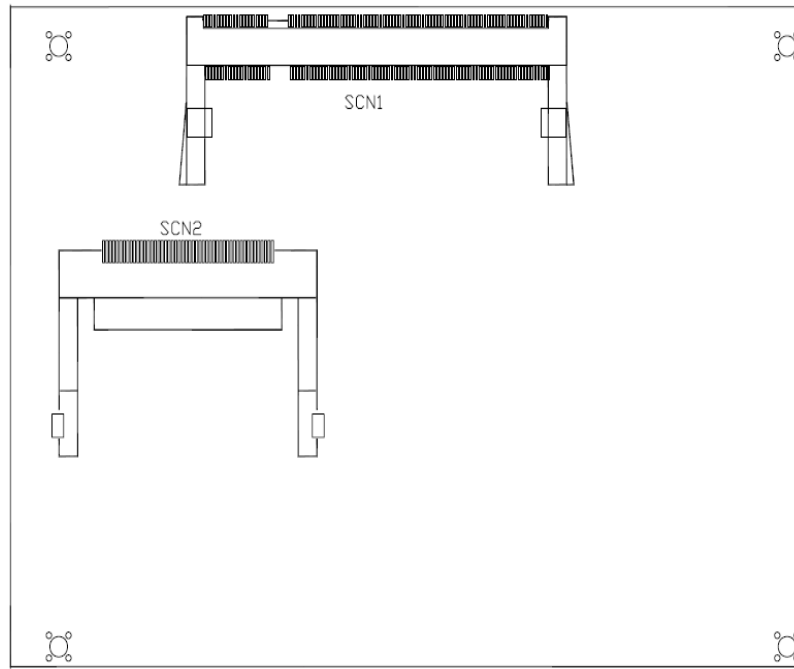
## CHAPTER 2

### JUMPERS AND CONNECTORS

#### 2.1 Board Layout



Component Side





**Solder Side**

## 2.2 Jumper Settings



Proper jumper settings configure the **SBC84825** to meet your application purpose. We are herewith listing a summary table of all jumpers and default settings for onboard devices, respectively.

Jumper	Default Setting		Jumper Setting
JP1	Audio Output Select ( Line Out )		Short 1-3 Short 2-4
JP2	Panel Power Voltage Select ( +3.3V )		Short 1-2
JP3	Compact Flash Power Voltage Select (+3.3V )		Short 1-2
JP4	Clear COMS Setting (Normal)		Short 1-2
JP5	COM1 Mode Select ( RS-232 )		Short 3-5 Short 4-6
JP6	COM1 Mode Select ( RS-232 )		Short 3-5 Short 4-6
JP8	COM1 Mode Select ( RS-232 )		Short 1-2
JP9	Auto Power ON/OFF Select ( ON )		Short 2-3
JP10	COM3 Mode Select	CN10 pin 1: DCD	Short 3-5 Short 4-6
		CN10 pin 8: RI	
JP11	COM4 Mode Select	CN10 pin 11: DCD	Short 3-5 Short 4-6
		CN10 pin 18: RI	
JP12	COM1 Mode Select	CN17 pin 1: DCD	Short 3-5 Short 4-6
		CN17 pin 9: RI	
JP13	COM2 Mode Select	CN12 pin 1: DCD	Short 3-5 Short 4-6
		CN12 pin 8: RI	
JP14	CRT Always ON/OFF Select ( ON )		Open

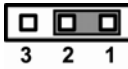
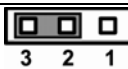
### 2.2.1 Audio Output Select: JP1

Description	Function	Jumper Setting
Audio Ouput Select	Line Out( Default )	
	Speaker Out	

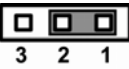
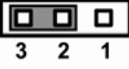
### 2.2.2 Panel Power Voltage Select: JP2

Description	Function	Jumper Setting
Panel Power Voltage Select	+3.3V ( Default )	
	+5V	










### 2.2.3 Compact Flash Power Voltage Select: JP3

Description	Function	Jumper Setting
Compact Flash Power Voltage Select	+3.3V (Default)	
	+5V	

#### 2.2.4 Clear COMS Setting: JP4





Description	Function	Jumper Setting
CMOS Clear Setting	Normal (Default)	
	Clear CMOS	

#### 2.2.5 COM1 Mode Select: JP5, JP6, JP8





Description	Function	Jumper Setting		
COM1 Mode Select	RS-232 (Default)	<b>JP5</b> 	<b>JP6</b> 	<b>JP8</b> 
	RS-422	<b>JP5</b> 	<b>JP6</b> 	<b>JP8</b> 
	RS-485	<b>JP5</b> 	<b>JP6</b> 	<b>JP8</b> 

## 2.2.6 COM1~COM4 Mode Select : JP10,JP11,JP12,JP13

### JP12





Description	Function	Jumper Setting
COM1 (CN17)	Pin 1=DCD (Default)	
	Pin 1=5V	
	Pin 9=RI (Default)	
	Pin 9=+12V	

### JP13





Description	Function	Jumper Setting
COM2 (CN12)	Pin 1=DCD (Default)	
	Pin 1=5V	
	Pin 8=RI (Default)	
	Pin 8=+12V	



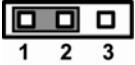

JP10

Description	Function	Jumper Setting
COM3 (CN10)	Pin 1=DCD (Default)	
	Pin 1=5V	
	Pin 8=RI (Default)	
	Pin 8=+12V	



JP11

Description	Function	Jumper Setting
COM4 (CN10)	Pin 11=DCD (Default)	
	Pin 11=5V	
	Pin 18=RI (Default)	
	Pin 18=+12V	

### 2.2.7 Auto Power ON/OFF Select: JP9

Description	Function	Jumper Setting
Auto Power Botton for AT/ATX mode select	OFF	
	ON ( Default )	

### 2.2.8 CRT Always ON/OFF Select: JP14

Description	Function	Jumper Setting
RGB signal always output select.	ON ( Default )	
	OFF	

## 2.3 Connectors

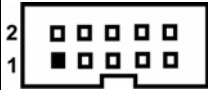
Connectors connect the board with other parts of the system. Loose or improper connection might cause problems. Make sure all connectors are properly and firmly connected. Here is a summary table shows you all connectors on the **SBC84825** Series.

Connectors	Label
Auido Connector	CN1
LVDS Connector	CN2
Backlight Inverter Connector	CN3
Flat Panel Bezel Connector	CN4
Mini PCI express Card Socket	CN5
SATA Power Connector	CN6
System Power Connector ( Option )	CN7
SATA Connector	CN8
Digital I/O Connector	CN9
Serial Port Connector	CN10,CN12,CN17
USB Connector	CN11,CN15,CN16
SMBus Connector	CN13
RJ45 LAN Connector	CN14
PS/2 Keyboard Mouse Connector	CN18
VGA Connector	CN19
System Power Connector	ATX1
CPLD Programming Header	JP7
DDRII SO-DIMM Connector	SCN1
Compact Flash Card Connector	SCN2

### 2.3.1 Audio Connector: CN1

**CN1** is a 10-pin 2.00mm pitch box header connector for the audio function.

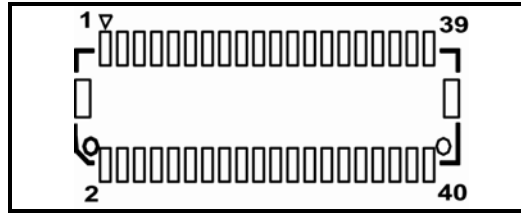
Pin	Signal	Pin	Signal
1	MIC-IN	2	GND
3	Line In L	4	GND
5	Line In R	6	GND
7	Audio Out L	8	GND
9	Audio Out R	10	GND



### 2.3.2 LVDS Connector: CN2

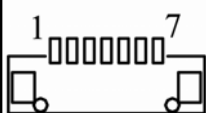
**CN2** is a 40-pin connector for LVDS Interface LCD. It is strongly recommended to use the matching connector JST SHDR-40V-S-B.

Pin	Signal	Pin	Signal
1	+DVCCM1	2	+DVCCM1
3	+DVCCM1	4	+DVCCM1
5	+DVCCM1	6	+DVCCM1
7	N.C.	8	N.C.
9	GND	10	GND
11	N.C.	12	N.C.
13	N.C.	14	N.C.
15	GND	16	GND
17	N.C.	18	N.C.
19	N.C.	20	N.C.
21	GND	22	GND
23	TXO0-	24	N.C.
25	TXO0+	26	N.C.
27	GND	28	GND
29	TXO1-	30	TXO3-
31	TXO1+	32	TXO3+
33	GND	34	GND
35	TXO2-	36	TXOC-
37	TXO2+	38	TXOC+
39	GND	40	GND



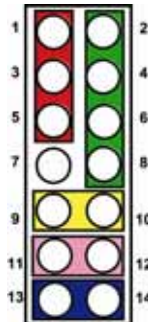
### 2.3.3 Backlight Inverter Connector: CN3

CN3 is a 7-pin connector for inverter. We strongly recommended you to use the matching DF13-7S-1.25C connector.

Pin	Signal	
1	12V	
2	12V	
3	5V	
4	Enable	
5	GND	
6	GND	
7	GND	

### 2.3.4 Flat Panel Bezel Connector: CN4

CN4 is a 2.54mm pitch pin header



■ **Power LED**

Pin 1 and Pin 5 connect the system power LED indicator with the corresponding switch on the case. Pin 1 is assigned as +, and Pin 3 & Pin 5 as -. The Power LED lights up when the system is powered ON. Pin 3 is defined as GND

■ **External Speaker and Internal Buzzer Connector**

Pin 2, 4, 6 and 8 can be connected to the case-mounted speaker unit or internal buzzer. While connecting the CPU card to an internal buzzer, please short pins 2-4; while connecting to an external speaker, you need to set pins 2-4 to Open and connect the speaker cable to pin 8 (+) and pin 2 (-).

■ **ATX Power On/Off Button**

Pin 9 and 10 connects the front panel's ATX power button to the CPU card, which allows users to control ATX power supply to be power on/off.

■ **System Reset Switch**

Pin 11 and 12 can be connected to the case-mounted reset switch that reboots your computer without turning OFF the power switch. It is a better way to reboot your system for a longer life of the system's power supply.

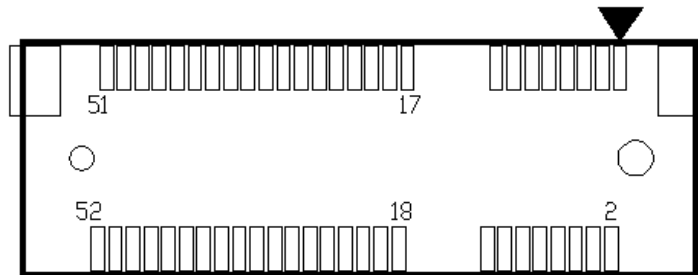
■ **HDD Activity LED**

This connection is linked to hard drive activity LED on the control panel. LED flashes when HDD is being accessed. Pin 13 and 14 connect the hard disk drive to the front panel HDD LED, Pin 13 assigned as -, and Pin 14 as +.

**2.3.5 Mini PCI Express Card Socket: CN5**

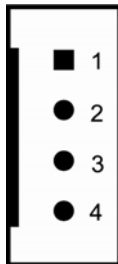
CN5 is a Mini PCI express Card socket with support for a USB 2.0 link. Besides if you disable the USB 2.0 controller, the Mini PCI Express card will not work.

The PCI express link that can be supported when without SATA function is chosen by resistors.



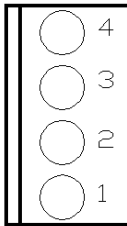
Pin	Signal	Pin	Signal
1	PCIE_WAKE-	2	+3.3V
3	N.C	4	GND
5	N.C	6	+1.5V
7	CLKREQ#	8	N.C
9	GND	10	N.C
11	REFCLK#	12	N.C
13	REFCLK	14	N.C
15	GND	16	N.C
17	N.C	18	GND
19	N.C	20	N.C
21	GND	22	PERST#
23	PEG_RXN2 (option)	24	+3.3VAUX
25	PEG_RXP2 (option)	26	GND
27	GND	28	+1.5V
29	GND	30	SMB_CLK
Pin	Signal	Pin	Signal
31	PEG_TXN2 (Option)	32	SMB_DATA
33	PEG_TXP2 (Option)	34	GND
35	GND	36	USB6-
37	N.C	38	USB6+
39	N.C	40	GND
41	N.C	42	N.C
43	N.C	44	N.C
45	N.C	46	N.C
47	N.C	48	+1.5V
49	N.C	50	GND
51	N.C	52	+3.3V

### 2.3.6 SATA Power Connector: CN6

Pin	Signal	
1	+12V	
2	GND	
3	GND	
4	+5V	


### 2.3.7 System PowerConnector: CN7 (Option)

CN7 is a 3.96mm pitch connector. It co-lays with ATX1 connector. The default type is ATX1 connector.

Pin	Signal	
1	+12V	
2	GND	
3	GND	
4	+5V	

### 2.3.8 SATA Connector: CN8

CN8 is the SATA connector for high-speed SATA interface ports. It can be connected to hard disk devices.

Pin	Signal	
1	GND	
2	SATA_TX+	
3	SATA_TX-	
4	GND	
5	SATA_RX-	
6	SATA_RX+	
7	GND	



### 2.3.9 Digital IO Connector: CN9

CN9 is a 8-channel digital I/O connector that meets requirements for a system customary automation control. The digital I/O can be configured to control cash drawers, sense warning signals from an Uninterrupted Power System (UPS), or perform store security control. The digital I/O is controlled via software programming.

Pin	Signal	Pin	Signal	
1	DIO0	2	DIO3	
3	DIO1	4	DIO4	
5	DIO2	6	DIO5	
7	GND	8	DIO6	
9	GND	10	DIO7	

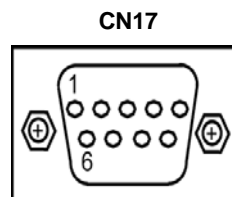
### 2.3.10 Serial Port Connector: CN10, CN12, CN17

**CN17** is a DB9 connector for COM1 to support RS-232/422/485.

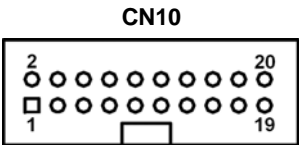
**CN12** is a 2.00mm pitch box header for COM2. **CN10** is a 1.27mm pitch box header for COM3 and COM4. COM2~COM4 just support RS-232. All ports supply +5V power capability on DCD, and +12V on RI, depending on jumper setting.

**SBC84825 does not support modem ring wake up function.**

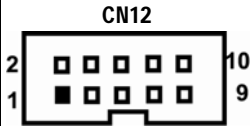
Pin	Signal
1	Data Carrier Detect (DCD1)
2	Receive Data (RXD1)
3	Transmit Data (TXD1)
4	Data Terminal Ready (DTR1)
5	GND
6	Data Set Ready (DSR1)
7	Request to Send (RTS1)
8	Clear to Send (CTS1)
9	Ring Indicator (RI1)



Pin	Signal	Pin	Signal
1	Data Carrier Detect (DCD3)	2	Data Set Ready (DSR3)
3	Receive Data (RXD3)	4	Request to Send (RTS3)
5	Transmit Data (TXD3)	6	Clear to Send (CTS3)
7	Data Terminal Ready (DTR3)	8	Ring Indicator (RI3)
9	Ground (GND)	10	N.C.
11	Data Carrier Detect (DCD4)	12	Data Set Ready (DSR4)
13	Receive Data (RXD4)	14	Request to Send (RTS4)
15	Transmit Data (TXD4)	16	Clear to Send (CTS4)
17	Data Terminal Ready (DTR4)	18	Ring Indicator (RI4)
19	Ground (GND)	20	N.C.



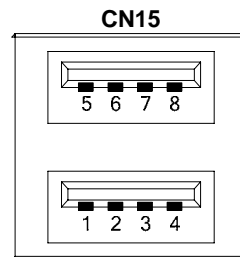
Pin	Signal	Pin	Signal
1	Data Carrier Detect (DCD2)	2	Data Set Ready (DSR2)
3	Receive Data (RXD2)	4	Request to Send (RTS2)
5	Transmit Data (TXD2)	6	Clear to Send (CTS2)
7	Data Terminal Ready (DTR2)	8	Ring Indicator (RI2)
9	Ground (GND)	10	N.C.



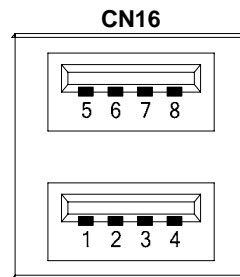
### 2.3.11 USB Connector: CN11, CN15, CN16

**CN15 & CN16** are double deck connector for port0 ~ port3. **CN11** is a 2.00mm pitch wafer for port4 & port5.

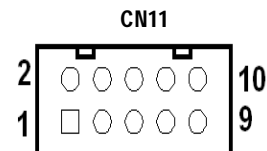
Pin	Signal
1,5	USB_PWR01
2	USB0 -
3	USB0+
6	USB1 -
7	USB1+
4,8	GND



Pin	Signal
1,5	USB_PWR23
2	USB2 -
3	USB2+
6	USB3 -
7	USB3+
4,8	GND




Pin	Signal	Pin	Signal
1	USB_PWR45	2	USB_PWR45
3	USB4 -	4	USB5 -
5	USB4+	6	USB5+
7	GND	8	GND
9	GND	10	GND



### 2.3.12 SMBus Connector: CN13

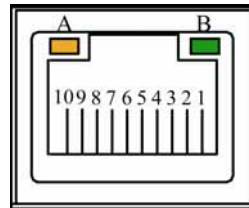
CN13 is a 2.00mm pitch wafer for SMBUS interface support.

Pin	Signal	
1	SMB_CLK	
2	SMB_DATA	
3	GND	

### 2.3.13 RJ45 LAN Connector: CN14

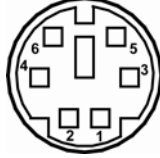
CN14 is the RJ-45 connector is for Ethernet. Just plug one end of the cable into it and connect the other end (phone jack) to a 1000/100/10-Base-T hub.

Pin	Signal
1	MDI0+
2	MDI0-
3	MDI1+
4	MDI1-
5	CT1
6	CT2
7	MDI2+
8	MDI2-
9	MDI3+
10	MDI3-
A	Active LED
B	10 : No bright 100 : Green 1000 : Orange



**2.3.14 PS/2 Keyboard and Mouse Connector: CN18**

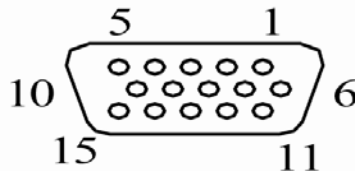
The board supports a PS/2 keyboard and mouse interface. CN18 is a DIN connector for PS/2 keyboard Connection via “Y” Cable.

Pin	Signal	
1	Keyboard Data	
2	Mouse Data	
3	GND	
4	KBVCC	
5	Keyboard Clock	
6	Mouse Clock	

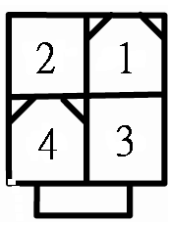
**2.3.15 VGA Connector: CN19**

CN19 is a standard 15-pin DB15 connector commonly for the VGA display.

Pin	Signal	Pin	Signal	Pin	Signal
1	Red	2	Green	3	Blue
4	N.C	5	GND	6	GND
7	GND	8	GND	9	+5V
10	GND	11	N.C	12	DDC DATA
13	Horizontal Sync	14	Vertical Sync	15	DDC CLK

**2.3.16 System Power Connector: ATX1**

ATX1 is a power connector, which co-lays with CN7, for only +12V.

Pin	Signal	
1	GND	
2	GND	
3	+12V_DC	
4	+12V_DC	

### 2.3.17 CPLD Programming Header: JP7

JP7 is a 2.00mm pitch pin header for programming CPLD.

***The header is just for internal use.***

Pin	Signal	
1	+3.3V_SBY	1 <input checked="" type="checkbox"/>
2	GND	2 <input type="checkbox"/>
3	TCK	3 <input type="checkbox"/>
4	TDO	4 <input type="checkbox"/>
5	TDI	5 <input type="checkbox"/>
6	TMS	6 <input type="checkbox"/>

## CHAPTER 3

### HARDWARE DESCRIPTION

#### 3.1 Microprocessors

The **SBC84825** Series support Intel<sup>®</sup> ATOM<sup>™</sup> processor Z510P/Z530P, which make your system operated under Windows 2000/XP, and Linux environment. The system performance depends on the microprocessor. Make sure all correct settings are arranged for your installed microprocessor to prevent the CPU from damages.

#### 3.2 BIOS

The **SBC84825** Series uses Award Plug and Play BIOS with a single 8Mbit FWH Flash, DMI, Plug and Play.

#### 3.3 System Memory

The **SBC84825** Series industrial CPU card supports one 200-pin unbuffered DDR2 SODIMM socket for a maximum memory of 2GB DDR2 SDRAMs. The memory module can come in sizes of 64MB, 128MB, 256MB, 512MB, 1GB and 2GB. The Device density supports 512 Mb and 1024-Mb with the width of x16.

#### 3.4 I/O Port Address Map

The Intel ATOM<sup>™</sup> processor Z510P/Z530P can communicate via I/O ports. There are total 1KB port addresses available for assignment to other devices via I/O expansion cards.

Address	Devices
000-01F	DMA controller #1
020-02D 024-025 028-029 02C-02D	Interrupt controller #1
02E-02F	Forwarded to LPC(LPC Super I/O 2)
030-031 034-035 038-039 03C-03D	Interrupt controller #2
040-043 050-053	Timer/Counter (8254)
04E-04F	Forwarded to LPC(LPC Super I/O 1)
060-06F	Forwarded to LPC(Microcontroller for Keyboard Controller)
070-077	Real time clock, NMI
080-091	DMA page register

092	Processor I/F(Reset Generator)
093-09F	DMA page register
0A0-0BF	Interrupt controller #2
0C0-0DF	DMA controller #2
0F0	Processor I/F
0F8-0FF	Math processor
170-177	Forward to SATA(SATA Controller)
1F0-1F7	Forward to SATA(SATA Controller)
250-25F	HR I/O
300-31F	Prototype card
376	Forward to SATA(SATA Controller)
378-37F	Parallel Port (LPT)
380-38F	SDLC #2
3A0-3AF	SDLC #1

Address	Devices
3B0-3BF	MDA video card
3C0-3CF	EGA card
3D0-3DF	CGA card
3F6	Forward to SATA (SATA Controller)
3F8-3FF	Serial port #1 (COM1)
3E8-3EF	Serial port #3 (COM3)
2F8-2FF	Serial port #2 (COM2)
2E8-2EF	Serial port #4 (COM4)

### 3.5 Interrupt Controller

The **SBC84825 Series** is a 100% PC compatible control board. It consists of 16 interrupt request lines, and four out of them can be programmable. The mapping list of the 16 interrupt request lines is shown as the following table.

IRQ	Parity check error
IRQ0	System timer output
IRQ1	Keyboard
IRQ2	Interrupt rerouting from IRQ8 through IRQ15
IRQ3	Serial port #2
IRQ4	Serial port #1
IRQ5	PCI Device Share
IRQ7	—



IRQ8	Real time clock
IRQ9	ACPI Controller
IRQ10	—
IRQ11	—
IRQ12	PS/2 Mouse
IRQ13	Math coprocessor
IRQ14	Primary IDE channel
IRQ15	—

## **CHAPTER 4**

### **PHOENIX-AWARD BIOS UTILITY**

The Phoenix-Award BIOS provides users with a built-in Setup program to modify basic system configuration. All configured parameters are stored in a battery-backed-up RAM (CMOS RAM) to save the Setup information whenever the power is turned off.

#### **4.1 Entering Setup**

There are two ways to enter the Setup program. You may either turn ON the computer and press <Del> immediately, or press the <Del> and/or <Ctrl>, <Alt>, and <Esc> keys simultaneously when the following message appears at the bottom of the screen during POST (Power on Self Test).

##### **TO ENTER SETUP PRESS DEL KEY**

If the message disappears before you respond and you still want to enter Setup, please restart the system to try it again. Turning the system power OFF and ON, pressing the "RESET" button on the system case or simultaneously pressing <Ctrl>, <Alt>, and <Del> keys can restart the system. If you do not press keys at the right time and the system doesn't boot, an error message will pop out to prompt you the following information:

**PRESS <F1> TO CONTINUE, <CTRL-ALT-ESC> OR <DEL> TO ENTER SETUP**

## 4.2 Control Keys

Up arrow	Move cursor to the previous item
Down arrow	Move cursor to the next item
Left arrow	Move cursor to the item on the left hand
Right arrow	Move to the item in the right hand
Esc key	Main Menu -- Quit and delete changes into CMOS Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
PgUp/“+” key	Increase the numeric value or make changes
PgDn/“-“ key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
(Shift) F2 key	Change color from total 16 colors. F2 to select color forward, (Shift) F2 to select color backward
F3 key	Reserved
F4 key	Reserved
F5 key	Restore the previous CMOS value from CMOS, only for Option Page Setup Menu
F6 key	Load the default CMOS value from BIOS default table, only for Option Page Setup Menu
F7 key	Load the Setup default, only for Option Page Setup Menu
F8 key	Reserved
F9 key	Reserved
F10 key	Save all the CMOS changes, only for Main Menu

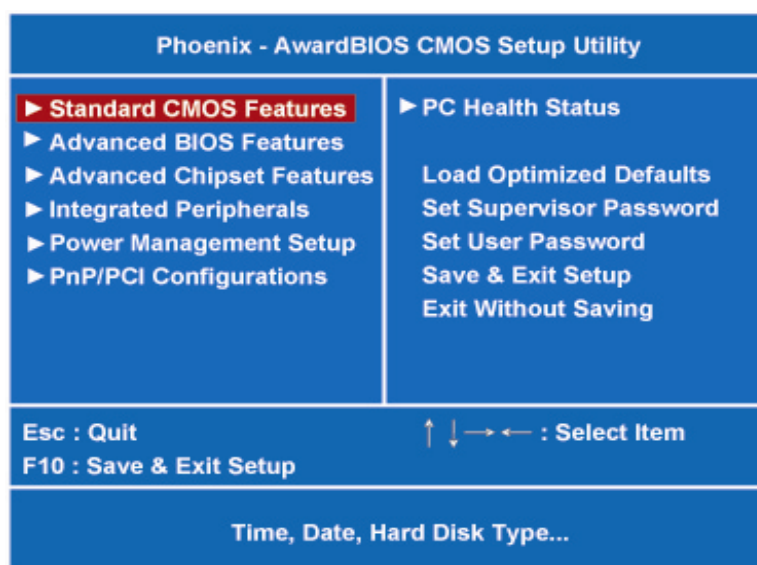
### 4.3 Getting Help


**Main Menu** The online description of the highlighted setup function is displayed at the bottom of the screen.


**Status Page Setup Menu/Option Page Setup Menu** Press <F1> to pop out a small Help window that provides the description of using appropriate keys and possible selections for highlighted items. Press <F1> or <Esc> to exit the Help Window.

### 4.4 The Main Menu

Once you enter the Award BIOS CMOS Setup Utility, the Main Menu appears on the screen. In the Main Menu, there are several Setup functions and a couple of Exit options for your selection. Use arrow keys to select the Setup Page you intend to configure then press <Enter> to accept or enter its sub-menu.

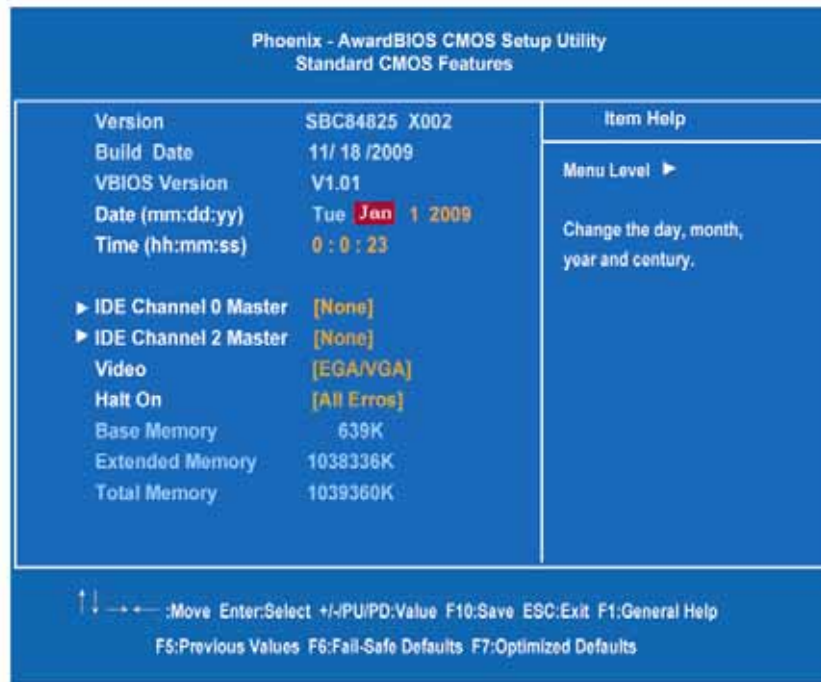


 **NOTE** If your computer can not boot after making and saving system changes with Setup, the Award BIOS will reset your system to the CMOS default settings via its built-in override feature.

 **NOTE** It is strongly recommended that you should avoid changing the chipset's defaults. Both Award and your system manufacturer have carefully set up these defaults that provide the best performance and reliability.

## 4.5 Standard CMOS Setup Menu

The Standard CMOS Setup Menu displays basic information about your system. Use arrow keys to highlight each item, and use <PgUp>



<PgDn> key to select the value you want in each item.

### ● Date

The date format is <day>, <date> <month> <year>. Press <F3> to show the calendar.

day	It is determined by the BIOS and read only, from Sunday to Saturday.
date	It can be keyed with the numerical/ function key, from 1 to 31.
month	It is from January to December.
year	It shows the current year of BIOS.

- **Time**

This item shows current time of your system with the format <hour> <minute> <second>. The time is calculated based on the 24-hour military-time clock. For example, 1 p.m. is 13:00:00.

- **IDE Primary Master/Primary Slave**

These items identify the types of each IDE channel installed in the computer. There are 45 predefined types (Type 1 to Type 45) and 2 user's definable types (Type User) for Enhanced IDE BIOS. Press <PgUp>/<+> or <PgDn>/<-> to select a numbered hard disk type, or directly type the number and press <Enter>. Please be noted your drive's specifications must match the drive table. The hard disk will not work properly if you enter improper information. If your hard disk drive type does not match or is not listed, you can use Type User to manually define your own drive type. If selecting Type User, you will be asked to enter related information in the following items. Directly key in the information and press <Enter>. This information should be provided in the documentation from your hard disk vendor or the system manufacturer. If the HDD interface controller supports ESDI, select "Type 1". If the HDD interface controller supports SCSI, select "None". If the HDD interface controller supports CD-ROM, select "None".

<b>CYLS.</b>	number of cylinders	<b>LANDZONE</b>	landing zone
<b>HEADS</b>	number of heads	<b>SECTORS</b>	number of sectors
<b>PRECOMP</b>	write precom	<b>MODE</b>	HDD access mode

If there is no hard disk drive installed, select NONE and press <Enter>.

- **Video**

Select the display adapter type for your system.

- **Halt On**

This item determines whether the system will halt or not, if an error is detected while powering up.

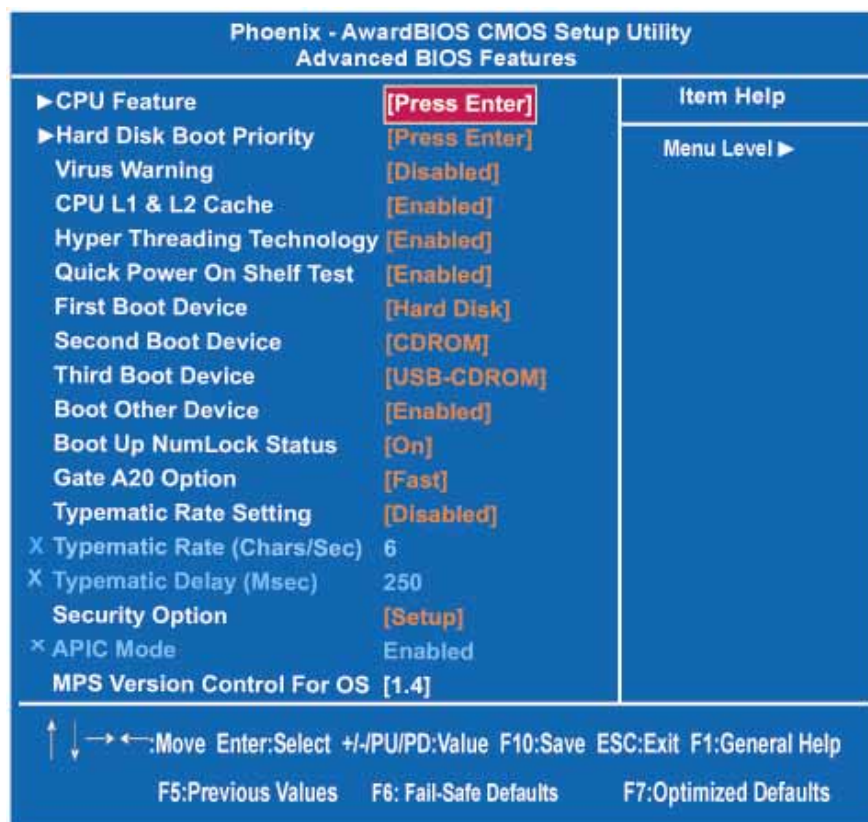
<b>No errors</b>	The system booting will halt on any errors detected. (default)
<b>All errors</b>	Whenever BIOS detects a non-fatal error, the system will stop and you will be prompted.

<b>All, But Keyboard</b>	The system booting will not stop for a keyboard error; it will stop for other errors.
------------------------------	---

Press <Esc> to return to the Main Menu page.

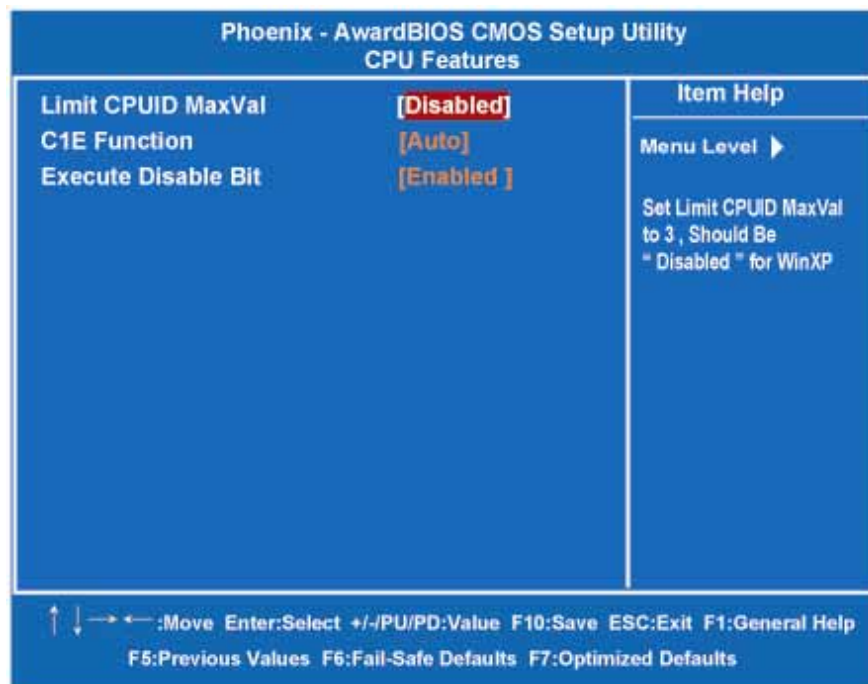
#### 4.6 Advanced BIOS Features

This section allows you to configure and improve your system, to set up some system features according to your preference.



- **CPU Features**

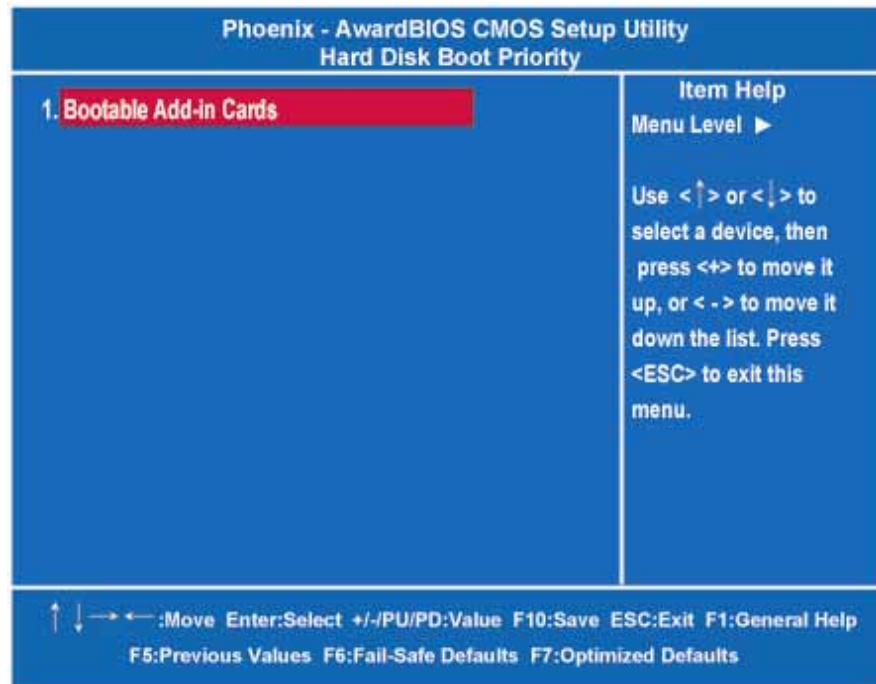
Scroll to this item and press <Enter> to view the CPU Feature sub menu.





- **Harddisk boot priority**

Scroll to this item and press <Enter> to view the sub menu to decide the disk boot priority



- **Quick Power On Self Test**

This option speeds up Power on Self Test (POST) after you turn on the system power. If set as Enabled, BIOS will shorten or skip some check items during POST. The default setting is "Enabled".

Enabled	Enable Quick POST
Disabled	Normal POST

- **First/Second/Third Boot Device**

These items let you select the 1st, 2nd, and 3rd devices that the system will search for during its boot-up sequence. There is a wide range of options for your selection.

- **Boot Other Device**

This item allows the user to enable/disable the boot device not listed on the First/Second/Third boot devices option above. The default setting is "Enabled".

- **Boot Up NumLock Status**

Set the the Num Lock status when the system is powered on. The default value is "On".


- **Security Option**

This item allows you to limit access to the system and Setup, or just to Setup. The default value is "Setup".

System
Setup

**System** System requires correct password before booting, and also before permitting access to the Setup page.

**Setup** System will boot, but requires correct password before permitting access to Setup. (Default value)

 **NOTE** To disable the security, select **PASSWORD SETTING** at Main Menu and then you will be asked to enter a password. Do not type anything, just press <Enter> and it will disable the security. Once the security is disabled, the system will boot and you can enter Setup freely.

- **APIC Mode**

Use this item to enable or disable APIC (Advanced Programmable Interrupt Controller) mode that provides symmetric multiprocessing (SMP) for systems.

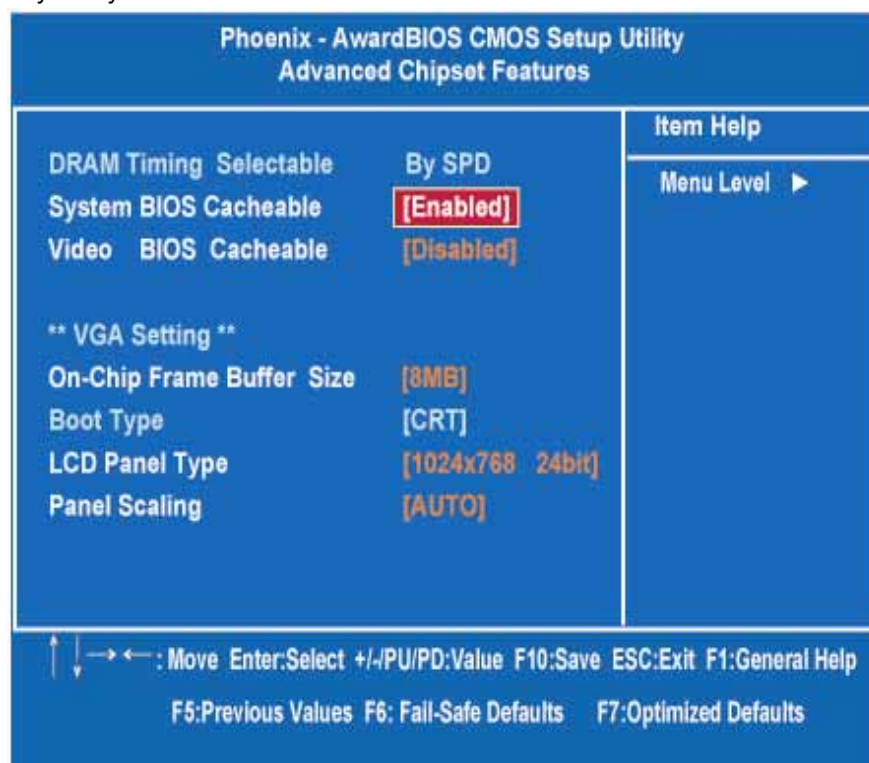
- **MPS Version Control For OS**

This item specifies the version of the Multiprocessor Specification (MPS). Version 1.4 has extended configuration tables to improve support for multiple PCI bus configurations and provide future expandability.

Press <Esc> to return to the Main Menu page.

## 4.7 Advanced Chipset Features

This section contains completely optimized chipset's features on the board that you are strongly recommended to leave all items on this page at their default values unless you are very familiar with the technical specifications of your system hardware.



- **DRAM Timing Selectable**

Use this item to increase the timing of the memory. This is related to the cooling of memory.

- **System BIOS Cacheable**

Selecting Enabled allows caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result. The default value is "Disabled".

- **Video BIOS Cacheable**

This item allows you to change the Video BIOS location from ROM to RAM. Video Shadow will increase the video speed.

\*\*\* **VGA Setting** \*\*\*

- **On-Chip Frame Buffer Size**

Use this item to set the VGA frame buffer size.

- **Boot Type**

This item is to select Display Device that the screen will be shown.

- **LCD Panel Type**

This item is to allow you to adjust the panel resolution.

- **Panel Scaling**

This item shows the setting of panel scaling and operates the scaling function that the panel output can fit the screen resolution connected to the output port.

Press <Esc> to return to the Main Menu page.

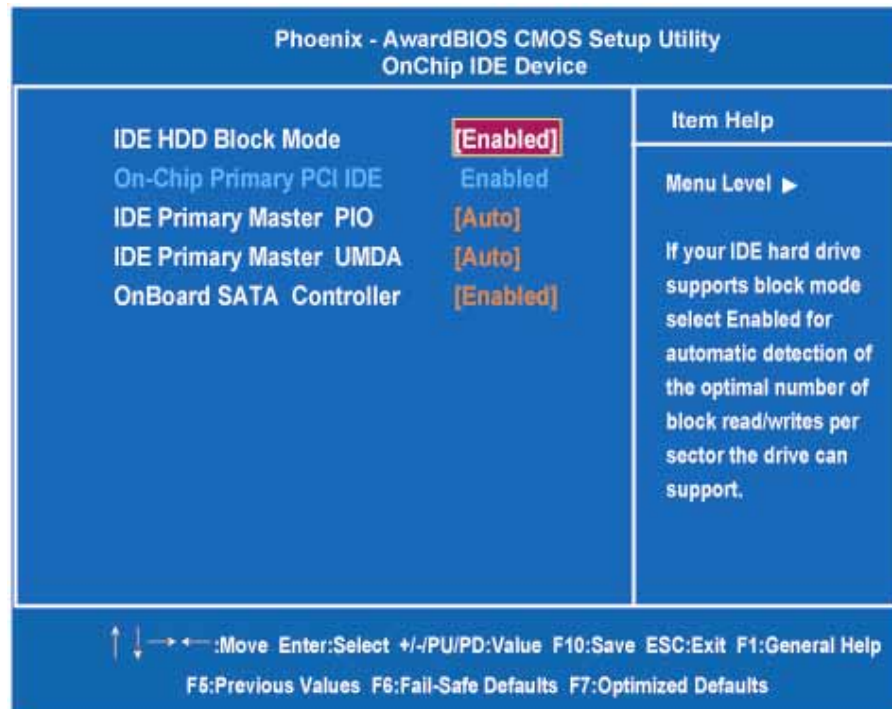
## 4.8 Integrated Peripherals

This section allows you to configure your OnChip IDE Device, SuperIO Device and Onboard Device.



- **OnChip IDE Device**

Scroll to this item and press <Enter> to view the sub menu OnChip IDE Device.



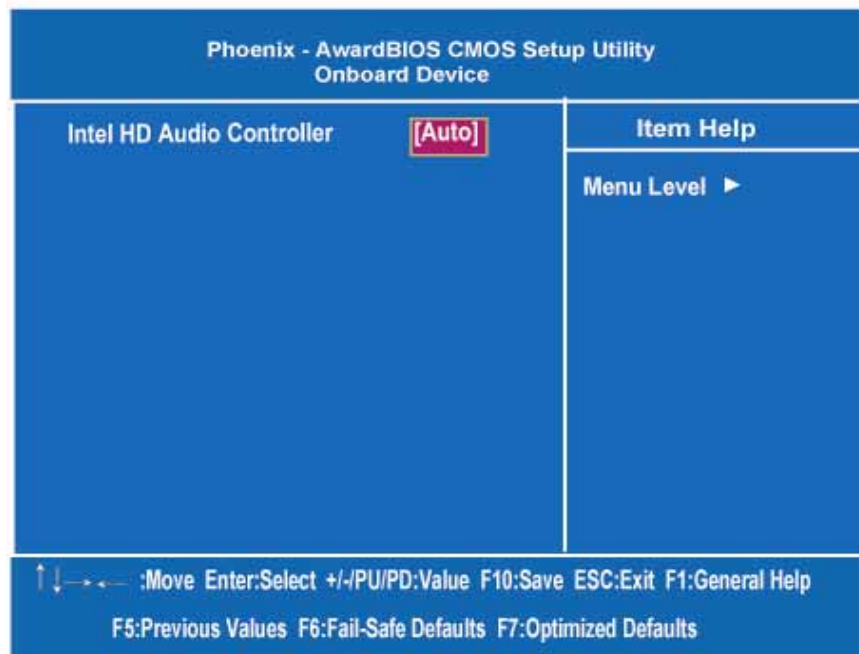
➤ **IDE HDD Block Mode**

Block mode is also called block transfer, multiple commands, or multiple sectors read/write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support.

Press <Esc> to return to the Integrated Peripherals page.

➤ **Onboard Device**

Scroll to this item and press <Enter> to view the sub menu Onboard Device.

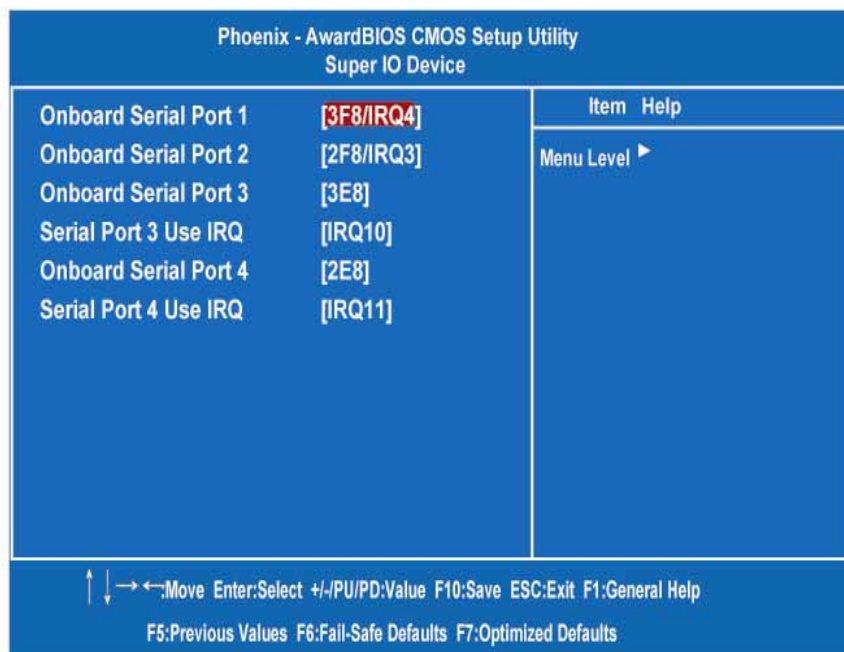


➤ **Intel HD Audio Controller**

Use this item to enable an Intel HD Audio controller.

Press <Esc> to return to the Integrated Peripherals page.

- **Super IO Device**



- **Onboard Serial Port 1/2/3/4**

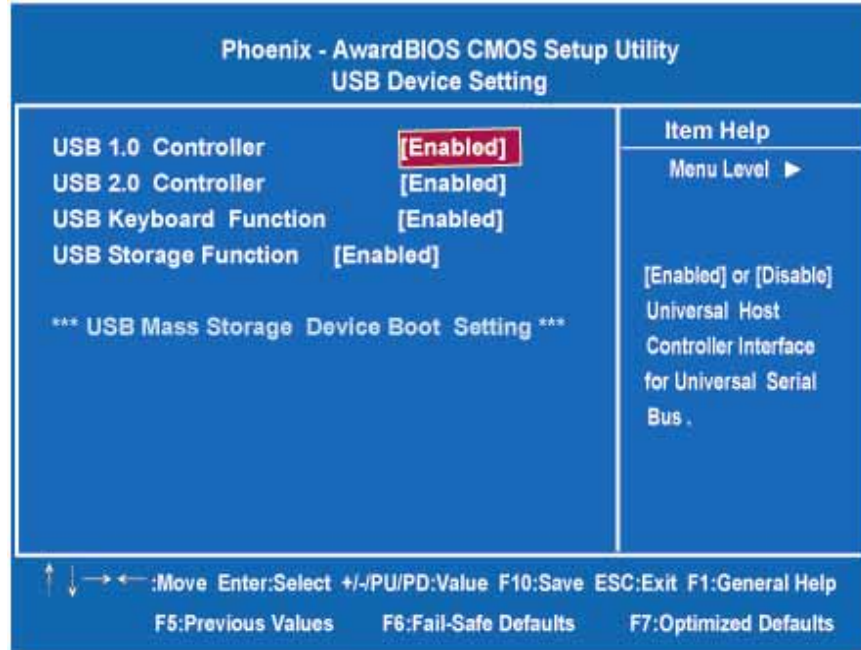
Select an address and corresponding interrupt for the serial port. There are several options for your selection.

Press <Esc> to return to the Integrated Peripherals page.



- **USB Device Setting**

Scroll to this item and press <Enter> to view the sub menu USB Device Setting.



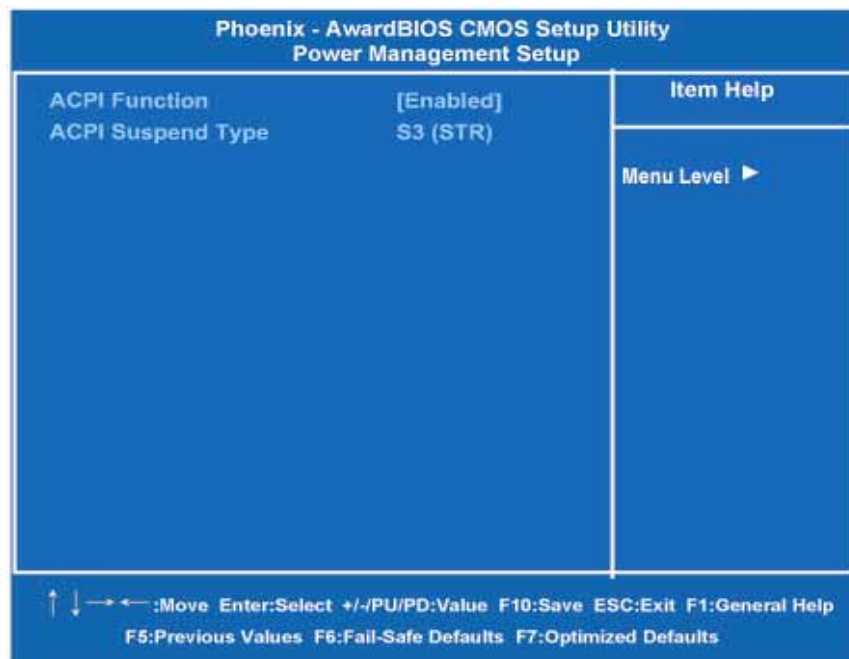
Press <Esc> to return to the Integrated Peripherals page.

- **Onboard Lan Boot ROM**

Use this item to enable or disable the Boot ROM function of the onboard LAN chip when the system boots up.

## 4.9 Power Management Setup

The Power Management Setup allows you to save energy of your system effectively. It will shut down the hard disk and turn OFF video display after a period of inactivity.

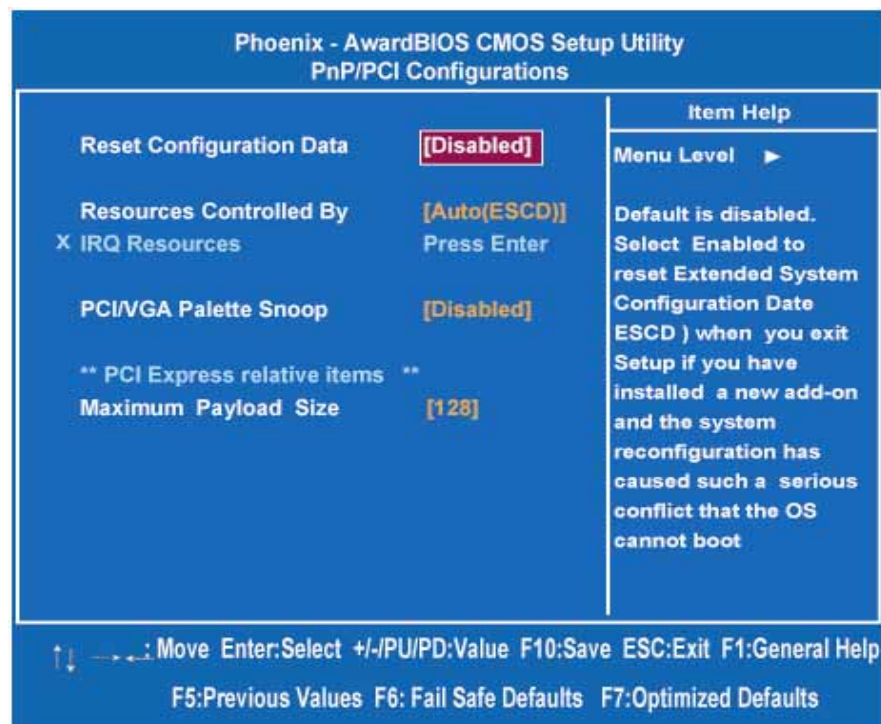


- **ACPI Function**

Advanced Configuration and Power Management (ACPI).  
The function is always "Enabled".

#### 4.10 PnP/PCI Configuration Setup

This section describes the configuration of PCI (Personal Computer Interconnect) bus system, which allows I/O devices to operate at speeds close to the CPU speed while communicating with other important components. This section covers very technical items that only experienced users could change default settings.



- **Reset Configuration Data**

Normally, you leave this item Disabled. Select Enabled to reset Extended System Configuration Data (ES CD) when you exit Setup or if installing a new add-on cause the system reconfiguration a serious conflict that the operating system can not boot. Options: Enabled, Disabled.

- **Resources Controlled By**

- The Award Plug and Play BIOS can automatically configure all boot and Plug and Play-compatible devices. If you select Auto, all interrupt request (IRQ), DMA assignment and Used DMA fields disappear as the BIOS automatically assign them. The default value is "Auto". The other option is "Manual"

- **IRQ Resources**

When resources are controlled manually, assign each system interrupt to one of the following types in accordance with the type of devices using the interrupt:

1. Legacy ISA Devices compliant with the original PC AT bus specification, requiring a specific interrupt (such as IRQ4 for serial port 1).
- 2 . PCI/ISA PnP Devices compliant with the Plug and Play standard,

whether designed for PCI or ISA bus architecture. The default value is "PCI/ISA PnP".

- **PCI/VGA Palette Snoop**

Some non-standard VGA display cards may not show colors properly. This item allows you to set whether MPEG ISA/VESA VGA Cards can work with PCI/VGA or not. When enabled, a PCI/VGA can work with a MPEG ISA/VESA VGA card; when disabled, a PCI/VGA cannot work with a MPEG ISA/VESA Card.

**\*\* PCI Express relative items \*\***

- **Maximum Payload Size**

When using DDR SDRAM and Buffer size selection, another consideration in designing a payload memory is the size of the buffer for data storage. Maximum Payload Size defines the maximum TLP (Transaction Layer Packet) data payload size for the device.

Press <Esc> to return to the Main Menu page.

#### 4.11 PC Health Status

This section supports hardware monitoring that lets you monitor those parameters for critical voltages, temperatures and fan speed of the board.

Phoenix - AwardBIOS CMOS Setup Utility		
PC Health Status		
Current CPU Temperature	26°C/ 70°F	Item Help Menu Level ►
Current System Temperature	31°C/ 87°F	
Vcore	0.84 V	
12 V	11.64 V	
5 V	5.12 V	
3.3 V	3.34 V	
5VSB	5.12 V	

↑ ↓ → ← :Move Enter:Select +/-PU/PD:Value F10:Save ESC:Exit F1:General Help  
F5:Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults

#### System Component Characteristics

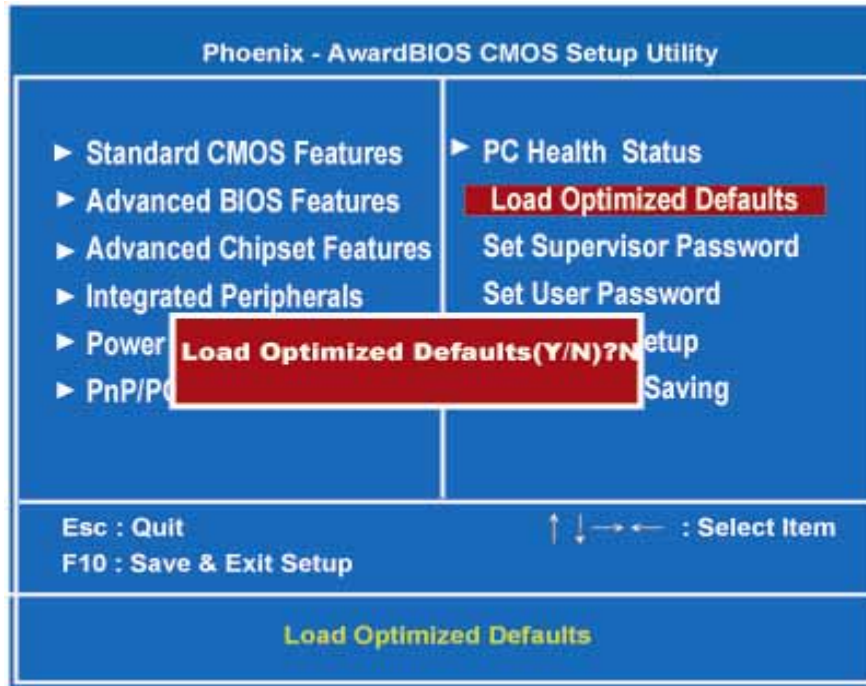
These items provide you with information about the system's current operating status. You can't change these items.

- 1 Current CPU Temperature
- 2 Current System Temperature
- 3 Vcore/12V/5V/3.3V /5VSB

Press <Esc> to return to the Main Menu page.

#### 4.12 Load Optimized Defaults

This option allows you to load your system configuration with default values. These default settings are optimized to enable high performance features.



To load CMOS SRAM with SETUP default values, please enter "Y". If not, please enter "N".

### 4.13 Set Supervisor/User Password

You can set a supervisor or user password, or both of them. The differences between them are:

- 1 **Supervisor password:** You can enter and change the options on the setup menu.
- 2 **User password:** You can just enter, but have no right to change the options on the setup menu.

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

#### ENTER PASSWORD

Type a maximum eight-character password, and press <Enter>. This typed password will clear previously entered password from the CMOS memory. You will be asked to confirm this password. Type this password again and press <Enter>. You may also press <Esc> to abort this selection and not enter a password.

To disable the password, just press <Enter> when you are prompted to enter a password. A message will confirm the password is getting disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

#### PASSWORD DISABLED

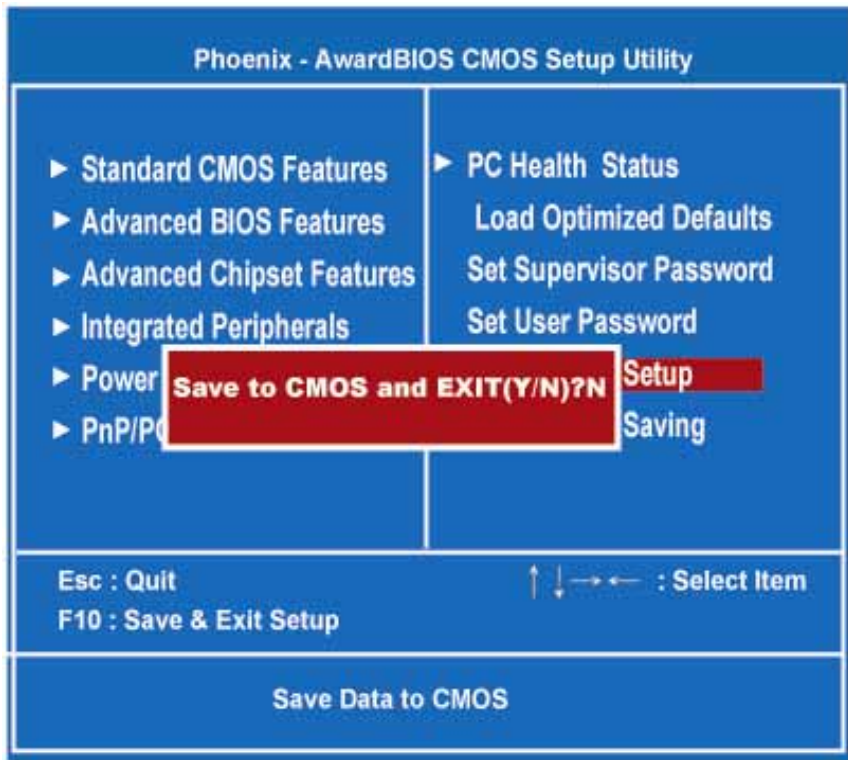
When a password is enabled, you have to type it every time you enter the Setup. It prevents any unauthorized persons from changing your system configuration.

Additionally, when a password is enabled, you can also require the BIOS to request a password every time the system reboots. This would prevent unauthorized use of your computer.

You decide when the password is required for the BIOS Features Setup Menu and its Security option. If the Security option is set to "System", the password is required during booting up and entry into the Setup; if it is set as "Setup", a prompt will only appear before entering the Setup.

### 4.14 Save & Exit Setup

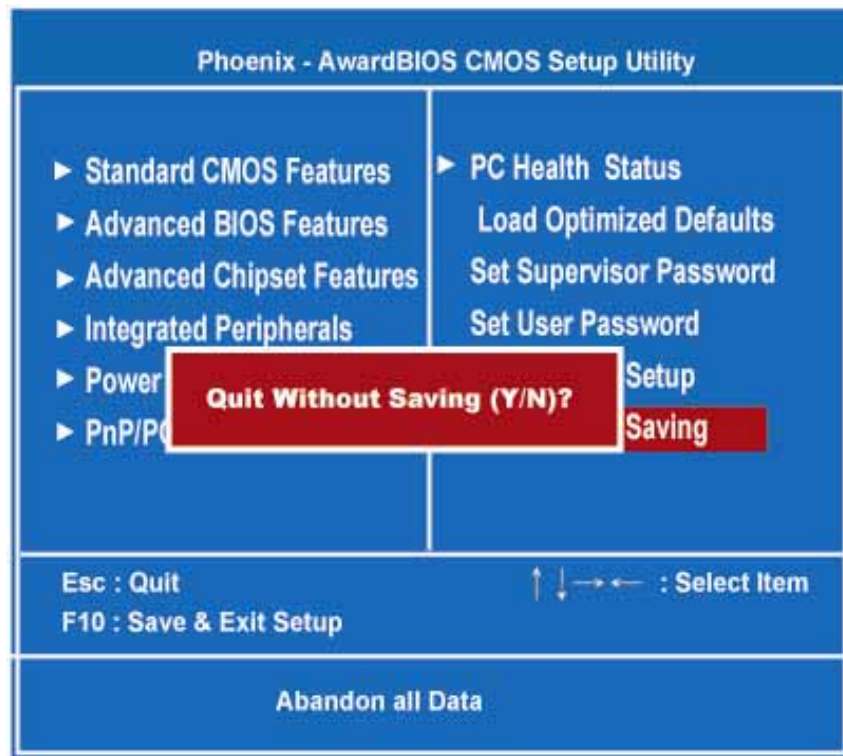
This section allows you to determine whether or not to accept your modifications. Type "Y" to quit the setup utility and save all changes into





#### 4.15 Exit Without Saving

Select this option to exit the Setup utility without saving changes you have made in this session. Type "Y", and it will quit the Setup utility without saving your modifications. Type "N" to return to the Setup utility.



## CHAPTER 5

### INSTALLATION OF DRIVERS

The device drivers are located on the Product Information CD-ROM that comes with the SBC84825 Series package. The auto-run function of drivers will guide you to install the utilities and device drivers under a Windows system. You can follow the onscreen instructions to install these devices:

- „ Chipset
- „ VGA
- „ LAN
- „ Audio

#### 5.1 Installing Chipset Driver

- 1 Run the SETUP.EXE program from the driver directory in your ®
- 2 An Intel® License Agreement appears to show you the important information. Click “Yes” to next step.
- 3 Please wait while running the following setup operations.





(3-1)



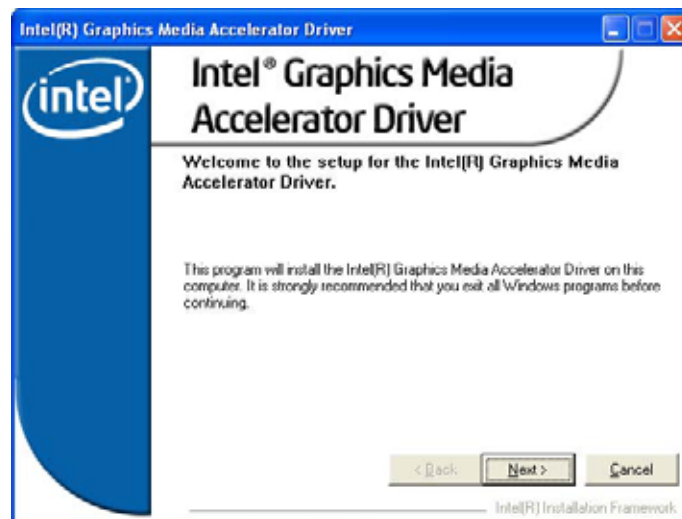
(3-2)

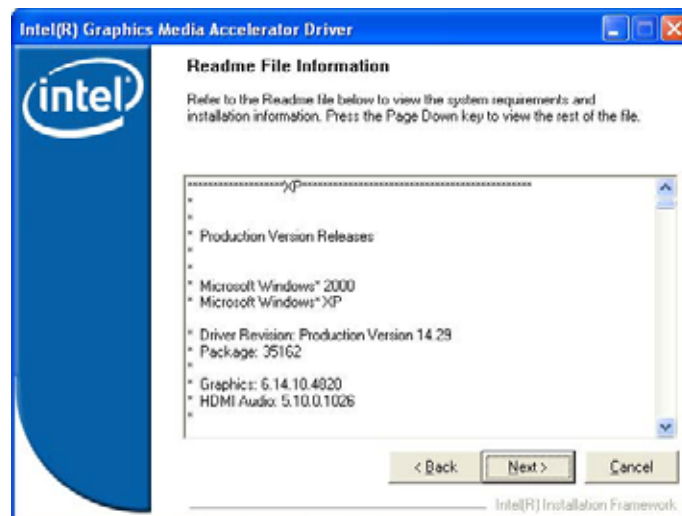
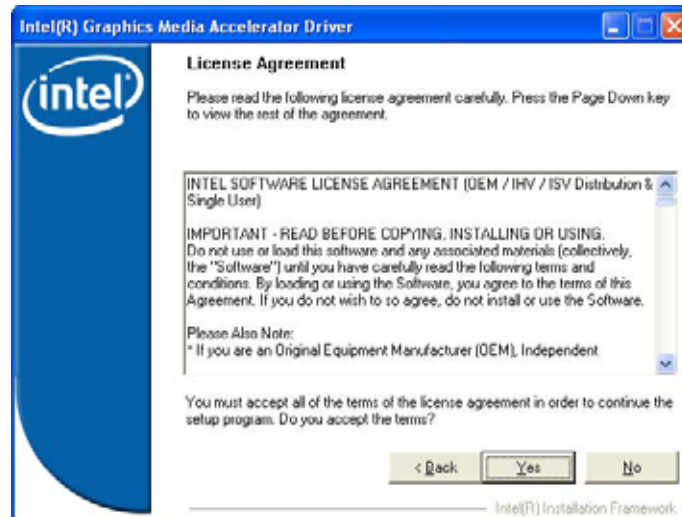
4. Click "Finish" to complete the setup process.
5. You will be asked to reboot your computer when the installation is completed. Please click "Yes, I want to restart my computer now" if you don't need to install any other drivers. Otherwise, please click "No, I will restart my computer later", and go on next step.



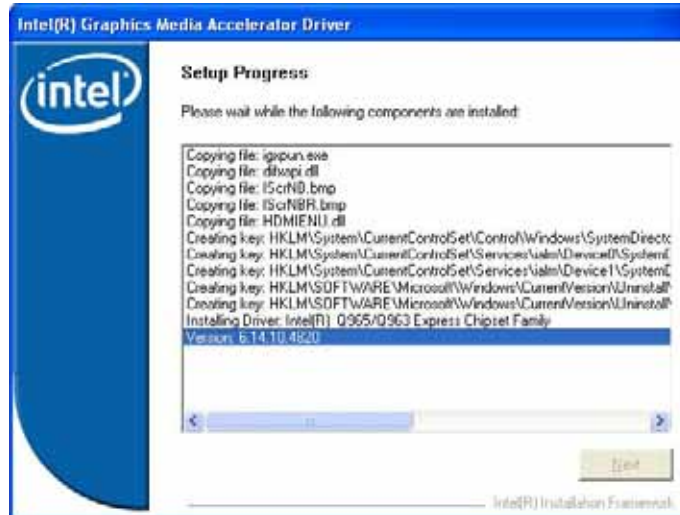
## 5.2 Installing VGA Driver

- 1 Run the SETUP.EXE program from the driver directory in your driver CD. Click "Next" to next step.
- 2 An Intel License Agreement appears to show you the important information. Click "Yes" to next step.
- 3 The message of Readme File Information appears to show you the system requirements and installation information. Please click "Next".

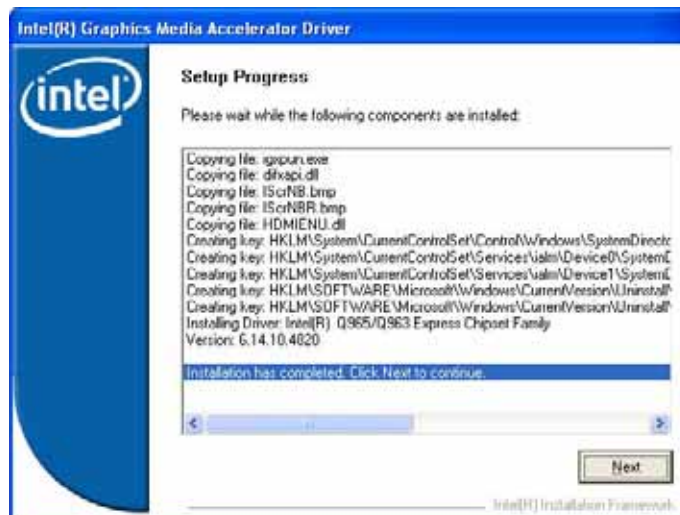




4. Please wait while running the following setup operations.



- 1 When this message appears, please click “Next”.
- 2 You will be asked to reboot your computer when the installation is completed. Please click “Yes, I want to restart my computer now” if you don’t need to install any other drivers. Otherwise, please click “No, I will restart my computer later”, and click “Finish” to complete the installation.





 **NOTE** After installing VGA driver, if you restart, please press Hot Key “Ctrl+Alt+F1” to back VGA because the default display is LVDS LCD.

### 5.3 Installing LAN Driver

Run the InstallShield Wizard for Ethernet from the driver

Click “Install” to start the installation.

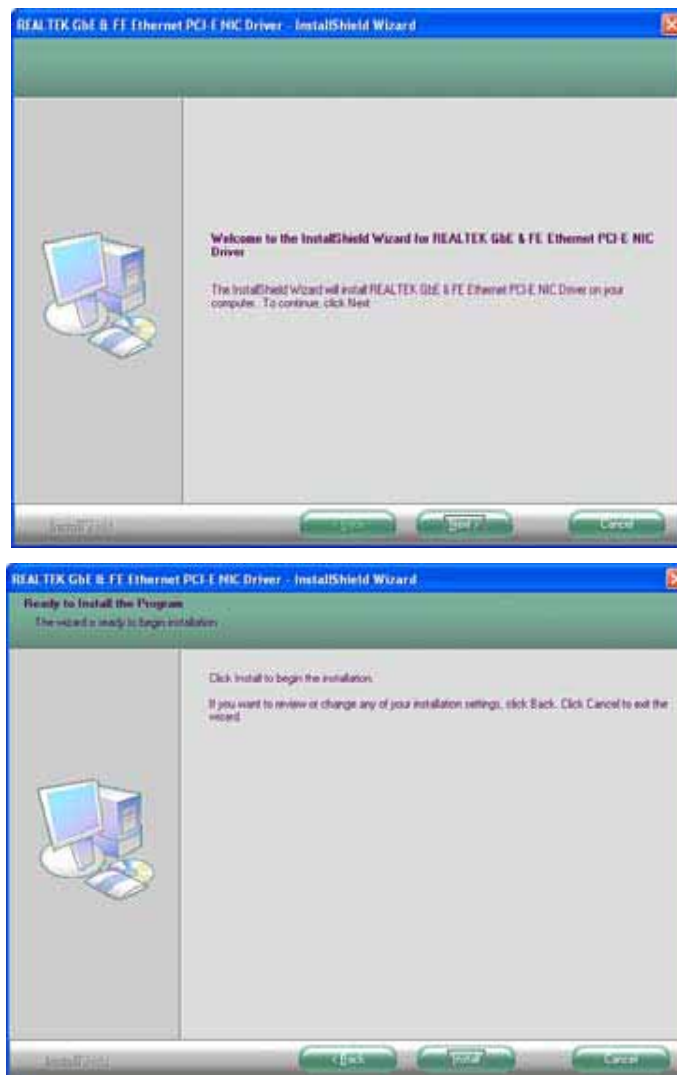
3. Please wait while running the following installation operation.

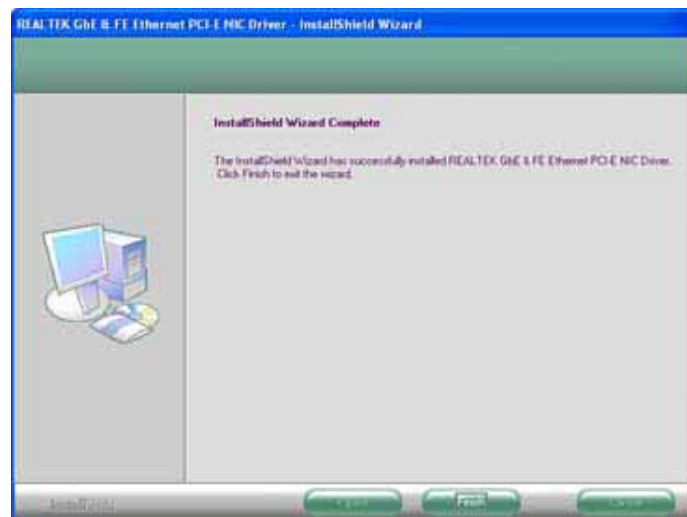
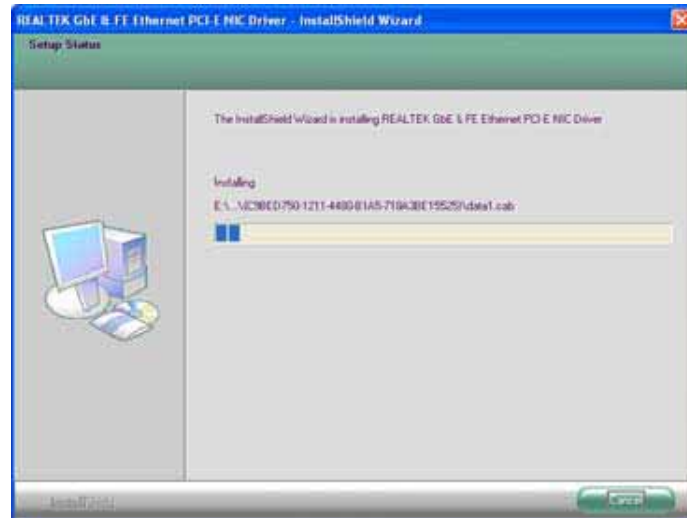
4. Click “Finish” to complete the installation.



## 5.4 Installing Audio Driver

Run the InstallShield Wizard for Audio from the driver directory in yourdriver CD. Click “Next” to next step.  
Please wait while running the following installation operation.







3. You will be asked to reboot your computer when the installation is completed. Please click “Yes, I want to restart my computer now” if you don’t need to install any other drivers. Otherwise, please click “No, I will restart my computer later”, and click “Finish” to complete the installation.



## APPENDIX A

### WATCHDOG TIMER

#### Watchdog Timer Setting (From Super I/O W83627DHG-PT)

After the system stops working for a while, it can be auto-reset by the Watchdog Timer. The integrated Watchdog Timer can be set up in the system reset mode by program.

- **Using the Watchdog Function**

Start

↓

Un-Lock WDT:

O 2E 87 ; Un-lock super I/O

O 2E 87 ; Un-lock super I/O

↓

Select Logic device:

O 2E 07

O 2F 08

↓

WDT Function:

O 2E 2D

O 2F 20 ; Multit function pin select for WDT.

↓

Activate WDT:

O 2E 30

O 2F 01

↓

Set Second or Minute :

O 2E F5

O 2F N      N=00 for second or 08 for minute.

↓

Set base timer :

O 2E F6

O 2F M ; =00,01,02,...FF(Hex) ,Value=0 to 255

- Timeout Value Range
  - 1 to 255
  - Minute / Second
- Program Sample

Watchdog Timer can be set to system reset after 5-second timeout.

O 2E, 87	
O 2E, 87	
O 2E, 07	
O 2F, 08	Logical Device 8
O 2E, 2D	
O 2F, 20	
O 2E, 30	Activate
O 2F, 01	
O 2E, F5	
O 2F, N	Set Minute or Second N=08 (Min),00(Sec)
O 2E, F6	
O 2F, M	Set Value M = 00 ~ FF

## APPENDIX B

### Digital I/O Software Programming

- GPI program sample:

O 2E 87	
O 2E 87	
O 2E 07	
O 2F 09	Select Device 9
O 2E 2C	
O 2F 12	Setting pin 88~90 are GP32~34
O 2E 30	
O 2F 02	Active GPIO3
O 2E F0	
O 2F FF	0 = output ; 1 = Input
O 2E F1	If output pin, it can be read or written; if input pin, it can be read only.
I 2F	

- GPO program sample:

O 2E 87	
O 2E 87	
O 2E 07	
O 2F 09	Select Device 9
O 2E 2C	
O 2F 12	Setting pin 88~90 are GP32~34
O 2E 30	
O 2F 02	Active GPIO3
O 2E F0	
O 2F 00	0 = output ; 1 = Input
O 2E F1	
O 2F FF	GP30~GP37 output 1