



EtherStax™ Stackable Industrial I/O Family Modbus TCP/UDP/IP 10/100MB Ethernet I/O

Analog I/O Models
Single-Ended 16CH Current & 16CH Voltage Input
w/ 16CH Current Outputs (ES2151 Model), or
w/ 16CH Voltage Outputs (ES2152 Model)

USER'S MANUAL



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8500-781-D10A001

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Symbols on equipment:



Means "Refer to User's Manual (this manual) for additional information".

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For additional information, please visit our web site at www.acromag.com and download our whitepaper 8500-765, Introduction To Modbus TCP/IP, or 8500-648, Introduction to Modbus.

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IMPORTANT SAFETY CONSIDERATIONS

You must consider the possible negative effects of power, component, wiring, sensor, or software failure in the design of any type of monitoring or control system. This is very important where property loss or human life is involved. It is important that you perform satisfactory overall system design and it is agreed between you and Acromag, that this is your responsibility.

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If you already know the basics of connecting power, connecting a network cable, and using a web-browser, and you only need some help establishing communication. Here is a brief outline of what you must do to start communicating with this device right away and where to go if you need help.

This is an Ethernet device with built-in web capability. This allows you to use your web-browser to set it up and configure it. All Ethernet devices have a unique IP address that you are required to know in order to use your web-browser to actually communicate with them.

What if you do not already know the IP address of the unit?

All Acromag Ethernet devices include an alternate default mode of operation with a fixed IP address set to **128.1.1.100**. Additionally, the user-programmable IP address that is used outside of default mode is also initially set to 128.1.1.100 from the factory. If this unit is factory fresh, you can talk to it at this address in either mode.

If your unit is not factory fresh and may have another IP address set, then...

You need to place the unit in its Default Mode, which allows you to address it at IP address 128.1.1.100 (<http://128.1.1.100>).

You place this unit into Default Mode by depressing the toggle switch to the position marked "DFT" for about 4 seconds (see front figure at right), just until the yellow STATUS LED (opposite side of unit) starts blinking slowly to indicate the unit is in the Default Mode.

Try browsing the unit with your web browser address at <http://128.1.1.100>. If your unit is in default mode, you should be presented with the home page.

If you are using IP address 128.1.1.100, and you still can't talk to the unit...

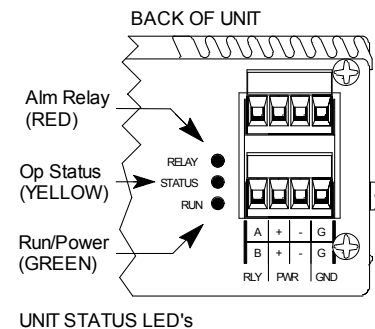
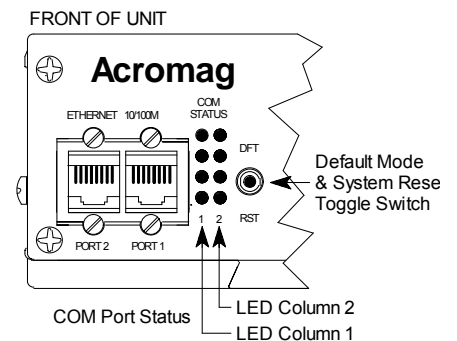
You cannot talk to this device at IP address 128.1.1.100 if the Network Interface Card you are using to connect to our device is set to an IP address outside of the address domain established by this default address. You must set the IP address of your network interface to an address like 128.1.1.x, where x is an integer from 1 to 254, except 100 (our default address). This procedure is covered in document 8500-815 shipped with your unit. It is also detailed in Application Note 8500-734, which you can obtain from the CDROM shipped with your unit, or optionally via download from our web site at www.acromag.com.

You managed to browse to the unit's Home Page, but now you need to get to the Network Configuration Page to set your own IP address...

In order to access any of the other web configuration pages, like the Network Configuration Page, you will need to first enter a Username = **User**, and Password = **password** to gain access (these are the default username and password settings for all EtherStax models and these entries are case-sensitive).

QUICK START

Guide to Quickly Establishing Communication



QUICK START

Guide to Quickly Establishing Communication

Your unit is not factory-fresh and you do not know the Username and Password settings...

If you forget your user name & password, you can always toggle the unit into default mode via the DFT toggle switch at the front of the unit (hold this toggle 4 seconds to invoke default mode). In this mode, the password and username will revert to the original defaults of "User" and "password" (unit assumes an IP address of 128.1.1.100 in its default mode), allowing you to re-invoke the Password Configuration Page and change the username and password settings as required.

If after applying power, your green RUN LED is not solid ON and is blinking instead, you need to do the following:

First check that your network cable is connected to the unit and to your PC. If you initially power the unit up without making your network connection, the green RUN LED will continue to blink. If you replace the network cable after powering-up, the RUN LED should stop blinking after about 10 seconds once a network link has been established. Note that once the link is established, and even if you later remove the cable, the green RUN LED should not continue to blink.

If the green RUN LED continues to blink after checking your network connections, then try resetting the unit by momentarily depressing the DFT/RST toggle switch to the RST position. After five seconds, the green RUN LED should remain ON.

If you have checked your network connections, tried resetting the unit, and the green RUN LED still continues to blink, then you may need to follow the procedure for restoring the EtherStax to its Initial Configuration. This procedure is located at the end of the Trouble-Shooting section of this manual under "Getting Out Of Trouble" on page 66. This is also the procedure used to sanitize the unit for de-commissioning. You should only do this as a last resort, as this procedure restores everything to its default state—all holding registers, network settings, i2o settings, and any calibration you may have performed. If you do use restore and want to return the unit to service, the IP address will have to be rewritten, and the calibration reference will additionally have to be restored separately via the Restore Factory Voltage Reference Value button of the Input Calibration Page. Next, be sure to also access the Output Calibration page and perform an Output Self-Calibration (inputs will be calibrated automatically). Any manual calibration that you have done is lost after restore and may need to be rechecked.

At this point, if the green RUN LED continues to blink, then you may need to return the unit for repair or reprogramming.

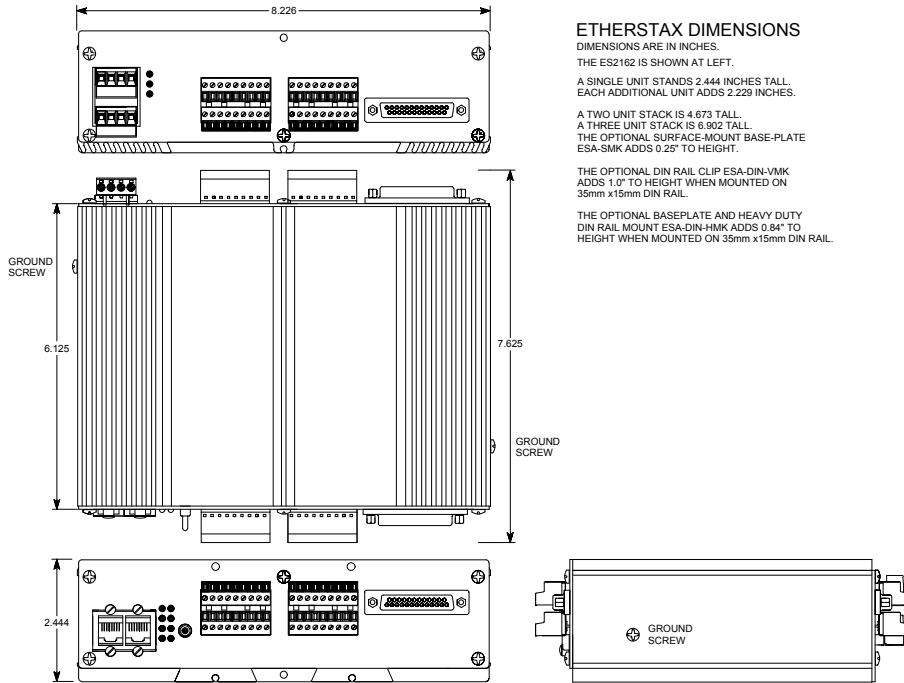
If you need additional help and you have already reviewed the material in this manual, please contact the factory.

Units are designed to interlock and stack together up to 3 units high. A stack of units can be bolted to a wall or flat surface, or mounted on deep-channel, "T" type, 35mm x15mm DIN rails (per DIN EN60715 TH35), depending on the optional mounting kit selected. Available mounting kits are shown below.

MOUNTING AND DIMENSIONS

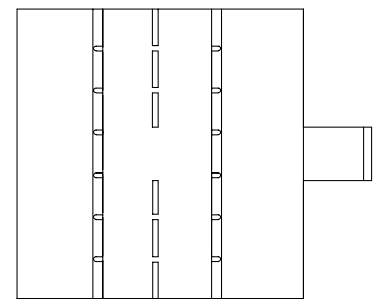
A single unit stands 2.444 inches tall. Each additional unit adds 2.229 inches.

A two-unit stack would be 4.673 inches tall. A three unit stack is 6.902 inches tall. Add any additional height as necessary to account for the mounting plate, DIN clip, and DIN rail, if required.

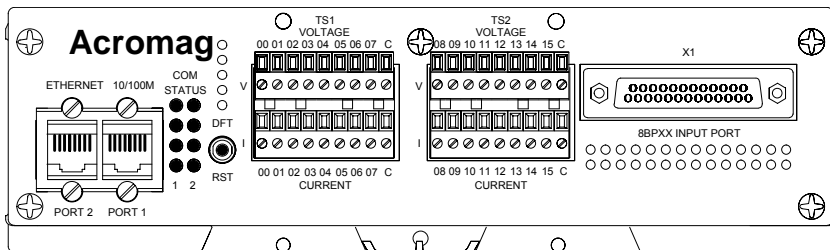
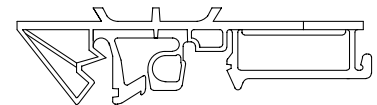


DIN Rail Vertical Mount Kit ESA-DIN-VMK (One or Two Units): This kit includes two plastic DIN clips (Rose Bopla #77003500) that slide into the dove-tail channel of the bottom of the housing. You can use one clip to mount a single unit, or both for added stability when stacking two units. If stacking more than two units on a DIN rail, see ESA-DIN-HMK.

ESA-DIN-VMK TOP VIEW



ESA-DIN-VMK SIDE VIEW



Position clip such that TOP is aligned with end of unit you want upright.
 TOP should coincide with the upper lip of the DIN rail

TOP ← **UPWARD**

This side of clip should align with top of rail

DIN MTG CLIP OF ESA-DIN-VMK

TO HANG: Tilt unit and place TOP of clip over upper lip of DIN rail. Press bottom towards rail to snap in place.

TO REMOVE: Push unit upward and tilt TOP of unit back towards you to disengage it from rail.

NOTE: ESA-DIN-VMK CONTAINS TWO OF THESE PIECES.

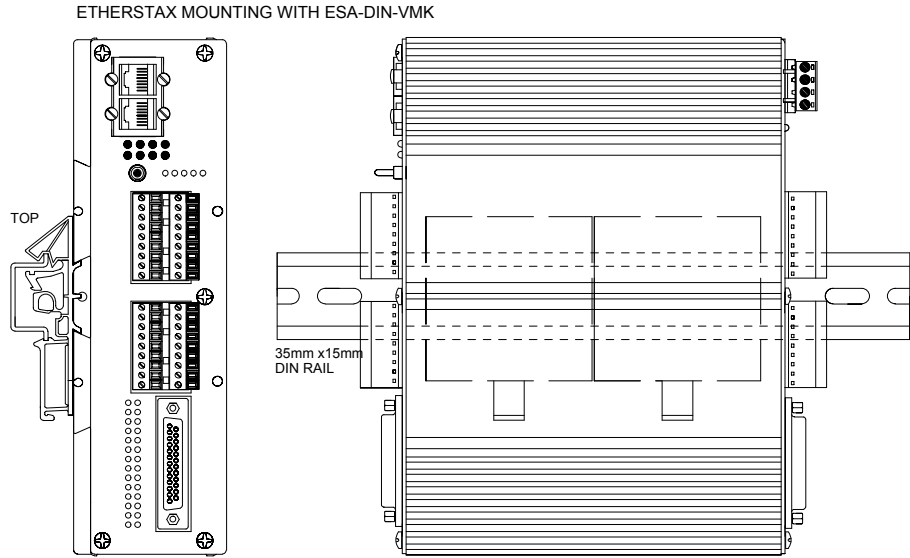
MOUNTING AND DIMENSIONS

Simply slide the clips of this kit into the dovetail channel at the bottom of the enclosure. You can use one clip, or both (recommended) DIN clips of this kit to mount a single unit. For a stack of two units, both clips must be used. To remove a unit from the DIN rail, you have to lift the assembly upward and tilt the top of the unit back to disengage it from the rail. If you choose to install both DIN clips for added security (recommended), then more pressure will be required to disengage the unit from the rail. To mount a stack of 3 units to a DIN rail, use the heavy-duty DIN kit model ESA-DIN-HMK instead.

The drawing at right shows how to mount a unit with the ESA-DIN-VMK kit.

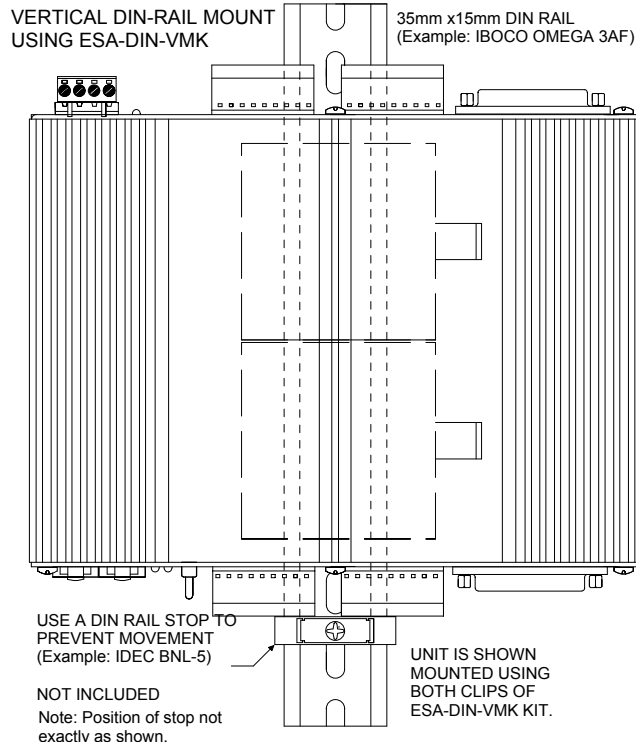
This kit includes two DIN clips for added stability, or for mounting a stack of two units. Note the orientation of the DIN clips relative to the rail.

To remove a unit from the rail, grip unit on each side and pull/push upward while tilting the top back to release the unit from the upper lip of the DIN rail.

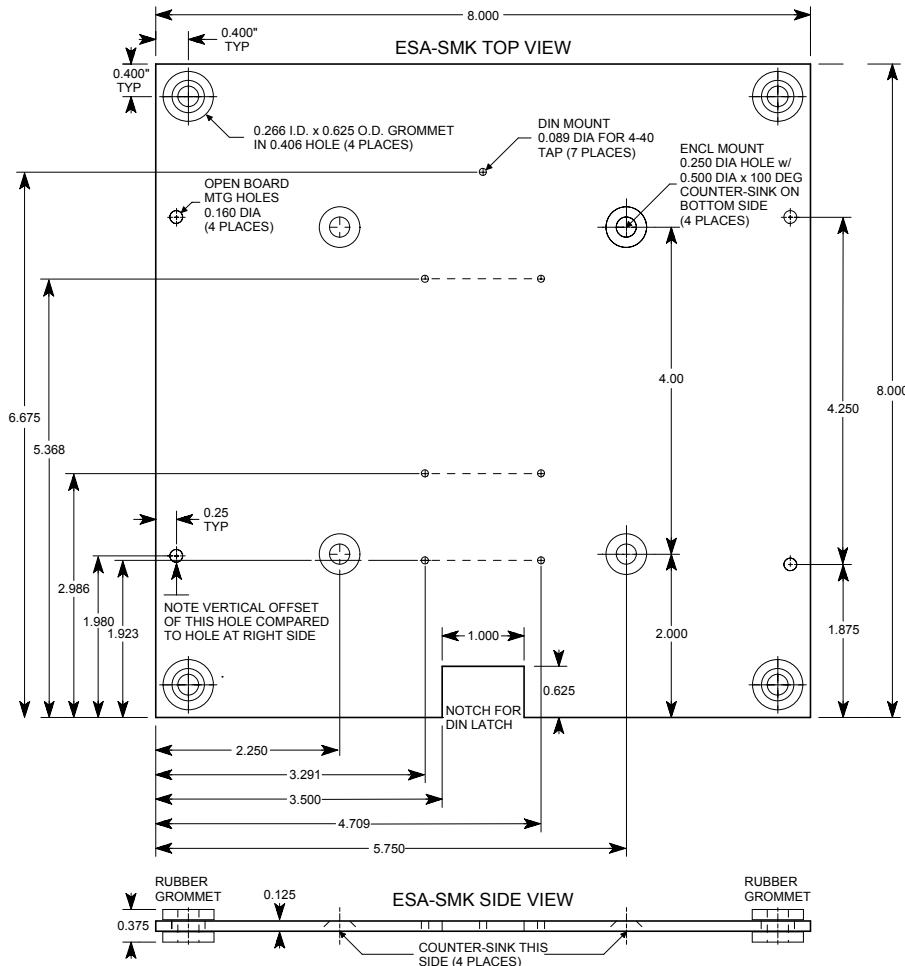


IMPORTANT: Stack no more than 2 units with ESA-DIN-VMK. Use both clips of this kit with two units stacked together.

You can use the ESA-DIN-VMK to mount a unit horizontally, or vertically as shown at right. Be sure to use a DIN rail stop to prevent the unit from moving along the rail with vertically mounted DIN rail.



Surface-Mount Kit ESA-SMK (One to Three units): This kit includes a shock-mounted aluminum base-plate and bolts that attach to the bottom of the housing. Mounting holes with rubber grommets at each corner support 1/4-inch bolts for mounting to flat surfaces. Up to three units may be stacked on this plate.



MOUNTING AND DIMENSIONS

Insert the four rubber grommets into the holes at each corner of the base-plate.

Then use the four 1/4-20x0.375, flat head, counter-sink bolts provided to bolt this plate to the bottom of the enclosure. Be sure to insert the bolts from the counter-sink side of the plate.

Add any additional units to your stack--you can safely stack up to three units on this plate.

Use 1/4-inch bolts (not provided) to bolt this assembly to a wall or flat surface. It is recommended that flat washers (not provided) be used to protect the rubber grommet.

This plate also includes the four holes necessary for mounting an open-frame circuit board to it (i.e. no enclosure with hardware of ESA-OMK).

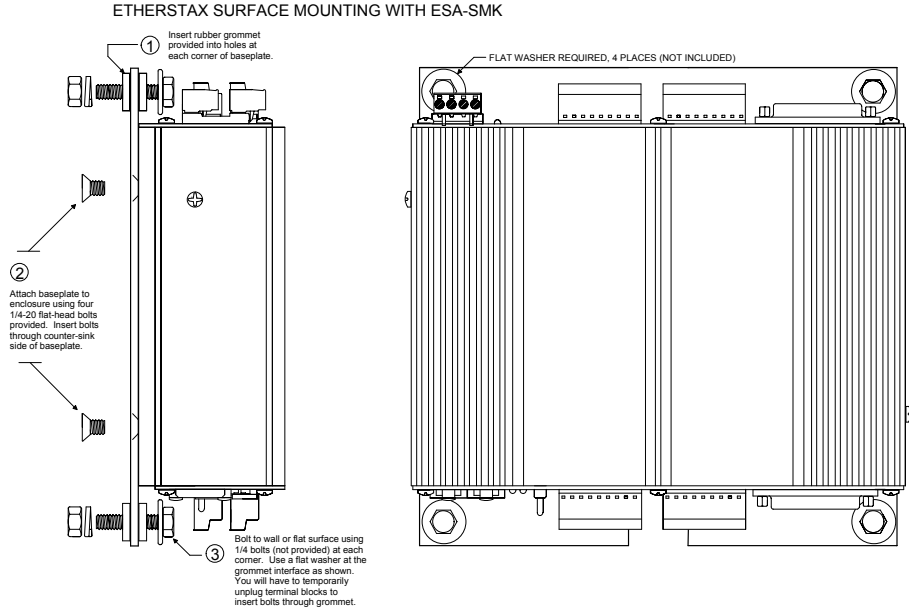
ESA-SMK Kit Contents:

- 1 Pre-Drilled Aluminum Base-Plate, 8 x 8 x 0.125.
- 4 1/4-20 x 0.375 Flat-Head, 100° Counter-Sink, Phillips
- 4 Rubber Grommet, 0.625 O.D. x 0.266 I.D

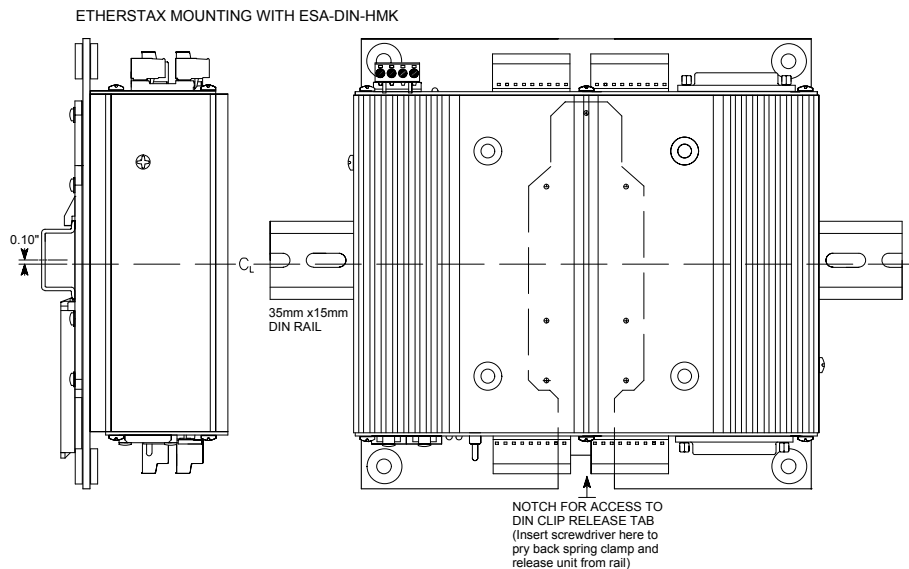
The 1/4-inch bolts and washers (recommended) to attach this assembly to a flat surface or panel are not provided.

For DIN rail mounting of this plate, see ESA-DIN-HMK

MOUNTING AND DIMENSIONS



DIN Rail Horizontal Mount Kit ESA-DIN-HMK (one to three units): This kit has the same base-plate as ESA-SMK above, but adds a heavy-duty DIN adapter (Phoenix UTA-159) and screws for mounting to 35x15mm T-type DIN rails. Up to three units may be stacked on this plate and mounted to a DIN rail.



ESA-DIN-HMK Kit Contents:

- 1 Pre-Drilled Aluminum Base-Plate, 8 x 8 x 0.125.
- 4 1/4-20 x 0.375 Flat-Head, 100° Counter-Sink
- 4 Rubber Grommet, 0.625 O.D. x 0.266 I.D.
- 1 Heavy-Duty DIN Adaptor (Phoenix UTA-159)
- 7 4-40 x 0.25 screw with lock-washer

To attach or remove the ESA-DIN-HMK to/from the DIN Rail, use a screwdriver tip inserted into the slot at the end of the DIN clip, in the area of the notch of the base-plate as shown below. Pry back to compress the DIN clip spring and release it from the rail. You may have to temporarily unplug the terminal blocks in the area of this notch to gain access to the DIN clip.

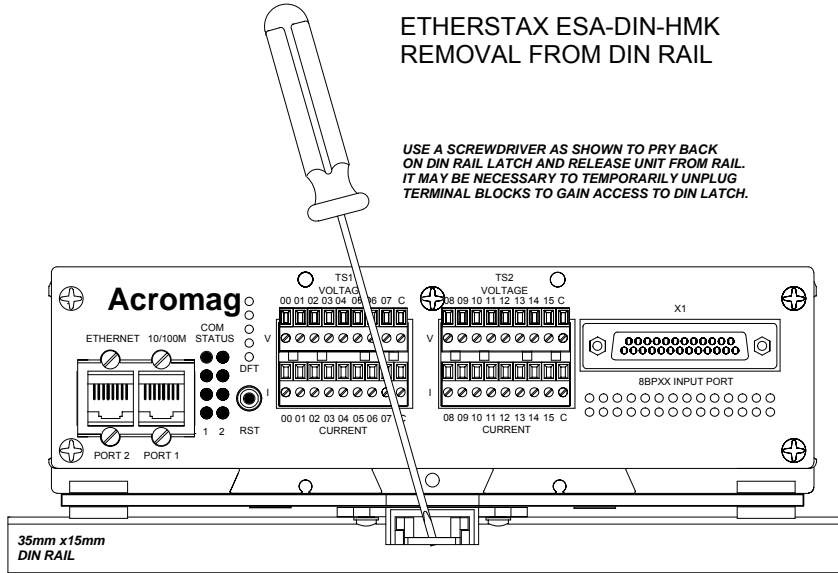
MOUNTING AND DIMENSIONS

IMPORTANT: Be sure to remove power before attempting to disengage unit from the DIN rail.

Be sure to grip unit firmly before disengaging unit from rail and avoid dropping it.

ETHERSTAX ESA-DIN-HMK REMOVAL FROM DIN RAIL

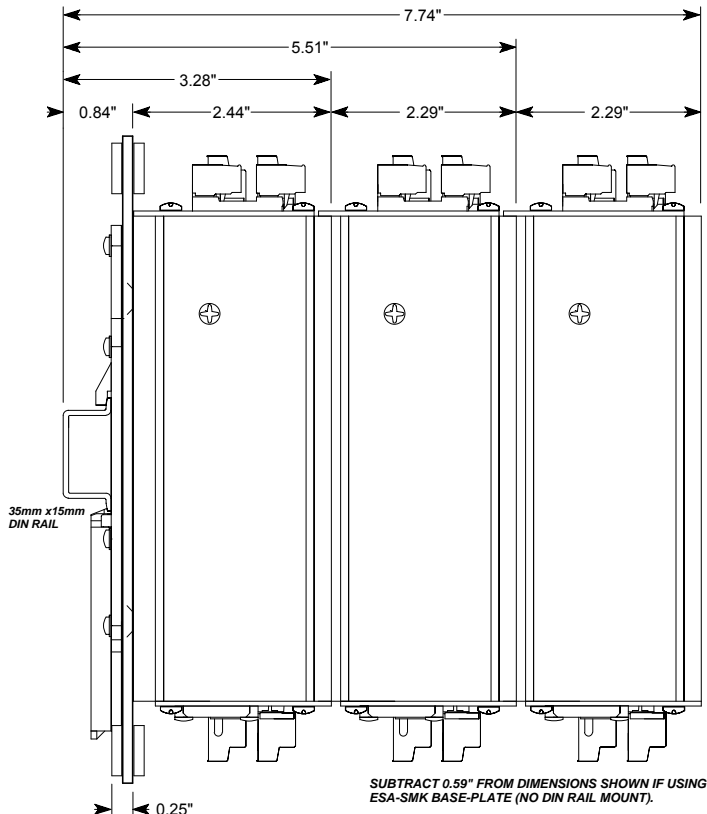
USE A SCREWDRIVER AS SHOWN TO PRY BACK ON DIN RAIL LATCH AND RELEASE UNIT FROM RAIL. IT MAY BE NECESSARY TO TEMPORARILY UNPLUG TERMINAL BLOCKS TO GAIN ACCESS TO DIN LATCH.



LOCATE DIN LATCH IN AREA OF NOTCH IN BASE-PLATE

ETHERSTAX ESA-DIN-HMK STACKING

UP TO 3 UNITS MAY BE STACKED ON A DIN RAIL USING ESA-DIN-HMK AS SHOWN.



SUBTRACT 0.59\" FROM DIMENSIONS SHOWN IF USING ESA-SMK BASE-PLATE (NO DIN RAIL MOUNT).

Note that you can stack up to 3 units on the ESA-DIN-HMK or ESA-SMK as shown at left.

Subtract 0.59 inches from dimensions shown if using ESA-SMK (i.e. no DIN rail mount).

MOUNTING AND DIMENSIONS

The drawing at right shows how to stack units together.

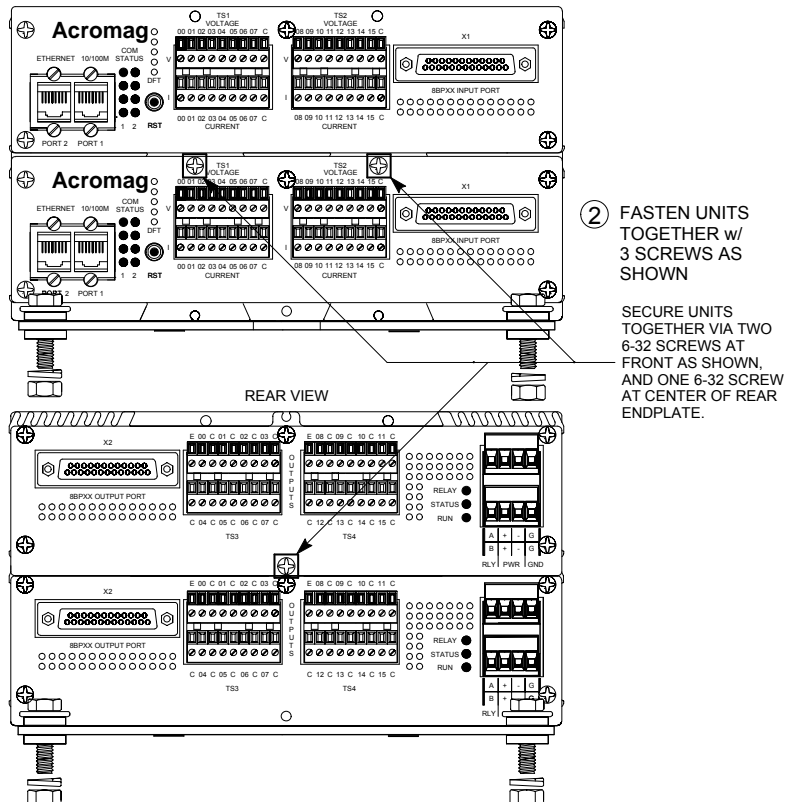
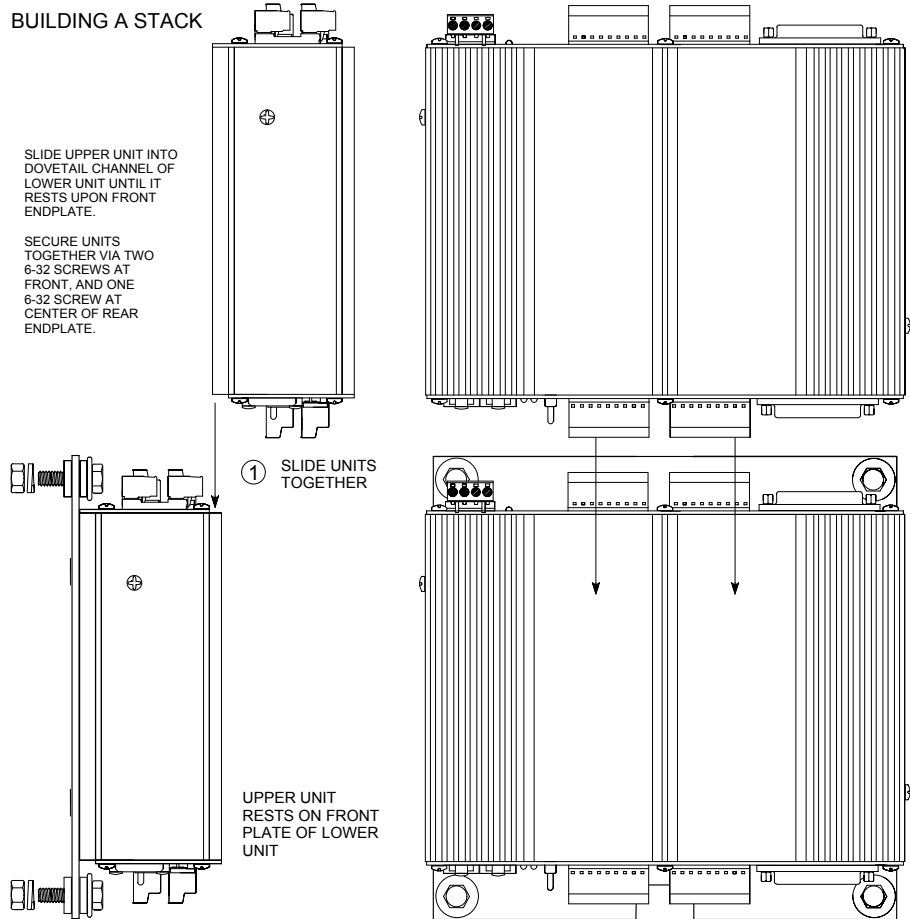
You can stack up to three units together in this manner.

WARNING: Be sure to grip the edges of unit firmly when stacking units and avoid dropping it.

BUILDING A STACK






SLIDE UPPER UNIT INTO DOVETAIL CHANNEL OF LOWER UNIT UNTIL IT RESTS UPON FRONT ENDPLATE.

SECURE UNITS TOGETHER VIA TWO 6-32 SCREWS AT FRONT, AND ONE 6-32 SCREW AT CENTER OF REAR ENDPLATE.



Open-Board Mounting Kit ESA-OMK: EtherStax units can be ordered and mounted without their enclosure. This kit includes the jack-screws, and fasteners necessary to stack two open circuit boards together (or two dual board assemblies like this model), plus the standoffs and screws for mounting this assembly to a flat surface. Note that this is also a replacement kit, as open-frame units already include these items (except for the 6-32 nylon nut). Use additional kits as required for stacking more than two boards in this manner.

ESA-OMK KIT CONTENTS

- TOP BOARD SCREW  #4 6-32x0.25 PAN HEAD SCREWS
- BD-TO-BD STANDOFF  #4 1/4-HEX MALE-FEMALE STANDOFF 1-9/16 LONG WITH 6-32 x0.375 FEMALE THREAD & 6-32 x0.250 MALE THREAD
- BOTTOM STANDOFF  #4 1/4-HEX FEMALE STANDOFF 3/8 LONG w/6-32 THREADS
- MTG PLATE SCREW  #4 6-32 x0.25 SEMS SCREW WITH INTEGRATED WASHER
- 6-32 NUT (NYLON)  #5 6-32 NYLON NUT, REPLACES STANDOFF OF UPPER BOARD

ESA-OMK kit items are also included with every open board assembly.

MOUNTING AND DIMENSIONS

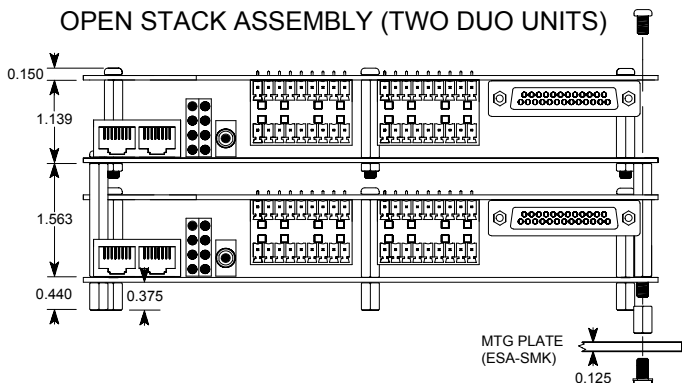
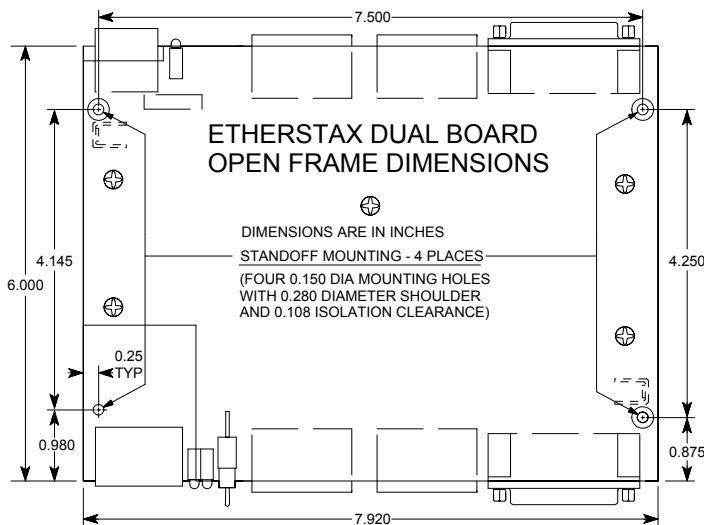
IMPORTANT: Units ordered without their enclosure do not retain safety agency listing, but are recognized components (see Specifications – Agency Approvals). Open-frame units are also vulnerable with respect to ESD. While the open unit retains all of its built-in transient suppression and filtering, the sensitive electronic circuits are left exposed to ESD damage without the protection of an enclosure.

You should take adequate measures to protect open-frame mounted units from dust, debris, and ESD.

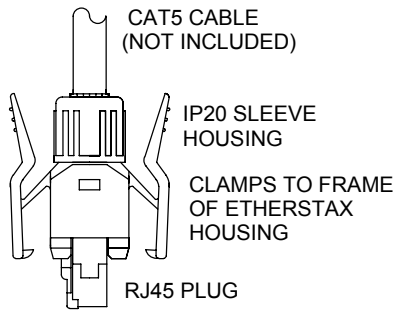
Thus, it is recommended that open units be mounted in a protective enclosure or cabinet.

Note: Open-frame units may also mount to the optional surface-mounted base plate ESA-SMK to facilitate surface or DIN-rail mounting. This plate has mounting holes located as shown to mate with those of the circuit board.

Be very careful when handling open-frame circuits to avoid ESD damage to the sensitive circuit components.



MOUNTING AND DIMENSIONS



ESA-CTK IP20 CAT5 CABLE TERMINATION KIT

Cable Termination Kit ESA-CTK: The EtherStax enclosure includes a panel mounted frame around the RJ45 network port that accommodates special IP20 clip-type plug connectors that help to secure network connections from shock and vibration. You can still utilize standard RJ45 modular plug connectors, but if you want the added security of this clip frame, then you have to use the compatible cable plug connectors provided by this kit. This kit provides the male plug and sleeve housing for one end of Category 5 Ethernet cable that will mate to this frame. Category 5 cable is not included. You will also require a modular crimping tool for attaching the plug to your cable (most standard RJ45 crimping tools will work).

Units ordered without their enclosure cannot utilize this clip.

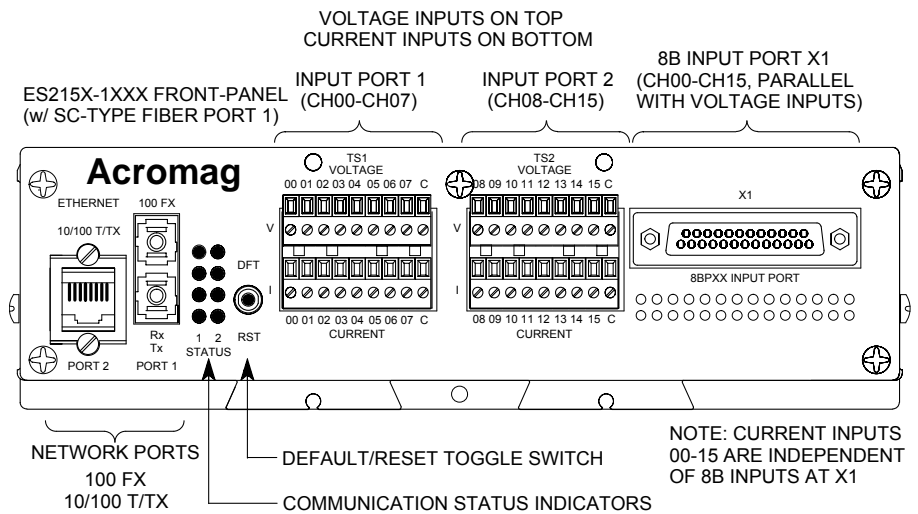
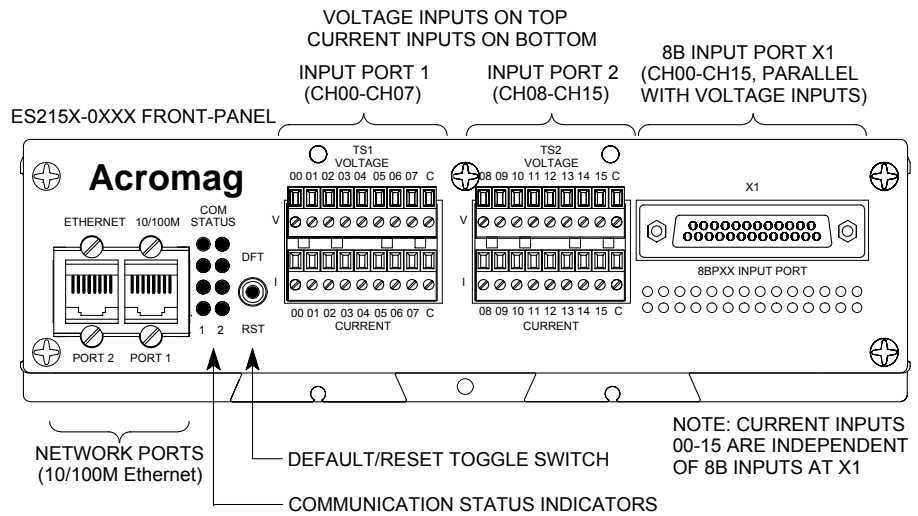
CONTROLS & INDICATORS

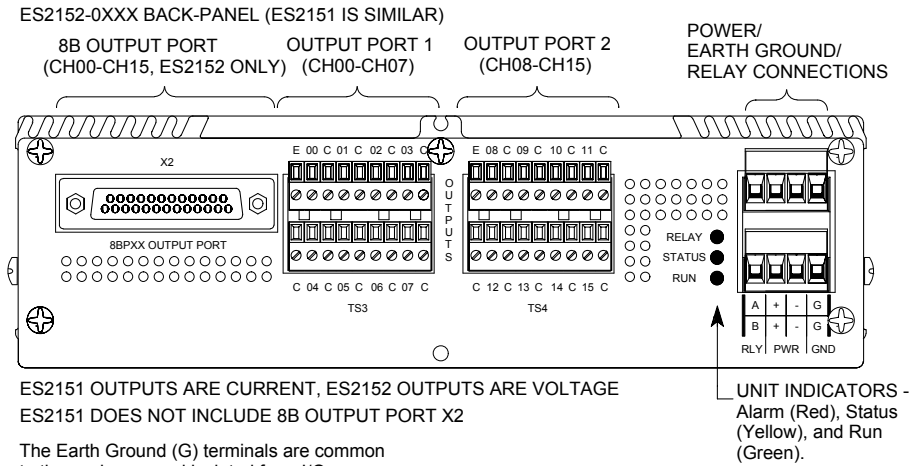
Front Panel

Two columns of status indicators for the network ports are used to indicate different things according to whether the unit is in switch mode, or hub/repeater mode. Refer to Specifications – Controls & Indicators for these definitions.

The toggle switch is used to toggle the unit into or out of Default Mode (toggle up & hold 4 seconds), or to reset the unit (toggle down). It can also be used to restore/sanitize a unit id held depressed while applying power (see Getting Out of Trouble)

In Default Mode, the yellow STATUS LED on the back of the unit will flash slowly and the unit will assume a fixed static IP address of "128.1.1.100", a default subnet mask of "255.255.255.0", a default username of "User", and a default password of "password".





CONTROLS & INDICATORS

Back Panel

The Green RUN LED (bottom) is ON if power is on and will blink in "wink" ID mode.

The Yellow STATUS LED (middle) blinks ON/OFF slowly in default communication mode and blinks rapidly if a watchdog timeout has occurred.

The Red RELAY LED (top) is ON if relay is energized (relay terminals A & B are closed).

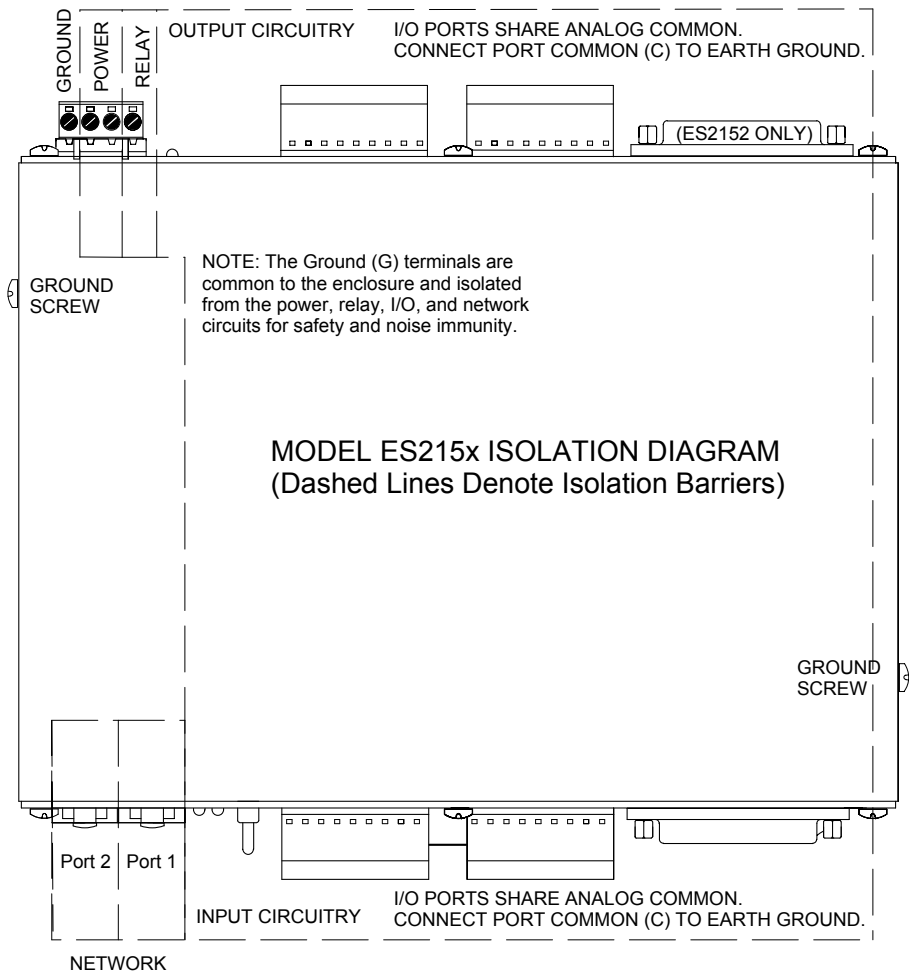
ISOLATION BARRIERS

Dashed Lines denote isolation barriers. Additionally, the enclosure is also isolated.

The I/O circuitry, network ports (each), power circuit, relay, and enclosure (earth ground) are isolated from each other for safety and noise immunity.

Note that the network ports are individually isolated from the rest of the circuit and from each other.

IMPORTANT: Transient suppression devices are internally shunted to earth ground, please connect the ground terminal to a suitable earth ground to complete this path and protect the unit. Ground may alternately connect to the ground screw on either side of the unit instead of the ground terminal.



CONNECTIONS

Network

For 100Base-TX systems, at a minimum, use data grade Unshielded Twisted-Pair (UTP) wiring that has a 100Ω characteristic impedance and meets the EIA/TIA Category 5 wire specifications.

It is recommended that you use a CAT-5 cable to connect this device to your PC.

For 10Base-T systems, you may use Category 3, Category 4, or Category 5/5E UTP/STP cable.

In either case, you are limited to 100 meters between any two devices.

For compatible male plug connectors, order the Cable Termination Kit, Acromag ESA-CTK.

RJ45 MDI AND MDI-X CONNECTIONS

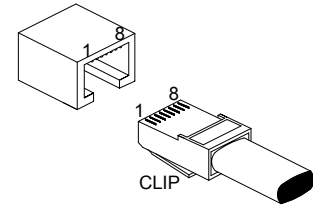
PIN	MDI WIRING	MDI-X WIRING
1	Transmit +	Receive +
2	Transmit -	Receive -
3	Receive +	Transmit +
4	Not Used	Not Used
5	Not Used	Not Used
6	Receive -	Transmit -
7	Not Used	Not Used
8	Not Used	Not Used

Note Crossover Connections

RECOMMENDED CABLE

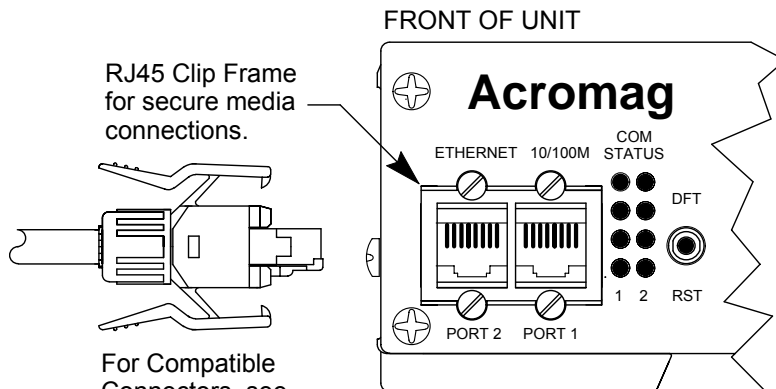
SPEED	DISTANCE	CABLE
10Base-T	100M	CAT 3, CAT 4, or CAT 5 UTP
100Base-TX	100M	CAT 5/5e UTP/STP

ETHERNET PORT



RJ-45 CONNECTOR

The Ethernet port of this unit is wired MDI-X by default, but includes automatic crossover (the Ethernet port of your PC is typically wired MDI). Thus, you can use either a straight-through or crossover cable to connect this device directly to a PC, Ethernet switch, or another unit.



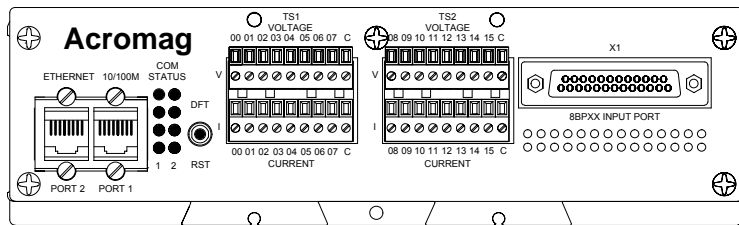
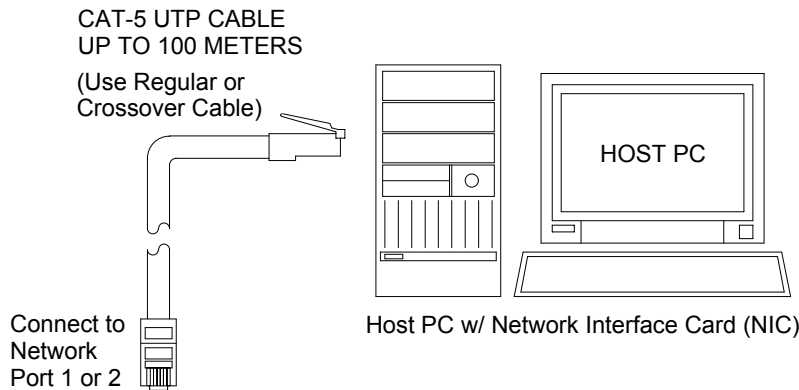
For Compatible Connectors, see ESA-CTK Cable Termination Kit

The RJ45 clip frame mates with the compatible connectors of the ESA-CTK for increased immunity to shock & vibration.

For increased immunity to shock and vibration, the RJ45 network connections include special clip frames that can be used with compatible plug connectors to help secure your network connection from breaking free under shock or vibration. You can still utilize industry standard RJ45 modular plugs, but if you want the extra security provided by this clip frame, then you can order compatible connectors via the Acromag ESA-CTK Cable Termination Kit. This kit includes the male plug and sleeve housing that mate to the RJ45 frame of the enclosure for one end of CAT5 cable (cable not included). You will also need a modular plug crimping tool for attaching the plug provided to your cable.

HOST PC CONNECTED DIRECTLY TO UNIT

Note: This MDI to MDI-X connection does not require a crossover cable.



If your unit is a Model ES2151/2152-1xxx, it includes both a 100FX fiber port and a 10BT/100BTX copper port. To connect directly to the fiber port from your PC, you will need a compatible NIC card installed in your PC, or a media converter. Note that the auto-crossing feature does not apply to fiber connections and the Tx and Rx fiber channels must be mechanically crossed.

Optionally, you may use an external Ethernet switch to connect to your EtherStax unit (recommended). The recommended approach for switched Ethernet is to connect one unit or Ethernet device per switch port. This is the most efficient and deterministic method of communication as it increases network throughput and eliminates data collisions.

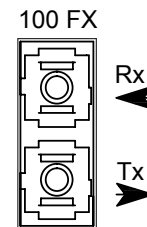
The next section reviews the operation of Ethernet hubs and switches as it relates to the built-in Ethernet switch of this device, which may optionally operate as an Ethernet hub/repeater. You can skip the next two pages if you are already familiar with these terms.

CONNECTIONS

Network – Basic Connections

Your host PC will require that a 10/100M network interface card (NIC) for Ethernet be installed to connect to the EtherStax unit. You may connect to port 1 or port 2 of the EtherStax. The EtherStax unit is auto-crossing, allowing you to use a regular or crossover cable to make connections.

IMPORTANT (Fiber Models): Some models will substitute an SC-type fiber port connector for port 1. The auto-crossing feature of these units does not apply to the fiber connection and transmit must be manually crossed over to receive, and visa-versa. Facing the front end-plate of the unit, the Transmit (Tx) channel is the bottom half of the SC style connector, while the top half is Receive (Rx).



COM STATUS	SWITCH MODE	HUB/REPEATER MODE
● ●	LED Column 1 - Port 1 LED Column 2 - Port 2	1=LED of Column 1 2=LED of Column 2
● ●	GREEN No Function in Switch Mode.	1=Hub Activity, 2=Hub Collision.
● ●	YELLOW Link/Activity - ON if Linked/Blinks if Activity.	1=MII/CPU Link/Activity, 2=MII/CPU Error.
● ●	YELLOW Full-Duplex/Collision - ON for Full-Duplex, Blinks for Half-Duplex Collisions, OFF for Half-Duplex and No Collisions.	1=Port 2 Link/Activity, 2=Error at Port 2.
● ●	YELLOW Speed - ON for 100Mbps, OFF for 10Mbps.	1=Port 1 Link/Activity, 2=Error at Port 1.
1 2	Refer to Specifications - Controls & Indicators Section for more detail.	

CONNECTIONS

Network - Background

Hubs & Switches

To properly network connect this device, you need to know a little bit about network hubs and switches. Please take a moment to review this material before installing your unit.

Switched Ethernet involves connecting one Ethernet device per switch port. This suppresses CSMA/CD and allows the segment to operate full speed in full duplex. A throughput of 100M at half-duplex effectively doubles with full-duplex. This provides a more reliable and deterministic communication link, as no data collisions are possible.

This device has a built-in Ethernet switch that can alternately operate as an Ethernet hub. To understand which mode to use and how to network connect Ethernet devices, you need to review switch operation and the differences between a switch and a hub. If you are already familiar with these terms, skip over this information and review the various network connections outlined in the following pages.

An Ethernet hub (or repeater) is a device that simply connects Ethernet nodes. Any message at one hub port is repeated on all ports. That is, hubs forward data packets they receive from a single station to all hub ports. As a result, all port devices connected to a single hub will share the same bandwidth. Then as nodes are added to the network hub, they compete for this finite amount of bandwidth (at 10Mbps or 100Mbps). This can cause data collisions to occur and makes network determinism impossible, particularly on busy networks. Determinism is a term that is used to describe the ability to guarantee that a packet is sent or received in a finite and predictable amount of time. In the past, lack of determinism is the main reason that Ethernet has had problems being accepted for use in critical control applications, as most control systems have a defined time requirement for packet transmission, typically less than 100ms.

An Ethernet switch (or switching hub) is an intelligent device that is used to more efficiently connect distributed Ethernet nodes than a hub. Unlike a simple hub, a switch provides *targeted* data transfer, as it will forward a data packet to a specific port or network segment, rather than all ports, thus freeing up bandwidth. The ability to target a packet to a specific port increases network throughput and helps to eliminate the collisions that historically make Ethernet non-deterministic.

- Switches act as intelligent repeaters to increase network distance.
- Switches split networks into separate collision domains at each port.
- Switches provides determinism by reducing collisions.
- Switches increase network bandwidth/throughput.
- Switches can provide supplemental error checking.

With Ethernet, any device can try to send a data frame at any time. The arbitration protocol for carrier transmission access of the Ethernet network is called Carrier Sense Multiple Access with Collision Detect (CSMA/CD). If two devices happen to send a data frame at the same time, then a collision may occur. With CSMA/CD, each device will first sense whether the line is idle and available for use. If it is, the device will begin to transmit its first frame. If another device also tries to send a frame at the same time, then a collision occurs and both frames are discarded. Each device then waits a random amount of time and retries its transmission until it is successfully sent.

Unlike other Ethernet devices, such as an Ethernet host adapter or Network Interface Card (NIC), the port of a switch does not require its own MAC address. During retransmission of a received packet, the switch port will instead look like the originating device by having assumed its source address. This is why the Ethernet collision domain is said to terminate at the switch port. That is, a two-port switch will effectively break a network into two distinct data links or segments (also called *collision domains*). Since all Ethernet nodes are able to recognize the occurrence of a collision, and since the detection of a collision is principal to the way Ethernet arbitrates media access, large domains containing many nodes can become cumbersome.

Thus, using an Ethernet switch to subdivide a large network into separate collision domains will certainly help to increase throughput. Each port of a switch forwards data to another port based on the MAC address contained in the received data packet/frame. In order to know which port to forward a data packet to, the switch will learn and store the MAC addresses of every device it is connected to, along with the associated port number (up to 1024 MAC addresses are stored in high speed SRAM). However, until the switch actually learns the switch port a particular MAC address resides at (after the first packet), it forwards this initial packet traffic to all ports. The switch will use the internal look-up table to quickly determine the location (port) of a node, establish a temporary connection between itself and the node, then terminate the connection once a packet is transferred. In this way, it increases network bandwidth and provides the network determinism required for critical control applications.

Most switches use a *store and forward* algorithm to process Ethernet frames. That is, it first stores the Ethernet frame and examines it for errors before forwarding it to its destination. Although in some case this method may seem to increase the forwarding time (latency) and possibly cause fragmentation, it can also effectively reduce the occurrence of error frames and improve overall throughput for most applications. This is particularly useful where there is heavy network traffic and or greater potential for noise and interference.

The optional hub/repeater mode of this switch provides low-latency network packet transmission that effectively reduces jitter on the network. Ethernet switches have higher inherent latency that varies with packet size due to their store-and-forward behavior. Thus, operation in switch mode adds latency and results in possible latency deviations up to 167us (jitter). In hub-mode, there is a maximum port-to-port latency of only 310ns with a total deviation of only 40ns. This is because hubs immediately repeat the bits arriving on one port at their other ports, rather than storing the entire message first before forwarding it as switches do. This sometimes makes them more useful for transmission of time-critical data, or for reducing latency where there is concentrated link traffic (like the main trunk of cascaded units).

We can also use the hub mode of this switch to implement media redundancy to this device. That is, if you connect the EtherStax to an external switch that happens to support media redundancy via a proprietary ring method, or the Spanning Tree Protocol (STP), or Rapid Spanning Tree Protocol (RSTP), then the EtherStax unit can be placed in "hub mode" and you can connect a cable to both ports. The external redundant switch will sense the redundant path and disable it temporarily. If the primary path should later fail, then the external switch can reactivate the other path, effectively providing media failover protection right to the unit.

Note that Acromag offers several industrial managed and unmanaged Ethernet switch models that can be used to interface to this product (please consult the factory or visit www.acromag.com).

Some examples of various types of network connections using Ethernet switches are included in the following pages.

CONNECTIONS

Network - Background

Hubs & Switches

The current tendency in critical industrial control applications is to connect one Ethernet device per switch port. This will produce the most deterministic mode of operation as the switch can operate full-duplex, with no chance of collisions. This ensures determinism, helping critical control applications to remain predictable and on-time.

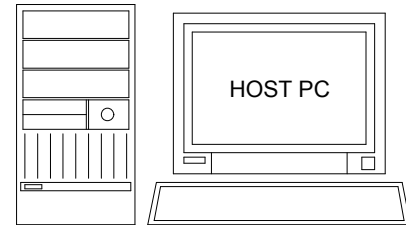
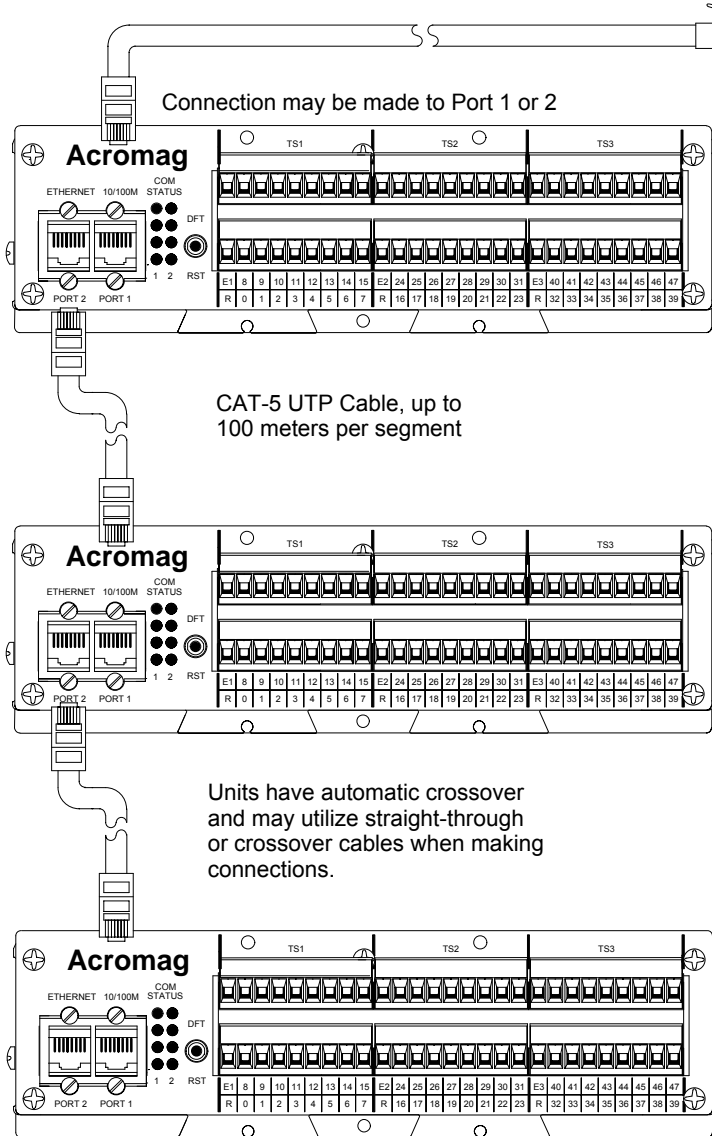
CONNECTIONS

Network

The unit includes two Ethernet ports for convenient cascaded network connections as shown here. This is also useful for extending the network as each segment may extend up to 100 meters.

Note that data collisions are still possible in the first two network segments shown below as these connections carry the data of more than one unit.

You can isolate each segment and prevent collisions using an external Ethernet switch connected as shown in the diagram of the following page (our recommended approach).



You can connect directly to a Host PC with a NIC installed, or via an Ethernet switch.

CASCADING UNITS

Connections may use Port 1 or Port 2.

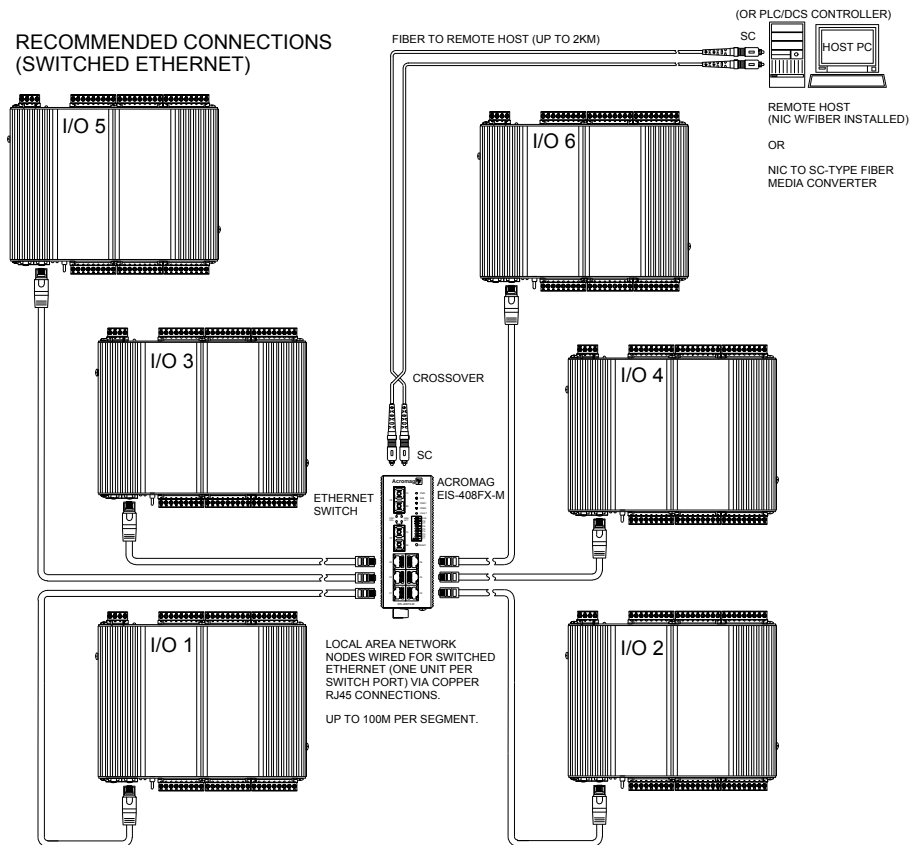
Limit cascaded connections to 4 segments.

Limit cable segments to 100M in length.

You may use straight-through or crossover cables.

TIP: You can significantly enhance the EMI/RFI performance of your network connections by using Category 5E STP cable (Shielded Twisted Pair) cable. The use of shielded cable is strongly recommended for installations in harsh industrial environments and/or in the presence of strong electrical fields. For more information on cable, refer to the Cables & Connectors section at the back of this manual.

You can use an Ethernet switch or switching hub to build a network of Etherstax units, similar to that shown below. The drawing depicts our recommended approach to distributing I/O via switched Ethernet—that is, one EtherStax unit is connected per switch port.



The drawing above shows how to network-connect EtherStax units to an 8-port Ethernet switch (such as Acromag Model EIS-408FX-M). Note that the I/O LAN is distributed locally using copper/RJ45 cable connections (up to 100M per segment), and then connected to a remote (distant) host using fiber cable. The copper connections may use standard or crossover cables, as both the EtherStax unit and the Ethernet switch include automatic crossover, but it is generally not considered good practice to use crossover cables when connecting to an auto-crossing switch.

The switch shown above could be eliminated, if you were connecting to an EtherStax Model ES2151/2152-1000, which includes one fiber port and one standard RJ45 port. For example, you could use the fiber port built into the EtherStax to connect to the distant host using fiber, then add an additional EtherStax locally via its RJ45 port, similar to that shown on the next page. However, the traffic of both units would still be concentrated in the main trunk from the host, and this does not follow the key principle of switched Ethernet, which seeks to suppress CSMA/CD and prevent data collisions by connecting only one device per switch port.

CONNECTIONS

Network

The drawing at left gives our recommended approach to making network connections to the EtherStax via switched Ethernet.

Here we show one EtherStax unit connected per switch port. Thus, each segment is limited to the traffic of only one device and no collisions are possible. This provides the most deterministic method of network communication. Only the segment between the host and the switch carries the traffic of multiple units and collisions are still possible in this segment.

Note that fiber connections must be crossed over, as the auto-crossing feature only applies to copper connections.

OBSERVATION: *The extra copper port of any of these units can optionally connect to other network devices, but the resulting concentrated traffic in the upstream network segment would violate the goal of switched Ethernet which is to limit the traffic on each segment to the traffic of one device, thereby suppressing CSMA/CD. This is generally not a problem for a small number of cascaded units.*

For many cascaded nodes, it is good practice for the upstream network segment to use a data rate that is 10x the data rate of the downstream nodes, otherwise careful attention must be paid to limiting the number of Ethernet devices that traffic on this segment.

CONNECTIONS

Network

The drawing at right gives an alternate method for connecting to a remote host, while still retaining the benefit of switched Ethernet between two nodes.

This still adheres to the principles of switched Ethernet because the network ports of the EtherStax are provided by an internal 3-port Ethernet switch.

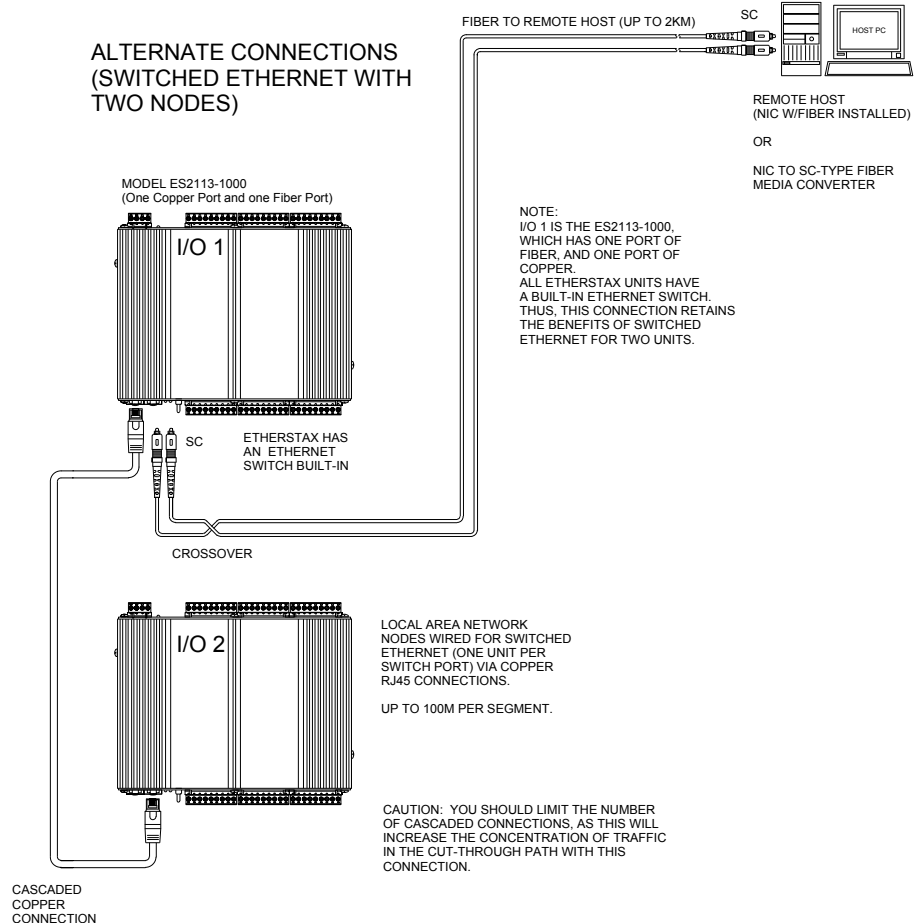
However, note that the fiber connection carries the traffic of both units and this segment is subject to data collisions.

Likewise, if you add a third unit, I/O3, cascaded from I/O 2, then the first copper segment carries the burden of the traffic of I/O 2 and I/O 3. This would not promote the benefit of switched Ethernet in this segment where we attempt to limit the traffic on a switched segment to a single device and suppress the need for CSMA/CD.

WARNING: Never use the EtherStax as a bridge device connected in series within your enterprise LAN, as the effect of concentrated traffic in the shared segment could inhibit communication to/from your EtherStax unit.

Ideally, the shared segment link that carries the concentrated 100Mbps traffic should operate at a higher data rate, like 1Gbps, which this switch does not support

Unfortunately, you cannot avoid concentrated traffic in the main host segment, even with a switch. As such, this aggregate path is usually chosen to operate at a higher data rate than the downstream segments. With the traffic of many Ethernet devices, this would mean that if the main trunk runs at 100MB, then the downstream nodes should operate at 10MB (or 100MB if the main trunk was 1Gbps, which the switch of the EtherStax units does not support). If you cannot easily increase the bandwidth of this segment, then you should be careful to limit the traffic in this shared segment by limiting the total number of Ethernet devices connected downstream.



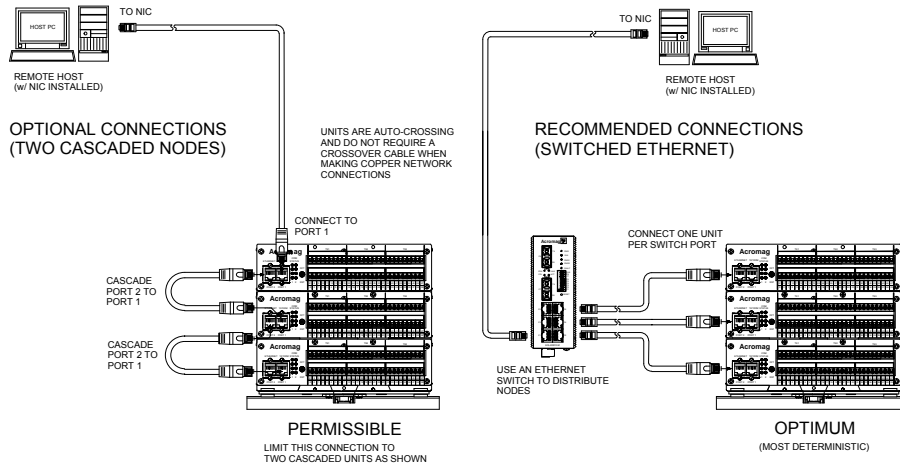
For optimum performance, the ideal recommended approach is to utilize an external Ethernet switch and connect no more than one Ethernet device per switch port—this is what is referred to as switched Ethernet. Connection in this manner avoids the negative effects of concentrated traffic and suppresses the need for collision detection. This effectively allows a segment to operate in full-duplex at the fastest possible speed. Thus, the throughput of 100M at half-duplex, can effectively approach 200MB when operating at full-duplex for switched Ethernet.

Although the connection shown above still retains the benefit of switched Ethernet without utilizing an external switch, it really only applies to the first two nodes. If you wanted to connect more than two nodes, but still retain the benefits of switched Ethernet, you would have to utilize an external Ethernet switch and connect one EtherStax unit per switch port.

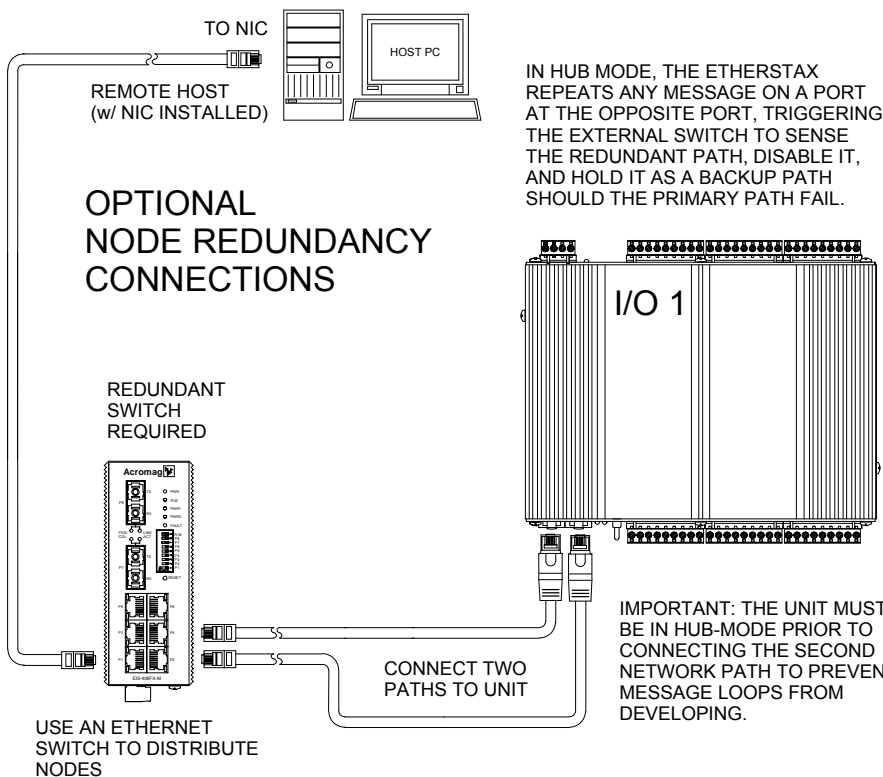
Although it may be possible to cascade more than two additional EtherStax units (3 units), it is recommended that the number of units connected in this fashion be limited to 3 total (the recommended physical height of a single stack of EtherStax units), as shown in the following drawing.

CONNECTIONS

Network



Again, for the most deterministic approach, utilize an Ethernet switch and distribute connections as one Ethernet device per switch port as shown in the above right drawing.



Network – Redundant Media Connections (Optional)

Recommended for High-Reliability Applications

When the EtherStax network port is placed in hub/ repeater mode, it can support media redundancy right to the node if connected to a redundant switch as shown at left.

Note: The EtherStax fiber port does not operate in hub mode and cannot be used in redundant path applications.

CONNECTIONS

Network -

Redundant Media Connections (Optional)

Recommended for High-Reliability Applications

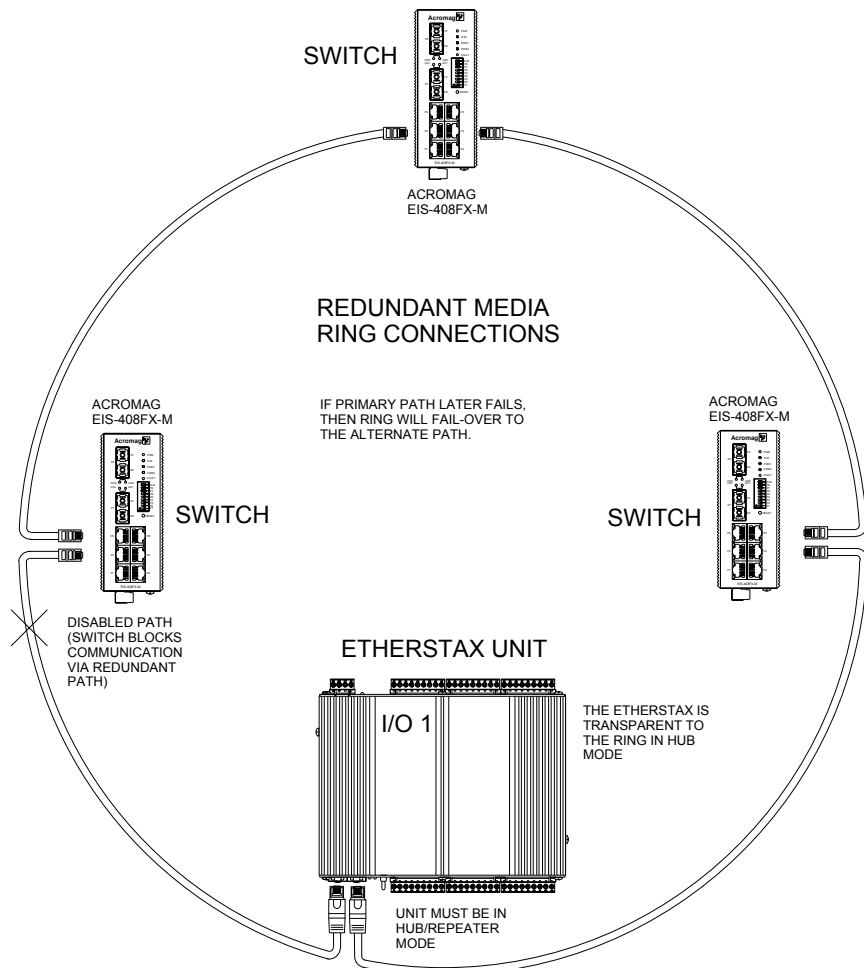
TIP: If you connect to both ports of this device at the same time from your network, and you notice that the unit is cyclically resetting itself, then this may indicate that your network connections or external network switch(es) are not already setup to handle redundant media connections. Do not connect to both network ports unless your network redundancy status has already been established. Otherwise, message loops may develop that could cause the unit to periodically reset itself.

Failure to place the EtherStax unit in hub/repeater mode for redundant media connections may prevent the external network switch from detecting the redundant path causing unpredictable results.

Note that all units operate at 100Mbps and half-duplex in hub/repeater mode.

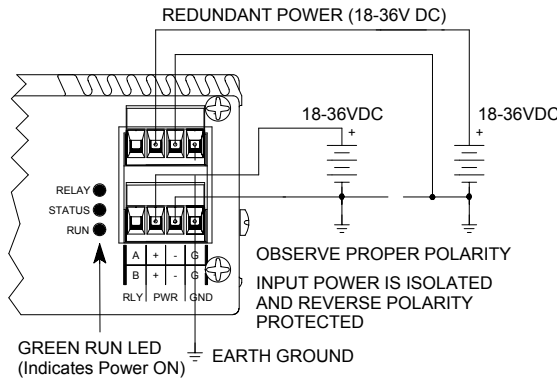
This device has dual Ethernet ports provided by a built-in Ethernet switch. Normally the second port connection provides a convenient cascaded network connection, or is used for extending the network, or to provide media conversion. But this integrated switch also has an alternate operating mode as a hub/repeater. As a hub, anything sent in one port is immediately transmitted out at the other port. This adds much lower latency. Additionally in this mode, if you connect both ports of this device to a redundant switch, or redundant switch network, the external switch will sense the redundant path and automatically disable the second path to this device, holding it as a backup in case of primary path failure. This behavior is completely managed by the redundant switch, making the EtherStax compatible with current proprietary media redundant ring methods, Spanning Tree (STP), or Rapid Spanning Tree (RSTP), but limited to half-duplex operation (hubs are half-duplex devices).

The figure below depicts the EtherStax unit connected to a redundant switch media ring. Here we use an Acromag EIS408FX-M switch to build the ring which supports redundant ring. The EtherStax unit must be placed in hub/repeater mode prior to making these connections. Connected this way, the EtherStax looks just like an Ethernet hub to the ring and operates transparent to the media ring. The redundant path fail-over and recovery are managed entirely by the external switch.



- ✓ Connect 18-36V DC to the power terminals PWR + and PWR – and observe proper polarity. Optionally connect redundant backup power to the second set of terminals. For supply connections, use No. 14 AWG wires rated for at least 75°C. **CAUTION:** Do not Exceed 36VDC peak.

DC POWER CONNECTIONS



Each of the power inputs is series diode-coupled, providing reverse polarity protection and allowing external redundant drive. With redundant power connections, the higher connected voltage will carry the load, and if that supply later fails, the lower voltage supply will carry the load. You cannot use the second set of power terminals to cascade power to other units because of this diode.

CAUTION: Risk of Electric Shock – More than one disconnect switch may be required to de-energize equipment before servicing.

NOTE – External Fuse: If unit is powered from a supply capable of delivering more than 3A to the unit, it is recommended that current to the unit be externally limited via a high surge tolerant fuse rated for a maximum current of 4A or less (for example, see Bel Fuse MJS series).

- ✓ Connect Earth Ground as shown in the connection drawings for best results. Additionally, connect the unit's GND terminal (G) to earth ground as shown above. Alternately, you may utilize the earth ground screw at each side of the enclosure to complete the earth ground path.

In some cases, additional earth grounding is recommended at your I/O (see Analog Input connections). If input sensors are already grounded, use caution and avoid adding ground connections which could create ground loops.

The enclosure is common to the ground terminals and isolated from the other circuits. Transient energy is shunted to this ground via isolation capacitors and transient voltage suppressors. You must connect earth ground to complete this path and ensure protection. Additional earth grounds may be recommended at the analog common leads (see connection drawings).

Power

Input Power ES215x-0

Voltage	Current
18VDC	263mA Max
24VDC	195mA Max
36VDC	132mA Max

Input Power ES215x-1

Voltage	Current
18VDC	322mA Max
24VDC	240mA Max
36VDC	162mA Max

Above is maximum power with alarm relay energized.

As a rule, your supply should be capable of providing at least twice the maximum current draw of the unit (for inrush). Your series fuse should also be minimum rated for greater than twice this current also.

Earth Ground

Warning: *To comply with safety and performance standards, use shielded cable and connect earth ground as noted. Failure to use good wiring and grounding practices may be unsafe and harm performance.*

Alarm Relay

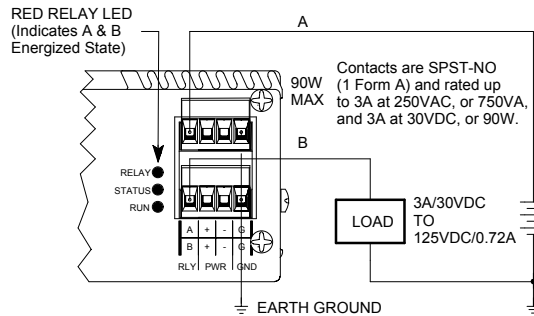
The relay LED indicates the energized state (ON) of these SPST contacts.

You can configure these contacts as failsafe or non-failsafe.

If you select failsafe contacts, then you can also use this relay to signal a power failure (contacts de-energize when power fails).

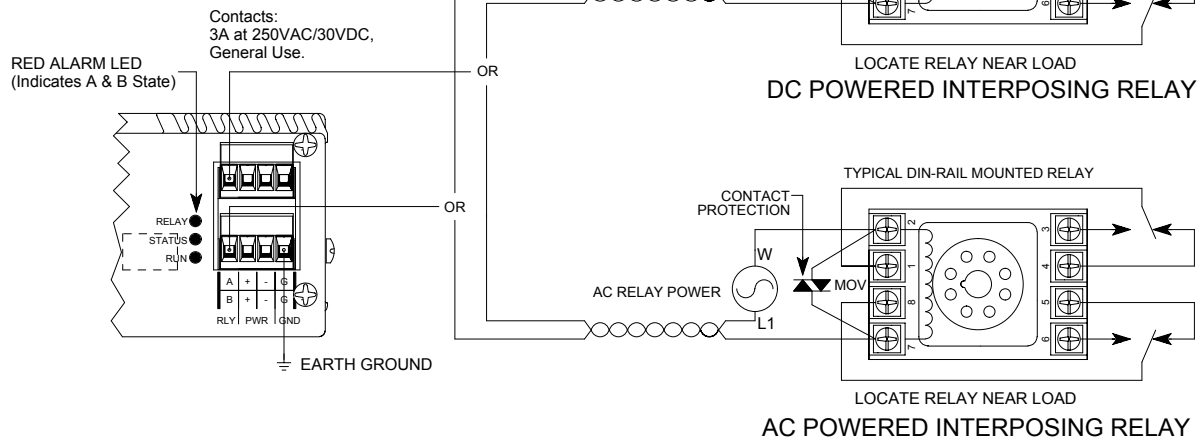
- ✓ The local alarm relay contacts are located adjacent to power and are labeled A & B. These contacts are switched for conditions of media failure (link loss), watchdog timeout, or power failure (failsafe only). Contacts are normally open type (de-energized), but are configurable as failsafe (normally energized), or non-failsafe (normally de-energized).

ALARM RELAY CONNECTIONS - DC LOAD



The alarm contacts are rated for general use at 3A and 30VDC/240VAC, 1/8HP at 250VAC/125VAC, and C300 Pilot Duty. For control of higher energy devices, an interposing relay may be used and is connected similar to the following:

INTERPOSING RELAY CONNECTIONS



When switching inductive loads it is good practice to have a contact rating that is 2-3x the steady-state requirements of the load.

IMPORTANT: External contact protection is required for use with inductive loads. Failure to use adequate protection may reduce the life of the contacts or damage the unit.

For AC inductive loads, use AC-rated capacitors, Metal-Oxide Varistors, or RC- snubbers to help shunt potentially high-reverse voltage transients that develop across inductive loads when the output is switched OFF. For DC inductive loads, use a reverse-biased diode to shunt this reverse voltage from the contacts as shown above. This will help protect the relay contacts and extend their life when switching inductive loads.

- ✓ Connect analog current and voltage signals to the correct input terminals as shown below. Each input terminal block has 8 channels of voltage input (top terminals), and 8 channels of current input (bottom terminals), and include a common return (C). Input common should also be connected to earth ground.

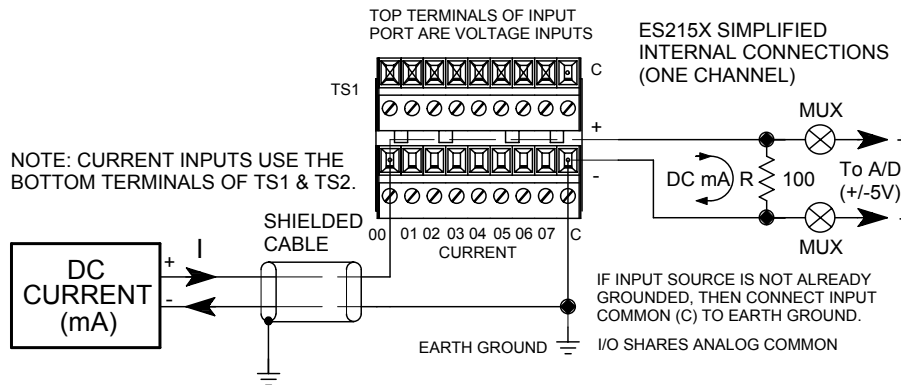
Current Inputs

Input current ($\pm 20\text{mA}$) is converted to voltage ($\pm 2.0\text{V}$) via precision 100Ω shunt resistors at each input. Internally, this drives a 16-bit A/D with a full dynamic input range of $\pm 5\text{V}$ ($\pm 20\text{mA}$), or $0-5\text{V}$ ($0-20\text{mA}/4-20\text{mA}$). We recommend that you utilize the unipolar current input ranges for a lower noise response.

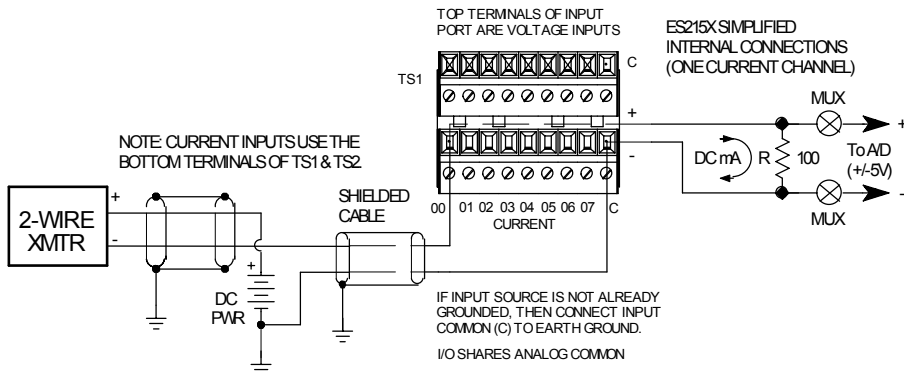
CONNECTIONS

Analog Inputs

The C terminal is connected to analog common of the I/O circuitry. Do not allow input channels to float. You should connect C to earth ground if your signal source is not already earth grounded.



Note that current inputs are independent of the 8B voltage inputs at X1 and may intermix freely with any 8B analog input modules connected via this interface.



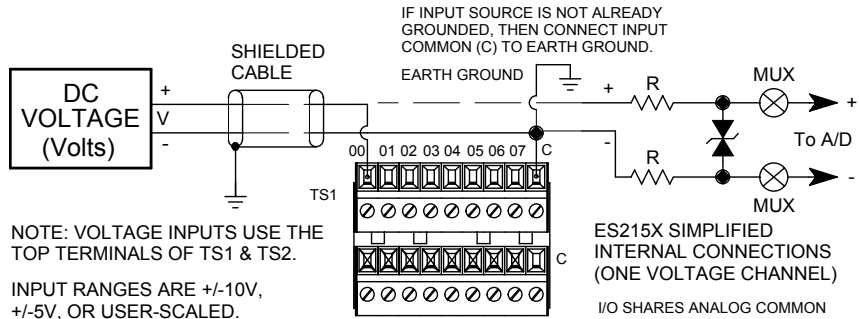
CONNECTIONS

Analog Inputs

Voltage Inputs

This unit can be set up to accept fixed voltage input ranges of ±10V or ±5V (16-bits). You can optionally rescale a portion of these native A/D ranges to smaller sub-ranges, down to a minimum 1/16th of the native span (or 0.625V in order to maintain minimum 12-bit performance).

Connect your input voltage between the input terminal and port common (C). Input is bipolar voltage. If your input source is not grounded, be sure to also connect analog common (C) to earth ground.



The voltage inputs terminals are in parallel with the 8B inputs at X1. Thus, you cannot drive voltage signals to both the voltage inputs of the unit and the 8B input interface. However, you can make use of the input terminals for monitoring the output signals of connected 8B modules.

Analog Outputs

ES2151 EXCITATION	LOAD RANGE
9V	0-265Ω
10V	0-312Ω
11V	0-358Ω
12V	0-405Ω
13V	0-451Ω
14V	0-498Ω
15V	0-545Ω
16V	0-590Ω
24V ¹	0-600Ω ¹

¹ With 24V excitation, the maximum operating ambient must derate to +35°C (-x000 enclosed models), or +40°C (-x010 open models).

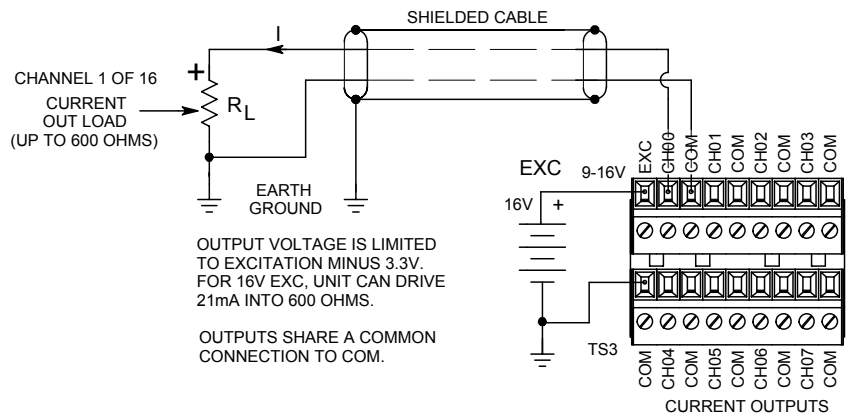
² The maximum load resistance is calculated from the excitation voltage as follows:

$$R_{load_max} = (V_{exc} - 3.3)/0.0215$$

Current Outputs (ES2151 Only, Requires Additional Excitation)

The ES2151 model has 16 channels of 0-20mA/4-20mA outputs. You must connect external excitation to operate the current outputs (ES2151 models only). You load resistance range will depend on your excitation voltage level (see table at left). Outputs include current limiting. Outputs are not isolated channel-to-channel and share a common connection.

CURRENT OUTPUT CONNECTIONS (ES2151 ONLY)



Note that in order to operate the current outputs of the ES2151, you must provide an excitation supply as shown above. The voltage outputs of the ES2152 do not require additional excitation.

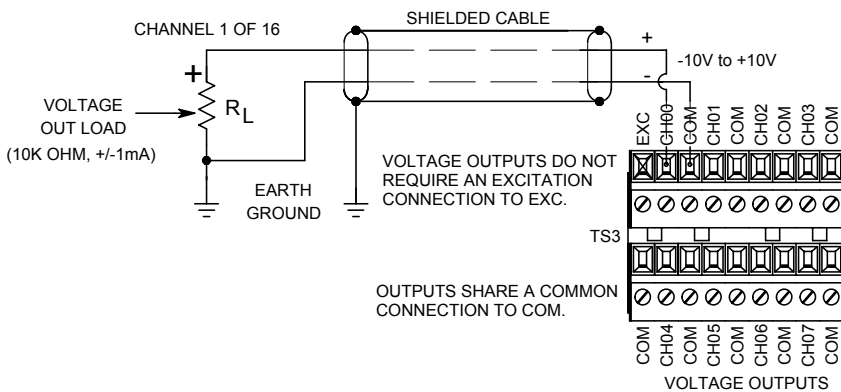
Voltage Outputs (ES2152, No Excitation Required)

This ES2152 model has 16 channels of ±10V outputs (16-bits). External excitation is not required to operate the voltage outputs. Outputs are not isolated channel-to-channel and share a common connection. Voltage outputs deliver up to ±10V into 10KΩ or greater (±1mA).

CONNECTIONS

Analog Outputs

VOLTAGE OUTPUT CONNECTIONS (ES2152 ONLY)



8B Inputs (ES2151 & ES2152) & 8B Outputs (ES2152 Only)

This unit can optionally connect its voltage inputs to an 8BP04, 8BP08, or 8BP16 carrier panel via the DB25 interface connector X1. ES2152 units can optionally drive its voltage outputs to an 8BP04, 8BP08, or 8BP16 carrier panel via DB25 interface connector X2. You cannot intermix 8B inputs and 8B outputs on the same carrier panel and you must respect that 8B input carriers connect via X1, while 8B output carriers connect via X2.

Series 8B Analog I/O

CAUTION: You cannot connect to 8B signals and to field inputs on the unit at the same time, or signals will be in contention and damage to the unit may result. Disconnect the 8B interface when you wish to drive the corresponding field inputs on the unit, unless you are being very careful to only connect to field inputs that are not being consumed by modules installed on the 8B panel.

Use an SCMCA006-01, -02, or -07 interface cable to connect an 8BP04, 8BP08, or 8BP16 back-panel to X1. This is simply a DB25 male to DB25 female cable assembly.

8B I/O modules will displace the corresponding I/O channel of the unit when connected. This means that you cannot connect an input signal to both the voltage input terminals of the unit and via an 8B back-panel, or signal contention may damage the unit. Likewise, you cannot wire the output channel of the unit while also driving an 8B output module on a connected carrier.

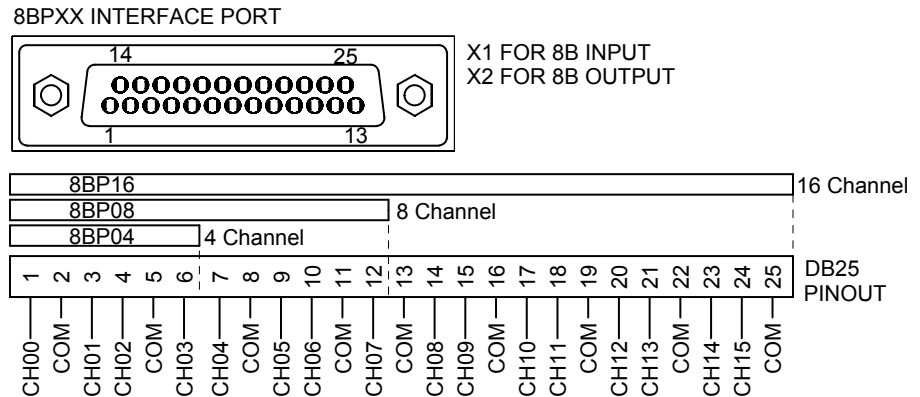
Note that the current inputs of this unit operate on an independent input path and may intermix with 8B inputs connected via an attached back-panel at X1. The X1 interface is intended for input modules only. The X2 interface of ES2152 models is for support of 8B output modules.

Note that separate power is connected to the back-panel. Input modules drive 0-5V, 1-5V, and ±5V to the voltage inputs, according to their model number. You cannot intermix 8B output modules with 8B input modules on the same back-panel.

CONNECTIONS

Series 8B Analog I/O

Industry-standard 8B signal conditioning modules mount on 16, 8, or 4 channel back-panels and are connected via the DB25 interface connectors X1 and X2. It's always the first channels of a port that are reserved for 8B if mixing I/O on the unit with I/O on a connected carrier, as shown in the following diagram.



IMPORTANT: 8B Inputs connect to X1 (ES2151 or ES2152), 8B outputs connect to X2 (ES2152). You cannot mix 8B I/O on the same carrier panel.

Ground the analog common terminal (C) of any port, even if connecting to an 8B carrier at X1 or X2. 8B modules are individually isolated and any ground connections made on the 8B I/O side of the carrier does not carry over to the analog I/O common of the port, and port common must be grounded.

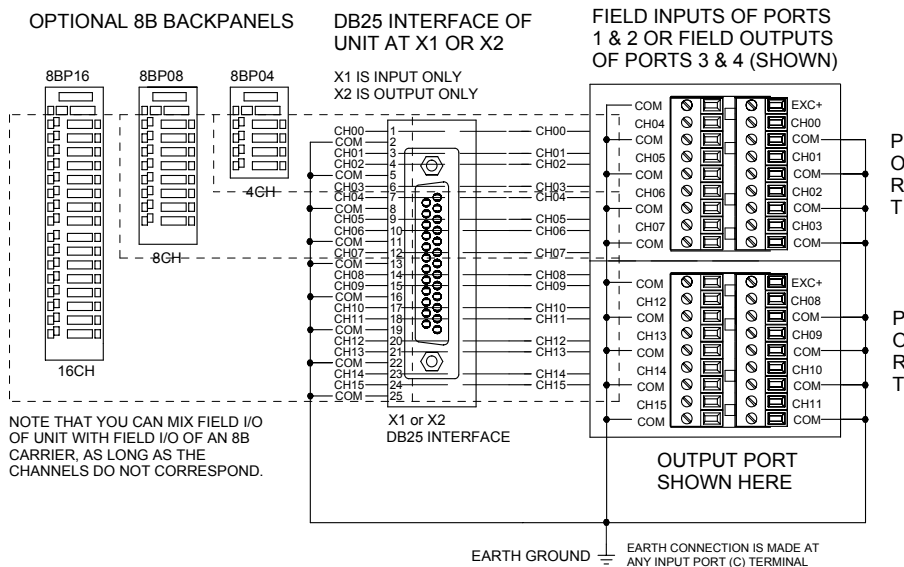
Unused 8B Input Channels (No Module Installed): It is recommended the output side of any unused 8B input channels be shorted on the 8B back-panel to keep the 8B input channel from floating. If you are intermixing field inputs of this unit with 8B inputs on a panel connected to X1, then do not do this for the 8B input channels that correspond to the field channels of the unit that are being driven by field signals.

Unused 8B Input Channels (Module Installed): It is recommended that the input side of any unused 8B input module be shorted on the 8B back-panel to keep the inputs from floating.

Unused 8B Output Channels (Module Installed or Not): Do not short the input or output side of these channels.

Input Range Selection (8B Inputs): You can select $\pm 5V$ or $\pm 10V$ for the 8B channel. However, with the corresponding input set to the $\pm 5V$ A/D range for the channel, the unit does not support any over-range capability of the 8B module and may not convert the exact $+5V$ or $-5V$ endpoints. Setting the input to the $\pm 10V$ input range will allow you to capture any over-range capability of the 8B module, but the effective resolution will be reduced by one half.

Channels of an 8B back-panel are mapped to inputs or outputs of the unit per the following diagram:



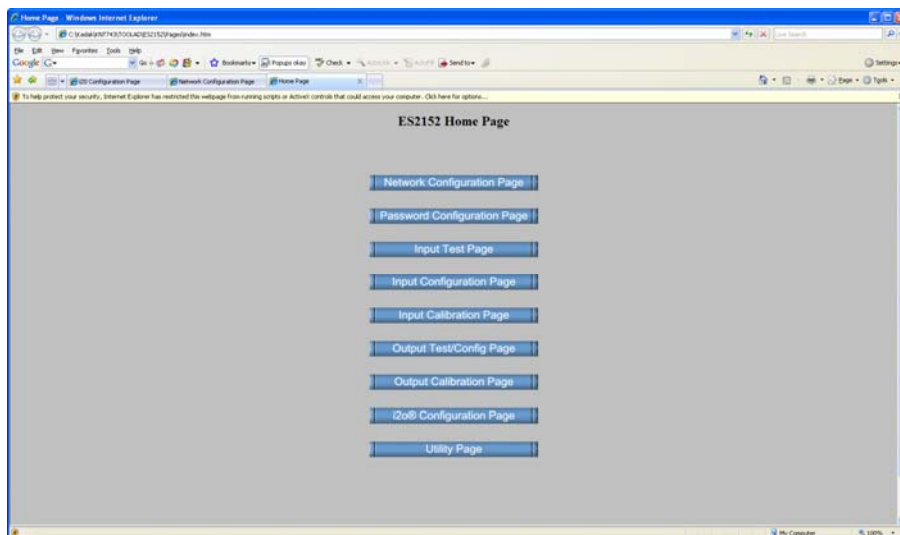
CONNECTIONS

Series 8B Analog I/O

This unit supports Modbus over TCP/IP and UDP/IP (a TCP/IP message will get a TCP/IP response, while a UDP/IP message will get a UDP/IP response). You may use your own software to issue Modbus commands to this device (see Modbus Registers), or you may use a standard web browser, as these units have built-in web pages that allow you to setup and control their operation. Simply execute your web browser, type the IP address assigned to your unit in the “Address” window (<http://128.1.1.100/> for our example), click [Go], and you will be presented with the Home Page window of the unit similar to that shown below:

WEB BROWSER

Home Page



The Home Page provides buttons to access the other web pages of this unit that are used to configure the network parameters, change the user name and password, configure the inputs, calibrate, and operate the unit.

Note that the unit's serial number and firmware number are included at the bottom of the Home Page for reference.

Not all parameters are programmable with Modbus commands issued to Modbus registers. In general, you would still have to use the embedded web pages to complete your configuration (for example, the network configuration parameters do not have Modbus registers). For additional details on various operating modes, please refer to the command descriptions of the Modbus Memory Map.

WEB BROWSER

Password Configuration Page

NOTE: Your password is limited to 10 characters. If you exceed 10 characters, your password will only be set to the first 10 characters you typed. This will become painfully apparent to you when you attempt to gain access later.

IMPORTANT: If you forget your user name & password, you can always toggle the unit into default mode via the DFT toggle switch at the front of the unit (hold this toggle 4 seconds to invoke default mode). In this mode, the password and username will revert to the original defaults-- a default IP address of 128.1.1.100, a username "User", and a password "password". This allows you to re-invoke the Password Configuration Page and change the username and password as required, to something you can remember.

TIP: If you do not want to bother with remembering a username and password to access a station, you can submit it blank. Then when it is queried for later, just leave the query fields blank and click OK to gain access.

For each new browser session that accesses the Home Page of this unit, you will be presented with a window prompting you to enter the current User Name and Password as shown at left. This information is required before the program will allow you to make any other selections. **The default user name and password is "User" and "password" respectively.** After entering these defaults, you should invoke the Password Configuration Page to change these parameters to something more meaningful for you.

Use up to 20 alphanumeric characters (case sensitive) to specify your username, and 10 alphanumeric characters (case sensitive) to specify a password. You will have to type these entries twice to help prevent errors.

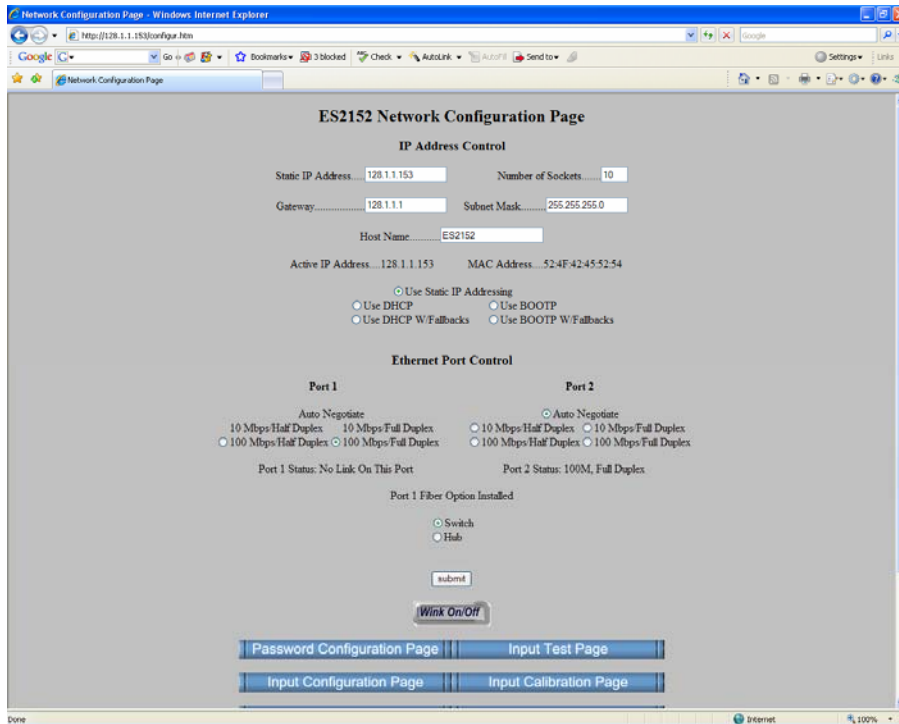
Click the **submit** button to write your changes to the unit.

After submitting your username & password changes, you will be prompted to enter your new username and password before being permitted further access to the web pages. Just be sure to use the new values you just set. If you already forgot your settings, then place the unit in the default mode and use the default username and password to access this page again and reset your assignments to something you can remember.

After setting your username and password, you can click the “Network Configuration Page” button and a screen similar to that shown below will appear. Use this screen to set the network configuration parameters for your unit (these parameters cannot be set via Modbus registers). Parameters are described below. You may have to consult your network administrator for help in completing the contents of this page.

WEB BROWSER

Network Configuration Page



WEB BROWSER

Network Configuration Page

Note that Acromag Series ES2100 Ethernet I/O devices may take from 3 to 30 seconds to boot upon power-up, depending on your network configuration and whether a DHCP server is present.

Note that the Number of Sockets limitation does not restrict access via Modbus UDP/IP, only TCP/IP, as UDP is a connectionless protocol.

Static IP Address is as the name implies—*static*, and represents a unique fixed IP Address that is generally assigned by your service provider or system administrator. The Default Mode static IP address assigned to this unit is 128.1.1.100 (refer to the product side label).

An IP Address is a unique identification number for any host (this unit) on any TCP/IP network (including the internet). The IP address is made up of four octets (8 bits), each octet having a value between 0-255 (00H-FFH). It is expressed here in decimal form, with a period placed between octets.

Note: If you are in Default Mode when you change the IP address noted, then when you click submit, your unit will leave the default mode and will assume the new IP address. This will look like you lost communication with your web browser, but you simply need to change the web browser address to continue communicating with the unit.

Note: In order to network your PC with an EtherStax unit, you may have to consult with your network administrator and either temporarily change the IP address in your TCP/IP configuration (see TCP/IP Properties of Network Configuration in Windows), or create a separate private network using a second network adapter installed in your PC (recommended). This is because your IP address is likely set to an address that is outside of the address domain of the unit's default IP address. The necessary steps for setting up this interface address will vary with your operating system. Refer to Acromag Application Note 8500-734, or document 8500-815, for help accomplishing this (located on the CDROM shipped with your unit or via download from our web site at www.acromag.com).

Number of Sockets is the number (1-10) of Modbus TCP/IP access points to allow for this host via port 502 (a well-known port reserved for Modbus/SCADA applications). You can restrict access by reducing this number.

If this unit is an i2o target device (it has its outputs controlled by another unit's inputs), then each i2o message sent to this device will require a socket and the Number of Sockets must be increased to allow for this. For example, if each port is being written via i2o, then number of sockets must be set to at least 2, and setting it to 1 would prevent the second port from being written.

A socket is a software mechanism that connects an application to a network protocol (socket is a software object, not a physical object). For example, a Modbus application program can send and receive TCP/IP messages by opening a socket and reading and writing data to and from the socket.

On TCP/IP and UDP networks, a port is an endpoint to a logical connection (a connection port) and the way that a client program specifies a specific server program on a computer network.

For example, a Modbus program will open TCP port 502 to be readable from other Modbus devices on the network (which also use port 502 to establish a connection). A port may have more than one socket active at a time and this server device will allow up to 10 sockets to operate simultaneously over its contact port 502.

Gateway refers to the IP Address of the gateway, if your local area network happens to be isolated by a gateway. Typically, it is assigned the first host address in the subnet. If a gateway is not present, then this field should contain an unused address within the host subnet address range.

A gateway is a device which links dissimilar networks and transfers data between them at the application layer level. In this way, Gateways essentially convert messages from one protocol to another.

A **Subnet Mask** is used to subdivide the host portion of the IP address into two or more subnets. The subnet mask will flag the bits of the IP address that belong to the network address, and the remaining bits correspond to the host portion of the address. The unique subnet to which an IP address refers to is recovered by performing a bitwise AND operation between the IP address and the mask itself, with the result being the sub-network address.

The **Host Name** (up to 20 characters) is the name to be assigned to this host if its address happens to be assigned dynamically using DHCP.

A DNS Server refers to the Domain Name Server used on a network, and is the device that relates symbolic names to actual numeric IP addresses. The DHCP server is responsible for dynamically passing out IP addresses.

The **Active IP Address** refers to the current IP Address being used by this host, as opposed to any new assignments being made via this page.

The **MAC Address** refers to the Media Access Control address that uniquely identifies the MAC hardware of this device. All Ethernet devices have their own MAC address. This is a unique fixed address that was assigned to the MAC at its manufacture. It is not to be confused with the dynamically assigned 32-bit IP Address, commonly denoted as four 8-bit numbers separated by periods (e.g. 128.1.1.100). Every manufacturer producing Ethernet hardware, has by assignment, a series of 48-bit addresses to use. They are restricted to use only the addresses in their series, and only one time, thus ensuring that no two computers in the world will ever have the same network address.

An Ethernet packet will include two 48-bit address fields appended to it that represent the MAC address of the sending computer, and the destination computer. In IEEE 802 networks, the Data Link Control (DLC) layer of the OSI Reference Model is divided into two sub-layers: the Logical Link Control (LLC) layer, and the Media Access Control (MAC) layer. It is the MAC layer that interfaces directly with the network media and where this address distinction is applied (each different type of network media requires a different MAC layer).

Use Static IP Addressing tells this unit to fix the IP address setting to the Static IP Address specified. By default, the unit is set to use Static IP Addressing and a Static IP Address of 128.1.1.100.

You can optionally choose to have the IP address assigned dynamically via DHCP/BOOTP, or DHCP/BOOTP w/Fallbacks. This will also require that you specify a valid Host Name (see above).

If this unit is an i2o target device, you must have a static IP address in order to address it via i2o.

Use DHCP tells the unit its IP address is to be obtained dynamically, and may change each time this device is connected to the network.

Use DHCP w/ Fallbacks works the same way, but will revert to the static IP address specified if your DHCP server cannot be found.

WEB BROWSER

Network Configuration Page

This device can be placed into a default communication mode via the DFT toggle switch at the front of the unit.

Default Mode uses a static IP address of "128.1.1.100", a default subnet mask of "255.255.255.0", a default username "User", and a default password "password".

WEB BROWSER

Network Configuration Page

If you are utilizing Hub mode, then speed/duplex will be forced to 100Mbps/half-duplex only.

The Default Communication Mode uses a static IP address of "128.1.1.100", a subnet mask of "255.255.255.0", a default username of "User", and a default password of "password".

Note: *Hub/repeater mode is 100MB at half-duplex only and auto-negotiation does not apply in hub mode.*

Important (ES2xxx-1 units): *Fiber ports are forced to 100MB and auto-negotiation and auto-crossing do not apply. Half or full duplex may still be selected, but operation is restricted to half duplex only in repeater mode.*

DHCP refers to Dynamic Host Configuration Protocol and its routine for assigning dynamic IP addresses to devices on a network. With dynamic addressing, a device can have a different IP address every time it connects to the network, and in some systems, the IP address can even change while it is still connected.

Use BootP tells the unit its IP address is to be obtained from a BootP server.

Use BootP w/ Fallbacks works the same way, but will revert to the static IP address specified if your BootP server cannot be found.

BootP refers to the Bootstrap Protocol which is an internet protocol that enables a diskless workstation to discover its own IP address, the address of a BOOTP server on the network, and a file to be loaded into memory to boot the machine. This enables the workstation or device server to boot without requiring a hard or floppy disk drive. BOOTP works similar to DHCP, but is usually found in older systems. This protocol is defined by RFC 951.

Ethernet Port Controls, Port 1 and Port 2

These controls allow you to select that speed and duplex be auto-negotiated (recommended), or you may force the speed to 10Mbps or 100Mbps, and the duplex to half or full. The existing port status is displayed just below these controls.

Note that full duplex communication will not be possible unless CSMA/CD is suppressed via a switched Ethernet connection at the port (i.e. only one other device is connected to this port). Connecting one unit per Ethernet switch port will constitute a switched Ethernet connection.

On units that have a fiber-optic SC type connector for port 1, only 100Mbps operation at full-duplex is possible. A message just below the port status will tell you if this unit has the port 1 fiber-optic option installed.

The unit includes a DFT mode toggle switch to cause the unit to assume a fixed default static IP address (128.1.1.100). This switch is at the front of the unit and used to toggle the unit into, or out of Default Mode. If the unit is already in default mode, then "Default Communications Mode" will be indicated at the bottom of this screen, and the unit's Status LED blinks.

Switch or Hub Selection

For mode control, this unit is set to "Switch" by default, but may optionally be set to "Hub". Hub mode is useful to reduce latency on the network, especially when cascading many devices, or to setup redundant media connections to this device, but is restricted to 100Mbps and half-duplex. You should review the information regarding hubs and switches in the Network Connections section of this manual for help discerning the difference between a switch and a hub, and for information on redundant media connections. Selecting Hub will force speed/duplex to 100Mbps/half-duplex.

Click the **Submit** button to complete any changes made on this page. Review the port status message to verify your port settings.

Click the **Wink On/Off** button to toggle the unit in/out of "wink" ID mode. In this mode, the unit's green RUN LED will blink to confirm identification and address setting.

This unit includes a special remote messaging functionality called i2o, for input-to-output communication. This allows an i2o source to send its input port data to eight contiguous output channels on another ES215x model. Additionally, you can even use i2o to map input port data to the output port of the same unit (for same signal type, voltage input to ES2152 voltage output, current input to ES2151 current output). The i2o Mapping page is shown below and used to specify the static IP addresses of the targeted remote unit(s). Use the scroll adjustment to view the entire page as shown. You can elect to send this units input port data to the target unit cyclically at the update rate specified, or upon a % change of Full-Scale Reading since the last update.

WEB BROWSER

i2o Configuration Page



i2o® Configuration Page

Port Number	% Span Change	Update Time(100ms)	Map To IP Address	Map To Holding Register	Mapping Method	Map To Internal Outputs
Port 1 Voltage	0.0 0.0	150 0	128.11.102 0.0.0.0	40351 0	<input type="radio"/> Unipolar to Unipolar <input type="radio"/> Bipolar to Unipolar <input type="radio"/> Bipolar to Bipolar <input type="radio"/> Unipolar to Bipolar	<input type="radio"/> NO <input type="radio"/> YES
Port 2 Voltage	0.0 0.0	0 0	0.0.0.0 0.0.0.0	0 0	<input type="radio"/> Unipolar to Unipolar <input type="radio"/> Bipolar to Unipolar <input type="radio"/> Bipolar to Bipolar <input type="radio"/> Unipolar to Bipolar	<input type="radio"/> NO <input type="radio"/> YES
Port 1 Current	0.0 0.0	0 0	0.0.0.0 0.0.0.0	0 0	<input type="radio"/> Unipolar to Unipolar <input type="radio"/> Bipolar to Unipolar <input type="radio"/> Bipolar to Bipolar <input type="radio"/> Unipolar to Bipolar	<input type="radio"/> NO <input type="radio"/> YES
Port 2 Current	0.0 0.0	0 0	0.0.0.0 0.0.0.0	0 0	<input type="radio"/> Unipolar to Unipolar <input type="radio"/> Bipolar to Unipolar <input type="radio"/> Bipolar to Bipolar <input type="radio"/> Unipolar to Bipolar	<input type="radio"/> NO <input type="radio"/> YES

Span = Full Scale Range End Point - Zero Range End Point, so the -10V to +10V range would have a 20V span.
 NOTE: Setting "% Span Change" and "Update Time" to 0 or setting "Map To IP Address" to 0.0.0.0, turns off I/O mapping for that port.
 NOTE: Turning on "Map To Internal Outputs" will cause any writes to those outputs to be ignored.
 NOTE: "Mapping Method" indicates how data is handled when mapped to an output.
 Selecting "Unipolar to Unipolar or Bipolar to Bipolar" will cause 0VDC/mA at the input to equal 0% at the output.
 Selecting "Bipolar to Unipolar" will cause 0VDC/mA at the input to equal 50% at the output.
 Selecting "Unipolar to Bipolar" will cause 0VDC/mA at the input to equal -100% at the output.

submit

Note: The i2o mapping feature may only be configured via the built-in web browser page as there are no Modbus registers for specifying these parameters.

While this unit supports Modbus TCP/IP and UDP/IP, i2o messages are only sent via Modbus TCP/IP.

The i2o messaging will consume one TCP/IP socket on the target device.

The i2o target device must have a static IP address in order to process i2o messages.

These units have 2 ports of 8 input channels of both current and voltage (32 total inputs), but only 2 ports of 8 output channels, current or voltage according to model number. Inputs are front-side connections, while outputs are back-side connections. Input Port 1 refers to voltage input channels 0-7, and current input channels 0-7. Input port 2 refers to voltage input channels 8-15 and current input channels 8-15. Output ports 1 and 2 refer to output channels 0-7, and 8-15. Outputs are current only for the ES2151, and voltage only for the ES2152.

You can map any input port to an output port of another ES215x or compatible target unit, or to separate units. However, you are not restricted to targeting an entire output port, but only eight contiguous output channels. Output registers of these models reside at the 16 addresses from 40351 to 40366. However, you must ensure that the target starting address on the remote unit is a block of eight contiguous output channel addresses. For example, if you are targeting eight output channels of another ES2151 or ES2152 unit, you would specify a starting address in the range 40351 (CH 0) to 40359 (CH 8). Addresses 40360-40366 are not valid because you need a block of 8 valid addresses.

WEB BROWSER

i2o Configuration Page

This messaging function works best if the target unit(s) are already online and ready to receive messages. It will still work if the target output units come online after the input units, but may take several minutes to "discover" the network targets and begin transmitting to them.

Likewise, if the input unit or the target unit(s) go offline, remote messaging will resume on its own when the connection is re-established, but this "healing" function may take several minutes depending on which device(s) went offline, why, and for how long.

Note that if you happen to perform the procedure for restoring a unit to its original configuration as outlined in the "Getting Out Of Trouble" section of this manual, all of the mapping variables are returned to their default values and mapping will have to be reconfigured.

This page also allows you to locally map input port 1 and/or input port 2 to output port 1 and/or output port 2 on the same unit (signals must be of same type). This is not i2o, but is similar enough to also include its configuration on the i2o page. One important difference is that if you map locally, you will not be able to manually write to any of those outputs being mapped to (writes are blocked), and writing to an output is also the method used to clear its watchdog timer timeout. Further, local mapping is restricted to using the same port and channel number as the input.

WARNING: The i2o mapping feature of this model maps an eight channel input port to eight contiguous output channels on another unit. You must specify the correct starting register address on your target unit such that you have addressed eight contiguous output channel registers, or it won't work and you may negatively affect the operation of your target output device.

Example i2o Target Output Devices Compatible With This Unit

ES2151: Up to 16 Current Output Channels, Registers 40351..40359

ES2152: Up to 16 Voltage Output Channels, Registers 40351..40359

ES2171: Up to 16 Voltage Output Channels, Registers 40351..40359

ES2172: Up to 16 Current Output Channels, Registers 40351..40359

Keep in mind that you are sending 8 channels of input at a time (one input port) to eight contiguous output channels starting at the addresses noted above. Remote channels do not have to be of the same type, and you can even send bipolar inputs to unipolar outputs.

The analog input data may be transferred cyclically, or upon exceeding a stated percent of input range span since the last update. However, if you select percent of span, you will still need to specify a cyclic update rate in order to keep the communication socket open and prevent a timeout if your %Span changes happen to occur at intervals greater than 90 seconds apart.

If you select a time of 0 with %Span specified, a default time value of 30 seconds will be used to ensure the connection remains open (i2o uses TCP/IP which is a connection-oriented protocol). You may want to make the update time longer to conserve network bandwidth while still preventing a timeout. If you disable %Span, then your output control messages will occur at every interval of your update time (for times greater than or equal to 0.5 seconds). Setting the update time to 0 disables cyclic messaging. Setting it to a time less than 0.5s may be less deterministic.

IMPORTANT: This module is designed to function as a Modbus TCP/IP slave/server. Normally, Modbus servers are not allowed to initiate messages on their own and may only respond to client/master requests. The i2o functionality of this unit is a special application that may cause confusion for some master/client devices linked to the same network. To avoid problems, other master devices on the network should be restricted from attempting to control i2o target devices. You can also make use of the "Number of Sockets" parameter to limit access to an i2o target device.

Note that i2o input ports map externally, and must always map to eight contiguous output channel registers on compatible target devices. Individual input ports may even be mapped to separate eight channel blocks on different units at different IP addresses.

Subsequent messages will be sent at a periodic rate specified via the update time. Note that the target output port channels may still be controlled independently, but their level will be overwritten by subsequent mapped i2o messages when this feature is enabled. To avoid problems, it is recommended that you do not attempt to separately control the mapped analog output ports directly.

% Span Change: Set the percent of input range span change since the last input update that when equaled or exceeded at any port channel, will trigger the input data of the port to be sent to the specified output port(s).

For example, if you set field to 10% of a $\pm 10V$ input range, then a change in signal of 2V or greater will trigger the i2o transfer.

Update Time (x100ms): Specify the time between messages as a multiple of 100ms from 1 to 900 (0.1-90 seconds). Specify 0 to turn cyclical i2o messaging OFF. If %Span is set to a non-zero value, a default time of 600 will be used (60 seconds) to keep the connection open. Times less than 5 (0.5s) will be less deterministic and are not recommended.

Map To IP Address: This is the Static IP Address of the target output device (another ES215x unit on the network). Either input port may be mapped to an output port, both to separate output ports at two different IP addresses, but never both to the same output port. Use "0.0.0.0" to turn this field back to its default (inactive) status.

Map To Holding Register: This is the starting register address for eight contiguous output channels in the holding register address space of your i2o target. For output channels of ES2151 and ES2152 models, you would type an address from 40351 to 40359 into this space.

Mapping Method: This selection essentially tells the firmware how to map 0 at the input to the output and only applies to remote i2o mapping, not local (same station) mapping. In some cases, the input signal range is bipolar and normalized to ± 30000 counts, while the output channel is unipolar and normalized to a 0-30000 count. For simplicity, all I/O ranges are normalized to these same values. But you can use i2o to map unipolar input ranges to bipolar output ranges, and visa versa. Selecting Uni-Uni or Bi-Bi will cause 0V/0mA at the input to equal 0% of the output range. Selecting Bi-Uni will cause 0V/0mA at the input to equal 50% of the output range. Selecting Uni-Bi will cause 0V/0mA at the input to equal -100% of the output range. Note that outputs of this unit are truncated to count limits near ± 30200 , but inputs can only achieve count limits near ± 30000 . The mapping method tells the firmware how to map the endpoints between the i2o input and the i2o target as follows.

i2o Input-to-Output	Ideal Endpoint Effect
Unipolar-to-Unipolar	Direct Unipolar Mapping: Maps 0-30000 (0-100%) at input to 0-30000 (0-100%) at output.
Bipolar-to-Bipolar	Direct Bipolar Mapping: Maps ± 30000 ($\pm 100\%$) at input to ± 30000 ($\pm 100\%$) at output.
Bipolar-to-Unipolar	Maps -30000 (-100%) to 0, 0 to 15000 (50%), and +30000 (+100%) to 30000 (+100%)
Unipolar-to-Bipolar	Maps 0 (0%) to -30000 (-100%), 15000 (50%) to 0, and +30000 (+100%) to +30000 (+100%).

Note that you can mismatch your selections relative to the I/O in order to truncate portions of your I/O range. For example, if I have a bipolar input and a unipolar target, and I select Unipolar to Unipolar, then the negative portion of my input (-30000 to 0, -100% to 0%) will map to 0, while the positive portion of my input maps directly to my output (0-30000, 0-100%).

WEB BROWSER

i2o Configuration Page

WEB BROWSER

i2o Configuration Page

Select "Bipolar" to map bipolar inputs to bipolar outputs, or select "Unipolar" to map -30000 (-100%) of the input to zero output (0%). If "Bipolar" is selected for an output that is unipolar, then the negative input range (-100% to 0) will map to zero at the output (truncated).

Likewise, if I select Unipolar-to-Bipolar for an input that is bipolar, then your negative input range may achieve unexpected/invalid output values. Be sure to make these selection properly to avoid unexpected results.

Map To Internal Outputs Selection (This is not i2o): This works similar to i2o, except locally (within the same unit) and will send this units input port to this unit's output port of the same type, channel and port number when "Yes" is selected. Note that this action still allows the same port inputs to control one or two remote output ports of another ES215x model.

IMPORTANT: If you map inputs to local outputs, then you will not be able to write to any of those outputs (direct writes are blocked). If those outputs include watchdog timers, this would also prevent you from clearing timeouts, except by system reset. So be careful in making this selection. Outputs on a remote unit can still be written directly even if they are i2o targets of another unit.

Also, if you map to internal outputs, the inputs will be immediately and continuously transmitted to the outputs on the same unit without a trigger, even if you provide a %Span Change and/or Update Time value. These triggers are only used for remote writes (true i2o).

Case Study – Example i2o Operation

CASE	% SPAN	UPDATE TIME	IP ADDRESS
Case 1	0	250	Non-Zero Address
Case 2	25	0	Non-Zero Address
Case 3	25	250	Non-Zero Address
Case 4	XX	XX	0.0.0.0 (Zero Address)
Case 5	0	0	Non-Zero Address

Results:

CASE 1: The current input value is sent via i2o every 25 seconds (250x100ms).

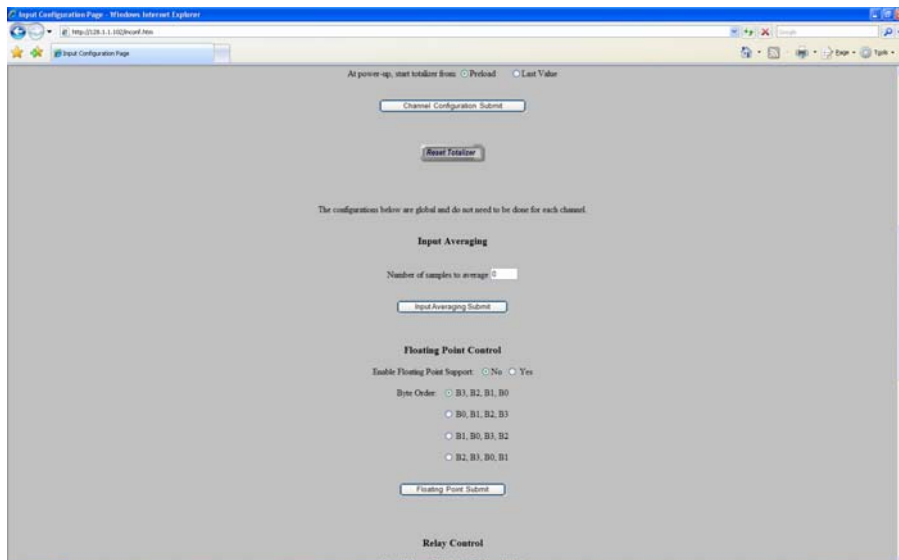
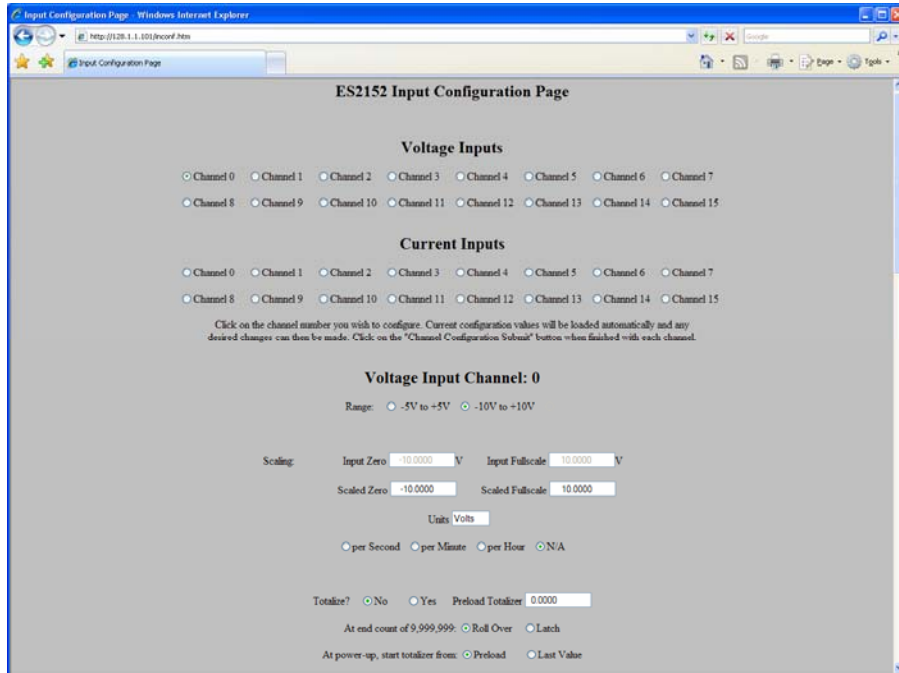
CASE 2: An i2o message is sent whenever the input changes by more than 25% of the input range span (i.e. 5V for a ±10V range). Additionally, the module will send a "heartbeat" message every 60 seconds to keep the receiving socket open and ready for input. This heartbeat value is the current %SPAN reference value (the last value sent if a new %SPAN value has not occurred).

CASE 3: An i2o message is sent whenever the input value changes by more than 25% of the input range span. Additionally, an i2o message containing the current input value will be sent every 25 seconds and the new reference value becomes the current input value.

CASE 4: No i2o messages will be sent.

CASE 5: No i2o messages will be sent.

After completing the username & password assignment, plus the network configuration parameters, you can use the Input Configuration page to setup your input channels, configure integration and totalization, configure the local alarm contacts, reset the totalizer channel, and even reset the unit. You can also enable or disable the output loop back feature (increases acquisition speed if disabled). The Input Configuration Page is shown below. Use the scroll bars on the right to view the entire page contents as follow:



WEB BROWSER

Input Configuration

Note: When you first enter a page that includes interactive controls like the submit buttons of this page, you may note that your first click on a control is ignored. This is because the first click only activates the control.

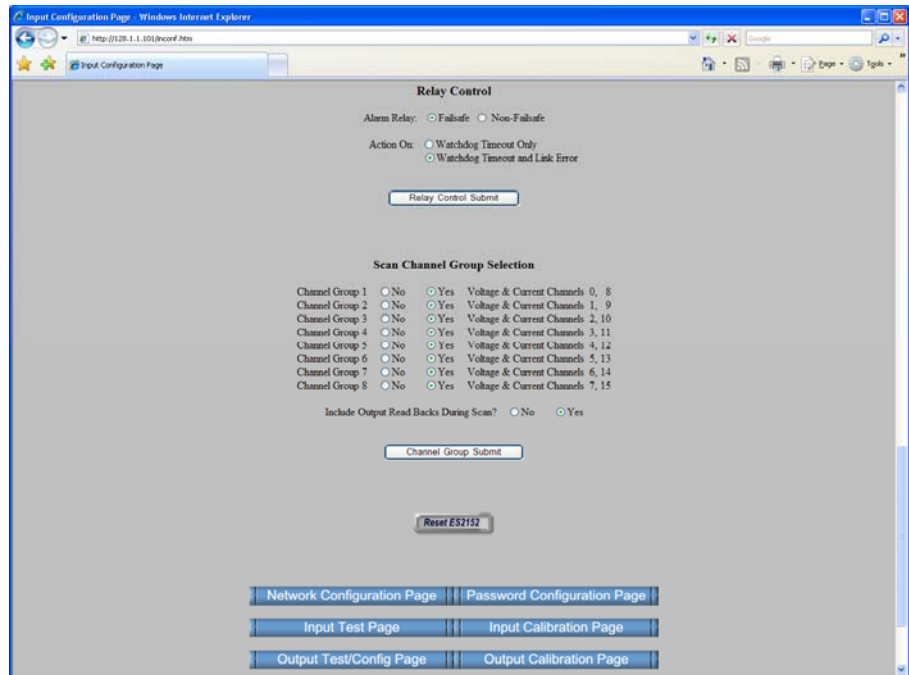
For voltage inputs, you can select from a native 16-bit A/D input range of $\pm 5V$, or $\pm 10V$. Each of these ranges can be rescaled to your own engineering units. However, your re-ranging must not divide the input span by more than 16 to maintain 12-bit minimum performance levels.

For current inputs, input ranges are sub-ranges of the native $\pm 5V$ A/D input range, or a 0-5V A/D input range. Inputs utilize a precision 100 Ω input shunt to convert input current to A/D voltage ($\pm 20mA$ into 100 Ω yields $\pm 2.0V$ to A/D). Any re-ranging must not divide the native A/D input current/voltage range by more than 16 to maintain minimum 12-bit performance levels.

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Input Configuration

Once you have carefully made your selections, click the "Submit" button to activate your configuration (reconfiguration takes effect immediately following "Submit").



The field inputs of these include two ports of eight single-ended input channels each, of both current and voltage, providing 16 single-ended field input channels for voltage (or 8B), and 16 single-ended field input channels for current. A DB25 input interface is provided for optional connection of up to sixteen 8B input modules mounted on a separate 8B carrier panel. Note that installed 8B input modules will displace the field voltage inputs of the unit, but do not affect the current inputs.

Input Configuration (Set Per Channel)

Simply select the channel to be configured, set its parameters, and click the Channel Configuration Submit button.

Channel Select (Top of Page): Click to bullet the voltage or current input channel you wish to configure. Input parameters are selected on a per-channel basis, one channel at a time, in any order. The current channel being configured is noted below the channel select buttons. When you are done setting up a channel, you must click the "Channel Configuration Submit" button (do this for each input channel).

Range (Per Channel): For voltage inputs, select a native 16-bit A/D input range of $\pm 10V$, or $\pm 5V$. For current inputs, you can select $\pm 20mA$, 0-20mA, or 4-20mA. Because a 100Ω shunt is used to convert input current to voltage, current ranges are sub-ranges of a native 16-bit A/D voltage range of $\pm 5V$ ($\pm 20mA$), or 0-5V (0-20mA/4-20mA). If you are setting up 8B inputs, selecting the $\pm 5V$ range would give you the greatest resolution, but might not convert any over-range of the 8B module. Selecting $\pm 10V$ would convert the entire range of the 8B input, but at half the resolution. If you are not utilizing all 16 voltage channels for 8B modules, then you can mix field voltage inputs on the unit with 8B inputs mounted on an attached 4 or 8 channel 8B carrier. The 8B interface always drives the first 4, 8, or 16 voltage channels of the unit.

Input Configuration (Set Per Channel)...continued

Input Zero (Fixed Per Range Selection): Default is same as input range zero and this cannot be changed (-20mA, 0mA, or 4mA for current inputs, -5V or -10V for voltage inputs). The voltage or current signal indicated will correspond to -100% of signal and a normalized count of -30000 (bipolar ranges), or 0% of signal and a normalized count of 0 (unipolar ranges).

Scaled Zero: For each input channel, enter the rescaled engineering units that is to correspond to -100% of input signal (bipolar), or 0% of input signal (unipolar). The default is generally the same as the input zero (-20mA, 0mA, 4mA, -5V, or -10V), but you can rescale this value to your own units as required for your application. Field is limited to a 32-bit number of up to 12 characters with a maximum of 4 decimal places. You must also Enable Floating Point Support to accomplish rescaling, but you do not have to also enable totalization.

Input Full-Scale (Fixed Per Range): Default is the same full-scale as the input range (+20mA, +5V, or +10V) and this cannot be changed. The full-scale indicated will correspond to +100% of input signal and a normalized count of +30000.

Scaled Full-Scale: For each input channel, enter the rescaled engineering units that is to correspond to +100% of input signal. Default is same as input full-scale (+20mA, +5V, or +10V), but you can rescale this value as required for your application. Field is limited to a 32-bit number with up to 12 characters with a maximum of 4 decimal places. You must also Enable Floating Point Support to accomplish rescaling, but you do not have to also enable totalization.

Note: By default, all input current ranges are sub-ranges of a larger 16-bit $\pm 5V$ or 0-5V A/D input range. The bipolar $\pm 20mA$ input range allows the input current to be measured in either direction between the terminals. The current inputs employ 100 Ω shunt resistors to convert the current signals to 2.0V full-scale (0.020A \times 100 Ω). This approximates 14.6-bit resolution with an A/D resolution of ± 13107 parts for $\pm 20mA$.

Units (5 characters): Enter the units of measure for your rescaled input zero and full-scale and this is an arbitrary definition used only for reference.

Time Base (per Second, per Minute, per Hour, No Application): With integration/totalization enabled, the instantaneous input is sampled at a slower rate of every every 20ms (no averaging), or 40ms (with input averaging). You can optionally integrate this signal by totalizing its time sliced instantaneous value. In order for the totalized value to accurately accumulate, you need to specify the time-base units of the input signal to perform the integration over. Select "per Second", "per Minute", "per Hour", or "NA". You must specify a time base other than NA for totalization to occur. Note that if "NA" is selected and Totalize=Yes, then 0.0 is added to the totalized value.

Totalize? & Preload Totalizer: Choose "Yes" for the Totalize? to totalize the input (this will also automatically enable floating point support). You can additionally choose to preload a totalized value on power-up or upon system reset by inserting a number in the Preload Totalizer field. In this way, you can choose that your post-power/reset preload value be taken from the Preload Totalizer field, or from the last totalizer value (before power-up or system reset). During operation, your incremental "time-sliced" measurement will be added to this total every 20ms (no averaging), or 40ms (with input averaging). Note that in order totalize an input, you MUST also Enable Floating Point Support by selecting "Yes" to that query.

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Input Configuration

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Input Configuration

NOTE: *If you are rescaling the input signal to a sub-range of the input, you will still have to interpolate the scaled endpoints to align with the fixed input zero and full-scale endpoints indicated.*

IMPORTANT: *Your effective resolution will decrease proportionally as you reduce the nominal signal span through rescaling. As such, your rescaled Input Zero and Input Full-Scale selections must not divide the full-scale A/D input range of $\pm 5V$ or $\pm 10V$ (10V or 20V span) by more than 16 in order to maintain minimum 12-bit performance (1 part in 4096, or ± 2048). For current inputs, multiply input current by 100Ω to get equivalent A/D input voltage.*

Note: A system reset always starts the totalizer from the preload value, or the last value, whichever is selected. A totalizer reset always starts from the preload value (never the Last Value).

Input Configuration (Set Per Channel)...continued

At the End Count of 9999999 (Rollover or Latch?): When your totalized value reaches the limit of its field (7 significant digits), you can click to select Roll Over and continue totalizing from zero, or restart from the the Preload Value, whichever is selected. Optionally, you can latch the totalized value. Note that if the added value exceeds the amount required to reach the limit of 9,999,999.0, the additional amount it is over will be added to the count when it rolls over (it assumes a free running counter and rolls over while ignoring the preload value).

Note (Counter Operation): This counter is limited to 7 digits of accuracy/resolution, starting with 4 places before and after the decimal point. Then, as the count grows beyond least 7 digits, the least significant digit is zeroed as the most significant digit is incremented. For example (note the transitions and digit shift left), the counter starts at 0000.0000 and increments to 0999.9999, then 9999.9990 to 99999.9900, then 999999.9000 to 9,999,999.0000, then it rolls over or latches as specified.

At Power-Up, Start Totalizer From (Preload or Last Value?): When you power-up, or reset the unit, you can direct the totalization to start from the Preload value in the Preload Totalizer field, or simply the last totalized value from before cycling power or resetting the unit.

Note: A system reset always starts the totalizer from the preload value, or the last value, whichever is selected here. However, Reset Totalizer always restarts from the preload value, even if Last Value is selected here.

Channel Configuration Submit Button: Click this button to write your channel configuration parameters to the selected channel. You must do this separately for each channel being configured.

Reset Totalizer: Click this button to reset the current channel's totalized value to the Preload Totalizer value.

Totalization Calculations

If totalization is enabled, and no input averaging is being done at any channel, the update rate is fixed at 20ms. If you include input averaging for any channel (i.e. set input average to a value greater than 1 at any channel), then the update rate becomes 40ms.

Example: During totalization, we know that unit gathers an instantaneous input every 20ms with no input averaging, or 40ms with input averaging greater than 1 at any channel. Then if the instantaneous scaled input indicates 500, units are gallons, and time base is set to "per Minute". Then $(500 \text{ gallons/min}) \times (1 \text{ minute}/60 \text{ seconds})$ equals a flow rate of 8.33 gallons/second. Assuming no averaging is being performed, a new sample is obtained every 20ms, then we can multiply 8.33gallons/sec by 0.020 sec/sample to get an incremental increase in volume of 0.16667 gallons/sample, and this amount is added to the totalized value. The maximum possible totalized value is 9,999,999, as it is limited to 7 significant digits.

Input Configuration – Global Controls (Not Set Per Channel)

Input Averaging: Enter the number of input samples to average together from 0-500 samples. Selecting 0 is equivalent to selecting 1 and designates that no averaging will be performed. You may increase this number from 1 to help filter the measured response of noisy input signals. If you set any input channel to an input averaging value greater than 1, then the update rate becomes 40ms.

Floating Point Control - Enable Floating Point Support (No or Yes)?:

This is normally set to “No” by default, but must be set to “Yes” if you are rescaling an input, or integrating/totalizing an input. Note that also selecting “Yes” to the Totalize query for any channel will automatically set this to “Yes”, but if you later disable floating point, the totalizer will just add 0.0 to the totalized value.

Selecting “No” to disable floating point support will reduce the amount of calculations that have to be performed and gives the processor more time to do other tasks besides acquiring data. This is recommended when you are also not rescaling or integrating and totalizing the input and can help to make critical control network applications more deterministic, particularly over networks with heavy traffic flow.

Consider that input data is normally acquired at a variable rate (see table above). Of this time, it takes about 80% to convert the data for all 32 channels. This leaves only 20% of the time to process all the other tasks required to store this data into Modbus registers, run the web server, and serve this data over the network.

You can increase the time devoted to servicing these other tasks by disabling floating point, effectively increasing network determinism. So leave it off if you don't need to rescale or totalize. Note that even with floating point disabled, the measured input value will still indicate a floating point number on the Test Page, but the scaling and totalizing fields of the Test Page will indicate “Inactive”.

Floating Point Control - Byte Order: Different Modbus systems will use different byte orders for the two 16-bit Modbus registers used to store a 32-bit floating point value. Select the byte order compatible with your system. Note that B0 refers to the Least Significant Byte, and B3 to the Most Significant Byte.

Floating Point Submit Button: Click here to execute your floating point control selections.

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Input Configuration

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Input Configuration

TIP: Selecting "Failsafe" will allow the unit to additionally signal a power-loss condition as the contacts will open when power is lost.

Input Configuration – Global Controls (Not Set Per Channel)

Relay Control

This unit includes a set of isolated SPST contacts at the input power terminal block (labeled A & B). This relay can be triggered upon a watchdog timeout, or for watchdog timeout and a link loss condition at the network ports (both ports must be inactive to generate a link-loss error such that no communication is possible).

Relay Control - Alarm Relay (Failsafe or Non-Failsafe): Click to select Failsafe or Non-Failsafe alarm contact. If you select "Failsafe", the contacts will be normally energized (closed), and de-energize (open) upon alarm. If you select "Non-Failsafe", the contacts will be normally de-energized (open), and energize upon alarm (close). If you select "Failsafe", these contacts will also trigger if power is lost to the unit.

Relay Control - Action On (Timeout or Timeout and Link Error): Click to enable relay to operate for watchdog timeout only, or watchdog timeout in combination with a Link Loss at its network port.

Relay Control Submit Button: Click here to execute your alarm relay reconfiguration.

Scan Channel Group Selection

This unit has two ports of 8 input channels of both current and voltage input. The unit acquires 32 channels of input data at high speed, by simultaneously reading both the current and voltage channels of a port, and at the same channel position between two ports. This minimizes multiplexer switching and optimizes scan times. The four channels that are acquired together form scan groups as follows:

Scan Group	Input Port 1		Input Port 2	
	V-IN CH	I-IN CH	V-IN CH	I-IN CH
	A/D CH0	A/D CH2	A/D CH1	A/D CH3
1	0	0	8	8
2	1	1	9	9
3	2	2	10	10
4	3	3	11	11
5	4	4	12	12
6	5	5	13	13
7	6	6	14	14
8	7	7	15	15

Note that each port of 8 channels is 8:1 multiplexed to a separate A/D channel of an eight channel 16-bit A/D converter. Channels are scanned sequentially, first by port, then channel order, as reflected by the order indicated in the table above. If you are not using all the channels, you can improve the efficiency of acquiring data by disabling unused input groups.

Note that if you are not totalizing or using input averaging, you can realize an increase in throughput by selectively enabling only the scan groups required by your application. You can even set whether to include a loop-back read of the output channels while scanning the inputs.

The time between samples varies according to the number of scan groups enabled, whether output loop back is enabled, whether totalization is being performed, and whether input averaging is enabled. The table below gives the scan times for combinations of these factors (Input Averaging assumes a maximum 500 sample input average). Note that if you are not totalizing or using input averaging, you can realize an increase in throughput by selectively enabling only the scan groups required by your application.

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Input Configuration

Scan Groups	Totalizer OFF and No Input Averaging ¹	
	No Output Loopback	Adding Output Loopback
1	0.77ms	2.44ms
2	1.36ms	3.03ms
3	1.97ms	3.64ms
4	2.53ms	4.20ms
5	3.12ms	4.79ms
6	3.73ms	5.40ms
7	4.26ms	5.93ms
8	5.00ms	6.67ms

¹ If totalization is enabled at any channel, the scan time is a flat rate of 20ms with no averaging at any channel, or 40ms with any input averaging at any channel set greater than 1.

Scan Group Selection (No or Yes): Click “Yes” next to the channel groups that you are using, and “No” next to unused groups.

Input Configuration – Global Controls (Not Set Per Channel)

Scan Channel Group Select - Include Output Read Back During Scan:

As part of the input scan sequence of both the Input Test Page and Output Test Page, you can elect to include reading the actual output voltage of the analog outputs (output loop back). Disabling read back of the outputs will reduce the input acquisition time. Note that the loop back values are indicated on both the Input Test Page and Output Test Page

Scan Channel Group Submit: Click this button to execute your selected channel scan groups.

Reset Button: Click this button to remotely perform a system reset of the unit. This is also equivalent to depressing the reset toggle switch to RST on the unit. Note that resetting a unit will also reset any timed-out output watchdog timers, restart any totalizer channels, and send outputs to their power-up value.

Series 8B Inputs

The voltage inputs of these models may alternately be driven by the voltage output signals from industry-standard 8B input signal conditioning modules mounted on 8BP04, 8BP08, or 8BP16 back panels. These panels connect via the X1 DB25 interface connector. Note that voltage input channels 0-15 directly correspond to DB25 connector X1, while current input channels are independent of X1. The following table outlines the channel mapping between the input ports of the unit, the DB25 interface connector X1, and the 8B back panel accessories that carry 8B plug-in modules:

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Input Configuration

Port 1 Field Channels – Current								Port 2 Field Channels - Current							
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Port 1 Field Channels – Voltage								Port 2 Field Channels - Voltage							
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
DB25 X1 INPUT Interface Connector															
16CH Back Panel 8BP16															
8CH Back Panel 8BP08								8	9	10	11	12	13	14	15
4CH 8BP04				4	5	6	7	8	9	10	11	12	13	14	15

It is important to note that inputs via 8B interface X1 will displace the corresponding voltage inputs on the unit for the same channels. If you have installed an 8B input module and connected to the X1 interface, you cannot also input a signal at the field terminals on the unit for the corresponding channel, or signal contention may result that could damage the unit. However, if you do not use all sixteen 8B inputs, or have connected a 4 or 8 channel, 8BP04 or 8BP08 back-panel, you can input a signal at unused channel inputs on the unit. For back panels with less than 16 inputs, it's always the first voltage channels of the two ports that are reserved for 8B.

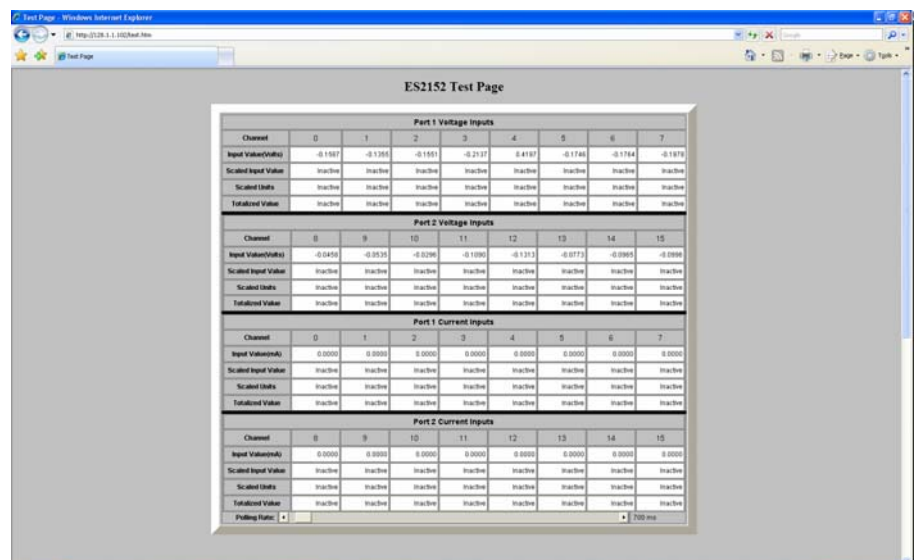
Series 8B input modules can be divided into three groups based on their output voltage: $\pm 5V$, 0-5V, or 1-5V (8B42-02 only). If you are connecting an 8BP back panel to the front DB25 interface connector X1, then you simply configure the corresponding input channels by selecting an input range of $\pm 5V$, or $\pm 10V$ to capture any over-range. The EtherStax unit will additionally allow you to rescale the input signal zero and full-scale to your own engineering units. For 8B input modules that output 0-5V or 1-5V, you will have to interpolate the scaled endpoints to align with the input range zero and full-scale values indicated.

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Input Test Page

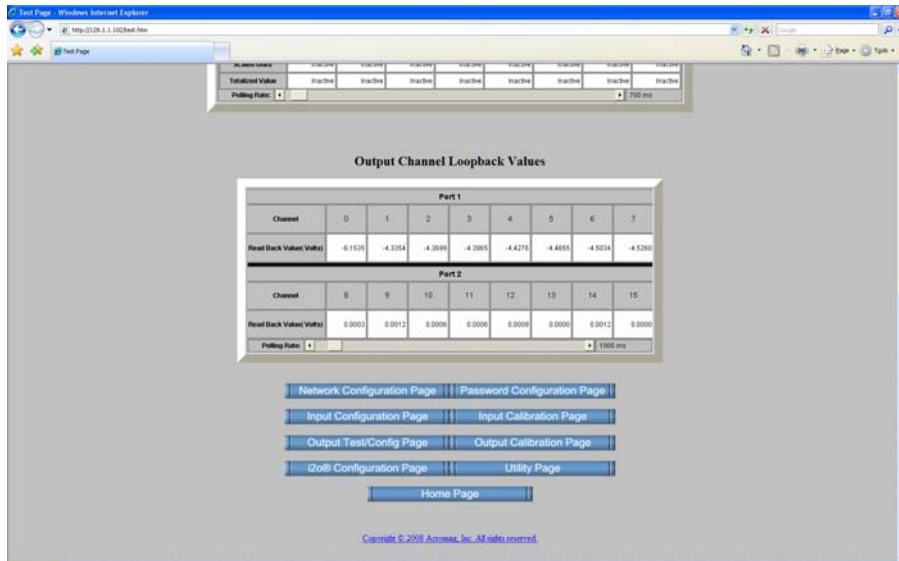
Note: If you are using Firefox as your web browser on a Linux based workstation, you may note that the I/O fields are blank when you switch directly between the Input and Output Test Pages. This does not occur if you directly access either page, but only if switching between them. This is related to having the same Java control on different pages (the Output Loopback applet appears on both Test Pages). If you instead select an interim page before switching between them, you can avoid this problem. This does not occur for Internet Explorer or Firefox on a Windows-based workstation.

After completing the username/password assignments, plus the network and input configuration parameters, you can click this page to monitor the voltage and current inputs of your unit, their scaled and totalized values with their units, and even the loop back values of your outputs. At the bottom of this page you can set the input polling rate for the inputs or the output loop-back values, or disable polling, by simply clicking and dragging this control to adjust as necessary.



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Input Test Page



Voltage Inputs & Current Inputs: Note that the 32 input channels of these modules are divided into 2 ports of 8 channels of each type (current and voltage). If a channel is not also configured for floating point, which is necessary to accomplish scaling and totalizing, then “Inactive” will be indicated for the scaled input value, units, and Totalized value.

Polling Rate: The input values update continuously at the Polling Rate set via this slide control. Click and drag this control to set the rate at which you wish to read the inputs via this web page. The rate is indicated just to the right of this control. Disable input polling by dragging it to the far right.

Output Channel Loop Back Fields: These fields indicate the actual output level read back via separate circuitry through the input A/D (output loop back). “Inactive” will be displayed for the read back values if you did not also choose to include the outputs in the input scanning sequence setup via the Input Configuration Page.

Note: Refer to the Input Configuration Page, Scan Channel Group Select Controls to include the reading back of the unit outputs in the input scan sequence.

Polling Rate: The output loop back values update continuously at the Polling Rate set via this slide control. Click and drag this control to set the rate at which you wish to read the inputs. The rate is indicated just to the right of this control. Disable output polling by dragging it to the far right.

Note (Loop Back Limited by A/D Limitations): Note that while it is possible to program the voltage outputs to approximately $\pm 10.2V$, the internal A/D cannot read values beyond its $\pm 10V$ limits. The input loop-back reading will remain at its maximum extent for output voltages that equal or exceed this range limit near $\pm 10V$.

TIP: If you notice that the Input Test Page has stopped scanning I/O or appears to have halted, simply click the refresh button to restart the polling. This may happen if the unit is interrupted for a period of time and it causes the Java applet to time out and stop sending requests for data. Clicking your browser refresh button will restart the Java requests.

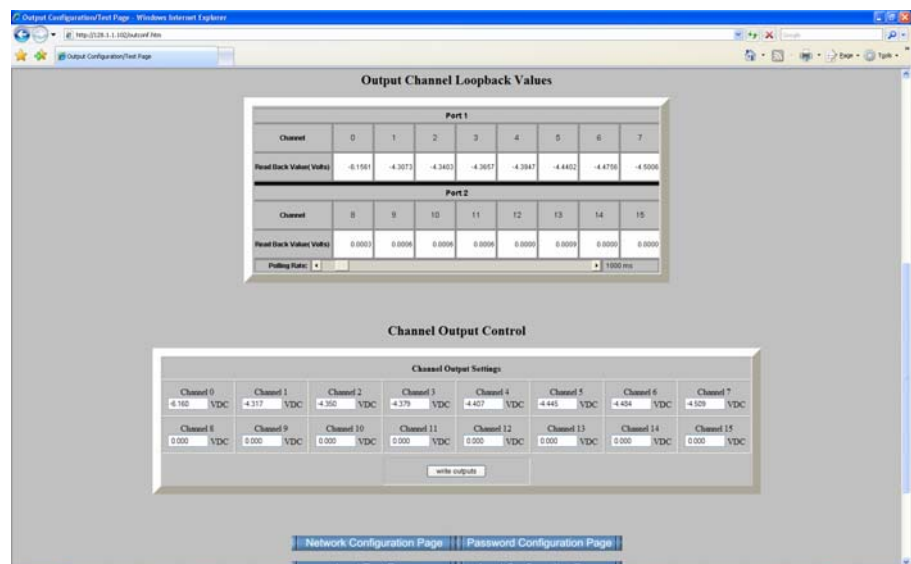
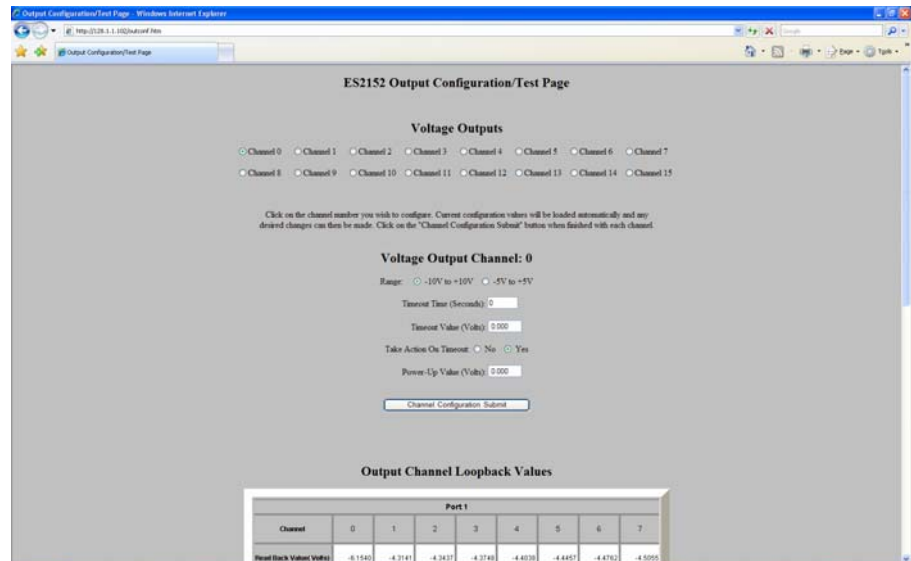
Output Configuration & Output Test Page

The ES2152 Output Configuration & Test page is shown at right. The ES2151 page is similar, but controls current outputs instead of voltage outputs. This page allows you to both configure, write, and monitor the operation of your outputs.

When you first enter a page that includes controls like the polling rate slide of this page, you may note that your first click on the control is ignored. This is because the first click activates the control.

TIP: If you notice that the Output Test Page has stopped scanning the outputs or appears to have halted, simply click the refresh button to restart the polling. This happens if the unit is interrupted for a period of time and it causes the Java applet to time out and stop sending requests for data. Clicking your browser refresh button will restart the Java requests.

After completing the username/password assignments, plus the network and input configuration parameters, you can click this page to both configure, write, and read the outputs of your unit.



For configuration, you click to select the output channel you wish to configure, and the channel's current configuration selections will be displayed. You can reselect the output range, set a watchdog time, a timeout value, the action to take on timeout, and even set an initial power-up value, all on a per channel basis. Simply make your selections and click the Channel Configuration Submit button to write your configuration. Repeat this for each output channel of interest.

Voltage or Current Output Configuration

Channel Select (Top of Page): Click to bullet the output channel you wish to reconfigure. Configuration is done on a per channel basis, one at a time.

Range: Click to bullet the output range you desire. For ES2152 models, you can select $\pm 5V$ or $\pm 10V$, while approximately $\pm 10.2V$ represents the full 16-bit dynamic range of the output DAC. You can select a unipolar current range of 0-20mA or 4-20mA for ES2151 models and the DAC uses 0-5V of its $\pm 10V$ output to drive 0-20mA to the field (14-bits). The 4-20mA outputs can be set to values between 3mA and 21.4mA, while the 0-20mA outputs can be set between 0 and 21.5mA.

Timeout Time (0-65535s): Enter the number of seconds from 0 to 65535 that represents the timeout time, or the time that must pass with no write-activity until a timeout is triggered.

Timeout Value: Enter the signal level the output should assume if a timeout occurs. For $\pm 5V$ outputs, you can set a value from -5.3V to +5.3V. For $\pm 10V$, you can set a value from -10.2 to +10.2V. For 0-20mA, you can set a value from 0-21.8mA.

Note: The unit will not assume a new timeout level submitted to it until the original timeout has been cleared via a write to the channel.

Take Action on Timeout: Select "Yes" to enable the output level change on timeout, or "No" to leave the output level the same. This only applies to the rewriting of the output to the Timeout Value, and cannot be used to block the alarm relay from being activated on timeout.

Power-Up Value: Enter the Output Level you want the output to be set to immediately following power-up, or reset. This operates even if watchdog timeout is 0 (disabled), and Take Action on Timeout is "No".

Channel Configuration Submit: Click this button to submit your output channel reconfiguration. After this, you would select the next channel to reconfigure and repeat the configuration process, channel by channel.

IMPORTANT – WATCHDOG TIMERS: The unit will not assume a new timeout level submitted to it until the original timeout has been cleared via a write to the channel, or a system reset (which restarts the timeout timer). Even if Take Action on Timeout is set to "No", the alarm relay will still trip on timeout, as "Take Action" only applies to writing the timeout value to the output. Note that you will not be permitted to write to a channel that also belongs to a locally mapped i2o port, preventing you from clearing a timeout.

Output Channel Loop Back Values

Output Channel Loop Back Fields: These fields indicate the actual output level read back via separate circuitry through the input A/D (output loop back). "Inactive" will be displayed for the read back values if you did not also choose to include the outputs in the input scanning sequence setup via the Input Configuration Page. Note that the voltage output swing is slightly greater than the voltage input range and looped-back voltage outputs will indicate the input range endpoints near $\pm 10V$ for voltages that go beyond these endpoints. ES2151 current outputs also include fault indication that can detect an open-load/wire break, an output resistance too large for the excitation, or an excitation voltage too low to support the output load (note that excitation must exceed max load voltage by $\sim 3.3V$).

Note: Refer to the Input Configuration Page, Scan Channel Group Select Controls to include the reading back of the unit outputs in the input scan sequence. If you elect not to include output read back in the input scan sequence, then "Inactive" will be displayed for the Output Channel Read Back Values of this page.

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Output Configuration & Output Test Page

Note: If you are using Firefox as your web browser on a Linux based workstation, you may note that the I/O fields are blank when you switch directly between the Input and Output Test Pages. This does not occur if you directly access either page, but only if switching between them. This is related to having the same Java control on different pages (the Output Loopback applet appears on both Test Pages). If you instead select an interim page before switching between them, you can avoid this problem. This does not occur for Internet Explorer or Firefox on Windows.

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Output Configuration & Output Test Page

Note (ES2151 Loop-back Fault Indication): The output channel loop-back fields also include an output fault indicator on current outputs. This indicator is used to detect an open load (wire-break), high load resistance, or an output voltage level approaching the excitation supply. Note that in order to detect a wire break and indicate a fault, a non-zero current must be programmed, which prevents it from working at or very near the 0mA endpoint of the 0-20mA range. Likewise, it only works with a valid excitation voltage applied above 8V. This means that low resistance loads below 300Ω may never trigger a fault. It will typically trigger a fault for valid excitation voltage levels less than 3V above the load voltage, indicating that either the load resistance must decrease, or the excitation voltage must increase.

Important: You cannot intermix 8B input and output modules on the same back-panel. Further, DB25 interface connector X1 of this unit is restricted for connection to 8B input module carriers, and X2 to 8B output module carriers.

Note: ES2151 current outputs are looped back prior to the last stage (it loops back the DAC voltage that drives the V/I converter). As such, the value indicated may be offset slightly from the actual output current, since it does not include any inaccuracy in the output stage and it is not corrected for any manual output calibration that is done at the output channel.

Polling Rate: The output loop back values update continuously at the Polling Rate set via this slide control. Click and drag this control to set the rate at which you wish to read the inputs. The rate is indicated just to the right of this control. Disable output polling by dragging it to the far right.

Output Control – Testing Your Outputs

Output Control Fields 0-15: Type the output value you wish to set the corresponding output to in these fields.

Write Outputs: Click this button to set the output(s) to the values you have entered into these fields.

Note: The Write Output fields will be auto-loaded with the values written to outputs mapped from inputs via i2o. If you map inputs to local outputs on the same unit, you will not be allowed to write those outputs and clicking the write output button will have no effect on the locally mapped output port.

Note (Output Loop Back): Note that while it is possible to program the voltage output to approximately ±10.2V, the internal A/D cannot read values beyond its ±10V limits, and the input reading will remain at its maximum extent for output voltages that equal or exceed this limit near ±10V.

Note (Loopback of Current Outputs): The value looped back represents the voltage measured at the output terminal of voltage outputs. But for current outputs, it represents the DAC voltage that drives the V/I converter of the current output, which is one stage removed from the output terminal and may be offset from the actual output current. Note that the DAC uses 0-5V of its ±10V output range to drive 0-20mA to the current output.

Control of 8B Output Modules (ES2152 Only)

The bipolar voltage output channels of ES2152 models may alternately drive 8B output modules mounted on a back-panel and connected to DB25 interface connector X2. DB25 connector X2 maps to field output ports 1 & 2 on the back-side of ES2152 units. The following table outlines the channel mapping between output ports, connector X2, and the 8B back panel that carries 8B plug-in modules:

Port 1 Field Output Channels								Port 2 Field Output Channels							
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
DB25 X2 OUTPUT Interface Connector															
16CH Back Panel 8BP16															
8CH Back Panel 8BP08								8	9	10	11	12	13	14	15
4CH 8BP04				4	5	6	7	8	9	10	11	12	13	14	15

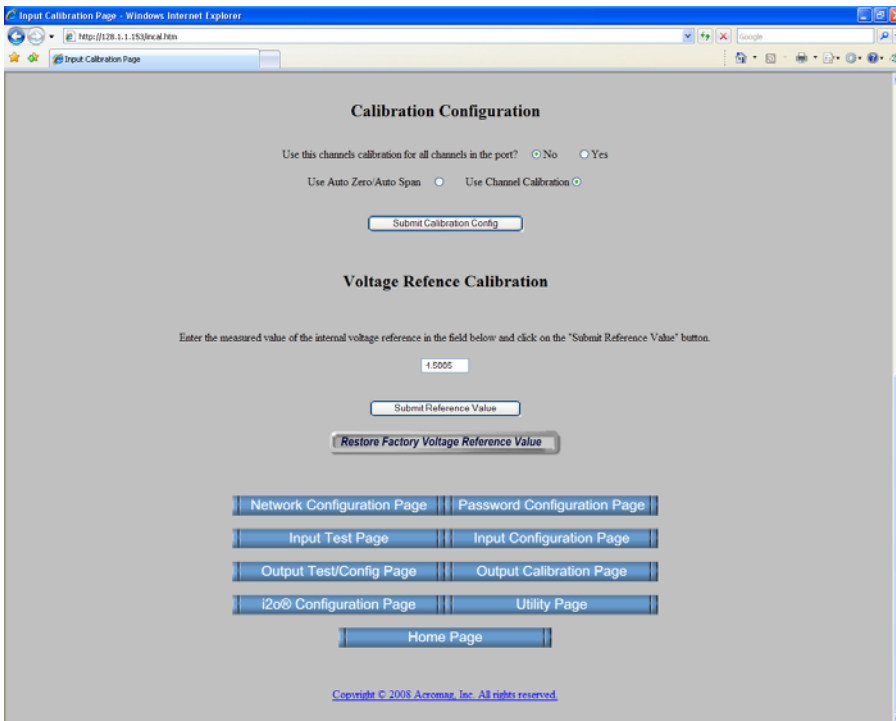
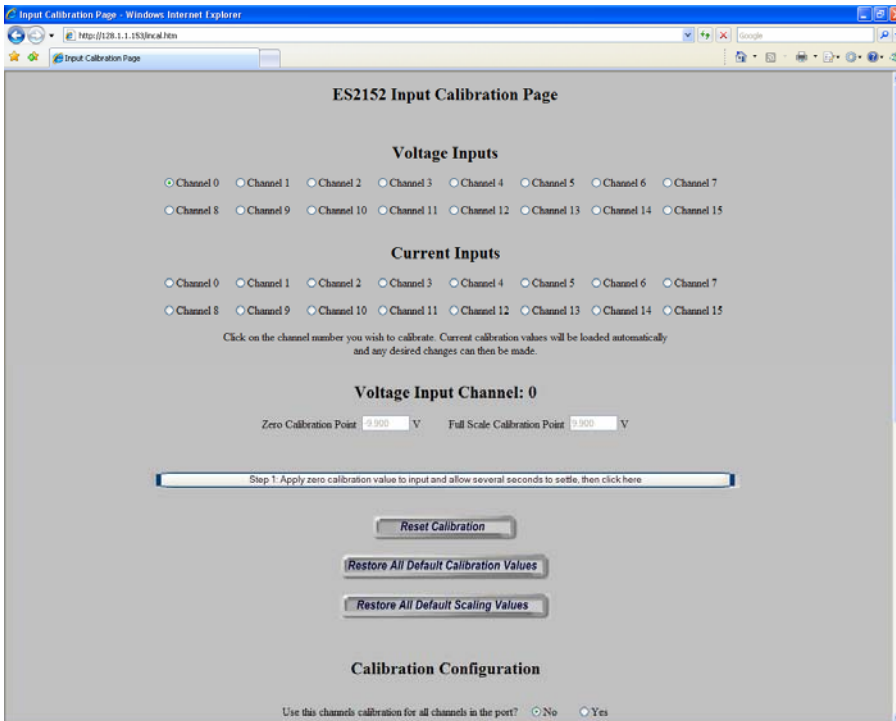
The outputs of each port are 8:1 multiplexed to separate A/D channels of an eight channel 16-bit A/D converter in order to provide output loop back monitoring of the actual output level. For back panels with less than 16 inputs, it's always the first channels of the port that are reserved for 8B.

Unlike the inputs of this model, there is no output scaling done. The output voltage is ±10V and it is up to the user to set the appropriate output voltage necessary to control the 8B output module. The drive signal required is ±5V, ±10V, 0-5V, and 0-10V, depending on output module model (see Specifications for available models). Output modules that do not utilize the full ±10V 16-bit DAC range will have a proportionally lower effective resolution.

WEB BROWSER

Input Calibration Page

IMPORTANT: This module normally performs an automatic calibration of zero and full-scale. Recalibration on a channel-by-channel basis is normally NOT required, except for calibration that must be done to a higher standard. Do not attempt to recalibrate a channel unless absolutely required, or to verify the accuracy of internal calibration, as inadvertent miscalibration will negatively affect channel performance.



WEB BROWSER

Input Calibration Page

Do not confuse input calibration with rescaling, which is accomplished via controls of the Input Configuration Page.

In normal operation, this unit automatically re-calibrates its base A/D input ranges every time it scans its 32 input channels by reading its own calibration reference voltage ($4.5V \pm 0.05\% \pm 5\text{ppm}/^\circ\text{C}$) at each A/D channel. This reference has been precisely measured and its value stored inside the unit from the factory. This is sufficient to achieve rated accuracy using automatic calibration. The relative accuracy of calibration can be improved via software calibration facilitated with the controls of this page, or instead by issuing the appropriate Modbus register calibration commands. But if you need to calibrate to your own standard, or you want to check the accuracy of the internal calibration, then you can utilize the controls of this page to manually calibrate the inputs instead.

You have 3 options for accomplishing input calibration. The first option is to simply accept the default response of automatic input calibration. The second option will allow you to achieve better accuracy by manually calibrating one channel of each type from each port, and then using that channels response to represent all 8 channels of the port (saves some time while improving accuracy). This effectively calibrates the A/D channel that the port connects to. Your third option is to manually calibrate each input channel using very accurate input signal and measurement equipment to achieve calibration. This would additionally compensate for the small errors contributed by the input filter, buffer amplifier, and input multiplexer, as well as the initial inaccuracy of the input shunt resistor of the current inputs ($\pm 0.05\%$). Which method you choose refers to your Calibration Configuration selected via controls on this page.

This page additionally allows the precision internal 4.5V calibration reference itself to be calibrated. An accurate voltage reference measurement is made between P1 header pins 1 and 2. The measured value representing the calibration voltage is recorded and stored inside FRAM memory that resides on the I/O board of the unit. The controls of this page allow a new value to be stored. This has already been done at the factory and should not need to be repeated unless miscalibration is suspected. In order to take this voltage measurement, it requires that the board assembly be carefully removed from the enclosure and this requires handling at an ESD-safe work station. We do not recommend that you do this in the field as it invites potential damage to sensitive internal circuitry.

Input Calibration - Voltage Inputs, Current Inputs

These web controls allow you to perform calibration on an input channel. Manual calibration is accomplished on a per channel basis, or optionally on a per port basis.

Channel Selector Bullets: Click to bullet the channel you wish to calibrate. Calibration is done on a per channel basis. Optionally, you can pick one channel from each port (group of 8 channels), and use that calibration to represent the port channels of the same type.

Zero Calibration Point Field: Indicates the low calibration endpoint for the selected input range. This is the input signal that you have to apply to accomplish zero calibration. Your input source must be of an accuracy greater than 0.03% of span to achieve better results than auto-calibration.

Full Scale Calibration Point Field: This field indicates the high calibration endpoint for the selected input range and is the input signal to apply to accomplish full-scale calibration. Your input source must be of an accuracy greater than 0.03% of span to achieve better results than auto-calibration.

Calibration Step Button: You click this button in a sequence of 3 steps to accomplish input calibration via this web page. Specific instructions at each step appear printed on this button as you click.

Reset Calibration Button: Click this button if you make a mistake in the calibration sequence and wish to start over from step 1.

Restore All Default Calibration Values Button: Click this if you believe channel calibration has been done in error or you are getting erratic results after recalibrating. This will affect all input channels at one time and the values restored are the ideal values, not the results of an actual calibration.

Restore All Default Scaling Values Button: Click this button if you wish to return the input scaling values of the selected channel to their default values (typically the input range signal endpoints). Note that this will affect all input scaling values and restores them to their ideal values.

Calibration Configuration

By default, zero and full-scale are calibrated automatically. You may optionally calibrate each channel separately to obtain the highest possible accuracy. Or to save time, you can calibrate one channel of each type from each port (i.e. one A/D channel) and select an option to allow that channel's calibration to calibrate the response for all 8 channels of the same port.

Use this channel's calibration for all channels of the port (Yes or No):

If you select "Yes", then the manual calibration already done for this channel will apply to all channels of the same port and of the same type (each port or group of 8 channels correspond to the same A/D channel and input type).

This selection should be done only after manually calibrating this channel.

This option can save time in avoiding having to calibrate every port channel separately. To distinguish the channels of the same port, each row of the channel selector at the top of this page represents one port (one port contains both a voltage port and a current port). Each group of 8 channels or ports are multiplexed to a separate A/D channel (all same port/same type channels share the same A/D channel and most initial error will be contributed by the A/D stage). When done properly with very accurate signal sources and measurement equipment, this method of calibration is generally better than auto-calibration, but not as accurate as calibrating per channel.

Note: If calibrating one channel per port, the other channel's should have "Use Channel Calibration" selected. Selecting "yes" will automatically select "Use Channel Calibration" for the remaining port channels of the same port and type. You can still override this auto-selection, by subsequently selecting another option for any number of the other port channels.

Use Auto Zero/Auto Span Bullet: Clicking this bullet will use automatic calibration for the selected input channel (default behavior). With automatic calibration, the port utilizes the on-board $4.5V \pm 0.05\% \pm 5\text{ppm}/^\circ\text{C}$ reference signal and ground to calibrate the input. This on-board reference has been precisely measured at the factory and its value stored in the unit. In this way, the unit makes a correspondence between its A/D count and a known voltage to form the linear input response. This method of calibrating the inputs is the most convenient and will achieve rated accuracy of $\pm 0.05\%$ for voltage inputs, and $\pm 0.1\%$ for current inputs. It is less accurate for current inputs because it does not compensate for the initial inaccuracy of the input current shunt resistor ($100\Omega \pm 0.05\% \pm 10\text{ppm}/^\circ\text{C}$) and current inputs do not utilize the entire A/D input range.

WEB BROWSER

Input Calibration Page

For reference, one LSB of input signal is equivalent to the input full-scale voltage range divided by 65536 ($[\text{Full_Scale} - \text{Zero}]/65536$). In general, an uncalibrated A/D input can produce a zero offset error up to $\pm 28\text{LSB}$, and a full-scale error up to $\pm 0.4\%$. Each port (A/D channel) will match their zeros to within $\pm 10\text{LSB}$ of each other, and their full-scale measurements to within $\pm 15\text{LSB}$. Use calibration to compensate for these errors.

WEB BROWSER

Input Calibration Page

Reference Measurement:

This procedure is not normally required and has already been done at the factory. The reference voltage is measured between the pins of header P1, which are marked on the back of the top-side circuit board (do not disassemble mated boards). You will have to remove the front end-plate to pull the assembly from its enclosure to measure this voltage. This should only be performed at a static-safe workstation by qualified personnel, or damage to the unit may result.

Use Channel Calibration Bullet: Clicking this bullet will select manual (per-channel) calibration, which associates the current calibration with only the current channel. It is possible to achieve the greatest accuracy if you calibrate on a per channel basis and you have a precise input signal source and digital voltage meter, but it takes more time to accomplish.

Submit Calibration Configuration Button: Select your mode of calibration and click this button to write your selection to the unit. Then you can calibrate the channel as required, repeating it for each of the channels.

Voltage Reference Calibration

The on-board calibration reference used for automatic calibration can be separately calibrated by accurately measuring its value and entering it in the field provided in the Voltage Reference Calibration section of this page.

This has already been done at the factory and recalibration of this reference should not normally be required. You should not change the value indicated in this field unless authorized to do so, or performance may be negatively affected.

Voltage Reference Field (4.4968 to 4.5032): The internal calibration reference is precisely measured at the factory and its voltage value stored in the unit. That value is indicated in this field and is used to make a correspondence between the A/D response (digital count), and the voltage signal itself, in order to calibrate its linear response. You can enter your own measured value in this field if you wish to recalibrate it.

Submit Reference Value Button: Click this button after you have entered a measured reference voltage in the Voltage Reference field to store your measured value in non-volatile FRAM memory.

Restore Factor Voltage Reference Value Button: If you make a mistake and have entered the wrong value for the calibration reference, you can click this button to restore the original value measured from the factory. You would also click this button if you performed a system restore of the unit (see Troubleshooting – Getting Out of Trouble procedure).

This reference voltage is $4.5V \pm 0.05\% \pm 5\text{ppm}/^{\circ}\text{C}$, and the value in this field should read between 4.4968 and 4.5032. Steering circuitry prior to the port input buffer allows each A/D channel to connect to this reference voltage, and alternately to analog common (for zero). The unit utilizes the corresponding raw A/D output count for both zero and the reference voltage to calibrate the input by defining the equation of a straight line used to interpolate the input signal for any given A/D count.

Example Calibration

Inputs are automatically calibrated on a continuous basis during the normal scan sequence. This produces an input response that is typically better than $\pm 0.1\%$ accurate (current), and $\pm 0.05\%$ (voltage). If you need to achieve greater accuracy, and have the high precision signal source and DVM to do so, you may optionally manually calibrate the inputs on a channel-by-channel basis using your web-browser (Method 1 below, easier), or via Modbus register commands (Method 2 below).

If you perform manual calibration via the controls of this page, or you use the Modbus register commands to manually calibrate the inputs, this has the added benefit of being able to correct for any negative full-scale offset, as auto calibration only utilizes 0V and +4.5V to calibrate the input. Likewise, manual calibration additionally compensates for the inaccuracy of the input shunt resistor of the current inputs.

IMPORTANT: Allow the unit to warm up a few minutes prior to calibrating.

WEB BROWSER

Method 1 – Channel Calibration Using The Built-In Browser Interface:

Input Calibration Page

1. Bring up the browser interface and select the Input Calibration Page.
2. Allow the unit to warm-up a few minutes before continuing.
3. Browse to the bottom of the Input Calibration page and check that the reference field of the Calibration Page indicates a voltage between 4.4968 and 4.5032 (4.5V±0.07%). If you suspect this is inaccurate, this voltage will have to be precisely measured and then typed into this field first. Calibrating the reference requires that the unit be removed from its enclosure and is not recommended. See Optional Reference Calibration above.
4. For best results, Zero is always calibrated before full-scale. Note the Zero Calibration Point indicated in its field. You must apply this exact input signal to the input channel being calibrated to proceed. Your signal source should be accurate to better than ±0.05% (auto standard).

Current Inputs: The zero input signal is 0.5mA for unipolar ranges, and -20mA for the bipolar range. Note that for a -20mA input signal, you can simply reverse the input leads connected to your current source (this drives an A/D low-end calibration voltage of -2.0V via an internal 100Ω shunt).

Voltage Inputs: The zero input signal is a negative voltage of -9.9V or -4.9V, according to input configuration. Note that if you do not have a precision negative voltage, just flip the positive and negative terminal connections while using a precision positive voltage.

5. After applying the Zero Calibration signal to the channel, simply click the “Step 1...” button to calibrate the zero.

If you make a mistake, you can always click the “Reset Calibration” button to start over from Calibrate Zero Step 1.

6. Note the Full-Scale Calibration Point indicated in its field. You must apply this exact input signal to the input channel being calibrated. Your signal source should be accurate to better than ±0.05% (auto calibration standard).

Current Inputs: The full-scale calibration input signal is always +20mA. This is equivalent to delivering 20mA to the + terminal and returning it at the - terminal. This produces an A/D calibration voltage of +2.0V.

Voltage Inputs: The full-scale input signal is a positive voltage of +9.9V or +4.9V, according to your input configuration.

7. After applying the correct Full-Scale Calibration signal to the channel, simply click the “Step 2...” button to calibrate the full-scale.

If you make a mistake, you can always click the “Reset Calibration” button or the “Step” button to start over from Zero calibration Step 1.

The A/D ±5V or ±10V range is now calibrated for this channel.

If your calibration configuration selected “No” for the query “Use this channel calibration for all channels of this port”, then you should repeat this procedure for the other channels. If “Yes” was selected for this query, then you only need to repeat this procedure for 3 other channels, one of each type from each of the other 2 ports, to calibrate the unit completely.

WEB BROWSER

Input Calibration Page

Current inputs will utilize the $\pm 5V$ A/D range to calibrate $\pm 20mA$, and a 0-5V A/D range to calibrate 0-20mA and 4-20mA. This optimizes calibration by using as much of the A/D range as possible, while keeping the input drop low and will drive $\pm 2.0V$ into the A/D which has a native 16-bit input range of $\pm 5V$, or 0-2V into a native 16-bit A/D range of 0-5V.

Method 2 – Input Calibration Via The Modbus TCP/IP Interface:

1. Write the appropriate Channel Configuration Register to select the input range to be calibrated for the input channel. Voltage inputs can select $\pm 5V$ or $\pm 10V$. Current inputs will utilize the $\pm 5V$ A/D range to calibrate $\pm 20mA$, and a 0-5V A/D range to calibrate 0-20mA and 4-20mA. This optimizes calibration by using as much of the A/D range as possible, while keeping the input drop low and will drive $\pm 2.0V$ into the A/D which has a native 16-bit input range of $\pm 5V$, or 0-2V into a native 16-bit A/D range of 0-5V. This step is effectively not required to calibrate the unit.
2. Write a 16-bit word into the Enable Scan Groups register to at least enable the scan group that includes the input channel you wish to calibrate. Note that there are 8 scan groups and bit 0 of this word corresponds to scan group 1, while bit 7 of this word corresponds to scan group 8 (bits 8-15 are 0 and not used). A set bit in a position will enable the corresponding scan group.
3. Write 24106 (5E2AH) into the Calibration Access Register to remove write protection from the calibration registers.
4. For best results, Zero is always calibrated before full-scale. You must apply the exact input zero signal to the input channel being calibrated to proceed. Your signal source should be accurate to better than $\pm 0.05\%$ (auto standard).

Current Inputs: The zero input signal is 0.5mA for unipolar ranges, and -20mA for the bipolar range. Note that for a -20mA input signal, you can simply reverse the input leads connected to your current source (this drives an A/D low-end calibration voltage of -2.0V via an internal 100 Ω shunt).

Voltage Inputs: The zero input signal is a negative voltage of -9.9V or -4.9V, according to input configuration. Note that if you do not have a precision negative voltage, just flip the positive and negative terminal connections while using a precision positive voltage.

5. Write a 16-bit word into the correct Zero Cal Trigger Register with a set bit in the bit position that corresponds to the channel you wish to calibrate zero at. You would write to the Zero Cal Trigger for Voltage Channels register to address voltage channels 0-15 of ports 1 & 2, or the Zero Cal Trigger for Current Channels register to address current channels 0-15 of ports 1 & 2. A set bit in these registers will trigger the unit to sample the corresponding channel, capture its zero signal, and store the count. The unit will replace calibration coefficients immediately, with no reset required. Note that if you have zero signals at more than one channel, and the corresponding scan groups are enabled, you can effectively calibrate zero for many or all of the channels at once.
6. You must apply the exact input full-scale signal to the input channel being calibrated. Your signal source should be accurate to better than $\pm 0.05\%$ (auto calibration standard).

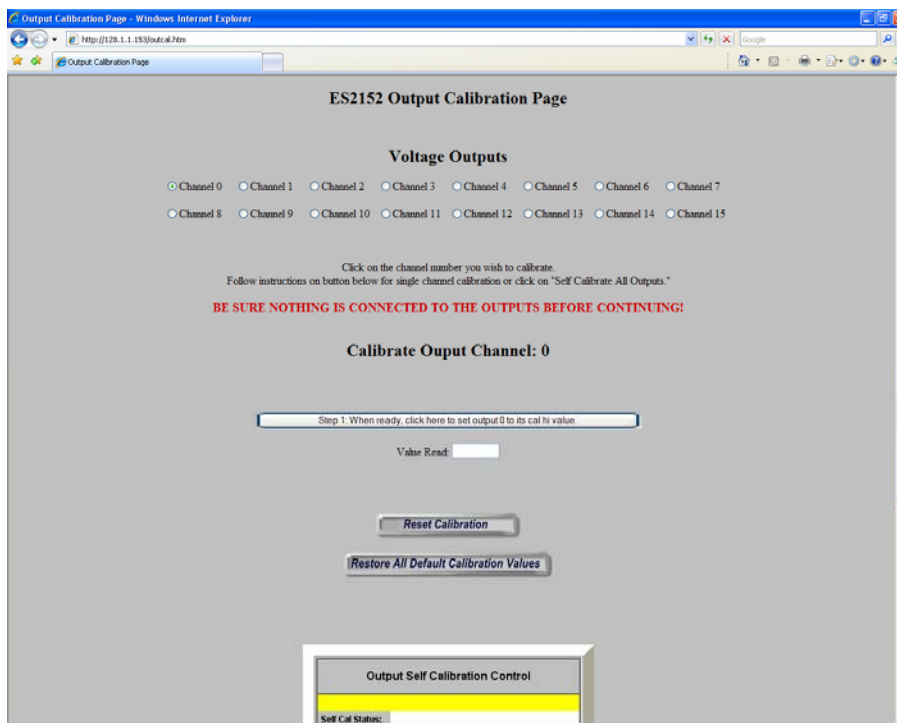
Current Inputs: The full-scale calibration input signal is always +20mA. This is equivalent to delivering 20mA to the + terminal and returning it at the - terminal. This produces an A/D calibration voltage of +2.0V.

Voltage Inputs: The full-scale input signal is a positive voltage of +9.9V or +4.9V, according to your input configuration.

Input Calibration Via The Modbus TCP/IP Interface...continued:

7. Write a 16-bit word into the correct Span Cal Trigger Register with a set bit in the bit position that corresponds to the channel you wish to calibrate full-scale at. You would write to the Span Cal Trigger for Voltage Channels register to address voltage input channels 0-15 of ports 1 & 2, or the Span Cal Trigger for Current Channels register to address current input channels 0-15 of ports 1 & 2. A set bit in these registers will trigger the unit to sample the corresponding channel, capture the full-scale signal, and store the digital count. The unit will replace calibration coefficients immediately, with no reset required. Note that if you have full-scale signals at more than one channel, and the corresponding scan groups are enabled, you can effectively calibrate full-scale for many or all of the channels at once.
8. Repeat these steps for the other input channels to be calibrated.
9. When finished calibrating, write 0x0000 to the Calibration Access Register to replace write protection for the calibration registers and to help prevent potential miscalibration.

When directed to do so, this unit can automatically recalibrate its analog output DAC by reading a known calibration reference voltage along with each of its output voltages. The calibration reference has been precisely measured at the factory and stored inside the unit, and this is sufficient to achieve rated accuracy. Self Calibration controls are included on this page to accomplish this. Further, if you need to calibrate to your own standard, want to check the accuracy of the internal calibration, or wish to improve the accuracy performance of current output models, then you can utilize other controls of this page to manually calibrate the outputs also.

WEB BROWSER**Input Calibration Page****Output Calibration Page**

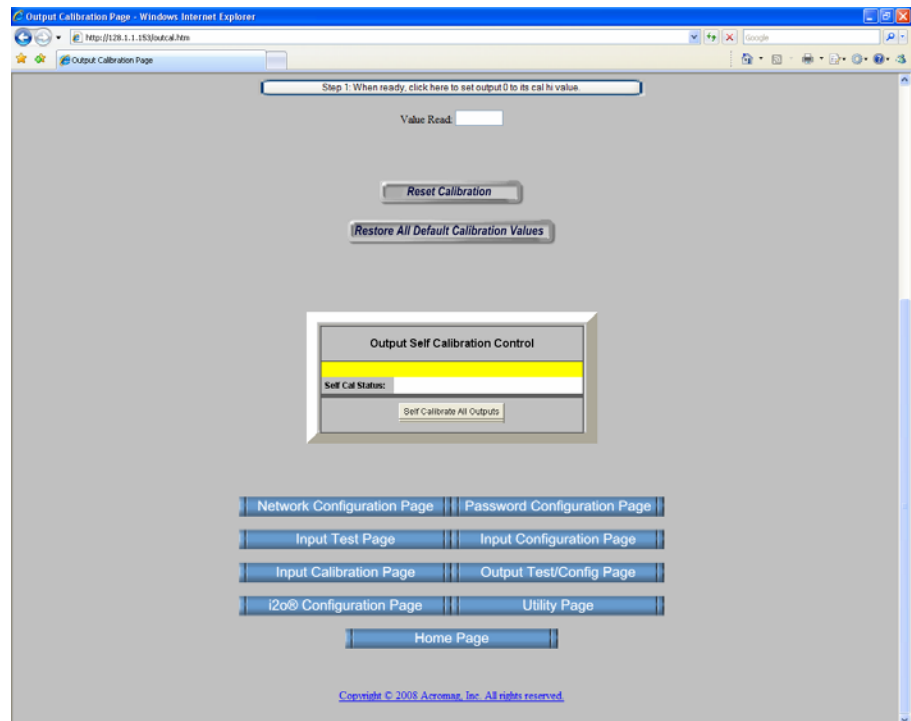
Refer to the Input Calibration page for controls to precisely calibrate the calibration reference that is also used to auto-calibrate the outputs. You should refer to that page if you want to calibrate the reference voltage used to calibrate the outputs. Do not auto-calibrate the outputs of this page until you have verified that the reference calibration voltage is correct.

WEB BROWSER

Output Calibration Page

For Output Calibration, if you perform an automatic output cal, it has the effect of replacing any/all manual calibrations that have been done (they share the same calibration coefficient registers and auto-cal is done for all channels at once). On the other hand, if you do an auto-calibration first, then a manual cal of a channel will over-write the auto cal results for that channel, leaving the other output channel calibration intact. Manual cal is meant to supplement an auto-cal, for tweaking only those channels that need it. Automatic calibration will be sufficient for most applications, and it can be used to increase the accuracy of current outputs.

Only automatic calibration has been done on units fresh from the factory.



This unit provides two different methods to achieve output calibration: automatically for all-channels at once using the internal A/D to make measurements, or manually on a per channel basis using your own DVM to make measurements.

WARNING: Since automatic calibration calibrates all output channels at once, while manual calibration is done per channel, performing an automatic output calibration after manual calibration will replace the results of the manual calibration. Thus, you should always auto-calibrate first, then you can apply subsequent manual calibration only on the channels you desire where you may need to improve specific performance.

Normally, you do not require manual calibration for the outputs, as the automatic output calibration will achieve rated performance. However, in the case of the current output ES2151 models, automatic calibration does not compensate for small errors contributed by the V/I converter that drives the current output. So for ES2151 models, following an automatic calibration with a manual calibration can be used to improve performance. Only automatic calibration has been done on units fresh from the factory.

Output Self Calibration Control (Used First Before Manual Calibration)

This control is located at the bottom of the page and should be done first before attempting to manually calibrate an output channel. Invoking this control will over-write all existing calibration for the output channels.

Self Calibrate All Outputs Button: Click this button to trigger an automatic output calibration of all outputs under program control while using an internal A/D channel to make output signal level measurements.

Self calibration utilizes an A/D channel of the unit, instead of a DVM, to measure each of the programmed output voltages in sequence, plus a known precision reference, all under program control for all output channels at once. It then stores the output response for tailoring the conversion of an output channel. Self calibration will achieve sufficient accuracy for most applications (better than $\pm 0.1\%$ of span for voltage outputs). But for the current outputs of the ES2151 model, it is somewhat less accurate because it only calibrates to the output of the voltage DAC, which drives a voltage to current converter for the current output. Any inaccuracy in the V/I converter is not compensated for using self calibration, and this could add up to an additional $\pm 0.1\%$ of error. Thus, self-calibration of ES2151 outputs will only achieve results better than $\pm 0.2\%$. You may use manual channel calibration (described above) to improve calibrated accuracy beyond these figures and as shipped from the factory.

WEB BROWSER

Output Calibration Page

Output Calibration - Voltage Out (ES2152) or Current Out (ES2151)

Manual calibration allows you to use your own DVM to make output signal measurements, and then store the measured value in the unit, and use this reference to accurately convert an output. It does this on a channel-by-channel basis and can achieve very accurate results, but requires more time to complete, as well as very accurate measurement equipment. Units shipped from the factory have only automatic calibration done and no manual calibration has been performed.

Channel Selector Bullets: Click to bullet the output channel you wish to calibrate. Output calibration is done on a per channel basis. Repeat the calibration process for each output, as required to improve performance.

Calibration Step Button: You click this button in a sequence of 5 steps to accomplish output calibration via this web page. Specific instructions at each step are typed on the face of this button. Follow these instructions to proceed.

Value Read Field: You enter the output value measured in this field, at the appropriate step (read Step button instructions for prompting). The unit will use your measured value to correlate the corresponding DAC digital count to the analog output signal level, and formulate a linear interpolation of the calibrated output.

Reset Calibration Button: Click this button if you make a mistake in the calibration sequence and wish to start over from step 1.

Restore All Default Calibration Values Button (Global): Click this button if you believe channel calibration has been done in error, or you are getting erratic results after recalibrating. Note that this will affect all output channels at one time. The values restored are the ideal values, not the results of an actual calibration, and may not achieve rated performance. No factory calibration has been done for the output channels, only automatic calibration. As such, you should only do this as a last resort for getting out of trouble.

WEB BROWSER

Output Calibration Page

Manual Output Calibration Example

While manually calibrating an output, you are required to precisely measure the high and low calibration signals of the selected output channel, and then enter your measured values at the appropriate step (read Step button). This is done separately for each of 16 output channels. The native DAC output is $\pm 10V$ and output calibration will use calibration endpoints of -9.75 and $+9.75$, even if you have selected the $\pm 5V$ subrange.

CAUTION: This procedure will change the output signal level. Be sure to disconnect any critical control equipment that may be inadvertently affected by varying the control signal connected to it.

1. Click to bullet the output channel you wish to calibrate.
2. Connect a precision voltmeter in parallel with your load for that channel (or an ammeter in series with your output current for the ES2151).
3. Click the Step 1 button to set the output to its high endpoint cal value.
4. Measure the signal and type your measured value into the "Value Read" field (to 4 decimal places with an accuracy better than 0.0125%). Click the Step 2 button to store the high calibration endpoint.
5. Click the Step 3 button to set the output to its low endpoint cal value.
6. Measure the output signal and type the measured value into the "Value Read" field (to 4 decimal places with an accuracy better than 0.0125%). Click the step 4 button to store the low calibration endpoint.
7. Calibration of the selected channel is complete. You can click the Step button again, the "Reset Calibration" button, or simply select another channel and repeat steps 1-6 for the remaining channels, one at a time.

If you make a mistake, you can always click the **Reset Calibration** button at any time to return to Step 1 and start over.

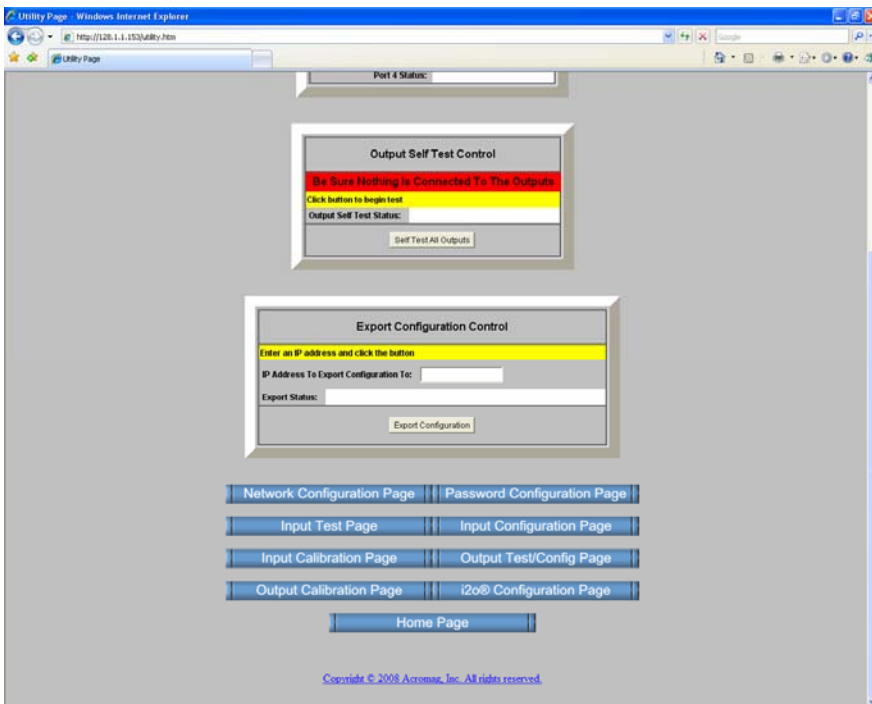
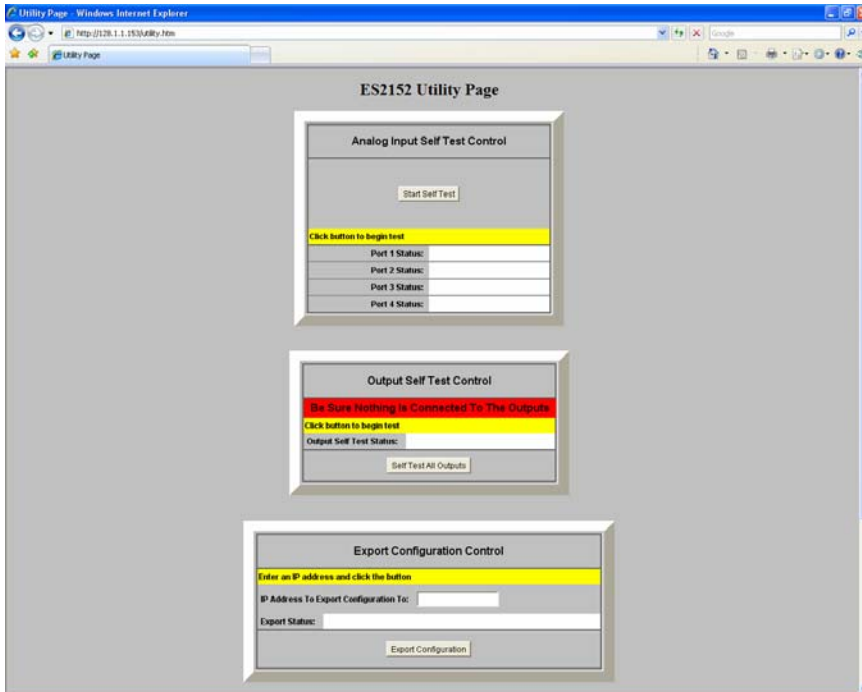
Optionally, you can click **Restore All Default Calibration Values** if your calibration was in error, or you get an erratic response after performing calibration. This returns the calibration of the selected channel to its original default (ideal) values. These values may not achieve rated performance. Use the automatic self-calibration to improve results and always before performing manual calibration.

You may also access a Utility Page that includes a couple of built-in utilities that allow you to verify input operation without wiring to the inputs (self-test control), verify output control (via loop back), and allow you to export your current configuration to another unit (export configuration control). You can select the Utility Page button from the Test Page screen to display a screen similar to that shown here:

WEB BROWSER

Utility Page

When you first enter a page that includes controls like this, you may note that your first click on a control is ignored. This is because the first click only activates the control.



WEB BROWSER

Utility Page

Connecting field input signals during self-test will not harm the unit, as the input port multiplexer is disabled during self-test and no signal contention is possible.

Analog Input Self Test Control

The Self-Test makes use of built-in calibration reference signals to check the operation of input ports. It can serve as a trouble-shooting aide if you are experiencing problems. It does not require that you connect anything to the input, and doing so will not affect the results of this test. Likewise, you do not have to disconnect your input signals to run this test.

Each port of this device (group of 8 channels) is multiplexed to a separate set of input buffers that feed separate A/D channels for current and for voltage. The Self Test Control will test the relative accuracy of the input ports by sampling the reference signals. Simply click the "Start Self Test" to begin testing. This triggers the unit to momentarily connect each A/D input channel's buffers (each port channel) to the internal reference signals (0V and 4.5V) and measure the response. If the resultant measurement is within an acceptable level of error with respect to rated accuracy, then the Port status will indicate "Passed". Note that this effectively tests the signal path of each port, but not each port's channel (i.e. it tests the circuit operation beginning from the output side of each input port's 8:1 input multiplexer). Thus, the Self-Test does not test operation of the port input multiplexer, or the integrity of the input filters, as the reference signals are switched to the input buffers of the four A/D channels, after the multiplexer.

Analog Output Self Test Control

The Self-Test makes use of built-in loop-back monitoring of the output signal. For the ES2152, outputs are looped back from the output terminal, but for the ES2151, outputs are looped back from the DAC driving the channel's V/I converter stage (one stage removed from field terminals). This test can serve as a trouble-shooting aide if you are experiencing problems. It does not require that you connect anything to the outputs, and doing so will not affect the results of this test. However, since output self-test does change the output, you may wish to disconnect critical control wiring to prevent inadvertent control of your application.

Export Configuration Control

The export function is a real time-saver when commissioning multiple units in similar fashion. Simply enter the IP address of the unit you wish to send your I/O configuration to (the unit must already be connected to the network). It is assumed that you have already communicated with the target unit and setup its network parameters. Next, click the Export Configuration button to transmit your I/O configuration.

Referring to the Modbus Memory Map for this device, this function sends the contents of all Holding Registers (4xxxx registers), right up to the wink register, to the IP address indicated (Registers 40001 to 40103). It does not send network configuration parameters which must be preset via the web browser. Likewise, it does not transfer scaled values, scaled units, preload values, nor any floating point information or items that are not represented in a register. The Export Status window will let you know if the configuration was received correctly at the remote station (destination stations may still require their own calibration).

POSSIBLE CAUSE	POSSIBLE FIX
<i>Green RUN LED Does Not Light...</i>	
Bad connections.	Recheck Power Connections
Try a system reset.	Use the RST toggle to reset the unit.
Internal +3.3V power has failed or a fatal processor (firmware) error has occurred.	Return unit for repair.
<i>Continuous Flashing Green RUN LED...</i>	
A network link has not been established.	Check your cable and switch/hub connections. Once a link is established, the green Run LED should not continue to blink but just remain ON. If it continues to blink, then the firmware may be in error.
Unit was not initially connected to network upon power-up, or the network cable is bad.	The RUN LED will continue to blink as the unit hunts for a network link. Connect a network cable to the unit to complete its initialization. This only occurs for initial communication.
Unit in "wink" mode.	Read Status register to verify "wink" status. Write 5555H to Wink Toggle Register to toggle wink mode off/on.
Unit failed to boot firmware.	A continuously flashing green Run LED can signify the unit has failed to initialize and may require repair if you are sure you have a good network connection and proper power voltage.
<i>Repeated System Resets Occur with Redundant Port Connections...</i>	
External network or switch is not setup to handle redundant media.	Use an external redundant switch to connect to both ports of this device.
<i>Cannot Communicate...</i>	
Power ON to the unit?	Check if green RUN LED is ON?
Fiber Connections not crossed over.	The auto-crossing feature does not apply to the fiber port. These connections must physically cross transmit to receive and visa-versa.
Wrong IP Address	Change IP address of unit or host PC so they match domains. Try the default unit address of 128.1.1.100.
<i>Cannot Communicate Following Restore Procedure...</i>	
Wrong IP Address	IP address has been restored to its default unit address of 128.1.1.100.
<i>Many Communication Errors...</i>	
Is cable segment longer than 100M?	Distance between two Ethernet nodes is limited to 100 meters with approved cable.
Correct Cable?	Shielded CAT-5/5E cable or equivalent is recommended.
Missing earth ground connection.	Connect earth ground to TS5 GND terminal adjacent to power terminal.
<i>Cannot Browse Unit...</i>	
Your browser may be setup to use a proxy server for LAN communications.	Temporarily disable the use of a proxy server by your browser (see procedure of next page).

TROUBLE-SHOOTING

Diagnostics Table

Upon power-up, after blinking momentarily the green "Run" LED should remain ON. This indicates the unit is properly powered and operating normally. If RUN continues to blink, then the unit may not be connected to the network or the cable is bad. Otherwise, a continuous blinking RUN LED can indicate unit is in "wink" ID mode, or it may be indicative of a firmware initialization error.

TROUBLE-SHOOTING

Diagnostics Table

POSSIBLE CAUSE	POSSIBLE FIX
<i>Redundancy Failover Protection Not Occurring...</i>	
Your unit's network configuration is not in hub/repeater mode.	Set the unit to hub/repeater mode (not switch mode) for redundant media applications.
<i>Cannot Access Web Pages (Unit Won't Accept Username & Password)...</i>	
Have you forgotten your username and password settings?	Return unit to Default Mode and use the default username and password to gain access to the Password Configuration Page to reset them to something you can remember.
<i>Fiber Port Not Communicating...</i>	
Is fiber cable crossed over? Have you selected 100MB and Full-Duplex for fiber port 1? Note the Tx channel of the unit is the bottom half of the SC fiber connector, while the Rx channel is the top half (facing front of unit).	The auto-crossing feature does not apply to fiber connections, which must physically crossover the transmit and receive channels. Further, the fiber port communicates at full-duplex and 100M only and auto-negotiation is not possible.
<i>Communication To Unit is Lost...</i>	
Was communication interrupted by severe interference or shock?	Reset the unit via the RST toggle or by cycling power.
<i>Unit Fails to Start-up or Initialize...</i>	
Input power voltage below 18V?	Check your power supply voltage and make sure that it is at least 18V and of sufficient capacity (select a current capacity at least 2x the maximum current draw of the unit).
<i>Adding another unit to network slows web page interaction considerably...</i>	
Does each unit have a unique MAC address? <i>All units are normally shipped with a unique MAC address assigned from the factory. An error in shipment could release a unit with a default MAC address (52:4F:42:45:52:54).</i>	Go to the Network Config Page and verify that each unit has a unique MAC address installed. This should always be the case. If you have 2 units with same MAC address, this will slow down communications considerably and you must contact the factory for MAC reassignment.
<i>Inputs Appear Noisy or Unstable...</i>	
Have you grounded your inputs? Note that un-grounded I/O requires an earth-ground connection at port common.	You must connect the Port Common terminal to earth ground if your input source is not already grounded.
Have you tried Input Averaging?	You can use controls on the Input Configuration Page to set the number of samples to average inputs over. Try increasing this number above 1 to minimize noise (up to 500).
<i>Input Polarity is Wrong...</i>	
Are your input terminals reversed?	Observe proper polarity for voltage inputs. Current can be input to the input (+) or common (COM) terminals if a non-polarized range is selected.

POSSIBLE CAUSE	POSSIBLE FIX
<i>Unit Fails Input Self Test...</i>	
Internal calibration reference has failed or has been miscalibrated.	Check the reference field of the Calibration Page and make sure a voltage between 4.4968 and 4.5032 (4.5V±0.07%) is indicated. If not, you may attempt to calibrate this yourself, or return the unit to Acromag for calibration, repair, or replacement.
<i>Cannot Calibrate Input Channel...</i>	
Have you also enabled the channel's scan group?	See the Enable Scan Groups Register and/or the Input Config web page to enable input to be sampled.
<i>Current Inputs Have Greater Inaccuracy...</i>	
Auto calibration of current inputs fails to compensate for errors in the current shunt resistor.	Refer to Input Cal Page and perform a manual cal of the input if auto calibration results are not sufficient.
<i>Cannot Write Outputs...</i>	
Have you mapped a local output port to your inputs via the i2o configuration page?	The i2o mapping function will block direct writes to a local output if mapped to its port. Make sure that you have not enabled this feature.
<i>Loop-back Reading of Current Outputs Has Offset Error...</i>	
ES2151 loop-back is based on the DAC voltage that drives the last V/I stage and fails to compensate for last stage offsets. Also, the loop-back value is not corrected for any manual calibration offsets.	Refer to the Output Calibration Page and perform a manual calibration of the outputs to improve performance if auto-cal results are not sufficient. This will not correct the loop-back reading on current outputs though.
<i>Cannot Clear a Watchdog Timeout...</i>	
You can clear a timeout by writing to an output that has timed out, but local i2o mapping to an output port on same unit will prevent you from directly writing to that output?	The i2o mapping function will block direct writes to a local output if mapped to its port, preventing writes from resetting the timeout. Verify that you have not enabled this feature.
<i>Channel Will Not Assume A New Timeout Level...</i>	
If you write a new timeout level to an output while it is timed out, it will not assume the new value until the current timeout has been cleared.	Write an interim value to the timed out output channel to clear its timeout status and assume a the new timeout level.
<i>ES2151 Current Outputs Have Greater Inaccuracy...</i>	
Auto calibration of current outputs fails to compensate for errors in the last stage (voltage to current converter).	Refer to the Output Calibration Page and perform a manual calibration of the outputs to improve performance if auto-cal results are not sufficient.
<i>Cannot Get i2o to Write the Output Port On Another Unit...</i>	
Have you specified a target starting address that represents 8 valid contiguous output addresses?	The i2o target is returning errors because the sending unit is trying to write beyond its valid output register space. Refer to the i2o Configuration Page and verify that the Map to Holding Register specified addresses eight contiguous register addresses (40351 to 40359 for ES215x models).

TROUBLE-SHOOTING

Diagnostics Table

If your problem still exists after checking your wiring and reviewing this information, or if other evidence points to another problem with the unit, an effective and convenient fault diagnosis method is to Exchange the unit with a known good unit. Acromag's Application Engineers can provide further technical assistance if required. Complete repair services are also available from Acromag.

TROUBLE-SHOOTING

Trouble Browsing Your Unit?

Refer to Acromag Application Note 8500-734 for help in setting up network communication with your unit (see CDROM shipped with unit or down-load it from www.acromag.com). This document gives details for changing your PC's TCP/IP configuration in order to communicate with hardware similar to your unit (see TCP/IP Properties of Network Configuration in Windows). If you have carefully followed this procedure and you still cannot browse your unit, you may have the web browser of your laptop or PC setup to use a proxy server when browsing the web. If you are using Internet Explorer, refer to the "Tools" pull-down menu, select "Internet options...", click the "Connections" tab, then click the "LAN Settings" button. Locate the Proxy server information and uncheck the box next to the statement "Use a proxy server for your LAN". Then click [OK] to return to the "Connections" screen and click [OK] again to save your settings. This should allow you to use Internet Explorer to browse the unit as required. However, to later restore your PC's connection to your company network, you may have to re-enable the use of a proxy server for your LAN.

Also note that the green run LED of the unit will blink continuously after power-up if you have failed to establish an initial network link with it. However, it does not start blinking later if you break a link that has been established since powering up. If you have verified that your network cable link is solid, then this LED can also blink continuously if the internal firmware has encountered a fatal error (see Getting Out of Trouble below). The third thing that can cause this LED to blink continuously is if the Wink ID function has been triggered (see Network Configuration page for this toggle control).

Getting Out Of Trouble & Sanitation Procedure



So, your EtherStax has apparently "gone wild", and resetting the unit did not correct your problem, then follow this procedure to restore it to its initial configuration and regain control.

If you do use restore and want to return the unit to service, you will also have to separately restore the calibration reference, and trigger the Output Self-Calibration procedure.

There is limited error checking to keep you from writing invalid values to a configuration register and operation may become unpredictable if you do this under certain conditions. If resetting the unit fails to restore order, then to regain control of the unit, the unit can either be re-downloaded at the factory, or you can try restoring its initial configuration by following this procedure:

Procedure For Restoring any EtherStax Unit to its Initial Configuration (Also used if you wish to sanitize the unit and return it to original state)

IMPORTANT: Use this only as a last resort, as this procedure will reset everything to its default state--all holding registers, network settings, i2o settings, and calibration (the permanently coded MAC ID does not change). It also restores the IP address to 128.1.1.100.

1. With unit power OFF, press and hold the front-panel toggle switch in the default (DFT upward) position.
2. Holding the toggle switch in the default position and apply power.
3. Note the green RUN LED will turn ON. Continue to hold the toggle at the DFT position for about 10 seconds until the green RUN LED turns OFF. Release the toggle switch at this point and the RUN LED will blink for 1-10 seconds as the unit acquires its address, then remains ON for normal operation. At this point, the unit is not in the default communication mode, but all registers are reset back to their default factory state.
4. If the green RUN LED never turned OFF while you held the DFT toggle during power-up, then reinitializing the unit has failed and you should try it again. This time, make sure that the DFT toggle switch is completely depressed and held until RUN turns OFF while powering the unit. Also make sure that you are holding the DFT toggle in the DFT direction (upward), rather than the RST direction (downward).

TECHNICAL REFERENCE

- **High Density Flexible Industrial I/O** – Supports up to 48 analog I/O channels comprised of up to 16 current inputs, 16 voltage inputs, and 16 analog current or voltage outputs according to model. All models provide a DB25 interface for optional connection to ABPXX back-panels for support of up to sixteen 8B input modules. ES2152 models add a second DB25 interface for additional support of up to sixteen 8B output modules. Series 8B modules and carriers support a variety of I/O types.
- **Field Voltage and Current Inputs Can Mix With External 8B Inputs** – Allows the unit's own field inputs to intermix with 8B inputs on external carriers for a wider variety of input signal types and range selection.
- **Output Loop Back** – Allows the unit to read its own outputs via independent multiplexing circuitry. This also helps to facilitate self calibration and self test of the outputs.
- **Built-In Calibration Source** – A very accurate reference signal source is built into the unit to facilitate self-calibration and self-test.
- **Input Self-Calibration** – Unit utilizes a precision calibration reference to automatically calibrate the A/D response.
- **Output Self-Calibration** – Unit utilizes output loop back and a precision calibration reference to automatically calibrate the DAC output voltage.
- **Web-Browser Reconfiguration** - Unit may be configured, controlled, monitored, and calibrated using a standard web browser over Ethernet.
- **Achieves End-Node Redundancy** – Dual network ports can accomplish media redundancy right to the unit, when also connected to redundant switches that support STP, RSTP, or most other proprietary ring redundancy methods.
- **Fully Isolated** – I/O channels (as a group), alarm relay, network ports, enclosure, and power are all isolated from each other for safety and increased noise immunity.
- **Wideband Inputs** – Unit can process 32 analog inputs and 16 analog outputs in as fast as 10ms (100Hz), with no input averaging.
- **16-bit A/D Input & D/A Conversion** – High-quality, high-resolution 16-bit A/D input and D/A output conversion.
- **Integrating/Totalizing Inputs Possible** – Scaling registers and non-volatile memory make signal integration & totalization possible.
- **Input Averaging/Digital Filtering:** Allows you to average input readings over a large number of samples (up to 500).
- **Optional User-Scaling** – Inputs may be rescaled to user-defined input sub-ranges or other engineering units to facilitate integration/totalization.
- **Field Voltage & Current Inputs Can Mix w/ External 8B Inputs** – Models allow the unit's own field inputs to be mixed with inputs from 8B module carriers for a wider variety of input signal types and ranges.
- **Safety Agency Approvals** – Enclosed models are CE, & cULus listed, for Class 1; Division 2; Groups A, B, C, D hazardous locations. Open board models are cULus Recognized Components, for Class 1, Division 2, Groups A, B, C, and D hazardous locations.
- **Selectable Modbus TCP/IP or UDP/IP Protocol Support** – Up to 10 sockets of support for Modbus using TCP/IP. Also supports UDP/IP.
- **Flexible IP Addressing** – Supports static, DHCP, or BOOTP.
- **10Base-T and 100Base-TX Support** – Auto-negotiated 10/100Mbps, Half or Full Duplex.
- **100BaseFX Support** – Optional models support a 100M FX fiber-optic connection at one of two ports.

KEY FEATURES

KEY FEATURES

...continued

- **Dual Ethernet Ports Make Network Connections Easy** – A built-in switch allows units to be connected in cascaded “daisy-chain” fashion without consuming another (external) switch port. The second port also allows the network distance to be extended an additional 100 meters (copper), or 2km (fiber).
- **Network Ports are Individually Isolated & Transient Protected** – The dual network ports of this device are safety-isolated from each other and have built-in transient protection from ESD, EFT, and other transients.
- **Auto MDI/MDI-X Crossover** – Requires no special up/down link port or crossover cables to connect to a PC, external Ethernet switch, or hub.
- **Low-Latency, Cut-Through Repeater Mode** – Reduces port-to-port latency jitter of switch mode from about 167us, to 40ns, for time critical applications or concentrated traffic links.
- **Extensive Operating & Diagnostic LED's Aide Troubleshooting** – Three LED's indicate power, operating mode, wink status, and relay state. Eight communication LED's indicate per-port activity, including: communication errors, link status, collision, speed, and duplex.
- **Nonvolatile Reprogrammable Memory** – Allows the functionality of this device to be reliably reprogrammed thousands of times.
- **Internal Watchdog** - A hardware watchdog timer is built into the DSP that causes it to initiate a self reset if the controller ever “locks up” or fails to return from an operation in a timely manner.
- **Convenient “Wink” ID Mode Support** – Blinks green RUN LED in wink mode as a tool to help identify specific remote units.
- **Local Alarm Function** – Unit includes a set of SPST-NO relay contacts that can signal link loss and/or power failure (failsafe only).
- **Wide Ambient Operation** – Reliable operation from -40°C to +75°C.
- **Hardened For Harsh Environments** - For protection from RFI, EMI, ESD, EFT, & surges. Has low radiated emissions per CE requirements.
- **Shock & Vibration Immunity** – To 5g random vibration per IEC60068-2-64, to 50g mechanical shock per IEC60068-2-27 (see Specifications).
- **Rugged and Stackable Aluminum Enclosure** – The anodized aluminum enclosure also allows units to be stacked and locked together.
- **“Plug-In” Terminal Blocks** - Make wiring removal, & replacement easy.
- **Enclosure Supports Surface or DIN-Rail Mount** – Optional surface or DIN-rail mounting, even when units are stacked together.
- **Open (No Enclosure) Option** – Units can be purchased without their enclosure and stacked together via threaded standoffs & 6-32 screws.
- **Wide-Range DC-Power w/ Redundant Power Connection** - Diode-coupled for use with redundant supplies and/or battery back-up. An extra power terminal is provided for optional standby backup power.

HOW IT WORKS

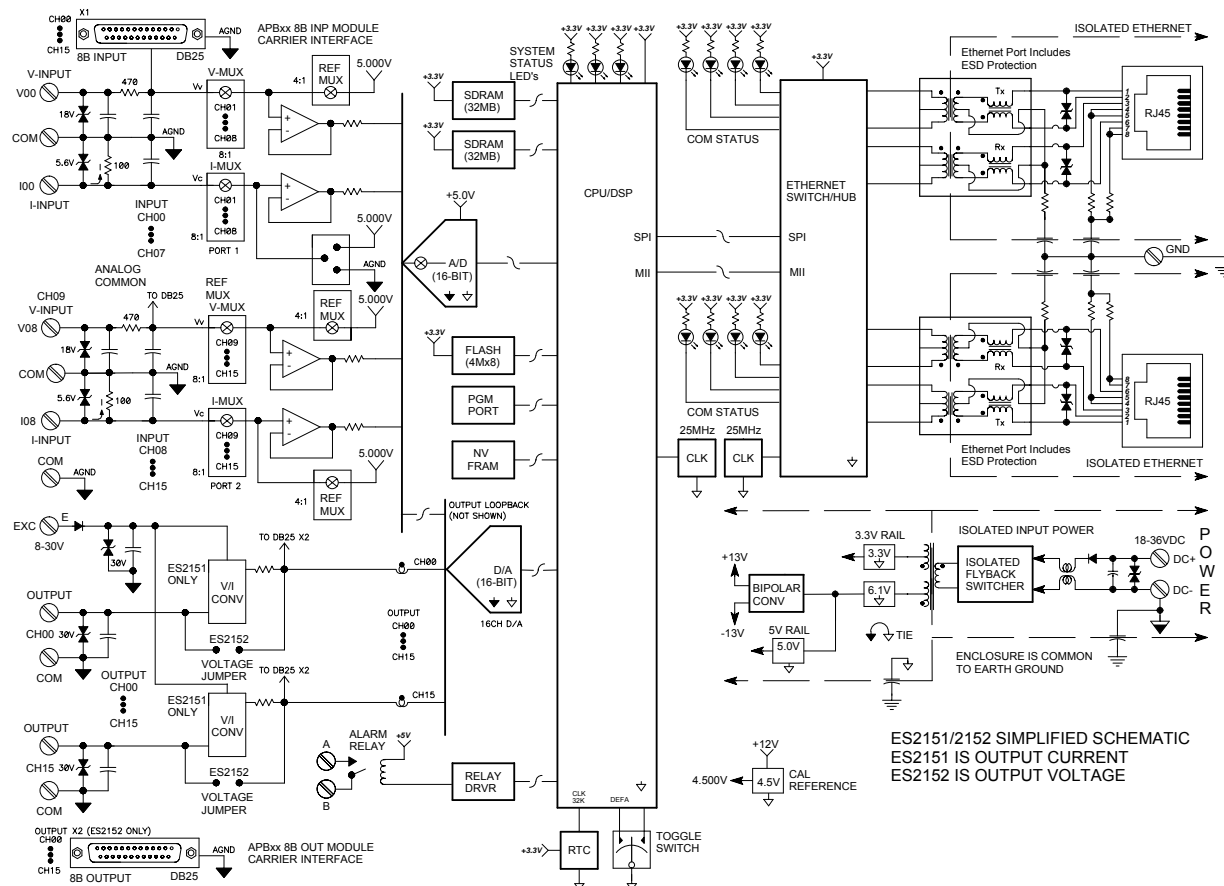
This unit will interface with up to 32 current and voltage inputs, plus 16 current or voltage outputs, and provide an isolated 10/100Mbps Ethernet interface for configuration, monitoring, calibration, and control of the I/O. All models include a female DB25 interface for optional connection to industry-standard 8B input module carriers (up to 16 channels). The ES2152 model adds a second female DB25 interface connector for optional interface to industry-standard 8B output module carriers (up to 16 channels). In this way, 8B I/O modules may intermix with field current and voltage I/O on the unit. Embedded configuration parameters are stored in non-volatile memory.

HOW IT WORKS

This circuit employs fault-tolerant 8:1 analog multiplexers to multiplex 16 voltage inputs and 16 current inputs to four separate channels of a 16-bit A/D converter. Two more multiplexers multiplex the sixteen analog output signals to two additional A/D channels for loop back monitoring. Additional circuitry can optionally connect each A/D input to 0V (zero), or a precision 5V reference to help accomplish self-calibration and self-test. The A/D can be configured for native 16-bit ranges of ±10V, or ±5V. Current inputs utilize a ±5V A/D range (a 100Ω shunt resistor converts input current to A/D voltage on these units).

The A/D's own MUX rescales the inputs via a resistor divider network utilizing a series 25KΩ resistor and a 17KΩ divider resistor, additionally coupled to an internal 4.096V A/D reference via a series 10KΩ resistor. The rescaled signals alternately connect to the ADC. The ADC uses a successive approximation algorithm and internal sample & hold circuit to convert the input signal to a 16-bit serial output stream which is transmitted to the CPU via an SPI interface.

The network interface utilizes a dedicated, 3-port, Ethernet switch to provide two external network ports to the internal CPU/MAC (third port). Both network ports are fully isolated and include transient protection. The embedded switch may also operate as an Ethernet hub, allowing automatic end-node media redundancy when both ports are connected to an external redundant switch (copper only). This also makes the node redundancy compatible with most proprietary ring redundancy methods, Spanning Tree (STP), or Rapid Spanning Tree (RSTP).



HOW IT WORKS

Key Observations

This device is packaged in a rugged aluminum enclosure which is common to the earth ground terminal and fully isolated from the internal circuitry for increased safety and noise immunity. The internal transient suppression devices are shunted to earth ground via safety rated isolation capacitors. A wide-input, high-efficiency, switching regulator (isolated flyback converter) provides isolated power to the unit. Refer to the simplified schematic below to help gain a better understanding of circuit operation.

- Ethernet ports are individually isolated from power, I/O, and each other, and include transient suppression.
- Analog current outputs require external excitation (ES2151 only). Note: The EXC voltage must be 3.3V greater than the load voltage.
- The current inputs operate independent of the voltage inputs and the DB25 8B inputs at X1.
- The DB25 X2 interface is installed on ES2152 models and intended for 8B output modules only.
- The input power and external excitation terminals are series-diode coupled for reverse polarity protection, which is also useful to facilitate redundant power connections.
- The aluminum enclosure is common to the earth ground terminal and transient suppression devices are shunted to this ground via isolation capacitors, maintaining isolation from earth ground. If an ungrounded enclosure should contact high-voltage, the connected circuits will be isolated from this fault condition.
- An I/O port common terminal (C) must be tied to earth ground to keep the I/O circuits from floating.

MODBUS REGISTERS

Register Functions

The "x" following the leading character represents a four-digit address location in user data memory.

The leading character is generally implied by the function code and omitted from the address specifier for a given function. The leading character also identifies the I/O data type.

Modbus registers are organized into reference types identified by the leading number of the reference address:

Reference	Description
0xxxx	<u>Read/Write Discrete Outputs or Coils</u> . A 0x reference address is used to drive out data to a digital out channel.
1xxxx	<u>Read Discrete Inputs</u> . The ON/OFF status of a 1x reference address is controlled by the corresponding digital input channel.
3xxxx	<u>Read Input Registers</u> . A 3x reference register contains a 16-bit number received from an external source—e.g. an analog signal.
4xxxx	<u>Read/Write Output or Holding Registers</u> . A 4x register is used to store 16-bits of numerical data (binary or decimal), or to send the data from the CPU to an output channel.

Note: The ON/OFF state of discrete inputs & outputs is represented by a 1 or 0 value assigned to an individual bit of a 16-bit data word (port), with sixteen 0x or 1x references per word. For channel mapping, the lsb of the word maps to the lowest numbered channel of a port and numbers increase sequentially as you move towards the msb.

All I/O values are accessed via the 16-bit Input Registers or 16-bit Holding Registers given in the Register Map. Input registers contain information that is read-only. For example, the current input value read from a channel, or the states of a group of digital inputs. Holding registers contain read/write information that may be configuration data or output data. For example, the high limit value of an alarm function operating at an input, or an output value for an output channel.

Each EtherStax unit has a default factory configuration as noted in the Specifications section. Your application will likely differ from the default configuration provided and the unit will need to be reconfigured. You may reconfigure most features of this unit by issuing the appropriate Modbus functions to Register Map registers, as required by your application. You may also use a standard web browser to access the built-in web pages of the unit to perform basic I/O, calibration, & reconfiguration.

Below is a subset of standard Modbus functions that are supported by this unit, along with the reference register address group that the function operates on. Use these functions to access the registers outlined in the Register Map for sending and retrieving data in order to monitor, configure, and control unit I/O:

CODE	FUNCTION	REFERENCE
01 (01H)	Read Coil (Output) Status	0xxxx
02 (02H)	Read Input Status	1xxxx
03 (03H)	Read Holding Registers	4xxxx
04 (04H)	Read Input Registers	3xxxx
05 (05H)	Force Single Coil (Output)	0xxxx
06 (06H)	Preset Single Register	4xxxx
15 (0FH)	Force Multiple Coils (Outputs)	0xxxx
16 (10H)	Preset Multiple Registers	4xxxx
17 (11H)	Report Slave ID (See Below)	<i>Hidden</i>

If an unsupported function code is sent to a unit, exception code 01 (Illegal Function) will be returned in the response. If a holding register is written with an invalid value, exception code 03 (Illegal Data Value) will be returned in the response message. You may refer to the Modbus specification for a complete list of possible error codes.

EtherStax ES2151/2152-x0x0 Report Slave ID Example Response¹

FIELD	DESCRIPTION
Unit ID	Echo Unit ID Sent In Query
Function Code	11
Byte Count	43
Slave ID (Model No.) ¹	06=ES2151-x0x0 (all model variations) 07=ES2152-x0x0 (all model variations)
Run Indicator Status	FFH (ON)
Firmware Number	41 43 52 4F 4D 41 47 2C 39 33 30 30 2D 31 37
String (Additional Data Field) ¹	35 41 2C 45 53 32 31 35 31 2D 78 78 78 78 ... ("ACROMAG,9300-175x,ES2151-xxxx,serial number&rev,six-byteMACID")

¹**Note:** ES2151 models share slave ID "06" and firmware number 9300-175. ES2152 models share ID "07" and firmware model 9300-176. The firmware number is also indicated on home page of the web browser.

For your convenience, the EtherStax mirrors the contents/operation of registers 0xxxx, 1xxxx, & 3xxxx (as applicable) into holding register space for systems and controllers that cannot directly access registers 0xxxx, 1xxxx, & 3xxxx. All Modbus registers can be written to, or read from, using either the standard methods described in the Modbus specification, or through mapping (mirroring) to the Holding Registers. The registers are mapped as follows and specifics follow the mapping:

MODBUS REGISTERS

Register Functions

For detailed information on Modbus, feel free to download our technical reference 8500-648, "Introduction To Modbus", at www.acromag.com. You can also find more information specific to Modbus TCP/IP by down-loading whitepaper 8500-765, "Introduction To Modbus TCP/IP". Additional information regarding Ethernet can also be found in our whitepaper 8500-747, "Introduction To Ethernet/IP".

Register Mirroring

MODBUS REGISTERS

0xxxx Coil Registers are mapped to 42xxx Holding Registers
 1xxxx Input Status Registers are mapped to 41xxx Holding Registers
 3xxxx Input Registers are mapped to 43xxx Holding Registers

For 3xxxx Input Registers, the format of the registers are identical and you only need to offset your address by 43000. For example: if you want to read

Input Register 1 through the Holding Registers, you would use the "Read Holding Registers" function with an address of 43001.

For the 1xxxx Input Status Registers, the return data is reformatted to match the Holding Register format. For example: if you request the Input Status for 16 digital inputs, instead of getting 2 bytes returned with the 16 bits representing the 16 digital inputs, you will get 16 separate words, each set to either 0000H (OFF), or FFFFH (ON).

For the 0xxxx Coil Registers, reads are handled in the same way as the 1xxxx Input Status Registers. You can also write to the coil registers by using the "Preset Single Register" function with an address offset of 42000. Setting the data to 0000H will turn the coil OFF, while setting the data to FF00H will turn the coil ON. Writing to multiple coils is not supported via register mirroring, you must use the "Write Multiple Coils" function for that.

Data Types

I/O values for ES2100 units are represented by the following simple data types for temperature, percentage, and discrete on/off.

Summary of Simple Data Types Used By EtherStax Models

Data Types and Description	
Discrete	A single bit of a 16-bit word with the bit number/position typically corresponding to a discrete channel number. Unless otherwise defined, a 1 bit means the corresponding output is closed or ON, or input is ON (active-low, near 0V). A 0 bit means the output is open or OFF, or the input is OFF or in its high state (usually >> 0V).
Analog Data (This Model)	A 16-bit signed integer with a possible range of -32768 to +32767. Analog input ranges of this model are normalized to ± 30000 , which represents $\pm 100\%$ of the pre-defined range with a resolution of 0.003%/lsb. For example, -100%, 0% and +100% are represented by decimal values -30000, 0, and 30000, respectively. A full possible range is -109% (-32768 decimal) to +109% (+32767 decimal). Thus, a 0-20mA input would be represented by a register count of 0-30000, and have a maximum value near ~21.8mA.
IEEE 754 (This Model)	This is a standard digital format used to represent real numbers on a computer. This unit uses single-precision, 32-bit, IEEE Standard 754 floating point number format for storage of normalized input data, scaled input data, and totalized input data. 32-bit data is stored in two successive 16-bit Modbus registers. For more information see web site at http://standards.ieee.org .
Temperature	A 16-bit signed integer value with resolution of 0.1°C/lsb. For example, a value of 12059 is equivalent to 1205.9°C, a value of -187 equals -18.7°C. The maximum possible temperature range is -3276.8°C to +3276.7°C.

The following table outlines the register map for all model variations of the EtherStax ES2151 & ES2152 I/O processors. The Modbus functions operate on these registers using the data types noted above (except for the Reset Slave and Report Slave ID functions).

Not all programmable features of this device will include a corresponding Modbus configuration register. Some functionality must be programmed via the built-in web browser interface. For example, parameters related to network communications do not have a Modbus register and are programmed solely through the built-in web interface.

Register Map

Model ES2151 Model ES2152

Ref	Addr.	Description	Data Type/Format
Input Registers (3x References, Read-Only)			
30001	0000	Unit Status <i>Check for fiber-optic option, wink mode, or default mode.</i>	Bit 15: Fiber Optic Flag 1 = Fiber Optic Transceiver Installed 0 = No Fiber (Dual Copper Ports). Bit 14: Wink Mode Flag 1 = Wink (Blinks Run LED for ID) (See Wink Unit Register) Bit 13: Default Mode Flag 1 = Default Mode Indication 0 = Unit not in Default Mode Bits 12-0: 0 (Not Used)
30002	0001	AO Watchdog Status	Bits 15-0 (bit position is AO channel) 1=Watchdog Timeout (Outputs) 0=No Watchdog Timeout
30003	0002	AO ES2151 Fault Status (ES2151 only) <i>Reserved, Not Used for ES2152 Models</i>	Bits 15-0 (bit position is AO channel) 1=No Fault Detected at Channel 0=Fault Detected at Channel This flag is cleared for the corresponding channel if the ES2151 output current cannot flow due to a wire break, excessive load resistance, or if the output voltage is approaching the excitation supply voltage (less than 3V of overhead). It will only operate if valid excitation (9-16V) is connected.
30004	0003	AI Voltage CH 00 Status <i>(Over/Under-range, Input Range)</i>	Bit 15-4: 0 (Not Used)
			Bit 3: Over-Range Flag
			Bit 2: Under-Range Flag
			b1 b0 Voltage Input Range
			0 0 ±5V DC
0 1 ±10 VDC			
1 0 (Not Used)			
1 1 (Not Used)			
30005	0004	CH 01 Status	See AI Voltage Channel 00 Status
30006	0005	CH 02 Status	See AI Voltage Channel 00 Status
30007	0006	CH 03 Status	See AI Voltage Channel 00 Status
30008	0007	CH 04 Status	See AI Voltage Channel 00 Status
30009	0008	CH 05 Status	See AI Voltage Channel 00 Status
30010	0009	CH 06 Status	See AI Voltage Channel 00 Status
30011	000A	CH 07 Status	See AI Voltage Channel 00 Status

Note (Fault Indication): The current output fault status is used to detect an open load (wire-break), high load resistance, or an output voltage level approaching excitation. To detect a wire break and indicate a fault, a non-zero current must be programmed, which prevents it from working at or very near the 0mA endpoint of the 0-20mA range. Likewise, it only works with a valid excitation voltage applied above 8V, which means that low resistance loads below 300Ω may never trigger a fault. It will typically trigger a fault for valid excitation voltage levels less than 3V above the load voltage, indicating that either the load resistance must decrease, or the excitation voltage must increase.

Register Map

Model ES2151
Model ES2152

Ref	Addr.	Description	Data Type/Format															
Input Registers (3x References, Read-Only)																		
30012	000B	CH 08 Status	See AI Voltage Channel 00 Status															
30013	000C	CH 09 Status	See AI Voltage Channel 00 Status															
30014	000D	CH 10 Status	See AI Voltage Channel 00 Status															
30015	000E	CH 11 Status	See AI Voltage Channel 00 Status															
30016	000F	CH 12 Status	See AI Voltage Channel 00 Status															
30017	0010	CH 13 Status	See AI Voltage Channel 00 Status															
30018	0011	CH 14 Status	See AI Voltage Channel 00 Status															
30019	0012	CH 15 Status	See AI Voltage Channel 00 Status															
30020	0013	AI Current CH 00 Status (Over/Under-range, Input Range)	Bit 15-4: 0 (Not Used) Bit 3: Over-Range Flag Bit 2: Under-Range Flag Bits 1,0: Input Range As Follows: <table border="1"> <thead> <tr> <th>b1 b0</th> <th>Current Input Range</th> </tr> </thead> <tbody> <tr> <td>0 0</td> <td>±20mA</td> </tr> <tr> <td>0 1</td> <td>0-20mA</td> </tr> <tr> <td>1 0</td> <td>4-20mA</td> </tr> <tr> <td>1 1</td> <td>(Not Used)</td> </tr> </tbody> </table>	b1 b0	Current Input Range	0 0	±20mA	0 1	0-20mA	1 0	4-20mA	1 1	(Not Used)					
b1 b0	Current Input Range																	
0 0	±20mA																	
0 1	0-20mA																	
1 0	4-20mA																	
1 1	(Not Used)																	
30021	0014	CH 01 Status	See AI Current Channel 00 Status															
30022	0015	CH 02 Status	See AI Current Channel 00 Status															
30023	0016	CH 03 Status	See AI Current Channel 00 Status															
30024	0017	CH 04 Status	See AI Current Channel 00 Status															
30025	0018	CH 05 Status	See AI Current Channel 00 Status															
30026	0019	CH 06 Status	See AI Current Channel 00 Status															
30027	001A	CH 07 Status	See AI Current Channel 00 Status															
30028	001B	CH 08 Status	See AI Current Channel 00 Status															
30029	001C	CH 09 Status	See AI Current Channel 00 Status															
30030	001D	CH 10 Status	See AI Current Channel 00 Status															
30031	001E	CH 11 Status	See AI Current Channel 00 Status															
30032	001F	CH 12 Status	See AI Current Channel 00 Status															
30033	0020	CH 13 Status	See AI Current Channel 00 Status															
30034	0021	CH 14 Status	See AI Current Channel 00 Status															
30035	0022	CH 15 Status	See AI Current Channel 00 Status															
30036	0023	AO CH 00 Configuration	Bit 15-2: 0 (Not Used) Bit 1-0: Range Per Model (See Below) <table border="1"> <thead> <tr> <th>b1 b0</th> <th>ES2151</th> <th>ES2152</th> </tr> </thead> <tbody> <tr> <td>0 0</td> <td>0-20mA</td> <td>±10V DC</td> </tr> <tr> <td>0 1</td> <td>4-20mA</td> <td>±5 VDC</td> </tr> <tr> <td>1 0</td> <td>(Not Used)</td> <td>(Not Used)</td> </tr> <tr> <td>1 1</td> <td>(Not Used)</td> <td>(Not Used)</td> </tr> </tbody> </table>	b1 b0	ES2151	ES2152	0 0	0-20mA	±10V DC	0 1	4-20mA	±5 VDC	1 0	(Not Used)	(Not Used)	1 1	(Not Used)	(Not Used)
b1 b0	ES2151	ES2152																
0 0	0-20mA	±10V DC																
0 1	4-20mA	±5 VDC																
1 0	(Not Used)	(Not Used)																
1 1	(Not Used)	(Not Used)																
30037	0024	AO CH 01 Cfg	See AO Channel 00 Configuration															
30038	0025	AO CH 02 Cfg	See AO Channel 00 Configuration															
30039	0026	AO CH 03 Cfg	See AO Channel 00 Configuration															
30040	0027	AO CH 04 Cfg	See AO Channel 00 Configuration															
30041	0028	AO CH 05 Cfg	See AO Channel 00 Configuration															
30042	0029	AO CH 06 Cfg	See AO Channel 00 Configuration															
30043	002A	AO CH 07 Cfg	See AO Channel 00 Configuration															
30044	002B	AO CH 08 Cfg	See AO Channel 00 Configuration															
30045	002C	AO CH 09 Cfg	See AO Channel 00 Configuration															

Ref	Addr.	Description	Data Type/Format
Input Registers (3x References, Read-Only)			
30046	002D	AO CH 10 Cfg	See AO Channel 00 Configuration
30047	002E	AO CH 11 Cfg	See AO Channel 00 Configuration
30048	002F	AO CH 12 Cfg	See AO Channel 00 Configuration
30049	0030	AO CH 13 Cfg	See AO Channel 00 Configuration
30050	0031	AO CH 14 Cfg	See AO Channel 00 Configuration
30051	0032	AO CH 15 Cfg	See AO Channel 00 Configuration
30052	0033	AI CH 00 V-Data	16-bit Signed Integer Input Data
30053	0034	AI CH 01 V-Data	16-bit Signed Integer Input Data
30054	0035	AI CH 02 V-Data	16-bit Signed Integer Input Data
30055	0036	AI CH 03 V-Data	16-bit Signed Integer Input Data
30056	0037	AI CH 04 V-Data	16-bit Signed Integer Input Data
30057	0038	AI CH 05 V-Data	16-bit Signed Integer Input Data
30058	0039	AI CH 06 V-Data	16-bit Signed Integer Input Data
30059	003A	AI CH 07 V-Data	16-bit Signed Integer Input Data
30060	003B	AI CH 08 V-Data	16-bit Signed Integer Input Data
30061	003C	AI CH 09 V-Data	16-bit Signed Integer Input Data
30062	003D	AI CH 10 V-Data	16-bit Signed Integer Input Data
30063	003E	AI CH 11 V-Data	16-bit Signed Integer Input Data
30064	003F	AI CH 12 V-Data	16-bit Signed Integer Input Data
30065	0040	AI CH 13 V-Data	16-bit Signed Integer Input Data
30066	0041	AI CH 14 V-Data	16-bit Signed Integer Input Data
30067	0042	AI CH 15 V-Data	16-bit Signed Integer Input Data
30068	0043	AI CH 00 I-Data	16-bit Signed Integer Input Data
30069	0044	AI CH 01 I-Data	16-bit Signed Integer Input Data
30070	0045	AI CH 02 I-Data	16-bit Signed Integer Input Data
30071	0046	AI CH 03 I-Data	16-bit Signed Integer Input Data
30072	0047	AI CH 04 I-Data	16-bit Signed Integer Input Data
30073	0048	AI CH 05 I-Data	16-bit Signed Integer Input Data
30074	0049	AI CH 06 I-Data	16-bit Signed Integer Input Data
30075	004A	AI CH 07 I-Data	16-bit Signed Integer Input Data
30076	004B	AI CH 08 I-Data	16-bit Signed Integer Input Data
30077	004C	AI CH 09 I-Data	16-bit Signed Integer Input Data
30078	004D	AI CH 10 I-Data	16-bit Signed Integer Input Data
30079	004E	AI CH 11 I-Data	16-bit Signed Integer Input Data
30080	004F	AI CH 12 I-Data	16-bit Signed Integer Input Data
30081	0050	AI CH 13 I-Data	16-bit Signed Integer Input Data
30082	0051	AI CH 14 I-Data	16-bit Signed Integer Input Data
30083	0052	AI CH 15 I-Data	16-bit Signed Integer Input Data
30084	0053	AO CH 00 Rd Data	16-bit Signed Int Readback Data
30085	0054	AO CH 01 Rd Data	16-bit Signed Int Readback Data
30086	0055	AO CH 02 Rd Data	16-bit Signed Int Readback Data
30087	0056	AO CH 03 Rd Data	16-bit Signed Int Readback Data
30088	0057	AO CH 04 Rd Data	16-bit Signed Int Readback Data
30089	0058	AO CH 05 Rd Data	16-bit Signed Int Readback Data
30090	0059	AO CH 06 Rd Data	16-bit Signed Int Readback Data
30091	005A	AO CH 07 Rd Data	16-bit Signed Int Readback Data

Register Map

Model ES2151 Model ES2152

The 16-bit Signed Integer Data stored here refers to the analog input range A/D count, but after normalizing it to ± 30000 for $\pm 100\%$ of the bipolar input range, or to 0-30000 for 0-100% of the unipolar input range.

The 16-bit Signed Integer Data stored here refers to the analog output loop-back count, but after normalizing it to ± 30000 for $\pm 100\%$ of the bipolar output range, or to 0-30000 for 0-100% of the unipolar output range.

Register Map

Model ES2151
Model ES2152

The 16-bit Signed Integer Data stored here refers to the analog range count, but after normalizing it to ± 30000 for $\pm 100\%$ of bipolar ranges, or to $0-30000$ for $0-100\%$ of unipolar input ranges.

Ref	Addr.	Description	Data Type/Format
<i>Input Registers (3x References, Read-Only)</i>			
30092	005B	AO CH 08 Rd Data	16-bit Signed Int Readback Data
30093	005C	AO CH 09 Rd Data	16-bit Signed Int Readback Data
30094	005D	AO CH 10 Rd Data	16-bit Signed Int Readback Data
30095	005E	AO CH 11 Rd Data	16-bit Signed Int Readback Data
30096	005F	AO CH 12 Rd Data	16-bit Signed Int Readback Data
30097	0060	AO CH 13 Rd Data	16-bit Signed Int Readback Data
30098	0061	AO CH 14 Rd Data	16-bit Signed Int Readback Data
30099	0062	AO CH 15 Rd Data	16-bit Signed Int Readback Data
30100	0063	V CH 00 Scaled LO	IEEE-754 Floating Point LO Bytes
30101	0064	V CH 00 Scaled HI	IEEE-754 Floating Point HI Bytes
30102	0065	V CH 01 Scaled LO	IEEE-754 Floating Point LO Bytes
30103	0066	V CH 01 Scaled HI	IEEE-754 Floating Point HI Bytes
30104	0067	V CH 02 Scaled LO	IEEE-754 Floating Point LO Bytes
30105	0068	V CH 02 Scaled HI	IEEE-754 Floating Point HI Bytes
30106	0069	V CH 03 Scaled LO	IEEE-754 Floating Point LO Bytes
30107	006A	V CH 03 Scaled HI	IEEE-754 Floating Point HI Bytes
30108	006B	V CH 04 Scaled LO	IEEE-754 Floating Point LO Bytes
30109	006C	V CH 04 Scaled HI	IEEE-754 Floating Point HI Bytes
30110	006D	V CH 05 Scaled LO	IEEE-754 Floating Point LO Bytes
30111	006E	V CH 05 Scaled HI	IEEE-754 Floating Point HI Bytes
30112	006F	V CH 06 Scaled LO	IEEE-754 Floating Point LO Bytes
30113	0070	V CH 06 Scaled HI	IEEE-754 Floating Point HI Bytes
30114	0071	V CH 07 Scaled LO	IEEE-754 Floating Point LO Bytes
30115	0072	V CH 07 Scaled HI	IEEE-754 Floating Point HI Bytes
30116	0073	V CH 08 Scaled LO	IEEE-754 Floating Point LO Bytes
30117	0074	V CH 08 Scaled HI	IEEE-754 Floating Point HI Bytes
30118	0075	V CH 09 Scaled LO	IEEE-754 Floating Point LO Bytes
30119	0076	V CH 09 Scaled HI	IEEE-754 Floating Point HI Bytes
30120	0077	V CH 10 Scaled LO	IEEE-754 Floating Point LO Bytes
30121	0078	V CH 10 Scaled HI	IEEE-754 Floating Point HI Bytes
30122	0079	V CH 11 Scaled LO	IEEE-754 Floating Point LO Bytes
30123	007A	V CH 11 Scaled HI	IEEE-754 Floating Point HI Bytes
30124	007B	V CH 12 Scaled LO	IEEE-754 Floating Point LO Bytes
30125	007C	V CH 12 Scaled HI	IEEE-754 Floating Point HI Bytes
30126	007D	V CH 13 Scaled LO	IEEE-754 Floating Point LO Bytes
30127	007E	V CH 13 Scaled HI	IEEE-754 Floating Point HI Bytes
30128	007F	V CH 14 Scaled LO	IEEE-754 Floating Point LO Bytes
30129	0080	V CH 14 Scaled HI	IEEE-754 Floating Point HI Bytes
30130	0081	V CH 15 Scaled LO	IEEE-754 Floating Point LO Bytes
30131	0082	V CH 15 Scaled HI	IEEE-754 Floating Point HI Bytes

Ref	Addr.	Description	Data Type/Format
Input Registers (3x References, Read-Only)			
30132	0083	I CH 00 Scaled LO	IEEE-754 Floating Point LO Bytes
30133	0084	I CH 00 Scaled HI	IEEE-754 Floating Point HI Bytes
30134	0085	I CH 01 Scaled LO	IEEE-754 Floating Point LO Bytes
30135	0086	I CH 01 Scaled HI	IEEE-754 Floating Point HI Bytes
30136	0087	I CH 02 Scaled LO	IEEE-754 Floating Point LO Bytes
30137	0088	I CH 02 Scaled HI	IEEE-754 Floating Point HI Bytes
30138	0089	I CH 03 Scaled LO	IEEE-754 Floating Point LO Bytes
30139	008A	I CH 03 Scaled HI	IEEE-754 Floating Point HI Bytes
30140	008B	I CH 04 Scaled LO	IEEE-754 Floating Point LO Bytes
30141	008C	I CH 04 Scaled HI	IEEE-754 Floating Point HI Bytes
30142	008D	I CH 05 Scaled LO	IEEE-754 Floating Point LO Bytes
30143	008E	I CH 05 Scaled HI	IEEE-754 Floating Point HI Bytes
30144	008F	I CH 06 Scaled LO	IEEE-754 Floating Point LO Bytes
30145	0090	I CH 06 Scaled HI	IEEE-754 Floating Point HI Bytes
30146	0091	I CH 07 Scaled LO	IEEE-754 Floating Point LO Bytes
30147	0092	I CH 07 Scaled HI	IEEE-754 Floating Point HI Bytes
30148	0093	I CH 08 Scaled LO	IEEE-754 Floating Point LO Bytes
30149	0094	I CH 08 Scaled HI	IEEE-754 Floating Point HI Bytes
30150	0095	I CH 09 Scaled LO	IEEE-754 Floating Point LO Bytes
30151	0096	I CH 09 Scaled HI	IEEE-754 Floating Point HI Bytes
30152	0097	I CH 10 Scaled LO	IEEE-754 Floating Point LO Bytes
30153	0098	I CH 10 Scaled HI	IEEE-754 Floating Point HI Bytes
30154	0099	I CH 11 Scaled LO	IEEE-754 Floating Point LO Bytes
30155	009A	I CH 11 Scaled HI	IEEE-754 Floating Point HI Bytes
30156	009B	I CH 12 Scaled LO	IEEE-754 Floating Point LO Bytes
30157	009C	I CH 12 Scaled HI	IEEE-754 Floating Point HI Bytes
30158	009D	I CH 13 Scaled LO	IEEE-754 Floating Point LO Bytes
30159	009E	I CH 13 Scaled HI	IEEE-754 Floating Point HI Bytes
30160	009F	I CH 14 Scaled LO	IEEE-754 Floating Point LO Bytes
30161	00A0	I CH 14 Scaled HI	IEEE-754 Floating Point HI Bytes
30162	00A1	I CH 15 Scaled LO	IEEE-754 Floating Point LO Bytes
30163	00A2	I CH 15 Scaled HI	IEEE-754 Floating Point HI Bytes
30164	00A3	AO CH00 Rd LO	IEEE-754 Floating Point LO Bytes
30165	00A4	AO CH00 Rd HI	IEEE-754 Floating Point HI Bytes
30166	00A5	AO CH01 Rd LO	IEEE-754 Floating Point LO Bytes
30167	00A6	AO CH01 Rd HI	IEEE-754 Floating Point HI Bytes
30168	00A7	AO CH02 Rd LO	IEEE-754 Floating Point LO Bytes
30169	00A8	AO CH02 Rd HI	IEEE-754 Floating Point HI Bytes
30170	00A9	AO CH03 Rd LO	IEEE-754 Floating Point LO Bytes
30171	00AA	AO CH03 Rd HI	IEEE-754 Floating Point HI Bytes
30172	00AB	AO CH04 Rd LO	IEEE-754 Floating Point LO Bytes
30173	00AC	AO CH04 Rd HI	IEEE-754 Floating Point HI Bytes
30174	00AD	AO CH05 Rd LO	IEEE-754 Floating Point LO Bytes
30175	00AE	AO CH05 Rd HI	IEEE-754 Floating Point HI Bytes
30176	00AF	AO CH06 Rd LO	IEEE-754 Floating Point LO Bytes
30177	00B0	AO CH06 Rd HI	IEEE-754 Floating Point HI Bytes
30178	00B1	AO CH07 Rd LO	IEEE-754 Floating Point LO Bytes

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Register Map

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Ref	Addr.	Description	Data Type/Format
Input Registers (3x References, Read-Only)			
30179	00B2	AO CH07 Rd HI	IEEE-754 Floating Pt HI Bytes
30180	00B3	AO CH08 Rd LO	IEEE-754 Floating Pt LO Bytes
30181	00B4	AO CH08 Rd HI	IEEE-754 Floating Pt HI Bytes
30182	00B5	AO CH09 Rd LO	IEEE-754 Floating Pt LO Bytes
30183	00B6	AO CH09 Rd HI	IEEE-754 Floating Pt HI Bytes
30184	00B7	AO CH10 Rd LO	IEEE-754 Floating Pt LO Bytes
30185	00B8	AO CH10 Rd HI	IEEE-754 Floating Pt HI Bytes
30186	00B9	AO CH11 Rd LO	IEEE-754 Floating Pt LO Bytes
30187	00BA	AO CH11 Rd HI	IEEE-754 Floating Pt HI Bytes
30188	00BB	AO CH12 Rd LO	IEEE-754 Floating Pt LO Bytes
30189	00BC	AO CH12 Rd HI	IEEE-754 Floating Pt HI Bytes
30190	00BD	AO CH13 Rd LO	IEEE-754 Floating Pt LO Bytes
30191	00BE	AO CH13 Rd HI	IEEE-754 Floating Pt HI Bytes
30192	00BF	AO CH14 Rd LO	IEEE-754 Floating Pt LO Bytes
30193	00C0	AO CH14 Rd HI	IEEE-754 Floating Pt HI Bytes
30194	00C1	AO CH15 Rd LO	IEEE-754 Floating Pt LO Bytes
30195	00C2	AO CH15 Rd HI	IEEE-754 Floating Pt HI Bytes
30196	00C3	V CH00 Totalized LO	IEEE-754 Floating Pt LO Bytes
30197	00C4	V CH00 Totalized HI	IEEE-754 Floating Pt HI Bytes
30198	00C5	V CH01 Totalized LO	IEEE-754 Floating Pt LO Bytes
30199	00C6	V CH01 Totalized HI	IEEE-754 Floating Pt HI Bytes
30200	00C7	V CH02 Totalized LO	IEEE-754 Floating Pt LO Bytes
30201	00C8	V CH02 Totalized HI	IEEE-754 Floating Pt HI Bytes
30202	00C9	V CH03 Totalized LO	IEEE-754 Floating Pt LO Bytes
30203	00CA	V CH03 Totalized HI	IEEE-754 Floating Pt HI Bytes
30204	00CB	V CH04 Totalized LO	IEEE-754 Floating Pt LO Bytes
30205	00CC	V CH04 Totalized HI	IEEE-754 Floating Pt HI Bytes
30206	00CD	V CH05 Totalized LO	IEEE-754 Floating Pt LO Bytes
30207	00CE	V CH05 Totalized HI	IEEE-754 Floating Pt HI Bytes
30208	00CF	V CH06 Totalized LO	IEEE-754 Floating Pt LO Bytes
30209	00D0	V CH06 Totalized HI	IEEE-754 Floating Pt HI Bytes
30210	00D1	V CH07 Totalized LO	IEEE-754 Floating Pt LO Bytes
30211	00D2	V CH07 Totalized HI	IEEE-754 Floating Pt HI Bytes
30212	00D3	V CH08 Totalized LO	IEEE-754 Floating Pt LO Bytes
30213	00D4	V CH08 Totalized HI	IEEE-754 Floating Pt HI Bytes
30214	00D5	V CH09 Totalized LO	IEEE-754 Floating Pt LO Bytes
30215	00D6	V CH09 Totalized HI	IEEE-754 Floating Pt HI Bytes
30216	00D7	V CH10 Totalized LO	IEEE-754 Floating Pt LO Bytes
30217	00D8	V CH10 Totalized HI	IEEE-754 Floating Pt HI Bytes
30218	00D9	V CH11 Totalized LO	IEEE-754 Floating Pt LO Bytes
30219	00DA	V CH11 Totalized HI	IEEE-754 Floating Pt HI Bytes
30220	00DB	V CH12 Totalized LO	IEEE-754 Floating Pt LO Bytes
30221	00DC	V CH12 Totalized HI	IEEE-754 Floating Pt HI Bytes
30222	00DD	V CH13 Totalized LO	IEEE-754 Floating Pt LO Bytes
30223	00DE	V CH13 Totalized HI	IEEE-754 Floating Pt HI Bytes
30224	00DF	V CH14 Totalized LO	IEEE-754 Floating Pt LO Bytes
30225	00E0	V CH14 Totalized HI	IEEE-754 Floating Pt HI Bytes

Ref	Addr.	Description	Data Type/Format
Input Registers (3x References, Read-Only)			
30226	00E1	V CH15 Totalized LO	IEEE-754 Floating Pt LO Bytes
30227	00E2	V CH15 Totalized HI	IEEE-754 Floating Pt HI Bytes
30228	00E3	I CH00 Totalized LO	IEEE-754 Floating Pt LO Bytes
30229	00E4	I CH00 Totalized HI	IEEE-754 Floating Pt HI Bytes
30230	00E5	I CH01 Totalized LO	IEEE-754 Floating Pt LO Bytes
30231	00E6	I CH01 Totalized HI	IEEE-754 Floating Pt HI Bytes
30232	00E7	I CH02 Totalized LO	IEEE-754 Floating Pt LO Bytes
30233	00E8	I CH02 Totalized HI	IEEE-754 Floating Pt HI Bytes
30234	00E9	I CH03 Totalized LO	IEEE-754 Floating Pt LO Bytes
30235	00EA	I CH03 Totalized HI	IEEE-754 Floating Pt HI Bytes
30236	00EB	I CH04 Totalized LO	IEEE-754 Floating Pt LO Bytes
30237	00EC	I CH04 Totalized HI	IEEE-754 Floating Pt HI Bytes
30238	00ED	I CH05 Totalized LO	IEEE-754 Floating Pt LO Bytes
30239	00EE	I CH05 Totalized HI	IEEE-754 Floating Pt HI Bytes
30240	00EF	I CH06 Totalized LO	IEEE-754 Floating Pt LO Bytes
30241	00F0	I CH06 Totalized HI	IEEE-754 Floating Pt HI Bytes
30242	00F1	I CH07 Totalized LO	IEEE-754 Floating Pt LO Bytes
30243	00F2	I CH07 Totalized HI	IEEE-754 Floating Pt HI Bytes
30244	00F3	I CH08 Totalized LO	IEEE-754 Floating Pt LO Bytes
30245	00F4	I CH08 Totalized HI	IEEE-754 Floating Pt HI Bytes
30246	00F5	I CH09 Totalized LO	IEEE-754 Floating Pt LO Bytes
30247	00F6	I CH09 Totalized HI	IEEE-754 Floating Pt HI Bytes
30248	00F7	I CH10 Totalized LO	IEEE-754 Floating Pt LO Bytes
30249	00F8	I CH10 Totalized HI	IEEE-754 Floating Pt HI Bytes
30250	00F9	I CH11 Totalized LO	IEEE-754 Floating Pt LO Bytes
30251	00FA	I CH11 Totalized HI	IEEE-754 Floating Pt HI Bytes
30252	00FB	I CH12 Totalized LO	IEEE-754 Floating Pt LO Bytes
30253	00FC	I CH12 Totalized HI	IEEE-754 Floating Pt HI Bytes
30254	00FD	I CH13 Totalized LO	IEEE-754 Floating Pt LO Bytes
30255	00FE	I CH13 Totalized HI	IEEE-754 Floating Pt HI Bytes
30256	00FF	I CH14 Totalized LO	IEEE-754 Floating Pt LO Bytes
30257	0100	I CH14 Totalized HI	IEEE-754 Floating Pt HI Bytes
30258	0101	I CH15 Totalized LO	IEEE-754 Floating Pt LO Bytes
30259	0102	I CH15 Totalized HI	IEEE-754 Floating Pt HI Bytes

Register Map

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Totalized inputs are sampled every 20ms with no input averaging, or 40ms with any input averaging enabled. Then your input signal per time base is multiplied by 20ms per sample to get your incremental increase or decrease of volume that is added to your totalized value. The max totalized value possible is 9,999,999.

Note: You must separately enable floating point if you enable the totalizer here (see Floating Point Enable register).

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40001	0 (0000)	AI Voltage input Channel 00 Configuration (Default is 0019H, see shading)	<u>Bit 15:</u> 0 (Not Used) <u>Bit 14:</u> 0=Calibration use AZ/AS 1=Use Manual Calibration <u>Bit 13:</u> 0 (Not Used) <u>Bit 12:</u> 0=Do NOT use this CH Cal for this port. 1=Use this CH Cal for Calibrating this port. <u>Bit 11:</u> 0 (Not Used) <u>Bit 10:</u> 0 (Not Used) <u>Bit 9:</u> 0 (Not Used) <u>Bit 8:</u> Totalizer Initial Value 0=Use Preload Value 1=Use Last Value <u>Bit 7:</u> Totalizer Action 0=Rollover 1=Latch <u>Bit 6:</u> Totalizer OFF/ON 0=OFF 1=ON <u>Bit 5:</u> 0 (Not Used) <u>Bits 4,3:</u> Totalizer Time Base 00 Per Second 01 Per Minute 10 Per Hour 11 NA (Reserved) <u>Bit 2:</u> 0 (Not Used) <u>Bits 1,0:</u> V-Input Range Selection. 00 ±5V 01 ±10V 10 Reserved 11 Reserved
40002	0001	AI-V CH01 Cfg	See explanation for channel 00 above.
40003	0002	AI-V CH02 Cfg	See explanation for channel 00 above.
40004	0003	AI-V CH03 Cfg	See explanation for channel 00 above.
40005	0004	AI-V CH04 Cfg	See explanation for channel 00 above.
40006	0005	AI-V CH05 Cfg	See explanation for channel 00 above.
40007	0006	AI-V CH06 Cfg	See explanation for channel 00 above.
40008	0007	AI-V CH07 Cfg	See explanation for channel 00 above.
40009	0008	AI-V CH08 Cfg	See explanation for channel 00 above.
40010	0009	AI-V CH09 Cfg	See explanation for channel 00 above.
40011	000A	AI-V CH10 Cfg	See explanation for channel 00 above.
40012	000B	AI-V CH11 Cfg	See explanation for channel 00 above.
40013	000C	AI-V CH12 Cfg	See explanation for channel 00 above.
40014	000D	AI-V CH13 Cfg	See explanation for channel 00 above.
40015	000E	AI-V CH14 Cfg	See explanation for channel 00 above.
40016	000F	AI-V CH15 Cfg	See explanation for channel 00 above.

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40017	0010	AI Current Input Channel 00 Configuration (Default is 0019H, see shading) Current Input shunt resistor is 100Ω ±0.05% ±10ppm/°C, which drives 2V full-scale at 20mA to A/D	Bit 15: 0 (Not Used) Bit 14: 0=Calibration use AZ/AS 1=Use Manual Calib Bit 13: 0 (Not Used) Bit 12: 0=Do NOT use this CH Cal for this port. 1=Use this CH Cal for Calibrating this port. Bit 11: 0 (Not Used) Bit 10: 0 (Not Used) Bit 9: 0 (Not Used) Bit 8: Totalizer Initial Value 0=Use Preload Value 1=Use Last Value Bit 7: Totalizer Action 0=Rollover 1=Latch Bit 6: Totalizer OFF/ON 0=OFF 1=ON Bit 5: 0 (Not Used) Bits 4,3: Totalizer Time Base 00 Per Second 01 Per Minute 10 Per Hour 11 NA (Reserved) Bit 2: 0 (Not Used) Bits 1,0: I-Input Range Selection. 00 ±20mA (±10V A/D) 01 0-20mAV (0-5V A/D) 10 4-20mA (0-5V A/D) 11 Reserved
40018	0011	AI-I CH01 Cfg	See explanation for channel 00 above.
40019	0012	AI-I CH02 Cfg	See explanation for channel 00 above.
40020	0013	AI-I CH03 Cfg	See explanation for channel 00 above.
40021	0014	AI-I CH04 Cfg	See explanation for channel 00 above.
40022	0015	AI-I CH05 Cfg	See explanation for channel 00 above.
40023	0016	AI-I CH06 Cfg	See explanation for channel 00 above.
40024	0017	AI-I CH07 Cfg	See explanation for channel 00 above.
40025	0018	AI-I CH08 Cfg	See explanation for channel 00 above.
40026	0019	AI-I CH09 Cfg	See explanation for channel 00 above.
40027	001A	AI-I CH10 Cfg	See explanation for channel 00 above.
40028	001B	AI-I CH11 Cfg	See explanation for channel 00 above.
40029	001C	AI-I CH12 Cfg	See explanation for channel 00 above.
40030	001D	AI-I CH13 Cfg	See explanation for channel 00 above.
40031	001E	AI-I CH14 Cfg	See explanation for channel 00 above.
40032	001F	AI-I CH15 Cfg	See explanation for channel 00 above.

Register Map

**Model ES2151
Model ES2152**

Totalized inputs are sampled every 20ms with no input averaging, or 40ms with any input averaging enabled. Then your input signal per time base is multiplied by 20ms per sample to get your incremental increase or decrease of volume that is added to your totalized value. The max totalized value possible is 9,999,999.

Note: You must separately enable floating point if you enable the totalizer here (see Floating Point Enable register).

Register Map

Model ES2151
Model ES2152

Ref	Addr.	Description	Data Type/Format															
Holding Registers (4x References, Read/Write)																		
40033	0020	AO CH 00 Configuration	Bit 15-2: 0 (Not Used) Bit 1-0: Range Per Model Below															
			<table border="1"> <thead> <tr> <th>b1b0</th> <th>ES2151</th> <th>ES2152</th> </tr> </thead> <tbody> <tr> <td>0 0</td> <td>0-20mA</td> <td>±10V DC</td> </tr> <tr> <td>0 1</td> <td>4-20mA</td> <td>±5 VDC</td> </tr> <tr> <td>1 0</td> <td>(Not Used)</td> <td>(Not Used)</td> </tr> <tr> <td>1 1</td> <td>(Not Used)</td> <td>(Not Used)</td> </tr> </tbody> </table>	b1b0	ES2151	ES2152	0 0	0-20mA	±10V DC	0 1	4-20mA	±5 VDC	1 0	(Not Used)	(Not Used)	1 1	(Not Used)	(Not Used)
b1b0	ES2151	ES2152																
0 0	0-20mA	±10V DC																
0 1	4-20mA	±5 VDC																
1 0	(Not Used)	(Not Used)																
1 1	(Not Used)	(Not Used)																
40034	0021	AO CH 01 Cfg	See AO Channel 00 Configuration															
40035	0022	AO CH 02 Cfg	See AO Channel 00 Configuration															
40036	0023	AO CH 03 Cfg	See AO Channel 00 Configuration															
40037	0024	AO CH 04 Cfg	See AO Channel 00 Configuration															
40038	0025	AO CH 05 Cfg	See AO Channel 00 Configuration															
40039	0026	AO CH 06 Cfg	See AO Channel 00 Configuration															
40040	0027	AO CH 07 Cfg	See AO Channel 00 Configuration															
40041	0028	AO CH 08 Cfg	See AO Channel 00 Configuration															
40042	0029	AO CH 09 Cfg	See AO Channel 00 Configuration															
40043	002A	AO CH 10 Cfg	See AO Channel 00 Configuration															
40044	002B	AO CH 11 Cfg	See AO Channel 00 Configuration															
40045	002C	AO CH 12 Cfg	See AO Channel 00 Configuration															
40046	002D	AO CH 13 Cfg	See AO Channel 00 Configuration															
40047	002E	AO CH 14 Cfg	See AO Channel 00 Configuration															
40048	002F	AO CH 15 Cfg	See AO Channel 00 Configuration															
40049	0030	AO CH00 Time (Default is 0000H, timer disabled)	AO Channel 00 Timeout Time Value is represented as a 16-bit unsigned integer from 0-65535 seconds representing the time that must expire before triggering a watchdog timeout if outputs have not been updated. A value of 0 disables the timer.															
40050	0031	AO CH01 Time	AO Channel 01 Timeout Time Value															
40051	0032	AO CH02 Time	AO Channel 02 Timeout Time Value															
40052	0033	AO CH03 Time	AO Channel 03 Timeout Time Value															
40053	0034	AO CH04 Time	AO Channel 04 Timeout Time Value															
40054	0035	AO CH05 Time	AO Channel 05 Timeout Time Value															
40055	0036	AO CH06 Time	AO Channel 06 Timeout Time Value															
40056	0037	AO CH07 Time	AO Channel 07 Timeout Time Value															
40057	0038	AO CH08 Time	AO Channel 08 Timeout Time Value															
40058	0039	AO CH09 Time	AO Channel 09 Timeout Time Value															
40059	003A	AO CH10 Time	AO Channel 10 Timeout Time Value															
40060	003B	AO CH11 Time	AO Channel 11 Timeout Time Value															
40061	003C	AO CH12 Time	AO Channel 12 Timeout Time Value															
40062	003D	AO CH13 Time	AO Channel 13 Timeout Time Value															
40063	003E	AO CH14 Time	AO Channel 14 Timeout Time Value															
40064	003F	AO CH15 Time	AO Channel 15 Timeout Time Value															

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40065	0040	AO CH00 TO Val (Default is 0000H, or go to 0% following a timeout)	AO Channel 00 Post Timeout Level which is the value an output is sent to after a timeout has occurred. This level is expressed as a 16-bit signed integer with a value normalized to ± 30000 for $\pm 100\%$ of input range (see Note 1).
40066	0041	AO CH01 TO Val	AO Channel 01 Post Timeout Level
40067	0042	AO CH02 TO Val	AO Channel 02 Post Timeout Level
40068	0043	AO CH03 TO Val	AO Channel 03 Post Timeout Level
40069	0044	AO CH04 TO Val	AO Channel 04 Post Timeout Level
40070	0045	AO CH05 TO Val	AO Channel 05 Post Timeout Level
40071	0046	AO CH06 TO Val	AO Channel 06 Post Timeout Level
40072	0047	AO CH07 TO Val	AO Channel 07 Post Timeout Level
40073	0048	AO CH08 TO Val	AO Channel 08 Post Timeout Level
40074	0049	AO CH09 TO Val	AO Channel 09 Post Timeout Level
40075	004A	AO CH10 TO Val	AO Channel 10 Post Timeout Level
40076	004B	AO CH11 TO Val	AO Channel 11 Post Timeout Level
40077	004C	AO CH12 TO Val	AO Channel 12 Post Timeout Level
40078	004D	AO CH13 TO Val	AO Channel 13 Post Timeout Level
40079	004E	AO CH14 TO Val	AO Channel 14 Post Timeout Level
40080	004F	AO CH15 TO Val	AO Channel 15 Post Timeout Level
40081	0050	AO CH00 Start (Default is 0000H, start from 0% of span)	AO CH00 Initial Startup Value which is the level that the output is initially programmed to. This level is expressed as a 16-bit signed integer with a value normalized to ± 30000 for $\pm 100\%$ of input range (see Note 1).
40082	0051	AO CH01 Start	AO CH01 Initial Startup Val, Signed Int
40083	0052	AO CH02 Start	AO CH02 Initial Startup Val, Signed Int
40084	0053	AO CH03 Start	AO CH03 Initial Startup Val, Signed Int
40085	0054	AO CH04 Start	AO CH04 Initial Startup Val, Signed Int
40086	0055	AO CH05 Start	AO CH05 Initial Startup Val, Signed Int
40087	0056	AO CH06 Start	AO CH06 Initial Startup Val, Signed Int
40088	0057	AO CH07 Start	AO CH07 Initial Startup Val, Signed Int
40089	0058	AO CH08 Start	AO CH08 Initial Startup Val, Signed Int
40090	0059	AO CH09 Start	AO CH09 Initial Startup Val, Signed Int
40091	005A	AO CH10 Start	AO CH10 Initial Startup Val, Signed Int
40092	005B	AO CH11 Start	AO CH11 Initial Startup Val, Signed Int
40093	005C	AO CH12 Start	AO CH12 Initial Startup Val, Signed Int
40094	005D	AO CH13 Start	AO CH13 Initial Startup Val, Signed Int
40095	005E	AO CH14 Start	AO CH14 Initial Startup Val, Signed Int
40096	005F	AO CH15 Start	AO CH15 Initial Startup Val, Signed Int
40097	0060	AO Watchdog Timeout Action per channel (Def = FFFFH, No Change)	Bits 15-0 (bit position is AO channel) 1= Change Output on Timeout 0= Do Not Change Output on Timeout

Register Map

Model ES2151
Model ES2152

Register Map

Model ES2151
Model ES2152

Scan Group Channel Pairs:

Group 1=I & V CH0,8;

Group 2=I & V CH1,9;

Group 3=I & V CH2,10;

Group 4=I & V CH3,11;

Group 5=I & V CH4,12;

Group 6=I & V CH5,13;

Group 7=I & V CH6,14;

Group 8=I & V CH7,15.

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40098	0061	Input Averaging (Def = 0000H, No Averaging)	Set to 0 or 1 for No Averaging. Set to an integer greater than 1 to specify the number of samples to average (500 samples maximum).
40099	0062	Reserved	Reserved
40100	0063	Reserved	Reserved
40101	0064	Global Floating Point Enable and Byte Order (Def = 0000H, disabled)	Bit 15: 0=OFF 1=ON Bits 14-2: 0 (Not Used) Bits 1,0: Data Word Byte Order 00 b3 b2 b1 b0 01 b0 b1 b2 b3 10 b1 b0 b3 b2 11 b2 b3 b0 b1
40102	0065	Local Relay Failsafe/ Non-Failsafe Selection & Alarm Trigger (Def=0100H, Alarm for link loss, use failsafe relay)	Bits 15-9: 0 (Not Used) Bit 8: 0=Alarm on WD Timeout Only (Not Link Error) 1=Alarm on WD Timeout and on Link Error Bits 7-1: 0 (Not Used) Bit 0: 0=Failsafe 1=Non-Failsafe Relay contacts can signal a media failure (link loss). Relay can also signal a power loss if set to failsafe.
40103	0066	Enable Scan Groups & Enable Loopback (Def = 00FFH, all groups enabled)	Bits 15-9: 0 (Not Used) Bit 8: 0=DisLpbk 1=EnaLpbk Bit 7: 0=Disable1=Ena Grp 8 Bit 6: 0=Disable1=Ena Grp 7 Bit 5: 0=Disable1=Ena Grp 6 Bit 4: 0=Disable1=Ena Grp 5 Bit 3: 0=Disable1=Ena Grp 4 Bit 2: 0=Disable1=Ena Grp 3 Bit 1: 0=Disable1=Ena Grp 2 Bit 0: 0=Disable1=Ena Grp 1
40104	0067	Reserved	Reserved – Do Not Use

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40105	0068	Wink Toggle & Cal Access & Restore Input Calibration & Restore Scaling & Restore Output Calibration & System Reset Register	Write 21845 (5555H) here to cause the unit to “wink” its green Run LED in order to ID the unit. Write the same value a second time to stop “winking”. Use the Unit Status Register wink mode flag to determine the wink state. Write 24106 (5E2AH) to remove write protection from the calibration registers that follow (registers 40339-40346). All other values apply write-protection to the calibration registers. Write 44718 (AEA EH) to restore the default calibration coefficients. Write 60138 (EAE AH) to restore the default scaling values. Write 43690 (AAA AH) to restore the factory output cal values. Write 41429 (A1D5H) to this register to cause a system reset and reboot. <i>(Write 43981 (ABCDH) to save factory output calibration-Reserved for factory use only)</i>
		<p><i>Use Wink to help identify network units. Cal Access allows manual calibration of unit. Reset drives a system reset (there is also a RST switch on the unit).</i></p> <p><i>This register will always read back as 0000H.</i></p>	

Ref	Addr.	Description	Data Type/Format
40106	0069	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40107	006A	V-CH00 ±5V Cal HI Val	16-bit Signed Integer Data
40108	006B	V-CH00 ±5V Cal LO Val	16-bit Signed Integer Data
40109	006C	V-CH00 ±10V Cal HI Val	16-bit Signed Integer Data
40110	006D	V-CH00 ±10V Cal LO Val	16-bit Signed Integer Data
40111	006E	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40112	006F	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40113	0070	V-CH01 ±5V Cal HI Val	16-bit Signed Integer Data
40114	0071	V-CH01 ±5V Cal LO Val	16-bit Signed Integer Data
40115	0072	V-CH01 ±10V Cal HI Val	16-bit Signed Integer Data
40116	0073	V-CH01 ±10V Cal LO Val	16-bit Signed Integer Data
40117	0074	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40118	0075	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40119	0076	V-CH02 ±5V Cal HI Val	16-bit Signed Integer Data
40120	0077	V-CH02 ±5V Cal LO Val	16-bit Signed Integer Data
40121	0078	V-CH02 ±10V Cal HI Val	16-bit Signed Integer Data
40122	0079	V-CH02 ±10V Cal LO Val	16-bit Signed Integer Data
40123	007A	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40124	007B	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40125	007C	V-CH03 ±5V Cal HI Val	16-bit Signed Integer Data
40126	007D	V-CH03 ±5V Cal LO Val	16-bit Signed Integer Data
40127	007E	V-CH03 ±10V Cal HI Val	16-bit Signed Integer Data
40128	007F	V-CH03 ±10V Cal LO Val	16-bit Signed Integer Data
40129	0080	<i>Reserved</i>	<i>Reserved – Do Not Use</i>

Register Map

Model ES2151
Model ES2152

Shaded registers from 40106 to 40332 are write-restricted and reserved for internal firmware and factory use (these registers are modified indirectly by the calibration registers 40339-40346). Do not attempt to directly modify the contents of the shaded registers.

Register Map

Model ES2151
Model ES2152

Shaded registers from 40106 to 40332 are write-restricted and reserved for internal firmware and factory use (these registers are modified indirectly by the calibration registers 40339-40346). Do not attempt to directly modify the contents of these registers.

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40130	0081	Reserved	Reserved – Do Not Use
40131	0082	V-CH04 ±5V Cal HI Val	16-bit Signed Integer Data
40132	0083	V-CH04 ±5V Cal LO Val	16-bit Signed Integer Data
40133	0084	V-CH04 ±10V Cal HI Val	16-bit Signed Integer Data
40134	0085	V-CH04 ±10V Cal LO Val	16-bit Signed Integer Data
40135	0086	Reserved	Reserved – Do Not Use
40136	0087	Reserved	Reserved – Do Not Use
40137	0088	V-CH05 ±5V Cal HI Val	16-bit Signed Integer Data
40138	0089	V-CH05 ±5V Cal LO Val	16-bit Signed Integer Data
40139	008A	V-CH05 ±10V Cal HI Val	16-bit Signed Integer Data
40140	008B	V-CH05 ±10V Cal LO Val	16-bit Signed Integer Data
40141	008C	Reserved	Reserved – Do Not Use
40142	008D	Reserved	Reserved – Do Not Use
40143	008E	V-CH06 ±5V Cal HI Val	16-bit Signed Integer Data
40144	008F	V-CH06 ±5V Cal LO Val	16-bit Signed Integer Data
40145	0090	V-CH06 ±10V Cal HI Val	16-bit Signed Integer Data
40146	0091	V-CH06 ±10V Cal LO Val	16-bit Signed Integer Data
40147	0092	Reserved	Reserved – Do Not Use
40148	0093	Reserved	Reserved – Do Not Use
40149	0094	V-CH07 ±5V Cal HI Val	16-bit Signed Integer Data
40150	0095	V-CH07 ±5V Cal LO Val	16-bit Signed Integer Data
40151	0096	V-CH07 ±10V Cal HI Val	16-bit Signed Integer Data
40152	0097	V-CH07 ±10V Cal LO Val	16-bit Signed Integer Data
40153	0098	Reserved	Reserved – Do Not Use
40154	0099	Reserved	Reserved – Do Not Use
40155	009A	V-CH08 ±5V Cal HI Val	16-bit Signed Integer Data
40156	009B	V-CH08 ±5V Cal LO Val	16-bit Signed Integer Data
40157	009C	V-CH08 ±10V Cal HI Val	16-bit Signed Integer Data
40158	009D	V-CH08 ±10V Cal LO Val	16-bit Signed Integer Data
40159	009E	Reserved	Reserved – Do Not Use
40160	009F	Reserved	Reserved – Do Not Use
40161	00A0	V-CH09 ±5V Cal HI Val	16-bit Signed Integer Data
40162	00A1	V-CH09 ±5V Cal LO Val	16-bit Signed Integer Data
40163	00A2	V-CH09 ±10V Cal HI Val	16-bit Signed Integer Data
40164	00A3	V-CH09 ±10V Cal LO Val	16-bit Signed Integer Data
40165	00A4	Reserved	Reserved – Do Not Use
40166	00A5	Reserved	Reserved – Do Not Use
40167	00A6	V-CH10 ±5V Cal HI Val	16-bit Signed Integer Data
40168	00A7	V-CH10 ±5V Cal LO Val	16-bit Signed Integer Data
40169	00A8	V-CH10 ±10V Cal HI Val	16-bit Signed Integer Data
40170	00A9	V-CH10 ±10V Cal LO Val	16-bit Signed Integer Data
40171	00AA	Reserved	Reserved – Do Not Use
40172	00AB	Reserved	Reserved – Do Not Use
40173	00AC	V-CH11 ±5V Cal HI Val	16-bit Signed Integer Data
40174	00AD	V-CH11 ±5V Cal LO Val	16-bit Signed Integer Data
40175	00AE	V-CH11 ±10V Cal HI Val	16-bit Signed Integer Data
40176	00AF	V-CH11 ±10V Cal LO Val	16-bit Signed Integer Data
40177	00B0	Reserved	Reserved – Do Not Use
40178	00B1	Reserved	Reserved – Do Not Use

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40179	00B2	V-CH12 ±5V Cal HI Val	16-bit Signed Int Data
40180	00B3	V-CH12 ±5V Cal LO Val	16-bit Signed Int Data
40181	00B4	V-CH12 ±10V Cal HI Val	16-bit Signed Int Data
40182	00B5	V-CH12 ±10V Cal LO Val	16-bit Signed Int Data
40183	00B6	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40184	00B7	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40185	00B8	V-CH13 ±5V Cal HI Val	16-bit Signed Int Data
40186	00B9	V-CH13 ±5V Cal LO Val	16-bit Signed Int Data
40187	00BA	V-CH13 ±10V Cal HI Val	16-bit Signed Int Data
40188	00BB	V-CH13 ±10V Cal LO Val	16-bit Signed Int Data
40189	00BC	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40190	00BD	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40191	00BE	V-CH14 ±5V Cal HI Val	16-bit Signed Int Data
40192	00BF	V-CH14 ±5V Cal LO Val	16-bit Signed Int Data
40193	00C0	V-CH14 ±10V Cal HI Val	16-bit Signed Int Data
40194	00C1	V-CH14 ±10V Cal LO Val	16-bit Signed Int Data
40195	00C2	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40196	00C3	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40197	00C4	V-CH15 ±5V Cal HI Val	16-bit Signed Int Data
40198	00C5	V-CH15 ±5V Cal LO Val	16-bit Signed Int Data
40199	00C6	V-CH15 ±10V Cal HI Val	16-bit Signed Int Data
40200	00C7	V-CH15 ±10V Cal LO Val	16-bit Signed Int Data
40201	00C8	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40202	00C9	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40203	00CA	I-CH00 ±20mA Cal HI Val	16-bit Signed Int Data
40204	00CB	I-CH00 ±20mA Cal LO Val	16-bit Signed Int Data
40205	00CC	I-CH00 0-20mA Cal HI Val	16-bit Signed Int Data
40206	00CD	I-CH00 0-20mA Cal LO Val	16-bit Signed Int Data
40207	00CE	I-CH00 4-20mA Cal HI Val	16-bit Signed Int Data
40208	00CF	I-CH00 4-20mA Cal LO Val	16-bit Signed Int Data
40209	00D0	I-CH01 ±20mA Cal HI Val	16-bit Signed Int Data
40210	00D1	I-CH01 ±20mA Cal LO Val	16-bit Signed Int Data
40211	00D2	I-CH01 0-20mA Cal HI Val	16-bit Signed Int Data
40212	00D3	I-CH01 0-20mA Cal LO Val	16-bit Signed Int Data
40213	00D4	I-CH01 4-20mA Cal HI Val	16-bit Signed Int Data
40214	00D5	I-CH01 4-20mA Cal LO Val	16-bit Signed Int Data
40215	00D6	I-CH02 ±20mA Cal HI Val	16-bit Signed Int Data
40216	00D7	I-CH02 ±20mA Cal LO Val	16-bit Signed Int Data
40217	00D8	I-CH02 0-20mA Cal HI Val	16-bit Signed Int Data
40218	00D9	I-CH02 0-20mA Cal LO Val	16-bit Signed Int Data
40219	00DA	I-CH02 4-20mA Cal HI Val	16-bit Signed Int Data
40220	00DB	I-CH02 4-20mA Cal LO Val	16-bit Signed Int Data
40221	00DC	I-CH03 ±20mA Cal HI Val	16-bit Signed Int Data
40222	00DD	I-CH03 ±20mA Cal LO Val	16-bit Signed Int Data
40223	00DE	I-CH03 0-20mA Cal HI Val	16-bit Signed Int Data
40224	00DF	I-CH03 0-20mA Cal LO Val	16-bit Signed Int Data

Register Map

Model ES2151 Model ES2152

Shaded registers from 40106 to 40332 are write-restricted and reserved for internal firmware and factory use (these registers are modified indirectly by the calibration registers 40339-40346). Do not attempt to directly modify the contents of these registers.

Register Map

Model ES2151 Model ES2152

Shaded registers from 40106 to 40332 are write-restricted and reserved for internal firmware and factory use (these registers are modified indirectly by the calibration registers 40339-40346). Do not attempt to directly modify the contents of these registers.

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40225	00E0	I-CH03 4-20mA Cal HI Val	16-bit Signed Int Data
40226	00E1	I-CH03 4-20mA Cal LO Val	16-bit Signed Int Data
40227	00E2	I-CH04 ±20mA Cal HI Val	16-bit Signed Int Data
40228	00E3	I-CH04 ±20mA Cal LO Val	16-bit Signed Int Data
40229	00E4	I-CH04 0-20mA Cal HI Val	16-bit Signed Int Data
40230	00E5	I-CH04 0-20mA Cal LO Val	16-bit Signed Int Data
40231	00E6	I-CH04 4-20mA Cal HI Val	16-bit Signed Int Data
40232	00E7	I-CH04 4-20mA Cal LO Val	16-bit Signed Int Data
40233	00E8	I-CH05 ±20mA Cal HI Val	16-bit Signed Int Data
40234	00E9	I-CH05 ±20mA Cal LO Val	16-bit Signed Int Data
40235	00EA	I-CH05 0-20mA Cal HI Val	16-bit Signed Int Data
40236	00EB	I-CH05 0-20mA Cal LO Val	16-bit Signed Int Data
40237	00EC	I-CH05 4-20mA Cal HI Val	16-bit Signed Int Data
40238	00ED	I-CH05 4-20mA Cal LO Val	16-bit Signed Int Data
40239	00EE	I-CH06 ±20mA Cal HI Val	16-bit Signed Int Data
40240	00EF	I-CH06 ±20mA Cal LO Val	16-bit Signed Int Data
40241	00F0	I-CH06 0-20mA Cal HI Val	16-bit Signed Int Data
40242	00F1	I-CH06 0-20mA Cal LO Val	16-bit Signed Int Data
40243	00F2	I-CH06 4-20mA Cal HI Val	16-bit Signed Int Data
40244	00F3	I-CH06 4-20mA Cal LO Val	16-bit Signed Int Data
40245	00F4	I-CH07 ±20mA Cal HI Val	16-bit Signed Int Data
40246	00F5	I-CH07 ±20mA Cal LO Val	16-bit Signed Int Data
40247	00F6	I-CH07 0-20mA Cal HI Val	16-bit Signed Int Data
40248	00F7	I-CH07 0-20mA Cal LO Val	16-bit Signed Int Data
40249	00F8	I-CH07 4-20mA Cal HI Val	16-bit Signed Int Data
40250	00F9	I-CH07 4-20mA Cal LO Val	16-bit Signed Int Data
40251	00FA	I-CH08 ±20mA Cal HI Val	16-bit Signed Int Data
40252	00FB	I-CH08 ±20mA Cal LO Val	16-bit Signed Int Data
40253	00FC	I-CH08 0-20mA Cal HI Val	16-bit Signed Int Data
40254	00FD	I-CH08 0-20mA Cal LO Val	16-bit Signed Int Data
40255	00FE	I-CH08 4-20mA Cal HI Val	16-bit Signed Int Data
40256	00FF	I-CH08 4-20mA Cal LO Val	16-bit Signed Int Data
40257	0100	I-CH09 ±20mA Cal HI Val	16-bit Signed Int Data
40258	0101	I-CH09 ±20mA Cal LO Val	16-bit Signed Int Data
40259	0102	I-CH09 0-20mA Cal HI Val	16-bit Signed Int Data
40260	0103	I-CH09 0-20mA Cal LO Val	16-bit Signed Int Data
40261	0104	I-CH09 4-20mA Cal HI Val	16-bit Signed Int Data
40262	0105	I-CH09 4-20mA Cal LO Val	16-bit Signed Int Data
40263	0106	I-CH10 ±20mA Cal HI Val	16-bit Signed Int Data
40264	0107	I-CH10 ±20mA Cal LO Val	16-bit Signed Int Data
40265	0108	I-CH10 0-20mA Cal HI Val	16-bit Signed Int Data
40266	0109	I-CH10 0-20mA Cal LO Val	16-bit Signed Int Data
40267	010A	I-CH10 4-20mA Cal HI Val	16-bit Signed Int Data
40268	010B	I-CH10 4-20mA Cal LO Val	16-bit Signed Int Data
40269	010C	I-CH11 ±20mA Cal HI Val	16-bit Signed Int Data
40270	010D	I-CH11 ±20mA Cal LO Val	16-bit Signed Int Data

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40271	010E	I-CH11 0-20mA Cal HI Val	16-bit Signed Int Data
40272	010F	I-CH11 0-20mA Cal LO Val	16-bit Signed Int Data
40273	0110	I-CH11 4-20mA Cal HI Val	16-bit Signed Int Data
40274	0111	I-CH11 4-20mA Cal LO Val	16-bit Signed Int Data
40275	0112	I-CH12 ±20mA Cal HI Val	16-bit Signed Int Data
40276	0113	I-CH12 ±20mA Cal LO Val	16-bit Signed Int Data
40277	0114	I-CH12 0-20mA Cal HI Val	16-bit Signed Int Data
40278	0115	I-CH12 0-20mA Cal LO Val	16-bit Signed Int Data
40279	0116	I-CH12 4-20mA Cal HI Val	16-bit Signed Int Data
40280	0117	I-CH12 4-20mA Cal LO Val	16-bit Signed Int Data
40281	0118	I-CH13 ±20mA Cal HI Val	16-bit Signed Int Data
40282	0119	I-CH13 ±20mA Cal LO Val	16-bit Signed Int Data
40283	011A	I-CH13 0-20mA Cal HI Val	16-bit Signed Int Data
40284	011B	I-CH13 0-20mA Cal LO Val	16-bit Signed Int Data
40285	011C	I-CH13 4-20mA Cal HI Val	16-bit Signed Int Data
40286	011D	I-CH13 4-20mA Cal LO Val	16-bit Signed Int Data
40287	011E	I-CH14 ±20mA Cal HI Val	16-bit Signed Int Data
40288	011F	I-CH14 ±20mA Cal LO Val	16-bit Signed Int Data
40289	0120	I-CH14 0-20mA Cal HI Val	16-bit Signed Int Data
40290	0121	I-CH14 0-20mA Cal LO Val	16-bit Signed Int Data
40291	0122	I-CH14 4-20mA Cal HI Val	16-bit Signed Int Data
40292	0123	I-CH14 4-20mA Cal LO Val	16-bit Signed Int Data
40293	0124	I-CH15 ±20mA Cal HI Val	16-bit Signed Int Data
40294	0125	I-CH15 ±20mA Cal LO Val	16-bit Signed Int Data
40295	0126	I-CH15 0-20mA Cal HI Val	16-bit Signed Int Data
40296	0127	I-CH15 0-20mA Cal LO Val	16-bit Signed Int Data
40297	0128	I-CH15 4-20mA Cal HI	16-bit Signed Int Data
40298	0129	I-CH15 4-20mA Cal LO	16-bit Signed Int Data
40299	012A	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40300	012B	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
Ref	Addr.	Description	Data Type/Format
40301	012C	AO CH00 Cal HI	AO CH00 Raw DAC Count HI
40302	012D	AO CH00 Cal LO	AO CH00 Raw DAC Count LO
40303	012E	AO CH01 Cal HI	AO CH01 Raw DAC Count HI
40304	012F	AO CH01 Cal LO	AO CH01 Raw DAC Count LO
40305	0130	AO CH02 Cal HI	AO CH02 Raw DAC Count HI
40306	0131	AO CH02 Cal LO	AO CH02 Raw DAC Count LO
40307	0132	AO CH03 Cal HI	AO CH03 Raw DAC Count HI
40308	0133	AO CH03 Cal LO	AO CH03 Raw DAC Count LO
40309	0134	AO CH04 Cal HI	AO CH04 Raw DAC Count HI
40310	0135	AO CH04 Cal LO	AO CH04 Raw DAC Count LO
40311	0136	AO CH05 Cal HI	AO CH05 Raw DAC Count HI
40312	0137	AO CH05 Cal LO	AO CH05 Raw DAC Count LO
40313	0138	AO CH06 Cal HI	AO CH06 Raw DAC Count HI
40314	0139	AO CH06 Cal LO	AO CH06 Raw DAC Count LO
40315	013A	AO CH07 Cal HI	AO CH07 Raw DAC Count HI
40316	013B	AO CH07 Cal LO	AO CH07 Raw DAC Count LO

Register Map

Model ES2151 Model ES2152

Shaded registers from 40106 to 40332 are write-restricted and reserved for internal firmware and factory use (these registers are modified indirectly by the calibration registers 40339-40346). Do not attempt to directly modify the contents of these registers.

Register Map

Model ES2151
Model ES2152

Shaded registers from 40106 to 40332 are write-restricted and reserved for internal firmware and factory use (these registers are modified indirectly by the calibration registers 40339-40346). Do not attempt to directly modify the contents of these registers.

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40317	013C	AO CH08 Cal HI	AO CH08 Raw DAC Count HI
40318	013D	AO CH08 Cal LO	AO CH08 Raw DAC Count LO
40319	013E	AO CH09 Cal HI	AO CH09 Raw DAC Count HI
40320	013F	AO CH09 Cal LO	AO CH09 Raw DAC Count LO
40321	0140	AO CH10 Cal HI	AO CH10 Raw DAC Count HI
40322	0141	AO CH10 Cal LO	AO CH10 Raw DAC Count LO
40323	0142	AO CH11 Cal HI	AO CH11 Raw DAC Count HI
40324	0143	AO CH11 Cal LO	AO CH11 Raw DAC Count LO
40325	0144	AO CH12 Cal HI	AO CH12 Raw DAC Count HI
40326	0145	AO CH12 Cal LO	AO CH12 Raw DAC Count LO
40327	0146	AO CH13 Cal HI	AO CH13 Raw DAC Count HI
40328	0147	AO CH13 Cal LO	AO CH13 Raw DAC Count LO
40329	0148	AO CH14 Cal HI	AO CH14 Raw DAC Count HI
40330	0149	AO CH14 Cal LO	AO CH14 Raw DAC Count LO
40331	014A	AO CH15 Cal HI	AO CH15 Raw DAC Count HI
40332	014B	AO CH15 Cal LO	AO CH15 Raw DAC Count LO
40333	014C	Reserved	Reserved – Do Not Use
40334	014D	Reserved	Reserved – Do Not Use
40335	014E	Reset Totalizer Trigger for Input Voltage Channels 15-0 <i>Register always reads back as 0000H</i>	Bit position corresponds to <u>voltage</u> input channel number to reset the totalizer at (lsb is lowest numbered channel). 1=Reset Totalizer 0=No Action
40336	014F	Reset Totalizer Trigger for Input Current Channels 15-0 <i>Register always reads back as 0000H</i>	Bit position corresponds to <u>current</u> input channel number to reset the totalizer at (lsb is lowest numbered channel). 1=Reset Totalizer 0=No Action
40337	0150	Reserved	Reserved – Do Not Use
40338	0151	Reserved	Reserved – Do Not Use
40339	0152	Zero Cal Trigger for Voltage Channels (V-IN Ch 15-00 of Ports 1 & 2)	Register bits are used to trigger the A/D to sample the corresponding voltage input(s) and store the zero signal. Write a set bit to cause corresponding voltage channel to be calibrated (lsb is lowest numbered channel of this group). You must first write 24106 to Calibration Access Register 40105 to remove write-protection from these calibration registers.

Ref	Addr.	Description	Data Type/Format
<i>Holding Registers (4x References, Read/Write)</i>			
40340	0153	Zero Cal Trigger for Current Channels (I-IN Ch 15-00 of Ports 1 & 2)	Register bits are used to trigger the A/D to sample the corresponding current input(s) and store the zero signal. Write a set bit to cause the corresponding current channel to be calibrated (lsb is lowest numbered channel of this group). You must first write 24106 to Calibration Access Register 40105 to remove write-protection from these calibration registers.
40341	0154	Span Cal Trigger for Voltage Channels (V-IN Ch 15-00 of Ports 1 & 2)	Register bits are used to trigger the A/D to sample the corresponding voltage input(s) and store the positive full-scale calibration signal. Write a set bit to cause the corresponding voltage channel to be calibrated (lsb is lowest numbered channel of this group). You must first write 24106 to Calibration Access Register 40105 to remove write-protection from these calibration registers.
40342	0155	Span Cal Trigger for Current Channels (I-IN Ch 15-00 of Ports 1 & 2)	Register bits are used to trigger the A/D to sample the corresponding current input(s) and store the positive full-scale calibration signal. Write a set bit to cause the corresponding current channel to be calibrated (lsb is lowest numbered channel of this group). You must first write 24106 to Calibration Access Register 40105 to remove write-protection from these calibration registers.
40343	0156	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40344	0157	<i>Reserved</i>	<i>Reserved – Do Not Use</i>
40345	0158	<i>Output Zero Cal Trigger for Output Ch 15-00 of Ports 1 and 2</i>	<i>This register is used to trigger the A/D to write the output(s) and store the measured zero output signal. Write a set bit to cause the corresponding voltage channel to be calibrated (lsb is lowest numbered channel of this group). You must first write 24106 to the Calibration Access Register 40105 to remove write-protection from these calibration registers.</i>

Register Map

Model ES2151
Model ES2152

Register Map

Model ES2151 Model ES2152

Write to these registers to set the analog output level. The value written is a normalized D/A count with $\pm 100\%$ of output range represented by ± 30000 (see Note 1).

The shaded register addresses are those that are suitable i2o target (mapped) register addresses if writing to outputs on this unit via i2o configured on another unit. These represent the starting target addresses of eight contiguous output registers required to write a port via i2o.

Ref	Addr.	Description	Data Type/Format
Holding Registers (4x References, Read/Write)			
40346	0159	Output Span Cal Trigger for Output Ch 15-00 of Ports 1 and 2	This register is a trigger for the A/D to write the output(s) and store the measured full-scale signal. A set bit will cause the corresponding voltage channel to be calibrated (lsb is lowest numbered channel of group). You must first write 24106 to Calibration Access Register 40105 to remove write-protection from these calibration registers.
40347	015A	Reserved	Reserved – Do Not Use
40348	015B	Reserved	Reserved – Do Not Use
40349	015C	Reserved	Reserved – Do Not Use
40350	015D	Reserved	Reserved – Do Not Use
40351	015E	AO CH 00 Value	16-bit Signed Integer Output Value
40352	015F	AO CH 01 Value	16-bit Signed Integer Output Value
40353	0160	AO CH 02 Value	16-bit Signed Integer Output Value
40354	0161	AO CH 03 Value	16-bit Signed Integer Output Value
40355	0162	AO CH 04 Value	16-bit Signed Integer Output Value
40356	0163	AO CH 05 Value	16-bit Signed Integer Output Value
40357	0164	AO CH 06 Value	16-bit Signed Integer Output Value
40358	0165	AO CH 07 Value	16-bit Signed Integer Output Value
40359	0166	AO CH 08 Value	16-bit Signed Integer Output Value
40360	0167	AO CH 09 Value	16-bit Signed Integer Output Value
40361	0168	AO CH 10 Value	16-bit Signed Integer Output Value
40362	0169	AO CH 11 Value	16-bit Signed Integer Output Value
40363	016A	AO CH 12 Value	16-bit Signed Integer Output Value
40364	016B	AO CH 13 Value	16-bit Signed Integer Output Value
40365	016C	AO CH 14 Value	16-bit Signed Integer Output Value
40366	016D	AO CH 15 Value	16-bit Signed Integer Output Value
41001 . . .		This block Mirrors 1xxxx Registers.	Refer to Register Mirroring. 1xxxx Input Status Registers are mapped to the 41xxx Holding Register space using an address offset of 41000.
42001 . . .		This block Mirrors 0xxxx Registers.	Refer to Register Mirroring. 0xxxx Coil Registers are mapped to the 42xxx Holding Register space using an address offset of 42000.
43001 . . .		This block Mirrors 3xxxx Registers.	Refer to Register Mirroring. 3xxxx Input Registers are mapped to the 43xxx Holding Register space using an address offset of 43000.

Notes (Register Map):

1. The 16-bit A/D & D/A ranges use a count of ± 32768 for their native $\pm 5V$ and/or $\pm 10V$ ranges. All I/O ranges are normalized to a count of ± 30000 by the unit corresponding to $\pm 100\%$ for bipolar ranges, or 0-30000 corresponding to 0-100% for unipolar ranges. Over-range is 2768.
2. Current inputs use a 100 Ω input shunt to drive $\pm 2.0V$ for $\pm 20mA$ input, 0-2.0V for 0-20mA, and 0.4-2.0V for 4-20mA. All current ranges are normalized to ± 30000 by the unit and this corresponds to $\pm 100\%$.

ES2151/2152 Default Register Settings

REGISTER	HEX	ACTION
AI Channel Config Registers	0019H	Use Auto-Zero/Span Cal
		Do not use CH Cal for Port
		Totalizer to use Preload
		Totalizer action is Rollover
		Totalizer is OFF
		Totalizer Time Base is NA
		Input is $\pm 20\text{mA}$ or $\pm 10\text{V}$
AO Channel Config Registers	0000H	Output is $\pm 10\text{V}$ or 0-20mA
AO Channel Timeout Time	0000H	0s, Timer is disabled
AO Channel Timeout Value	0000H	0.0000
AO Channel Power-Up/Reset Value	0000H	Set Initial Value to 0%
AO Take Action on Timeout	FFFFH	Do Change on Timeout
Digital Filter/Input Averaging	0000H	0, No Input Averaging
Floating Point Enable & Byte Order	0000H	Disable, b3-b2-b1-b0 Order
Local Relay Failsafe/Non-Failsafe Select and Alarm Trigger	0100H	Enable Failsafe and Alarm for Watchdog Timeout and Link Error.
Enable Scan Groups Register	00FFH	All Scan Groups Enabled
Wink Register	0000H	OFF, Do Not Wink

Default Register Settings

Here is a summary of the ES2151/2152 default register settings and corresponding default behavior.

Note that all of these parameters are addressed via holding registers 40001 to 40103, and these are the contents that are exported via the Export Configuration Control of the Utility Web Page. They are also the values restored to via the Restore function.

Not all parameters of this device can be set via Modbus registers. In general, parameters related to I/O will have a Modbus register, while those related to network communication must be set via the web interface. Here is a list of configuration parameters which do not have a Modbus register.

Username & Password
 Static IP Address
 Number of Modbus Sockets
 Subnet Mask
 Gateway Address
 Host Name
 Select Static, DHCP/BOOTP, or DHCP/BOOTP w/Fallback Addressing
 Wink On/Off
 Select Network Port Hub Mode or Switch Mode Operation
 Copper Network Port 1 & 2 Forced Speed & Duplex or via Auto-Negotiation
 Fiber Port Half or Full Duplex Selection
 Self-Test Utility
 Export Configuration & Export IP Address

Configuration Parameters Not Programmable Via Modbus Registers

Use the built-in web interface screens to set these parameters, which are generally required to setup communications.

The web interface is not limited to communication parameters, but will allow you to also exploit other features of the product normally set via Modbus registers.

SPECIFICATIONS

Description

This high-density, industrial Ethernet I/O system provides 16 voltage input channels, 16 current input channels, and 16 current outputs (ES2151) or 16 voltage outputs (ES2152). Units include dual isolated 10/100M Ethernet ports for monitoring, calibration, and control of the I/O via Modbus TCP/IP or UDP/IP. Both models include a DB25 interface connector to alternately connect a Series 8B expansion panel of 4, 8, or 16 8B input modules. The ES2152 adds a second DB25 interface for connecting to a Series 8B expansion panel of 4, 8, or 16 output modules. The unit is DC-powered with redundant power input and reverse polarity protection. Field inputs are wide-band, multi-ranging, and single-ended. Sixteen bit A/D conversion is used and input ranges may be rescaled to sub-ranges or other engineering units in order to facilitate integration/totalization. Input channels include transient protection. Output channels employ 16-bit D/A conversion. Dual network ports allow the unit to be conveniently cascaded with other EtherStax units, or for implementation of end-node redundancy schemes when connected to Ethernet switches that implement redundancy. Dual network ports also provide a low-latency/low-jitter hub/repeater mode. I/O channels, the alarm relay, network ports, and the power circuit are isolated from each other, and from earth ground (chassis). Non-volatile reprogrammable memory in the unit stores configuration and calibration information.

Model Numbers

Examples:

ES2151-0000 (16 Current Output)
 ES2151-0010 (wo/Enclosure)
 ES2151-1000 (w/Fiber Port)
 ES2151-1010 (w/Fiber, wo/Encl.)

ES2152 models are similar, but have 16 voltage outputs and add a second DB25 interface connector for connecting to 8BP expansion panels for 8B output modules (sold separately).

EtherStax model prefix "ES" denotes the EtherStax Ethernet I/O family. The trailing "21" digits denote the 2100 series. The "51" following "21" denotes a 16 channel current output model. A "52" following "21" denotes a 16 channel voltage output model. Both models combine 16 current and 16 voltage inputs with 16 outputs. The ES2151 has one DB25 interface connector for optional connection to industry standard 8BP expansion panels which support a wide variety of input module types. The ES2152 model has two DB25 connectors for connection to 8BP expansion panels, one for 8B input modules, one for 8B output modules. Other options are selected via a hyphenated four digit suffix to the model (-xxxx). The first suffix digit specifies the physical connection (0=Dual 10/100M copper, 1=one 10/100M copper port and one 100BaseFX fiber port). The second suffix digit specifies the protocol (0=Modbus TCP/IP & UDP/IP). The third digit specifies the housing (0=aluminum extrusion, 1=open-frame with no housing). The last digit specifies the input power (0=18-36V DC).

Mounting Options

DIN Rail, Surface, or Open-Frame Mounting

Standard units can interlock and stack together up to 3 units high. A single unit or stack of units can be bolted to a flat surface, or mounted on deep-channel "T" type DIN rails (35mm x15mm), depending on the optional mounting kit selected. These mounting options are listed below. Detailed drawings of these items are included in the Mounting and Dimensions section at the front of this manual.

ESA-DIN-VMK, DIN Rail Vertical Mount Kit: This kit includes two plastic DIN clips that slide into the dovetail channel at the bottom of the housing. You can use one clip to mount a single unit, or both clips for added stability, or when stacking two units. The "vertical" designation refers to the orientation of an EtherStax unit relative to the DIN rail.

ESA-DIN-HMK, DIN Rail Horizontal Mount Kit: This kit includes the same bolt-on aluminum base plate as ESA-SMK above, but adds a heavy-duty DIN clip and screws to mount the base plate on a DIN rail, allowing up to three units to be stacked together and mounted on the rail. The "horizontal" designation refers to the orientation of an EtherStax relative to the DIN rail.

ESA-SMK, Surface-Mount Kit: This kit includes a shock mounted aluminum base plate and four bolts to attach to the bottom of a housing, allowing one to three units to be bolted to a wall or flat surface.

ESA-OMK, Open Mounting Kit: EtherStax units can be ordered without their enclosure and already include the threaded standoffs and screws necessary to stack two circuits together, plus the screws and standoffs for mounting the assembly to a flat surface. Units may optionally bolt to the surface-mount base plate of ESA-SMK or ESA-DIN-HMK with this hardware. This kit contains the identical replacement hardware for stacking two open circuits together and/or mounting them to a flat surface.

Models include two ports with 8 current input channels and 8 voltage input channels in each port. Current inputs accept up to $\pm 21.6\text{mA}$ DC. Voltage inputs utilize the $\pm 10\text{V}$ or $\pm 5\text{V}$ full-scale input ranges of the A/D. Each port multiplexes (8:1) to a separate A/D channel. Current inputs incorporate precision 100Ω shunt resistors to convert input current to voltage. For the bipolar $\pm 20\text{mA}$ input, this drives $\pm 2.0\text{V}$ full-scale to the $\pm 5\text{V}$ input channel of a 16-bit A/D. For the unipolar 0-20mA & 4-20mA input ranges, this drives 0-2V and 0.4-2V to the 16-bit 0-5V range of the A/D. Inputs are wideband and include transient voltage suppression. I/O must be wired and configured for the intended input type and range (see Connections for details). Voltage inputs are bipolar and can be input in either polarity. Current inputs are bipolar and unipolar. The following paragraphs summarize this model's input types, ranges, and applicable specifications.

DC Input Range: Voltage inputs support a bipolar, single-ended field range of $\pm 10\text{V}$ or $\pm 5\text{V}$, selectable on a per channel basis which corresponds to the actual full-scale input range of the A/D with no over-range. Current inputs utilize the $\pm 5\text{V}$ and 0-5V A/D ranges with 100Ω precision shunt resistors (0.125W) at the inputs to convert current to voltage. Thus, $\pm 20\text{mA}$ DC ($\pm 21.6\text{mA}$ Max) drives $\pm 2\text{V}$ full-scale to the A/D, while 0-20mA and 4-20mA drive 0-2V and 0.4-2V full-scale to the A/D. Inputs may optionally rescale to support sub-ranges of nominal ranges, or to accomplish integration and totalization of the input signal. All input ranges are normalized to ± 30000 for $\pm 100\%$ of range, or 0-30000 for 0-100% of range (over-range is thus, 2768 or $\sim 9.2\%$). Positive current or voltage is delivered to the (+) input terminal and returned at the common (C) input terminal.

Input Resolution: 305.176uV/bit ($\pm 10\text{V}$), or 152.588uV/bit ($\pm 5\text{V}$), or 1.5259uA/bit ($\pm 20\text{mA}$, 0-20mA, 4-20mA). The internal 16-bit A/D resolution is ± 32768 parts for both the $\pm 5\text{V}$ and $\pm 10\text{V}$ ranges, and ± 13107 (14.6 bits) for $\pm 20\text{mA}$ (this drives only $\pm 2\text{V}$ full-scale to the $\pm 5\text{V}$ 16-bit A/D input channel). Resolution is 1 part in 13107 for the 0-20mA range, or 1 part in 10486 for 4-20mA range (2621 to 13107). All bipolar input ranges are normalized to ± 30000 counts for $\pm 100\%$ of the input range, or 0-30000 counts for 0-100% of the unipolar input range. Your effective resolution will vary with range selection and input scaling.

Normalized Resolution for Voltage Input Ranges

RANGE	$\pm 10\text{V}$	$\pm 5\text{V}$
Raw A/D	± 32768	± 32768
Resolution	305.176uV/bit	152.588uV/bit
PPM	15.26ppm	15.26ppm
Normalized	± 30000	± 30000

SPECIFICATIONS

Mounting Options

Analog Inputs

16 Current Inputs and
16 Voltage Inputs

SPECIFICATIONS

Analog Inputs

16 Current Inputs and
16 Voltage Inputs

Normalized Resolution for Current Input Ranges¹

RANGE	±20mA	0-20mA	4-20mA
Raw A/D	±13107bits	0-13107bits	2621-13107bits
Resolution	1.5259uA/bit	1.5259uA/bit	1.5259uA/bit
PPM	38.15ppm	76.30ppm	95.36ppm
Normalized	±30000	0-30000	0-30000

¹ Current inputs use a 100Ω shunt and the ±5V (16-bit) and 0-5V (16-bit) A/D Ranges. Input ranges are normalized to ±30000 for ±100%, and 0-30000 for 0-100%. Voltage inputs use the full-scale input range of the A/D converter and do not support over-range.

Limits to Re-Scaling Nominal Ranges: To achieve a minimum acceptable resolution of 12 bits (±2048 parts), rescaling should not divide the nominal A/D base ranges of ±5V or ±10V by more than 16 (0.0625x). Current input voltage equals input current x100Ω and a ±5V A/D range is used. A peak reading occurs at a normalized count of 32768 (109% of full-scale), with full-scale corresponding to 30000 counts (100%).

RANGE	±10V	±5V	±20mA (uses ±5V)	0-20/4-20mA (uses 0-5V)
MIN SPAN	1.25V or ±0.625V	0.625V or ±0.3125V	0.625V or ±0.3125V, ±3.125mA	0.3125V or 3.125mA

Input Reference Test Conditions: 0-20mA, ±5V input; ambient temperature = 25°C; 24VDC supply.

Input Over Voltage Protection: Bipolar Transient Voltage Suppressors (TVS), clamp level less than 50V and greater than 18V.

Input Impedance: 4MΩ minimum (ES2152), 100Ω (ES2151).

Input Update Rate: Time between samples varies according to the number of scan groups enabled, whether output loop back is enabled, whether totalization is being performed, and whether input averaging is enabled. The table below gives the scan times for combinations of these factors (Input Averaging assumes a maximum 500 sample input average). Note that if you are not totalizing or using input averaging, you can realize an increase in throughput by selectively enabling only the scan groups required by your application.

Scan Grps	Totalizer OFF and No Input Averaging		Totalizer ON and No Input Averaging	Totalizer ON or OFF and with Input Averaging
	No Output Loopback	Add Output Loopback		
1	0.77ms	2.44ms	20ms	40ms
2	1.36ms	3.03ms	20ms	40ms
3	1.97ms	3.64ms	20ms	40ms
4	2.53ms	4.20ms	20ms	40ms
5	3.12ms	4.79ms	20ms	40ms
6	3.73ms	5.40ms	20ms	40ms
7	4.26ms	5.93ms	20ms	40ms
8	5.00ms	6.67ms	20ms	40ms

¹ Rates above apply with floating point and scaling applied. Note that if totalization is enabled at any channel, the scan time is a flat rate of 20ms with no averaging at any channel, and 40ms with any input averaging greater than 1.

Input Scan Groups: Unit gathers input data at high speed utilizing scan groups, which are separate groups of 4 channels organized to minimize multiplexer switching. Every 8 input channels (each port) is separately multiplexed to an A/D channel using fault-tolerant 8:1 multiplexers (current inputs are separately multiplexed from voltage inputs). There are 8 scan groups of 4 channels each and each channel of a scan group is associated with the same channel of an 8:1 port multiplexer (the 4 port multiplexers share the same address lines). So scan group 1 is the first channel of each port multiplexer, scan group 2 the second channel of each port multiplexer, and so on. Thus, all channels are gathered at a rate that is dependent on the number of scan groups enabled. Scan groups can be individually enabled/disabled to realize faster throughput for a smaller group of channels. Normally, 60% of the scan cycle time is used to convert the channel data, while 40% is used to do other tasks. If totalization is not being performed, and no input averaging is being done, then an increase in throughput can be realized by disabling scan groups (see Input Update Rate table).

Input Calibration: The unit automatically calibrates zero and span every input cycle using a known on-board calibration reference signal with sufficient accuracy for most applications. Optionally, inputs may be calibrated manually by driving an input channel externally, and it is also possible to manually calibrate an entire input port based on the calibration of one channel from that port (a manual cal time saver). Calibration can be done on a per channel basis and a unit can also mix manually calibrated channels with automatically calibrated channels.

Input Accuracy: Better than $\pm 0.05\%$ of span for voltage inputs, and $\pm 0.1\%$ for current inputs, for nominal input ranges with auto-calibration and reference test conditions. Includes the effects of repeatability, terminal point conformity, and linearization, but does not include sensor error.

Input Temperature Drift: Better than $\pm 50\text{ppm}/^\circ\text{C}$ ($\pm 0.005\%/^\circ\text{C}$).

Input Analog to Digital Converter (A/D): A 16-bit successive-approximation converter, Linear Technology LTC1859IG.

Input Filter: Normal mode filtering fixed per input type.

Input Filter Bandwidth: -3dB at 125KHz, typical. Bandwidth is dominantly restricted to the update rate of 32 channels (see table).

Input Noise Rejection (Common Mode): Better than -72dB @ 60Hz, typical with 100 Ω input unbalance.

Input Cable Length: I/O port interface cables should not exceed 30m in length for rated performance.

Floating Point: May be enabled/disabled. Enable Floating Point (FP) to rescale an input signal and/or for integration/totalization. Disable FP only if not rescaling or totalizing. Like disabling scan groups, disabling FP support reduces the amount of calculations that have to be performed and gives the processor more time to do other tasks than acquire data. This helps make critical network applications more deterministic, particularly over networks with heavy traffic flow. Note with FP disabled, the measured input value will still indicate a floating point number, but scaling and totalizing fields will indicate "Inactive".

Byte Order: The unit allows you to specify the byte order for 32-bit floating point values. Different Modbus systems will use different byte orders for the two 16-bit Modbus registers used to store a 32-bit floating point value. Select the byte order compatible with your system. Note that B0 refers to the Least Significant Byte and B3 to the Most Significant Byte.

SPECIFICATIONS

Analog Inputs

16 Current Inputs and
16 Voltage Inputs

SPECIFICATIONS

Analog Inputs

16 Current Inputs and
16 Voltage Inputs

8B Input Support: See the **8B Expansion Interface** section that follows Analog Outputs.

Input Integration/Totalization: If totalization is enabled, the instantaneous input is sampled at a slower fixed rate of 20ms (with no input averaging), or 40ms (with any input averaging). You can choose to integrate this signal by totalizing its time sliced instantaneous value. To totalize, you must separately enable Floating Point Support. You also need to scale the input appropriately and specify the time-base units to perform the integration over ("per Second", "per Minute", "per Hour", or "NA"). You must specify a time base other than NA for totalization to occur. Note that if "NA" is selected and Totalize=Yes, then 0.0 is added to the totalized value. The totalized value is non-volatile and you can even preload a totalized value on power-up, or system reset. Otherwise, it will totalize from the last totalizer value before interrupting power or performing a system reset. During run time, your incremental "time-sliced" measurement value will be added to this total every scan time. The software also gives the capability to reset the current totalized value to zero. For example, if during totalization, we gather an instantaneous input sample every 20ms. Then if your instantaneous scaled input value indicates 500, and units are gallons, and the time base is set to "per Minute". Then $(500 \text{ gallons/minute}) \times (1 \text{ minute}/60 \text{ seconds})$ equals a flow rate of 8.33 gallons/per second. If a new sample is obtained every 20ms, then we multiply 8.33gallons/second by 0.020seconds/sample to get an incremental increase of volume of 0.1666 gallons/sample, and this amount is added to your totalized value. Note that even with floating point disabled, the measured input value will still indicate a floating point number, but the scaling and totalizing fields will indicate "Inactive" with floating point disabled.

Open-Frame Models (ES2151/2152-0010 & ES2151/2152-1010)

These models are UL/cUL Recognized components suitable for use in Hazardous Locations per Class 1, Division 2, Groups A, B, C, and D, where the acceptability of the combination is determined by Underwriters Laboratories. These components have been judged on the basis of required spacings in the standard for Industrial Control Equipment, UL 508, Table 36.1, which would cover the component itself if submitted for unrestricted Listing. As a condition of Acceptability when installed in end-user equipment, consider the following:

1. The device shall be installed in compliance with the enclosure, mounting spacing, casualty (including markings), and segregation requirements of the ultimate application.
2. The accessibility of the live parts through openings in the enclosure, reliable retention of guards or barriers for prevention of risk of electric shock, etc. shall be considered in the end product evaluation.
3. The acceptability of the connection headers shall be determined in the end product.
4. These devices shall be operated within their electrical ratings and in an ambient temperature not exceeding 75°C.
5. When used in end product, programmable controllers must meet requirements for use in Class I, Groups A, B, C, and D, Division 2 or Class I, Zone 2, Group IIC Hazardous Locations.
6. The following temperature code should be noted: "T4A".

Voltage Output Specifications (ES2152 Only):

Output Range: Select $\pm 5V$ or $\pm 10V$, per channel. Outputs are limited to approximately $\pm 5.3V$, and $\pm 10.2V$ typical.

Output Load (of unit, not 8B output): $\pm 1mA$ DC max drive current into $10K\Omega$ or greater ($\pm 10V$), or $5K\Omega$ or greater ($\pm 5V$). Use an 8B voltage output module for greater voltage drive capability.

Output Accuracy: Better than $\pm 0.05\%$ (voltage outputs) using auto calibration (as shipped from factory). Output accuracy can be improved to better than $\pm 0.025\%$ by manually calibrating voltage outputs. Units as shipped from the factory have not been manually calibrated.

Output Resolution: $\pm 10V$ range is 16-bits (± 32768), or 1 part in 65536 internal, normalized to ± 30000 . $\pm 5V$ range is 15-bits (± 16384), or 1 part in 32768 internal, normalized to ± 30000 . All I/O ranges are normalized to ± 30000 (Bipolar, $\pm 100\%$), or 0-30000 (Unipolar, 0-100%).

Output Maximum Voltage: 10.2V, typical.

Output Impedance: 0.5Ω maximum.

Output Temperature Drift: Better than $\pm 60ppm/^{\circ}C$ ($\pm 0.006\%/^{\circ}C$).

Output Short Circuit Protection: Included

Output Response Time: The time delay from the write of an output register to 98% of the output transition is approximately 7.0125ms.

8B Output Support: See the **8B Expansion Interface** section that follows this section.

DAC Count (Voltage): All output ranges are normalized to ± 30000 which corresponds to $\pm 100\%$ of output range. A normalized count is limited to a 16-bit signed maximum count of 32768, which is approximately 109.2%. In the table, "Internal" refers to the actual D/A count, while "Register" is the ideal program value for the output register (i.e. the normalized value).

Internal DAC Count (Ideal) Versus Voltage Output Range

Range	Range	DAC -100%	DAC 0%	DAC 100%
$\pm 5V$	Register	-30000	0	+30000
	Internal	-16384	0	+16384
$\pm 10V$	Register	-30000	0	+30000
	Internal	-32767	0	+32768

Current Output Specifications (ES2151 Only):

Output Ranges: Select 0-20mA DC or 4-20mA DC. The 4-20mA range is a sub-range of the 0-20mA range.

Output Accuracy: Output is auto-calibrated from the factory to better than $\pm 0.1\%$ for current outputs. Manual calibration of current outputs can be used to improve output accuracy to better than 0.05%. Units shipped from the factory have not been manually calibrated.

Output Excitation: 9-16V Excitation required for current outputs (see the Power Requirements – Excitation specifications).

Note (Current Output Calibration & Loopback): Unit can be triggered to auto-calibrate the outputs. However, for ES2151 models, this does not compensate for potential offset in the output V/I stage (up to $\pm 0.07\%$), as it loops back the DAC voltage driving the V/I converter. Use manual per-channel calibration to calibrate to a higher rated accuracy better than $\pm 0.05\%$. Manual calibration of the outputs is not done from the factory. Likewise, if current outputs are looped back, the current indicated will not be corrected for any last stage or manual calibration offset.

SPECIFICATIONS**Analog Outputs – Voltage (ES2152 Only)**

16 Voltage Outputs with DB25 8B Interface

Note: This unit can auto-calibrate its DAC output by reading each output with its own A/D along with a known reference voltage ($4.5V \pm 0.05\% \pm 5ppm/^{\circ}C$). This is sufficient to achieve accuracy suitable for most applications. The output accuracy can still be improved by several orders of magnitude by manually calibrating the output. Units shipped from the factory have not been manually calibrated.

Auto-calibrating the current outputs does not compensate for small errors of the last output stage and manual calibration can be used to improve their performance.

SPECIFICATIONS

Analog Outputs – Current (ES2151 Only)

16 Process Current Outputs

Output Maximum Current: Limited in firmware to 21.5mA (0-20mA) and 21.4mA (4-20mA). Limits are set by the 16-bit signed integer maximum of 32768, and a normalized DAC count of 0-30000 for 0-100%. This ideally corresponds to 21.84mA for 0-20mA range, and 21.48mA for 4-20mA range.

Output Compliance: 13V (w/16V excitation). See Power Requirements – Excitation of the Environmental specifications on page 106.

Output Load Resistance Range: 0-600Ω. Output resistance is a function of excitation voltage as $R_{load_max} = (V_{exc} - 3.3)/0.0215$. See the Power Requirements – Excitation specifications on page 106 for a table of load resistance versus excitation voltage.

Output Response Time: Measured from the write of an output register to 98% of the output step transition into a 250Ω load is approximately 7.0125ms.

Output Resolution (Current): A 16-bit, ±10V DAC drives a V/I converter such that output current is the DAC voltage divided by 249. With ±32768 representing ±10V, an internal count of 0-16318 yields 0-20mA, and 3264-16318 yields 4-20mA. This is 1.226uA/bit, or 1 part in 16318 for 0-20mA, 1 part in 13054 for 4-20mA. From the programmers perspective, all unipolar output ranges are normalized to 0-30000 for 0-100% of range and the effective resolution is limited by the internal DAC resolution as described. The normalized over-range count is limited to a 16-bit signed integer of 32768, which is 32768/30000, or 109.2%. This yields an ideal over-range value of approximately 21.84mA at 32768 for 0-20mA range, and 21.48mA for 4-20mA range.

IDEAL RANGE LIMITS WRT DAC COUNT

	DAC CT	Voltage	Current	PGM COUNT
0-20mA Range				
0mA	0	0.000V	0.000mA	0
20mA	16318	4.980V	20.000mA	30000
OVR	17824	5.4395V	21.84mA	32768
4-20mA Range				
4mA	3264	0.996V	4.000mA	0
20mA	16318	4.980V	20.000mA	30000
OVR	17523	5.3476V	21.48mA	32768

8B Expansion Interface

For Connection to 8B Inputs at X1, and 8B Outputs at X2 of ES2152 Only

8B Modules, Expansion Panels, and Accessories can be purchased from Acromag, or from DataForth (consult the factory).

A female DB25 connector marked X1 on the front of the unit allows you to alternately connect an 8BP04, 8BP08, or 8BP16 back-panel for installation of industry-standard 8B *input* modules (sold separately). A second female DB25 connector marked X2 on the back of ES2152 units allows you to connect an 8BP04, 8BP08, or 8BP16 back-panel of 8B *output* modules.

Each I/O voltage port of 8 field channels is multiplexed to a separate A/D input, while current inputs of the unit operate independent of voltage inputs and the X1 interface. Sixteen 16-bit voltage output DAC's drive the sixteen output channels of the unit. 8B modules will displace the corresponding field voltage I/O channels of the unit. Since 8B modules correspond to the voltage channels of a unit, they can freely intermix with the current inputs. It is also possible to intermix field voltage channels on the unit with 8B channels on a connected back-panel, as long as you do not wire to corresponding inputs.

Each DB25 interface provides up to 16 sockets for installation of 8B signal conditioning modules. There are ninety Series 8B input module types currently compatible with these carriers, plus twelve output module types (see Table). 8B I/O modules include transient protection and 1500VAC isolation (240VAC safety rated isolation). All 8B input modules drive an isolated output voltage of $\pm 5V$, $1-5V$, or $0-5V$ to the internal A/D channels via the DB25 interface. This unit samples these voltages at high-speed using its $\pm 5V$ or $\pm 10V$ A/D input range. Input module ranges are rescaled by the unit as required by the application. Likewise, 8B output modules are driven by $0-5V$, $\pm 5V$, and $\pm 10V$. Sixteen bit $\pm 10V$ DAC's are used to drive the output channels.

IMPORTANT: 8B Output Modules are NOT supported by the ES2151, or interface connector X1. 8B Output modules can be driven via interface connector X2 of the ES2152. You cannot intermix 8B output modules with input modules on the same carrier without introducing contention and risking damage to I/O circuitry. Install only 8B input model types on any 8BP carriers connected to the X1 DB25 interface, and 8B output model types on any 8BP carriers connected to the X2 DB25 interface.

8B Compatible Panels: Industry standard 8B module carriers 8BP04 (4 channel), 8BP08 (8 channel), and 8BP16 (16 channel). Panels and modules are sold separately. These items can be purchased from Acromag, or directly from Dataforth. Consult factory.

8B Compatible Modules: See table of following page for input module types. Modules mount in pin sockets on the carrier circuit board and are retained via module-captive 4-40 machine screws (one per module). Refer to the Acromag web site at www.acromag.com for the most up to date listing of modules

8B Compatible Cable: System interface cable is SCMCA006-xx (xx is -01, -02, or -07 and refers to the length in meters). This is a DB25 Male to Female interface cable for connecting 8BP04/08/16 back panels to the EtherStax unit, or other host systems.

8B I/O Channels: ES2151 models support up to 16 8B input channels only, while ES2152 models support up to 16 8B input channels, plus up to 16 8B output channels. Channels are mapped as shown below.

8B I/O Module Power: 8B back-panels require separate power hookup ($+5V$ or $12-28V$ DC, according to power configuration).

8B I/O Channel Isolation: 8B I/O modules safely isolate the input signal for 240VAC continuous and pass a 1500Vrms isolation test. Unit provides 0.108 inches of minimum clearance to maintain the reinforced 240VAC continuous safety isolation rating for each barrier.

8B Input Configuration (Per Channel): 8B inputs output a signal of $0-5V$, $\pm 5V$, or $+1V$ to $5V$ according to model. Unit has $\pm 5V$ and $\pm 10V$ A/D ranges. It is up to the user to scale the 8B signal per the application.

8B Input Scaling (Per Channel): Allows input ranges to be rescaled to other engineering units by specifying the 0% & 100% input signal endpoints. This also allows wider-range 8B models to mimic smaller range 8B models. Rescaling should not divide the input range by more than 16 (0.0625x) to maintain minimum 12-bit performance levels of ± 2048 bits.

SPECIFICATIONS

8B Expansion Interface

For Connection to 8B Inputs at X1, and 8B Outputs at X2 of ES2152 Only

SPECIFICATIONS

8B Expansion Interface

For Connection to 8B Inputs at X1, and 8B Outputs at X2 of ES2152 Only

Modules that do not utilize the full ±10V 16-bit DAC range will have a proportionally lower effective resolution (see Table).

8B I/O Update Rate: See Input Update Rate for the rate that the unit scans 8B inputs at X1. The time that unit updates outputs at X2 varies, as inputs take priority. Actual throughput may vary according to 8B module model number as each 8B modules adds its own latency to these figures and this time can be determined from the specific module data sheets.

8B Output Scaling: None. Unit output channel voltage is ±10V and it is up to the user to set the appropriate output voltage range to control the 8B output modules. This control range varies with the output module model and this is indicated in the table below:

8B Output Modules (Sold Separately): Refer to the following table:

8B Model	Input Signal ¹	Output Range ²	8B Model	Input Signal ¹	Output Range ²
Current Output Modules (100Hz)			Voltage Output Modules (1KHz)		
8B39-01	0-5V	4-20mA	8B49-01	0-5V	±5V
8B39-02	±5V	4-20mA	8B49-02	±5V	±5V
8B39-03	0-5V	0-20mA	8B49-03	±5V	0-5V
8B39-04	±5V	0-20mA	8B49-04	0-10V	±10V
8B39-07	±5V	±20mA	8B49-05	±10V	±10V
			8B49-06	±10V	0-10V
			8B49-07	±5V	±10V

¹ The Input Signal refers to the internal signal required to drive the output.

² The Output Range is the isolated field output signal of the module.

8B I/O Channel Mapping: Refer to the following tables to map 8B input channels at X1 to 8B inputs via the DB25 interface connector X1, and 8B output channels at X2, to 8B outputs via DB25 interface connector X2 (ES2152 only).

X1		Both Models Include X1 Input Interface													
Input Port 1 Field Voltage Channels								Input Port 2 Field Voltage Channels							
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
4CH 8BP04				4	5	6	7	8	9	10	11	12	13	14	15
8CH Backpanel 8BP08								8	9	10	11	12	13	14	15
16CH Back Panel 8BP16															
DB25 X1 Interface Connector Includes 16 channels CH00-CH15-															

X2		ES2152 Only (X2 Interface Not Included on ES2151)													
Port 1 Voltage Output Channels								Port 2 Voltage Output Channels							
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
4CH 8BP04				4	5	6	7	8	9	10	11	12	13	14	15
8CH Back Panel 8BP08								8	9	10	11	12	13	14	15
16CH Back Panel 8BP16															
DB25 X2 Interface Connector Includes 16 output channels CH00-CH17															

8B INPUT Modules (Sold Separately)

Model	Input Signal	Output Range	Model	Input Signal	Output Range
8B30-01	±10mV	-5V to +5V	8B41-01	±1V	-5V to +5V
8B30-02	±50mV	-5V to +5V	8B41-02	±5V	-5V to +5V
8B30-03	±100mV	-5V to +5V	8B41-03	±10V	-5V to +5V
8B31-01	±1V	-5V to +5V	8B41-07	±20V	-5V to +5V
8B31-02	±5V	-5V to +5V	8B41-09	±40V	-5V to +5V
8B31-03	±10V	-5V to +5V	8B41-12	±60V	-5V to +5V
8B31-07	±20V	-5V to +5V	8B41-04	±1V	0 to +5V
8B31-09	±40V	-5V to +5V	8B41-05	±5V	0 to +5V
8B31-12	±60V	-5V to +5V	8B41-06	±10V	0 to +5V
8B31-04	±1V	0 to +5V	8B41-08	±20V	0 to +5V
8B31-05	±5V	0 to +5V	8B41-10	±40V	0 to +5V
8B31-06	±10V	0 to +5V	8B41-13	±60V	0 to +5V
8B31-08	±20V	0 to +5V	8B42-01	4-20mA	0 to +5V
8B31-10	±40V	0 to +5V	8B42-02	4-20mA	+1 to +5V
8B31-13	±60V	0 to +5V	8B45-01	0-500Hz	0 to +5V
8B32-01	4-20mA	0 to +5V	8B45-02	0-1KHz	0 to +5V
8B32-02	0-20mA	0 to +5V	8B45-03	0-2.5KHz	0 to +5V
8B34-01	Pt100Ω	0 to +5V	8B45-04	0-5KHz	0 to +5V
8B34-02	Pt100Ω	0 to +5V	8B45-05	0-10KHz	0 to +5V
8B34-03	Pt100Ω	0 to +5V	8B45-06	0-25KHz	0 to +5V
8B34-04	Pt100Ω	0 to +5V	8B45-07	0-50KHz	0 to +5V
8B35-01	Pt100Ω	0 to +5V	8B45-08	0-100KHz	0 to +5V
8B35-02	Pt100Ω	0 to +5V	8B47J-01	TC J	0 to +5V
8B35-03	Pt100Ω	0 to +5V	8B47J-02	TC J	0 to +5V
8B35-04	Pt100Ω	0 to +5V	8B47J-03	TC J	0 to +5V
8B36-01	0-100Ω	0 to +5V	8B47J-12	TC J	0 to +5V
8B36-02	0-500Ω	0 to +5V	8B47J-04	TC K	0 to +5V
8B36-03	0-1KΩ	0 to +5V	8B47J-05	TC K	0 to +5V
8B36-04	0-10KΩ	0 to +5V	8B47J-13	TC K	0 to +5V
8B37J	TC J	0 to +5V	8B47J-14	TC K	0 to +5V
8B37K	TC K	0 to +5V	8B47J-06	TC T	0 to +5V
8B37T	TC T	0 to +5V	8B47J-07	TC T	0 to +5V
8B37R	TC R	0 to +5V	8B50-01	±20mV	-5V to +5V
8B37S	TC S	0 to +5V	8B50-02	±50mV	-5V to +5V
8B38-01	±10mV	-5V to +5V	8B50-03	±100mV	-5V to +5V
8B38-02	±30mV	-5V to +5V	8B51-01	±1V	-5V to +5V
8B38-05	±20mV	-5V to +5V	8B51-02	±5V	-5V to +5V
8B38-31	±10mV	-5V to +5V	8B51-03	±10V	-5V to +5V
8B38-32	±30mV	-5V to +5V	8B51-07	±20V	-5V to +5V
8B38-35	±20mV	-5V to +5V	8B51-09	±40V	-5V to +5V
8B40-01	±10mV	-5V to +5V	8B51-12	±60V	-5V to +5V
8B40-02	±50mV	-5V to +5V	8B51-04	±1V	0 to +5V
8B40-03	±100mV	-5V to +5V	8B51-05	±5V	0 to +5V
			8B51-06	±10V	0 to +5V
			8B51-08	±20V	0 to +5V
			8B51-10	±40V	0 to +5V
			8B51-13	±60V	0 to +5V

SPECIFICATIONS**Supported 8B Input Modules (Modules Sold Separately)**

One DB25 input interface connector on this model provides support for optional connection of an 8B back panel. This allows interface with up to 16 isolated 8B input signal conditioning modules. Currently, there are 90 input model types listed in the table at left that are compatible with this carrier.

Input modules also include 1500VAC input isolation (240VAC safety isolation) and transient protection.

Note that input modules can be divided into 3 groups based on their output signal: ±5V, 0-5V, and 1-5V. This output signal is processed via 16-bit A/D conversion setup for -5V to +5V inputs.

Modules that do not utilize the full A/D range will have a proportionally lower effective resolution (see Table).

IMPORTANT: You cannot intermix 8B Output modules on the same back-panel as 8B input modules, or damage to the unit may result.

SPECIFICATIONS

Alarm Relay Output

This device includes a set of isolated relay contacts adjacent to power at the A & B terminals. The state of these contacts can be set as normally open (de-energized, non-failsafe), or normally closed (energized, fail-safe). These contacts will transfer states upon media failure (link loss), or power failure (if normally energized/failsafe operation is selected). A red relay LED indicates that the relay contacts are energized (closed).

Type: SPST-NO, 1 Form A, Class I, Division II approved.

Manufacturer Part: Omron, G6M-1A-DC5.

Maximum Ratings: AC rated to 3A at 250VAC, or 750VA maximum (100K operations minimum). DC rated to 3A at 30VDC, or 90W maximum. Your AC application switching voltage/ current must not exceed 750VA and 250VAC and 5A. Your DC application voltage/current must not exceed 90W and 125VDC and 5A.

Contact Resistance: 100 milliohms, maximum.

Agency Rating: 3A at 250VAC or 24VDC (General Use, 100K Operations), or 5A at 250VAC or 24VDC (Resistive Load, 6K Operations). UL508 File No. E41515/CSA C22.2 (No. 14) File No. LR31928. Hazardous Location ratings are 2A at 240VAC or 30VDC.

Minimum Permissible Load: 10mA at 5VDC at 120 operations/minute.

Memory

This unit contains both volatile and non-volatile memory. It does not contain any fixed or removable disk or tape drives, or memory cards. For security or sanitization considerations, review the following:

Flash Memory (Non-Volatile): This 4 Megabyte memory is used for storage register data, communication configuration parameters, and web-page information. It is user-modified via configuration. It is sanitized by holding the default switch while powering up the unit until the green Run LED turns OFF. At this time, the memory reverts to the factory default settings, except for the MAC ID and serial number which are fixed. Refer to "Getting Out of Trouble" section in this manual for more information.

FRAM (Non-Volatile): This 8 Kilobyte memory is resident on the I/O board and is used to store the channel configuration, calibration coefficients, and scaling information for the inputs. It is user-modified via channel setup and calibration. Its contents can be cleared to factory default calibration values by clicking the Restore All Default Calibration Values button of the Input Calibration web page.

SRAM (Volatile): This 132 kilobyte memory is integrated within the central processor and is used as scratchpad memory by the processor during run time. Its contents are cleared at power-down.

SDRAM (Volatile): This 64 Megabyte memory is external to the central processor and used as the run time memory for high-speed execution of this unit's internal program. Its contents are cleared on power-down.

Agency Approvals

Safety Approvals: Enclosed Models, ES2151/2152-0000 & ES2151/2152-1000 are CE marked (EMC Directive 2004/108/EC), and cULus Listed (UL508-Seventeenth Edition, ISA 12.12.01:2007, Canada Standard C22.2, No. 142-M1987 & 213-M1987) for Hazardous Locations, Class 1; Division 2; Groups A, B, C, and D. Open board models ES2151/2152-0010 & ES2151/2152-1010 are cULus Recognized Components for Hazardous Locations, Class 1; Division 2; Groups A, B, C, and D (UL508-Seventeenth Edition, ISA 12.12.01:2007, Canada Standard C22.2, No. 142-M1987 & 213-M1987).

SPECIFICATIONS

Enclosure & Physical

Enclosure Material: Extruded aluminum, 6063 T6 alloy, silver anodized finish, IP40 minimum rated.

Circuit Boards: Military grade fire-retardant epoxy glass per IPC-4101/98.

Dimensions: IP40 rated enclosure, 8.226 inches wide, 2.444 inches tall, 6.125 inches deep. Enclosed units stack together on 2.175 inch centers. Open frame units are 1.664" tall with 0.375" standoffs. Units will require an optional surface mounting plate and or DIN-rail mount to securely mount the unit (see below). Refer to the Mounting & Dimensions section at the front of this manual for more details.

Surface/Wall-Mounting: Requires optional surface-mounting kit ESA-SMK. See Mounting & Dimensions section for details on this option.

DIN-Rail Mounting: The unit can be mounted to 35x15mm, T-type DIN rails using optional ESA-DIN-VMK, or ESA-DIN-HMK mounting kits. Refer to the Mounting & Dimensions section for more details.

Open-Board Mounting/Stacking: For units ordered without an enclosure, enough 6-32 jack-screws and threaded standoffs for stacking two assemblies are included with every open unit and in the Acromag ESA-OMK Open Mounting Kit. Refer to the Mounting & Dimensions section for more details.

I/O Connectors: Removable plug-in type terminal blocks are rated for 8A/160V; AWG #16-28 stranded or solid copper wire. The torque ratings for field wiring terminals is 0.22-0.25 Nm.

Relay/Power Connectors: Removable plug-in type terminal blocks rated for 15A/300V; AWG #12-24 stranded or solid copper wire. The torque ratings for field wiring terminals is 0.5-0.6 Nm (5-7 lb-inches).

Network Connector (Copper): One or two 8-pin RJ-45 sockets according to model. Connections are wired MDI-X by default (like an Ethernet switch, as opposed to MDI), and include automatic MDI/MDI-X crossover. Connect using CAT-5 or better cable. For increased immunity to shock & vibration, Acromag offers an optional Cable Termination Kit (ESA-CTK) that includes the necessary hardware for building one end of your cable for mating to the IP20 clip-frame that surrounds the RJ45 port.

RJ-45	Signal	Description (MDI-X)
1	Rx+	Receive Positive
2	Rx-	Receive Negative
3	Tx+	Transmit Positive
4	Not Used	Connects to Pin 5
5	Not Used	Connects to Pin 4
6	Tx-	Transmit Negative
7	Not Used	Connects to Pin 8
8	Not Used	Connects to Pin 7

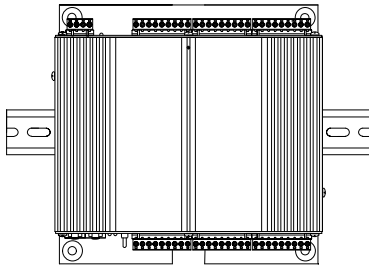
Network Connector ("-1xxx" Units w/100Base-FX Fiber): One 8-pin RJ45 socket as wired above (for port 2), plus one 100BaseFX, SC-Type, multi-mode fiber-optic connector (for port 1). Note that the auto-negotiation & auto-crossing features do not apply to the fiber connection and transmit and receive cables must be crossed manually when making fiber cable connections.

Shipping Weight: 3.8 pounds (1.8 Kg) packed (unit w/enclosure is 3.4lbs); 1.5 (0.7Kg) packed (open-frame unit/no enclosure is 1.05lbs).

SPECIFICATIONS

Environmental

IMPORTANT: It is recommended enclosed units be mounted as shown, with the front-endplate facing down, and back endplate facing up:



Mounting in this manner allows cool air to flow into the front (bottom), and hot air to pass out the back (top), through the vents provided and the open area around the terminals.

CAUTION: Risk of Electric Shock – More than one disconnect switch may be required to de-energize the equipment before servicing.

Operating Ambient Temperature: -40°C to +70°C (-40°F to +158°F), all models.

Storage Temperature: -40°C to +85°C (-40°F to +185°F).

Relative Humidity: 5 to 95%, non-condensing.

Isolation: I/O channels (as a group), alarm relay, power, and network circuits (individually) are isolated from each other for common-mode voltages up to 250VAC, or 354V DC off DC power ground, on a continuous basis (will withstand 1500VAC dielectric strength test for one minute without breakdown). Complies with test requirements of ANSI/ISA-82.01-1988 for voltage rating specified.

Installation Category: Suitable for installation in a Pollution Degree 2 environment with installation category (over-voltage category) II rating.

Electromagnetic Interference Immunity (EMI): Inputs/outputs have demonstrated resistance to inadvertent level changes with interference from switching solenoids, commutator motors, and drill motors.

Power Requirements (Unit Main): 18-36V DC SELV (Safety Extra Low Voltage), 5.8W max (ES2151/2152-1), 4.8W max (ES2151/2152-0). Observe proper polarity. Keep DC power cables less than 10m in length. Divide power by voltage to calculate max current. Select a supply that can deliver at least twice this amount. Current noted is with one network port linked and the alarm relay energized.

CAUTION: Do not exceed 36VDC peak, to avoid damage to the unit.

Power	Model ES2151/2152-0	Model ES2151/2152-1
18V	239mA Typ, 263mA Max	293mA Typ, 322mA Max
24V	177mA Typ, 195mA Max	218mA Typ, 240mA Max
30V	143mA Typ, 157mA Max	175mA Typ, 192mA Max
36V	120mA Typ, 132mA Max	147mA Typ, 162mA Max

Power Requirements (Excitation, ES2151 Models Only): Connect 9-16V DC between excitation terminal (E) and output common (C) for rated performance. The maximum load resistance will depend on your excitation voltage as follows:

ES2151 EXCITATION	LOAD RESISTANCE RANGE
9V	0-265Ω
10V	0-312Ω
11V	0-358Ω
12V	0-405Ω
13V	0-451Ω
14V	0-498Ω
15V	0-545Ω
16V	0-590Ω
24V ¹	0-600Ω ¹

Notes (ES2151 Excitation Requirements):

1. With 24V excitation, the maximum operating ambient must derate to +35°C (-x000 enclosed models), or +40°C (-x010 open models).
2. The maximum load resistance is calculated from the excitation voltage as follows: $R_{load_max} = (V_{exc} - 3.3)/0.0215$.

Shock & Vibration Immunity: Surface mounted unit with enclosure rated to 5G sinusoidal vibration and 5Grms Random Vibration, 10-500Hz, in 3 axis at 2 hours/axis per IEC60068-2-6 and IEC60068-2-64; Mechanical Shock to 50g, 3ms, with 3 half-sine shock pulses in each direction along 3 axis (18 shocks), and 30g, 11ms, with 3 half-sine shock pulses in each direction along 3 axis (18 shocks), per IEC60068-2-27.

Electromagnetic Compatibility (EMC) -

Minimum Immunity Per European Norm EN61000-6-2:2001

Electrostatic Discharge (ESD) Immunity: 4KV direct contact and 8KV air-discharge to the enclosure port per IEC61000-4-2.

Radiated Field Immunity (RFI): 10V/m, 80 to 1000MHz; 3V/m, 1.4 to 2.0 GHz; 1V/m, 2.0 to 2.7 GHz; per IEC61000-4-3.

Electrical Fast Transient Immunity (EFT): 2KV to power, and 1KV to signal I/O per IEC61000-4-4.

Conducted RF Immunity (CRFI): 10Vrms, 150KHz to 80MHz, per IEC61000-4-6.

Surge Immunity: 0.5KV to power per IEC61000-4-5. By standard definition, this test is not applicable to DC power input ports intended to be permanently connected to cables less than 10m in length. Further, this test is not applicable to I/O ports that interface via cables whose total length is less than 30m.

Emissions per European Norm EN61000-6-4:2007

Radiated Frequency Emissions: 30 to 1000MHz per CISPR16 Class A

WARNING: This is a Class A product. In a domestic environment, this product may cause radio interference in which the user may be required to take adequate measures.

IMPORTANT: Power and I/O wiring must be in accordance with Class I, Division 2 wiring methods of Article 501-4(b) of the National Electrical Code, NFPA 70 for installations in the US, or as specified in section 18-1J2 of the Canadian Electrical Code for installations within Canada and in accordance with the authority having jurisdiction.

This equipment is suitable for use in Class I, Division 2, Groups A, B, C, and D, or non-hazardous locations only.

WARNING – EXPLOSION HAZARD – Substitution of components may impair suitability for Class I, Division 2.

WARNING – EXPLOSION HAZARD – Do not disconnect equipment unless power has been switched off or the area is known to be non-hazardous.

The maximum surrounding air temperature is 75°C.

The torque ratings for field wiring terminals is 0.5-0.6Nm (4.4-5.3 lb-inches) for power & relay terminals), and 0.22-0.25Nm (1.9-2.2 lb-inches) for the input terminals.

Reference Standard: CNR indicates investigation to Canadian Standard C22.2, No's. 142-M1987 & 213-M1987; USR indicates investigation to United States UL Standards 508 Seventeenth Edition & ISA 12.12.01:2007.

MTBF (Mean Time Between Failure): MTBF in hours using MIL-HDBK-217F, FN2.

Temp	ES2151-00x0	ES2151-10x0	ES2152-00x0	ES2152-10x0
25°C	380,198 hrs	384,902 hrs	492,669 hrs	500,596 hrs
40°C	274,329 hrs	272,321 hrs	357,804 hrs	354,395 hrs

Per MIL-HDBK-217, Ground Benign, Controlled, $G_B G_C$.

SPECIFICATIONS

Environmental

These limits represent the minimum requirements of the applicable standard, but this product has typically been tested to comply with higher standards in some cases.

TIP: For critical applications or units subject to severe shock or interference, utilize the built-in watchdog timer and alarm relay to signal an interruption in communication, link loss, or optionally power failure (w/failsafe contacts).

Reliability Prediction

SPECIFICATIONS

Ethernet Interface

Note: Slow rate 10Base-T is not supported in hub mode. Hubs and repeaters are inherently half-duplex devices, and full-duplex communication will not be possible in hub/repeater mode. As such, auto-negotiation will also not be supported in hub mode.

Refer to Acromag Application Note 8500-734 for instructions on how to change the IP address of your PC network interface card in order to talk to this unit.

Optionally, port 1 may be selected to interface with fiber-optic cable at 100Mbps. This allows a unit to operate as a local media converter, with a fiber-optic home-run connection, and a local area copper network connection.

Fiber ports are fixed at 100Mbps, half or full duplex, and auto-negotiation and automatic MDI/MDI-X crossing does not apply.

In hub/repeater mode, ports are 100Mbps only at half-duplex and auto-negotiation does not apply.

Utilizes a built-in 3-port Ethernet switch to interface an internal MII processor, to dual external 10/100M Ethernet ports. This switch has two modes of operation—it may function as a two-port store & forward Ethernet switch (default), or as a low latency hub/repeater. Switch mode is useful to facilitate a cascaded network connection between units for stacking purposes, and may extend network distances another 100 meters per segment, without consuming an additional external Ethernet switch port. Hub/repeater mode is useful to facilitate end-node media redundancy right to this device when connected to external Ethernet switches that happen to support redundancy (proprietary ring, STP, or RSTP). Hub mode is also useful for low-latency cascaded network connections, or where multi-unit network traffic is concentrated. Switch mode is the recommended mode (default), with hub mode preserved for redundant media applications using copper connections, or where low-latency network connections are required.

Network Connector (Copper): One ("-1xxx"), or two ("-0xxx"), 8-pin RJ-45 sockets for 10BaseT/ 100BaseTX connections.

Network Connector (Fiber, ES2151/2152-1xxx Models): One duplex SC-type, multi-mode transceiver for IEEE 802.3u 100Base-FX cable connections. Multimode transmission distance is 2Km.

Wiring (Copper): Wired MDI-X (Ethernet switch), but unit supports automatic crossover for copper (RJ-45) connections.

Data Rate: Auto-sensed, 10Mbps or 100Mbps on copper connections, fixed to 100Mbps on fiber connection. In hub/repeater mode, the data rate is fixed to 100Mbps and auto-negotiation does not apply.

Duplex: Auto-negotiated, Full or Half Duplex. Half-duplex only in hub/repeater mode (auto-negotiation does not apply). The fiber port cannot operate half duplex in repeater mode.

Compliance: IEEE 802.3, 802.3u, 802.3x.

IP Address: Default mode static IP address is 128.1.1.100.

Transient Protection: Transient Voltage Suppressors are applied to both the transmit and receive channels of both ports.

Protocol: Modbus TCP/IP or UDP/IP with integrated web-browser reconfiguration. Unit will respond via UDP for messages received via UDP, and via TCP for messages received via TCP. Up to 10 Modbus TCP/IP sockets are supported using port 502 (reserved for Modbus). The number of sockets limit does not apply to messages sent via UDP/IP, as UDP is a connectionless protocol. Unit functionality is configured via memory map registers using Modbus commands & built-in web pages.

MAC Address Table: 1K MAC Address table.

Password/User-Name Default: Default web-browser password for access is "password" and the user-name is "User".

Network Distance: Distance between two devices on an Ethernet network is generally limited to 100 meters using recommended copper cable, and 2Km using multi-mode fiber optic cable, but may be extended using hubs and switches. However, the total round trip delay time along a network path must never exceed 512 bit times for collision detection to work properly.

IP Address: Can be preset by the user (static) and loaded from internal non-volatile memory, or it can be automatically acquired at startup via a network server using a BOOTP (Bootstrap Protocol), or DHCP (Dynamic Host Configuration Protocol). The unit includes a default mode toggle switch to cause the unit to assume a "known" fixed static IP address of 128.1.1.100, useful for troubleshooting purposes.

LED Indicators (Rear Panel):

Rear System Status Indication (Located next to power terminals)

RELAY (Red) – Indicates the energized state of the adjacent SPST-NO relay contacts A & B. ON indicates these contacts are closed. OFF indicates these contacts are open. ON by default following power-up indicates a failsafe contact setting (normally energized).

STATUS (Yellow) – Slowly blinks ON/OFF in default mode, blinks rapidly if a watchdog timeout has occurred.

RUN (Green) – Blinks momentarily upon power-up but turns constant ON if power is on and unit is OK. Continuous flashing after power-up may also indicate that the network cable was not connected or was bad on power-up. If you power-up without a network cable connected, this LED will flash until you connect the network cable. It will not start flashing if the cable is disconnected after a link has been established. Continuous flashing ON/OFF may also indicates unit is in “wink” ID mode.

Controls (Front-Panel):

External (User Access)

Reset/Default Address Toggle: This momentary toggle switch is located on the front panel adjacent to the network LED indicators and is used to either reset the unit (momentary toggle down), or toggle the unit into, or out of, Default Communication Mode (toggle up, hold for 4 seconds). In Default Mode, the unit assumes the fixed static IP address “128.1.1.100”, a default subnet mask “255.255.255.0”, a default username of “User”, and a default password of “password”. This switch can also be used to restore the unit to its initial factory configuration by holding the switch in its default position while powering up the unit (see “Getting Out Of Trouble” in the Troubleshooting section for more information). Reset is useful for trouble-shooting purposes without having to cycle power. If communication with a unit is ever lost, it can typically be restored by simply resetting the unit via this switch, or by cycling power.

Front Network Status Indication Per Port (next to network ports)

These LED’s indicate different information according to whether the network ports are in switch mode, or hub/repeater mode. In switch mode, column 1 corresponds to port 1 status and column 2 corresponds to port 2 status.

Note: Switch Mode is the default mode of communication for this device.

Port SWITCH Mode

Port 1 and Port 2 Indicator Columns 1 & 2, Top to Bottom

3 Green (No Function) – LED (top) has no function in switch mode.

2 Yellow (LINK/ACT) – Constant ON indicates auto-negotiation has successfully established a connection/link. Blinking indicates Ethernet activity on the link (Ethernet connection is busy/traffic is present). OFF indicates no link.

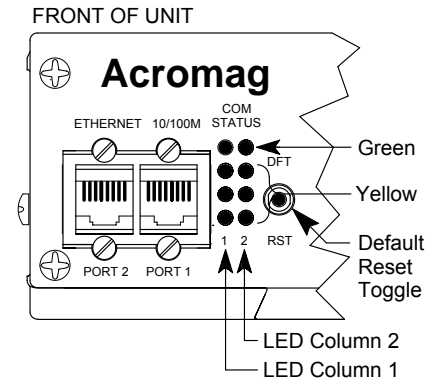
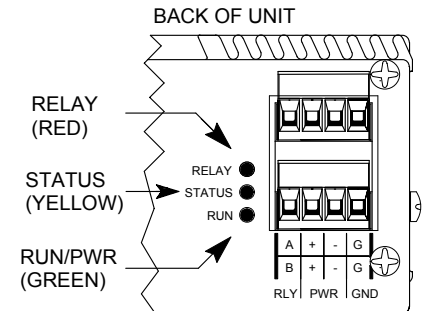
1 Yellow (FDX/COL) – Constant ON indicates full-duplex connection (no collisions possible). Intermittent ON indicates collisions (half-duplex). Constant OFF indicates half-duplex and no collisions.

0 Yellow (SPEED) – This LED (bottom) indicates 100Mbps speed (ON), and 10Mbps speed (OFF).

Note: Fiber ports are 100Mbps only, half or full duplex. In hub/repeater mode, both ports are 100Mbps only and half-duplex.

SPECIFICATIONS

Controls & Indicators



SWITCH MODE

LED Column 1 - Port 1
LED Column 2 - Port 2

GREEN - No Function in Switch Mode.

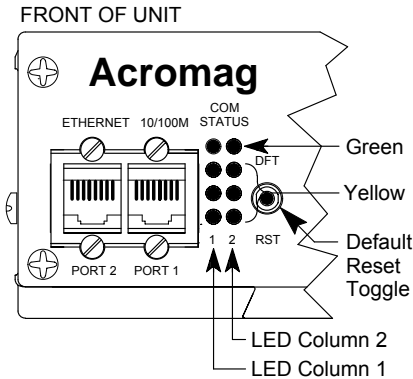
YELLOW - Link/Activity: ON if Linked/Blinks if Activity.

YELLOW - Full-Duplex/Collision: ON for FD, Blinks for HD Collisions, OFF for HD and No Collisions.

YELLOW - Speed: ON for 100Mbps, OFF for 10Mbps.

SPECIFICATIONS

Controls & Indicators



1=LED of Column 1
2=LED of Column 2

GREEN: 1=Hub Activity, 2=Hub Collision.

YELLOW: 1=MII/CPU Link/Activity, 2=MII/CPU Error.

YELLOW: 1=Port 2 Link/Activity, 2=Error at Port 2.

YELLOW: 1=Port 1 Link/Activity, 2=Error at Port 1.

Port HUB/REPEATER Mode

Indicator Column 2 (Top to Bottom)

3 Green (ACT) – The top LED indicates repeater activity on the link (ON or blinking).

2 Yellow (ERR3) – ON indicates an error has been encountered at internal port 3 (the processor MII port). Error is related to isolation, partition, jabber, or JK error.

1 Yellow (ERR2) – ON indicates an error has been encountered at network port 2 (the left port). Error is related to isolation, partition, jabber, or JK error.

0 Yellow (ERR1) – ON indicates an error has been encountered at network port 1 (the right port). Error is related to isolation, partition, jabber, or JK error.

Indicator Column 1 (Top to Bottom)

3 Green (Collision) – The top LED turns ON when a collision occurs.

2 Yellow (Link3/Rx ACT) – Constant ON indicates auto-negotiation has successfully established a connection/link at internal port 3 (the processor MII port). Blinking indicates receive activity on the link (connection is busy/traffic is present). OFF indicates no link.

1 Yellow (Link2/Rx ACT) – Constant ON indicates auto-negotiation has successfully established a connection/link at network port 2 (the left port). Blinking indicates receive activity on the link (connection is busy/traffic is present). OFF indicates no link.

0 Yellow (Link 1/Rx ACT) – Constant ON indicates auto-negotiation has successfully established a connection/link at network port 1 (the right port). Blinking indicates receive activity on the link (connection is busy/traffic is present). OFF indicates no link.

CABLES & CONNECTORS

Copper Connections

The minimum cable required for full operation of this device is Category 5. The term “Category” refers to classifications of UTP (Unshielded Twisted Pair) cables. There are 3 main categories of cable – Category 3, Category 4, and Category 5. The differences in classification is found in their electrical performance and this is documented in the TIA/EIA 568A standard. Category 5 cable includes four twisted wire pairs at eight twists per foot.

This device is designed for use in harsh industrial environments. Acromag recommends the use of shielded cable when wiring to this device. Select STP (Shielded Twisted Pair) cable rather than UTP (Unshielded Twisted Pair). The use of shielded cable will help protect the transmitted data from harmful EMI (Electromagnetic Interference) and RFI (Radio Frequency Interference). It will also help to lower your radiated emissions by keeping the cable from emitting EMI and RFI.

There are two types of cable: solid cable and stranded cable. Stranded cables are more flexible than solid cables. But since attenuation is higher for stranded cables than solid conductor cables, these are generally reserved for short runs and patch applications less than 6 meters.

Currently there are two types of shielding employed in Category 5 STP cable: single-shielded cable and double-shielded cable. Both of these cables have the same core and jacket as UTP cables, but also include a thin foil outer shield that covers all four twisted-wire pairs. Some variations will also include a drain wire that encircles the outer foil.

The double-shielded version adds an outer wire screen that wraps around the foil shield and also functions as a drain wire. The drain wire or wire screen typically makes contact at each end of the cable with the metal shield around special shielded RJ45 plug connectors. The metal shield of these connectors then makes contact with the metal shield of shielded RJ45 sockets (the EtherStax units do not have this shield because they are safety isolated from their enclosure/earth ground). The socket shield may make direct contact with earth ground, or it may capacitively couple to earth ground. In addition to minimizing radio frequency and electromagnetic interference, this arrangement also has the added benefit of enhanced protection from ESD (Electro-Static Discharge).

Acromag recommends the use of *enhanced* Category 5 cable (CAT-5e). This cable has all the characteristics of Category 5, but includes enhancements that help to minimize crosstalk. It is rated for frequencies up to 200MHz, double the rate of Category 5. Category 5e cable also has a greater number of turns-per-inch in its twisted pairs, making its performance more suitable for applications that make use of all four wire pairs for simultaneous bidirectional data transmission (full-duplex). This cable is defined in TIA/EIA-568A-5 (Addendum 5).

Note that you do not need to use a crossover cable to connect your PC to an EtherStax unit, as it is auto-crossing (copper only). However, the auto-crossing feature is not applicable to the fiber-port. Fiber ports require that transmit be manually crossed over to receive, and visa-versa.

You may obtain cable from other vendors in varied lengths and colors, as required for your application. For example, shielded CAT-5e cable is available from the following vendors:

- L-com Connectivity Products, www.L-com.com
- Pro-Link, www.prolink-cables.com

For very noisy environments or in the presence of strong electrical fields, you can obtain double-shielded CAT-5e cable and shielded RJ45 plugs from the following vendors (the EtherStax does not require shielded plugs as it uses unshielded RJ45 connectors):

- L-com Connectivity Products, www.L-com.com, see cable model TFSC2004 and shielded plug T8P8CSR.
- Regal Electronics, www.regalusa.com, see shielded plug model 1003B-8P8CSR-C5.

Complete premium double-shielded Category 5e standard and crossover cables in variable lengths can be obtained from Lumberg at www.lumbergusa.com (refer to their EtherMate line). For example, specify RJ45S-RJ45S-656/B/3M for a double-shielded, 3 meter straight cable. Specify RJ45S-RJ45S-656/BX/3M for a double-shielded, 3 meter crossover cable.

Acromag also offers the following cable termination kit for building cables that take advantage of the special clip-frame provided at the RJ45 socket of the EtherStax unit. These are not required as standard plugs are still supported, but will help to secure network connections for units subject to severe shock and vibration.

CABLES & CONNECTORS

Copper Connections

CABLES & CONNECTORS

Fiber Connections

Cable Termination Kit ESA-CTK: The EtherStax enclosure includes a panel mounted frame around the RJ45 network port that accommodates special IP20 clip-type plug connectors that help to secure the network connections from shock and vibration. You can still utilize standard RJ45 modular plug connectors, but if you want the added security of this clip frame, then you have to use the compatible cable plug connectors provided by this kit. This kit provides the male plug and sleeve housing for one end of Category 5 Ethernet cable that will mate to this frame. You can purchase these items from us by referencing Acromag ESA-CTK. The Category 5 cable is not included, but readily available from other vendors. You can use a standard modular crimping tool for attaching the RJ45 plug of this kit to your cable (for example, see Phoenix crimping tool CRIMPFOX-LC-RJ45S catalog #1207420). You will need one kit for each EtherStax connection.

Model ES2151/2152-1xxx units include an SC-type fiber-optic port for multi-mode fiber connection.

Note that the standard EtherStax units use SC-type (Stab & Click) fiber connectors. If your application requires ST (Stab & Twist) type fiber connectors, you can request this option from the factory at an additional charge.

You can obtain compatible fiber cable and accessories from a variety of other vendors, and some are listed below:

L-com Connectivity Products (www.L-com.com)
fiber.com (www.fiber.com)
Belkin (www.belkin.com)
CablesToGo (www.cablestogo.com)
CablesPlus (www.cablesplusUSA.com)

Be sure to specify dual or duplex, SC type cables or patch cords. SC cables utilize a snap-in connector that latches with a push-pull motion.

If you wish to build your own cables, you will also need special tools and equipment for cutting, splicing, and polishing the fiber.

With respect to the EtherStax, note that the auto-crossing feature does not apply to the fiber-optic ports, and the transmit and receive channels of these ports must be mechanically crossed over. Likewise, auto-negotiation does not apply to the fiber port, as the speed is fixed at 100MB. Units with a fiber port cannot be placed into hub/repeater mode, as this is a full-duplex fiber connection and hubs/repeaters operate half-duplex.

For reference, when facing the front endplate of the unit, the Transmit (Tx) channel is the bottom half of the SC fiber connector, while the Receive (Rx) is the top half of the fiber connector.