

# **User Manual**

# **IDK-2117 Series**

17" High Brightness SXGA (LED Backlight)



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- 5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

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Overview

# **1.1 General Description**

The Advantech IDK-2117 series comes with a 17" 1200 cd/m2 industrial grade LCD display, and an LED driving board. The series is also available with flexible options for touch screens and enhanced treatment such as AR surface treatment and optical bonding solution. IDK-2117 series supports 1200 cd/m2 high brightness with low power consumption at the maximum consumption of 29.47 W. Equipped with high level of brightness and wide operating temperature, IDK-2117 provides superior sunlight readability and is perfect for applications whether in semi-outdoor or outdoor environments.

# 1.2 Specifications

#### 1.2.1 LCD Panel

- **Display Size:** 17" LED backlight panel
- **Resolution:** 1280 x 1024
- Viewing Angle(U/D/L/R): 80°/80°/85°/85°
- Brightness: 1200 cd/m<sup>2</sup>
- Contrast Ratio: 1000:1
- Response Time(ms): 5ms
- Colors: 16.7 M
- Voltage: 5V
- Power Consumption: 29.47W
- Signal Interface: 2 channel LVDS
- Weight: 2200g(N series)
- Dimensions(W x H x D): N series: 358.5 x 296.5 x 16.8 mm R series: 358.5 x 296.5 x 18.19mm

#### 1.2.2 LED Driver Board

- Efficiency: 85%
- Output Current & Voltage: 1400 mA / 17.3 V
- Dimensions(W x H x D): 90 x 50 x 7 mm

#### **1.2.3 Touch Screen (R series)**

- **Touch Screen:** 5-Wire Resistive
- Light Transmission: 80 ±2 %
- Durability: 1 million times

#### **1.2.4 Environment**

- Operating Temperature: 0~50 °C
- Storage Temperature: -20 ~ 60°C
- Humidity: 5~ 95% @ 40°C, non-condensing

# **1.3 Mechanical Characteristic**



# **1.4 Functional Block Diagram**

The following diagram shows the functional block of the 17 inches Color TFT-LCD Module:



#### Figure 1.1 Function block diagram

### 1.5 Touch Screen driver

The T/S driver CD-ROM is in the accessory box and comes with the product.

### **1.6 Absolute Maximum Ratings**

Absolute maximum ratings of the module is as following:

#### **1.6.1 Absolute Ratings of TFT LCD Module**

ltem	Symbol	Min.	Max.	Unit	Conditions
Logic/LCD Drive Voltage	Vin	0.5	+5.5	[Volt]	Note 1, 2

### 1.6.2 Absolute Ratings of Backlight Unit

ltem	Symbol	Min.	Max.	Unit	Conditions
LED Light Bar Current	ILed	680*2	700*2	[mA]	Note 1, 2

### **1.6.3 Absolute Ratings of Environment**

ltem	Symbol	Min.	Max.	Unit	Conditions
Operating Temperature	TOP	0	+50	[oC]	
Operation Humidity	HOP	8	90	[%RH]	– – Note 3
Storage Temperature	TST	-20	+60	[oC]	- NOLE 3
Storage Humidity	HST	8	90	[%RH]	

**Note1:** With in Ta= 25°C

Note2: Permanent damage to the device may occur if exceed maximum values



Electrical Characteristics

# 2.1 Power Specification

Table 2.1: Power specification							
Symbol	Parameter	Min.	Тур.	Max.	Unit	Condition	
VDD	Logic/LCD Drive Voltage	4.5	5	5.5	[Volt]	±10%	
IDD	Input Current	-	1.05	1.16	[mA]	All black pattern (VDD=5V, at 75Hz)	
PDD	VDD Power	-	5.25	5.8	[Watt]	All black pattern (VDD=5V, at 75Hz)	
IRush	Inrush Current	-	-	3	[A]	Note 1	

Input power specifications are as follows:

#### Note1 Measurement condition:



### 2.1.1 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off.

Symbol	Parameter	Min.	Тур.	Max.	Unit	Condition
VTH	Differential Input High Threshold	-	-	100	[mV]	VCM=1.2V
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VCM=1.2V
VID	Input Differential Voltage	100	400	600	[mV]	
VICM	Differential Input Common Mode Voltage	1.0	1.2	1.5	[V]	VTH / VTL = 100mV

Note LVDS Signal Waveform.



# 2.2 Backlight Driving Conditions

Parameter guideline for LED Light Bar Driver is under stable conditions at 25°C (Room Temperature):

Table 2.3: Backlight driving conditions									
ltem	Symbol	Values		Unit	Condition				
		Min.	Тур.	Max.					
LED Voltage	VL	17.3		17.3	V	Note 2			
LED Current	IL	680		700	mA	Note 2			
LED life time	-	50,000	-	-	Hr	Note 1			

**Note1** The "LED lift time" is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25°C and typical LED Current at 700mA.

**Note2** The LED driving condition is defined for each LED module.(6 LED Serial, a LED includes 1 Chip).



**Note3** The variance of LED Light Bar power consumption is 10%. Calculator value for reference (IL x VL x 2 = PLED)

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Signal Characteristics

# 3.1 Pixel Format Image

Following figure shows the relationship between input signal and LCD pixel format.



### 3.2 Pin Description

The module using a pair of LVDS receiver SN75LVDS82(Texas Instruments) or compatible. LVDS is a differential signal technology for LCD interface and high speed data transfer device. Transmitter shall be SN75LVDS83(negative edge sampling) or compatible. The first LVDS port(RxOxxx) transmits odd pixels while the second LVDS port(RxExxx) transmits even pixels.

Table 3	.1: Pin Des	cription
Pin No.	Symbol	Description
1	RxO0-	Negative LVDS differential data input (Odd data)
2	RxO0+	Positive LVDS differential data input (Odd data)
3	RxO1-	Negative LVDS differential data input (Odd data)
4	RxO1+	Positive LVDS differential data input (Odd data)
5	RxO2-	Negative LVDS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)
6	RxO2+	Positive LVDS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)
7	GND	Power Ground
8	RxOC-	Negative LVDS differential clock input (Odd clock)
9	RxOC+	Positive LVDS differential clock input (Odd clock)
10	RxO3-	Negative LVDS differential data input (Odd data)
11	RxO3+	Positive LVDS differential data input (Odd data)
12	RxE0-	Negative LVDS differential data input (Even data)
13	RxE0+	Positive LVDS differential data input (Even data)
14	GND	Power Ground
15	RxE1-	Negative LVDS differential data input (Even data)
16	RxE1+	Positive LVDS differential data input (Even data)

Table	3.1: Pin De	scription
17	GND	Power Ground
18	RxE2-	Negative LVDS differential data input (Even data)
19	RxE2+	Positive LVDS differential data input (Even data)
20	RxEC-	Negative LVDS differential clock input (Even clock)
21	RxEC+	Positive LVDS differential clock input (Even clock)
22	RxE3-	Negative LVDS differential data input (Even data)
23	RxE3+	Positive LVDS differential data input (Even data)
24	GND	Power Ground
25	GND	Power Ground (For AUO test Aging+HVS mode)
26	NC	No contact
27	GND	Power Ground
28	VCC	+5.0V Power Supply
29	VCC	+5.0V Power Supply
30	VCC	+5.0V Power Supply

# 3.3 The Input Data Format

Signal Name	Description	Remark		
R7	Red Data 7			
R6	Red Data 6			
R5	Red Data 5			
R4	Red Data 4	Red-pixel Data, For 8 bits LVDS input, MSB: R5;		
R3	Red Data 3	LSB:R0		
R2	Red Data 2			
R1	Red Data 1			
R0	Red Data 0			
G7	Green Data 7			
G6	Green Data 6			
G5	Green Data 5			
G4	Green Data 4	Green-pixel Data, For 8 bits LVDS input, MSB:		
G3	Green Data 3	G7; LSB:G0		
G2	Green Data 2			
G1	Green Data 1			
G0	Green Data 0			
B7	Blue Data 7			
B6	Blue Data 6			
B5	Blue Data 5			
B4	Blue Data 4	Blue-pixel Data, For 8 bits LVDS input, MSB: B7;		
B3	Blue Data 3	LSB:B0		
B2	Blue Data 2			
B1	Blue Data 1			
B0	Blue Data 0			
RxCLKIN	LVDS Data Clock			
DE	Data Enable Signal	When the signal is high, the pixel data shall be valid to be displayed.		

VS	Vertical Synchronous Signal
HS	Horizotal Synchronous Signal

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.

# 3.4 Interface Timing

### 3.4.1 Timing Characteristics

Table 3.2: Timing Characteristics								
Signal	Parameter		Symbol	Min.	Тур.	Max.	Unit	
Clock Timing	Clock frequency		1/ T <sub>Clock</sub>	50	65	81	MHz	
	Vertical Section	Period	Τ <sub>V</sub>	776	806	1024		
Vsync Timing		Active	T <sub>VD</sub>	768	768	768	T <sub>Line</sub>	
Tinnig		Blanking	Τ <sub>VB</sub>	8	38	256	_	
	Horizontal Section	Period	т <sub>н</sub>	1054	1344	2048		
Hsync Timing		Active	T <sub>HD</sub>	1024	1024	1024	T <sub>Clock</sub>	
		Blanking	Т <sub>НВ</sub>	30	320	1024		

Note Frame rate is 60 Hz.

Note DE mode.

Note Typical value refer to VESA STANDARD

#### 3.4.2 Input Timing Diagram



# 3.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



#### **Power Sequence Timing**

Parameter		Value		Unit	
	Min.	Тур.	Max.		
T1	0.5	-	10	[ms]	
T2	0	40	50	[ms]	
Т3	200	-	-	[ms]	
T4	200	-	-	[ms]	
Т5	0	16	50	[ms]	
Т6	0	-	10	[ms]	
Τ7	1000	-	-	[ms]	

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.



Connector & Pin Assignment

### 4.1 TFT LCD Module

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

#### 4.1.1 Connector

Table 4.1: Connector					
Connector Name / Description	Signal Connector				
Manufacture	JAE or Compatible				
Connector Model Number	FI-XB30SSL-HF15 or Compatible				
Adapable Plug	JAE FI-X30HL or Compatible				

#### 4.1.2 Pin Assignment

Table 4.2: Pin Assignment					
Pin No.	Signal Name	Pin No.	Signal Name		
1	RxOIN0-	2	RxOIN0+		
3	RxOIN1-	4	RxOIN1+		
5	RxOIN2-	6	RxOIN2+		
7	GND	8	RxOCLKIN-		
9	RxOCLKIN+	10	RxOIN3-		
11	RxOIN3+	12	RxEIN0-		
13	RxEIN0+	14	GND		
15	RxEIN1-	16	RxEIN1+		
17	GND	18	RxEIN2-		
19	RxEIN2+	20	RxECLKIN-		
21	RxECLKIN+	22	RxEIN3-		
23	RxEIN3+	24	GND		
25	GND (AGMODE+HVS)	26	NC		
27	GND	28	VCC		
29	VCC	30	VCC		

# 4.2 Backlight Unit

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

Connector Name / Designation	LED Light Bar Connector / Backlight lamp
Manufacturer	TKP TERMINAL/ TKP HOVSING
Type Part Number	TKP TERMINAL 8820T/ TKP HOVSING 8821-03
Mating Type Part Number	Wire VL 1007 24 AWG

### 4.2.1 Signal for LED light bar connector

	Connector No.	Pin No.	Input	Color	Function
Upper	UpperCN1		HI 1	Red	Power supply for backlight unit
		2	GND 1	Black	Ground for backlight unit
Lower	-CN2	1	HI 2	Red	Power supply for backlight unit
		2	GND 2	Black	Ground for backlight unit

Cable Length : 250mm+/-10mm

#### 4.2.2 LED Driver Board

#### 4.2.2.1 Specification:

Table 4.3: \$	Specification					
Symbol	Characteristics	Condition	Min.	Тур.	Max.	Unit
	Voltage		10	12	15	V
	Efficiency	Vin=12V,		90		%
Input		lout=1.5A, Vout=18V				
	Power	voul-16v	3		30	W
			-			
	Voltage		18		24	V
Output	Current		150		1500	mA
Output	Current Accurancy	150mA <b></b>		±5	±10	%
	Protection		Therma	al/OVP		
	Thermal Shutdown			165		°C
Environment	Operating Junction Temperature				125	°C
Environment	Operating Tempera- ture		-20		+70	°C
	Storage Temperature		-40		+ 85	°C
	Dimmer range(Note. 1)		5		100	V
PWM Dim-	Dimmer VH		2		5	V
mer	Dimmer VL		0		1.5	V
	Dimmer Frequency		0.25	0.5	1	KHz
ON/OFF	Von		3.5		5.5	V
_	off		0		2	V





#### 4.2.2.2 Input connector pin define

Table 4.4: Input connector pin define				
Pin No.	Pin Define			
1	Vin(+12V)			
2	Vin(+12V)			
3	GND			
4	GND			
5	ON/OFF(+5V:ON;0V:OFF)			
6	Dimming(PWM)			

#### 4.2.2.3 Output connector pin define

Table 4.5: Output connector pin define					
Pin No.	Pin Define				
1	VLED-				
2	VLED+				

#### 4.2.2.4 Dimension



Figure 4.1 Dimension



**Optical Characteristics** 

## A.1 Optical Characterisctics

The optical characteristics are measured under stable conditions at 25°C (Room Temperature):

Table A.1: Optical Characteristics							
ltem	Unit	Conditions	Min.	Тур.	Max.	Note	
	[degree]	Horizontal (Right)		70		1	
Viewing Angle		CR = 10 (Left)		70			
Viewing Angle		Vertical (Upper)		55		_	
		CR = 10 (Lower)		65			
Luminance Uniformity	[%]	9 Points	75	85	-	2, 3	
	[msec]	Rising	-	10	20		
Optical Response Time		Falling	-	20	30	5	
		Rising + Falling	-	30	50	_	
Color/Chromaticity Coor-		White x	-	0.313	-	-4	
dinates (CIE 1931)		White y	-	0.322	-	-4	
Color Temp.	K		-	6500			
White Luminance	[cd/m <sup>2</sup> ]		1100	1200	-	4	
Contrast Ratio			-	550	-	4	

Note Optical Equipment: BM-7, DT-101, or equivalent

Note1 Definition of viewing angle

Viewing angle is the measurement of contrast ratio®R10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as below: 90° ( $\theta$ ) horizontal left and right, and 90° ( $\Phi$ ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.



Note2 9 points position



**Note3** The luminance uniformity of 9 points is defined by dividing the maximum luminance values by the minimum test point luminance

Minimum Brightness of nine points

 $\delta_{W9} =$  Maximum Brightness of nine points

#### **Note4** Measurement method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room. Optical Equipment: DT-100, or equivalent



#### Note5 Definition of response time

The output signals of photo detector are measured when the input signals are changed from "Full Black" to "Full White" (rising time), and from "Full White" to "Full Black "(falling time), respectively. The response time is interval between the 10% and 90% of amplitudes. Please refer to the figure as below.





**Handling Precautions** 

# **B.1 Optical Characterisctics**

The optical characteristics are measured under stable conditions at 25°C (Room Temperature)

- 1. Since front polarizer is easily damaged, pay attention not to scratch it.
- 2. Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3. Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4. When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5. Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6. Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7. Do not open or modify the Module Assembly.
- 8. Do not press the reflector sheet at the back of the module to any directions.
- 9. In case if a Module has to be put back into the packing container slot after once it was taken out from the container, please press at the far ends of the LED light bar reflector edge softly. Otherwise the TFT Module may be damaged.
- 10. At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11. After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12. Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.



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