

Description

PX4FMU is an onboard management unit for micro air vehicles. It combines an autopilot and inertial measurement unit and enables the control of an aircraft using a single-board solution. Additional I/O can be easily connected via the 30-pin expansion bus.

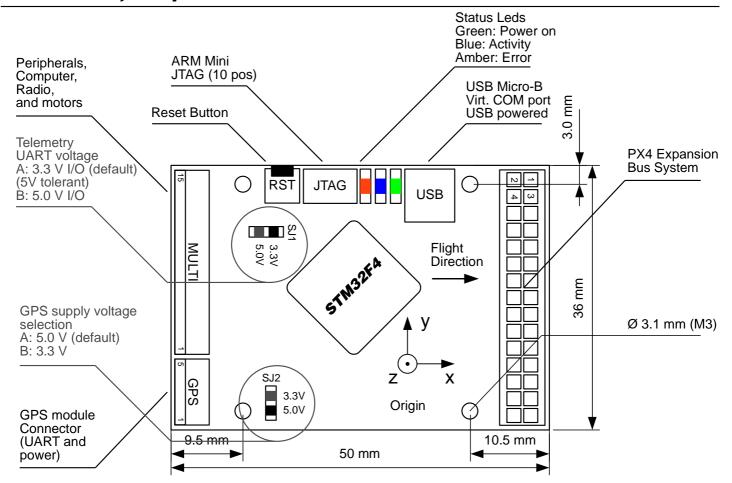
http://pixhawk.ethz.ch/px4/

Features

- 168 Mhz Cortex-M4 CPU (196 KB RAM, 1 MB Flash)
- 250 mW typical power consumption
- Reverse polarity protection on all power inputs
- 3D gyro, accelerometer and magnetometer, pressure sensors
- I2C, 3x UART, PPM, analog, GPS, 2x 5V GPIO, 4x PWM / Servo

- Expansion bus: CAN, 2x I2C, SPI, 4x analog, 2x UART, GPIOs
 USB Serial Port (Virtual COM Port / VCP) and bootloader
 50 x 36 x 6 mm (1.38x1.97x0.24"), 8g, 30x30 mm mounting holes
 4.5-6 V wide supply input range (incl. USB power)
 Selectable 3.3 V or 5 V IO for UART2 and GPS ports

Connectors, Jumpers and Dimensions



Pinout and absolute maximum Ratings

• Input: 4.3-6 V (VDD 5V), 20 mA onboard use, max. 800 mA for max peripheral load.

Reverse-polarity protected.

Output: 3.3 V (VDD_3V3), fuse-limited 500 mA EXT, 3.3 V, fuse-limited 200 mA GPS

GND NOT CONNECTED USART6_RX USART6_TX VDD_GPS (5V default)

VDD 5V GND CAN2_RX USART1_RX I2C3_SDA **UART6 RX** UART5_RX

I2C2_SDA USART2_RTS USART2_RX GPIO_EXT1 BUZZER ADC123_IN11 ADC123_IN13

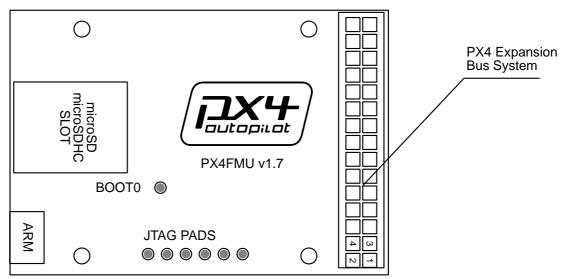
VDD 5V GND CAN2_TX USART1_TX I2C3_SCL ADC123_IN10 UART6_TX UART5_TX I2C2_SCL USART2_CTS USART2_TX PPM_INPUT GPIO_EXT2 GND

ADC123_IN12

GND I2C1_SCL I2C1_SDA USART2_TX / SRV3 / AR.TX USART2_CTS / SRV1 / AR.S4 USART2_RTS / SRV2 / AR.S3 UART2_RX / SRV4 / AR.RX USART1_TX USART1_RX PPM_INPUT (3-5V) BATTERY_MONITOR (3-18V) GPIO_EXT2 / AR.S2 GPIO_EXT1 / AR.S1 VDD_3V3 VDD_5V

Additional connectors (bottom side)

The footprints on the bottom side of the connector can be used by advanced users to interface additional boards or sensors.



Software Tools / Getting Started

Please check the most recent user manual at https://pixhawk.ethz.ch/px4/users/

Upgrading Firmware / Developing Custom Code

Please check the most recent developer instructions at https://pixhawk.ethz.ch/px4/dev/

Open Hardware License

PX4FMU is an open hardware design, following the OSHW 1.1 def nition licensed under the Creative Commons Attribution-ShareAlike 3.0 Unported (CC BY-SA 3.0) license. PX4FMU uses the BSD-licensed NuttX operating system as base for the PX4 software stack (http://nuttx.sourceforge.net).

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