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# RX Family C/C++ Compiler Package

Application Notes: RX Migration Guide, M16C Edition

This document explains the items that need to be checked for migration from M16C, for C/C++ Compiler V1 for the RX family.

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### Introduction

In this application notes, it explains the software migration method when C program made by M16C-family compiler is transplanted to RX-family compiler.

In Renesas RX-family compiler, the function to absorb the difference between the option and the language specification is supported inconsideration of the migration from and M16C-family to RX-family. As a result, the application part of the embedded sofutware can be smoothly transplanted.

In this application notes, it explains the use of the function that the difference between the option and the language specification of the compiler for the M16C-family, and the RX-family and compilers for the RX-family support it.

Please use this application notes when you transplanted to RX-family from M16C-family.



## 1. Options

Some specifications differ for the default options between M16C-family compilers and RX-family compilers. The following explains options that will likely require handling during migration from M16C to RX.

Note that the expansion code in assembly code as used in this document can be obtained by specifying "output=src" and "cpu=rx600".

When the "cpu" option is different, the expansion code in assembly language may also differ. Also, the expansion code in assembly language may change due to subsequent compiler improvements, so use this for reference.

Table 1-1 List of options

No	Functionality	M16C option	RX option	Reference
1	Specifying changes to enumeration size	fchar_enumerator	auto_enum	1.1
2	Specifying the size of double type variable	fdouble_32	dbl_size=4	1.2
3	Specifying the size of int type variables		int_to_short	1.3

### 1.1 Specifying changes to enumeration size

When the "fchar\_enumerator" option is specified for M16C-family compilers, the enumerator type is treated as an unsigned char type, not an int type.

To obtain the same results as those on RX-family compilers, specify the "auto\_enum" option. Note that the unsigned char type is only used for the enumerator type when the minimum enumerator value is 0, and the maximum value is 255. For all other cases, another type is used.

For details about changing the enumeration type size, see 1.2.3 Enumeration type size in C/C++ Compiler Package for the RX Family Application Notes: Compiler Usage Guide, Option Edition.

# 1.2 Specifying the size of double type variables

If the "fdouble\_32" option is not specified for an M16C-family compiler, the size of the double type is treated as 8 bytes (double precision).

For the same interpretation as RX-family compilers, specify the "dbl\_size=8" option.

**Format** 

 $dbl\_size = \{4|8\}$  : 4 by default



[How to specify this option in the Renesas IDE]

Choose Build and then RX Standard Toolchain, and perform the following settings in the displayed dialog box.

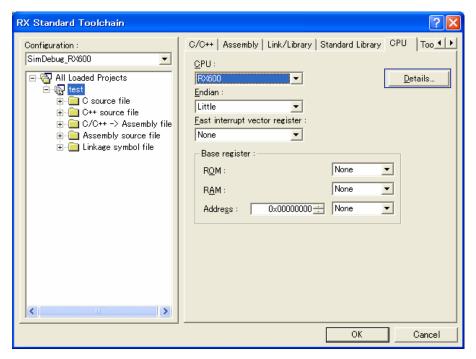


Figure 1-1

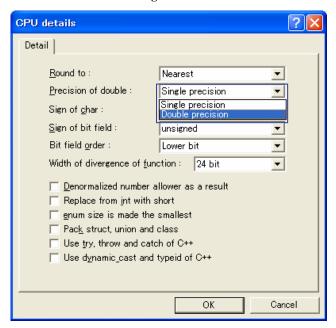


Figure 1-2



### 1.3 Specifying the size of int type variables

For M16C-family compilers, the size of the int type is treated as 2 bytes, whereas for RX-family compilers, the size of the int type is treated as 4 bytes.

When migrating M16C programs created based on the requirement that the size of the int type is 2 bytes to RX, specify the "int\_to\_short" option.

**Format** 

 $int\_to\_short$ 

[How to specify this option in the Renesas IDE]

In RX Standard Toolchain (Figure 1-1), choose Details, and perform the following settings in the displayed dialog box.

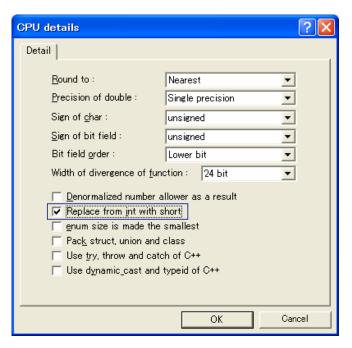


Figure 1-3



### Language specifications

This chapter explains the language specification most likely to require changes during RX migration.

Table 1-2 List of language specifications

No	Functionality	Reference			
1	Size of int type	2.1			
2	Size of the double type	2.2			
3	Integer promotion for the char type	2.3			
4	Placement of structure members 2.4				
5	Inline keyword	2.5			
6	#pragma STRUCT 2				
7	#pragma BITADDRESS 2.				
8	#pragma ROM	2.8			
10	#pragma PARAMETER	2.9			
11	asm 2.1				

### 2.1 Size of int type variables

On M16C-family compilers, the size of the int type is 2 bytes, whereas on RX-family compilers the size of the int type is 4 bytes in default. When M16C programs created based on the requirement that the size of the int type is 2 bytes are migrated to RX, they may not operate properly.

Example: Code for which operation is different due to variance in int type size

```
Source code

typedef union{
   long data;
   struct {
      int dataH;
      int dataL;
   } s;
} UN;

void main(void)
{
   UN u;
   u.data = 0x7f6f5f4f;

   if (u.s.dataH == 0x5f4f && u.s.dataL == 0x7f6f) {
      // When the size of the int type is 2 bytes (M16C)
   } else {
      // When the size of the int type is 4 bytes (RX)
   }
}
```

When migrating programs created based on the requirement that the size of the int type is 2 bytes to RX, specify the "int\_to\_short" option. For details about specifying this option, see 1.3 Specifying the size of int type variables.



### 2.2 Size of the double type

With M16C-family compilers, the size of the double type is 8 bytes, whereas with RX-family compilers, the size of the double type is 4 bytes in default. When M16C programs created based on the requirement that the size of the double type is 8 bytes are migrated to RX, they may not operate properly.

Example: Code for which operation is different due to variance in double type size

```
Source code

double d1 = 1E30;
double d2 = 1E20;

void main(void)
{
    d1 = d1 * d1;
    d2 = d2 * d2;

    if (d1 > d2) {
        // When the size of the double type is 8 bytes (M16C)
    } else {
        // When the size of the double type is 4 bytes (RX)
    }
}
```

When migrating programs created based on the requirement that the size of the double type is 8 bytes to RX, specify the "dbl\_size=8" option. For details about specifying this option, see 1.2 Specifying the size of double type variable.



### 2.3 Integer promotion for the char type

With M16C-family compilers, when char type data (including unsigned char, signed char types) is evaluated, it is not promoted to the int type, whereas with RX-family compilers, char type data is always promoted to the int type when evaluated. When migrating to MX programs created based on this condition in M16C, operation may not be performed the same as M16C.

Example: Code for which operation is different due to variance in integer promotion specifications for char type calculations

### Notes

Options exist for promotion to the int type when M16C-family compilers evaluate char type data (including unsigned char types and signed char types). If one of the following options is specified, no problems will occur for the integer promotion specification explained here.

- -fansi
- -fextend\_to\_int



#### 2.4 Placement of structure members

M16C-family compilers place structure members in the order of appearance for member data with alignment count 1, whereas RX-family compilers place structure members in the order of appearance for member data, according to the maximum alignment count for members. When M16C programs created based on the requirement that M16C structure placement is used are migrated to RX, they may not operate properly. In this case, the "pack" option can be specified to set the alignment count for structure members to 1. Free space will no longer be able to be created for structures with alignment counts of 1.

Structure alignment counts can also be specified using #pragma pack. If both the option and #pragma are specified at the same time, the #pragma specification takes precedence.

For details about this functionality, see 2.3 Specifying structure, shared structure, and class alignment count in C/C++ Compiler Package for the RX Family Application Notes: Compiler Usage Guide, Extended Functionality Edition.

**Format** 

pack unpack

[How to specify this option in the Renesas IDE]

Choose the CPU tab, then choose Details (Figure 1-1), and then perform the following settings in the displayed dialog box.

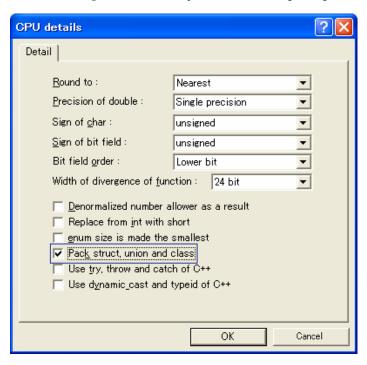


Figure 1-4



### 2.5 Inline keywords

M16C-family compilers support inline keywords, whereas RX-family compilers do not support inline keywords for ANSI-standard C89.

When a program using inline keywords for M16C is built on the RX ANSI-standard C89, inline keywords cause compilation errors for non-standard builds. This can be resolved by either of the following:

- When performing builds using ANSI-standard C89, convert inline code to #pragma inline.
- Perform builds using ANSI-standard C99.

The following shows an example of converting inline keywords to #pragma inline.

Example:

```
Source code (using inline keywords)
inline static int func(int a, int b)
{
   return (a + b) / 2;
}
int x;
main()
{
   x = func(10, 20);
}

Source code (using #pragma inline)

#pragma inline(func)
static int func(int a, int b)
{
   return (a + b) / 2;
}
int x;
main()
{
   x = func(10, 20);
}

x = func(10, 20);
}
```

The option settings for builds on ANSI-standard c99 are as follows.

**Format** 

lang={c|cpp|ecpp|c99}

[How to specify this option in the Renesas IDE]

From HEW, choose Build and then RX Standard Toolchain, and then perform the following settings in the displayed dialog box.

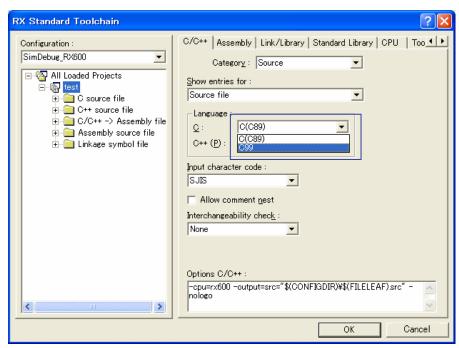


Figure 1-5



### Precautions

1. The ANSI-standard C99, C++, and #pragma inline specifications differ as follows.

#### Table 1-3 Inline specifications

		To minio opecinicationic	
	Default linkage	Linkage when inline expansion is not possible	Impact on declarative linkage
C99	Internal linkage	Internal linkage	When extern is specified:
inline keyword			External linkage
C++	Internal linkage	Internal linkage	When extern is specified:
inline keyword			Compilation error
#pragma inline	External linkage	External linkage	When static is specified:
			Internal linkage

2, Progressing inline key word and #pragam inline is different. #pragma inline is compulsion development.



### 2.6 #pragma STRUCT

M16C-family compilers can prohibit structure packing using #pragma STRUCT, and perform sorting for structure members. Since RX-family compilers lack the corresponding functionality, programs using #pragma STRUCT need special handling when migrated to RX.

#### (1) Prohibiting structure packing

By default, M16C-family compilers place structure members with alignment 1. This means that the structure size may be an odd number of bytes. To make the structure size an even number of bytes, specify #pragma STRUCT unpack. If this specification causes the structure size to be an odd number of bytes, 1 byte of padding is inserted to bring the structure size to an even number of bytes. One of the following needs to be performed to achieve the same effect for RX-family compilers.

- Specify #pragma pack to set the structure alignment to 1.
- If setting the alignment to 1 causes the structure size to be an odd number of bytes, have users enter a 1-byte dummy member for adjustment.

In addition to specifying #pragma pack, specifying the "pack" option is another way to set structure alignment to 1 on RX-family compilers. For details about this functionality, see 2.4 Placement of structure members.

#### Example:

M16C program with #pragma STRUCT unpack and program migrated to RX

```
Source code with #pragma STRUCT unpack specified RX source code
#pragma STRUCT s unpack
                                                   #pragma pack
struct s {
                                                   struct s {
                                                      short i;
   short
   char c;
                                                      char c;
   short j;
                                                      short j;
                                                      char dmy;
} ss;
                                                                  // To get a size with an even number
                                                                  // of bytes a 1-byte dummy member
                                                                  // is inserted
                                                   } ss;
                                                                                  Since the alignment
                              #pragma STRUCT
                                                                                  count is 1 due to
                             adds 1 byte of
                                                                                  pack, there is no
                             padding.
                                                                                  free space. The
                              The structure
            С
                  j
                                                                  C
                                                                        i
                                                                                  structure size is 6
                             size is 6 bytes.
                                                                                  bytes due to
                                                                      dmy
                free
                                                                                  explicitly
                                                                                  inserted members.
```

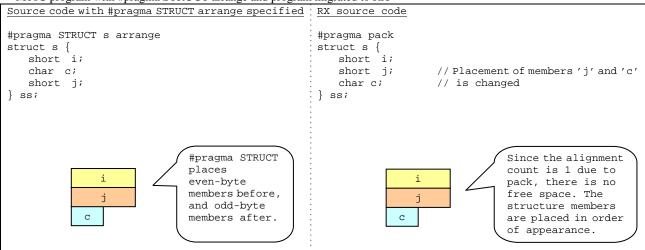


#### (2) Sorting structure members

RX-family compilers do not have any functionality to sort structure members. Programs need to be changed to perform member sorting.

#### Example:

M16C program with #pragma STRUCT arrange and program migrated to RX



Notes

Keep in mind that the meanings of #pragma STRUCT unpack for M16C and #pragma unpack for RX are different.

- #pragma STRUCT unpack (M16C)
  - Adjusts the size of the specified structure to an even number of bytes.
- #pragma unpack (RX)

Makes the alignment of the specified structure the same as the maximum alignment count for members.



### 2.7 #pragma BITADDRESS

M16C-family compilers can allocate variables to a specified bit position for an absolute address specified by #pragma BITADDRESS. Since RX-family compilers lack the corresponding functionality, programs using #pragma BITADDRESS need special handling when migrated to RX.

The following gives an example where the absolute address bit position matches the structure bit field.

#### Example:

M16C program with 100th position bit number 1 for #pragma BITADDRESS set to 1 and program migrated to RX

```
Source code with #pragma BITADDRESS specified
                                                RX source code
#pragma BITADDRESS io 1, 100H
                                                 struct bit_address {
_Bool io;
                                                    unsigned char b0:1;
                                                    unsigned char b1:1;
                                                    unsigned char b2:1;
void main(void)
                                                    unsigned char b3:1;
   io = 1;
                                                    unsigned char b4:1;
                                                    unsigned char b5:1;
                                                    unsigned char b6:1;
                                                    unsigned char b7:1;
                                                 };
                                                 #define io (((struct bit_address*)0x100)->b1)
                                                 void main(void)
                                                     io = 1;
```

#### Notes

When a program using the \_Bool keyword on an M16C-family compiler is built on a RX-family compiler with ANSI-standard C89, an error occurs because the \_Bool keyword is non-standard. RX-family compilers support the \_Bool keyword with ANSI-standard C99.



### 2.8 #pragma ROM

M16C-family compilers place variables specified by #pragma ROM in the rom section. Since RX-family compilers lack the corresponding functionality, when a program using #pragma ROM is migrated to RX, the const modifier needs to be used to place variables in the rom section, as shown in the following example.

### Example:

M16C program with variable 'i' placed in the rom section using #pragma ROM, and program migrated to RX

Who c program with variable 1 placed in the folia section using #pragma KOW, and program inigrated to KX					
Source code with #pragma ROM specified	RX source code				
<pre>#pragma ROM i unsigned short i;</pre>	const unsigned short i; // const keyword added				
Assembler expansion code	Assembler expansion code				
.SECTION rom_FE,ROMDATA,align .glb _i	.SECTION C_2,ROMDATA,ALIGN=2 .glb _i				
_i:	_i: ; static: i				
.byte 00H	byte 00H				
.byte 00H	.byte 00H				
	• Control of the cont				



### 2.9 #pragma PARAMETER

M16C-family compilers can use #pragma PARAMETER to store arguments in a register and declare passed assembler functions. Since RX-family compilers lack the corresponding functionality, programs using #pragma PARAMETER need special handling when migrated to RX.

RX-family compilers have no way to specify that arguments be stored in arbitrary registers. The argument interface for function calls needs to follow C/C++ generation rules. Change the argument interface for assembler functions specified by #pragma PARAMETER during RX migration to adhere to C/C++ generation rules.

For details about argument interfaces, see 1.2 Performing inline expansion in assembly code functions in C/C++ Compiler Package for the RX Family Application Notes: Compiler Usage Guide, Extended Functionality Edition.

#### Example:

M16C program using #pragma PARAMETER, and program combining the assembler function and argument interface in RX

```
Source code with #pragma PARAMETER specified
                                                  Source code using RX assembly code functions
C source code
                                                  C source code
int asm_func(unsigned int, unsigned int);
                                                  int asm_func(unsigned int, unsigned int);
#pragma PARAMETER asm_func(R0, R1)
                                                  void main(void)
void main(void)
                                                     int i, j;
   int i, j;
                                                     i = 0x7FFD;
   i = 0x7FFD;
                                                     j = 0x007F;
   j = 0x007F;
                                                     asm_func( i, j ); // Assembler function call
   asm_func( i, j ); // Assembler function call
                                                                         // As per the C/C++ argument
                     // 'i' is stored in R1, 'j'
                                                                         // interface,
                     // is stored in R0
                                                                         // 'i' is stored in R1, 'j' is
                                                                         // stored in R2
                                                  }
Assembler source code
                                                  Assembler source code
_asm_func:
   Code requiring 'i' to be in R1, 'j' to be in
                                                  _asm_func:
   R0 for passage
                                                     Changing code requiring 'i' to be in R1, 'j' to
                                                     be in R2 for passage
   RTS
                                                     RTS
```



#### 2.10 asm

M16C-family compilers can use the asm function to code assembly language routines (asm functions) within C source programs. Since RX-family compilers lack the corresponding functionality, programs using asm functions need special handling when migrated to RX

Assembly code functions exist to code assembly language in a C source program in RX. The contents coded in the asm function can sometimes be handled by being coded in the assembly code function.

For details about assembly code functions, see 1.2 Performing inline expansion in assembly code functions in C/C++ Compiler Package for the RX Family Application Notes: Compiler Usage Guide, Extended Functionality Edition.

#### Example:

M16C program using the asm function, and program using the assembly code function in  $RX\,$ 

(The code contents are not equivalent, but this can be used as an example of migration.)

```
Source code with the M16C asm function specified : Source code using the RX assembly code function
C source code
                                                  C source code
void func(void)
                                                  #pragma inline_asm interrupt_flag
                                                  static void interrupt_flag(void)
   asm("FSET I");
                                                     MOV.L
                                                            #00010000H,R5
                                                     MVTC R5, PSW
                                                  }
Assembler source expansion code
_func:
                                                  void func(void)
   FSET I
                                                  {
                                                     interrupt flag();
   rts
                                                  Assembler source expansion code
                                                  func:
                                                             #00010000H,R5
                                                     MOV.L
                                                     MVTC
                                                             R5, PSW
                                                     RTS
```

#### Precautions

- M16C can code variables in the asm function, but RX cannot.
- M16C can use a dummy asm function as a step to partially suppress optimization, but RX cannot.



### 3. Optimization option setting for migration from M16C-family

There is a difference in an optional setting method for optimization in the compiler of M16C-family and RX-family. Please refer to the following optimization option setting when embedded software transplant from M16C-family to RX-family and the performance is evaluated.

Optimization option setting of each compiler and comparison of ROM size

(The sample program for the measurement is described to the next page.)

M16C	Optimize OFF	Optimize Size			Optimize Speed		
	opt=0	opt=O3 OR	-	opt=OR_max	opt=O3 OS	-	opt=OS_max
main()	0xA2	0x80	-	0xA0	0xA1	=	0x27A
sort()	0xB3	0x79	-	0x79	0x7C	-	0x7B

RX	Optimize OFF	Optimize Size				Optimize Speed	
	opt=0	opt=1	opt=2	opt=max	opt=1 speed	opt=2 speed	opt=max speed
main()	0xD1	0x95	0x6A	0x6A	0x95	0xEB	0x11A
sort()	0xFD	0x69	0x65	0x65	0x6A	0x5F	0x5F

Optimization option setting for migration from M16C-family

- (1) Please specify it for RX-family above opt=1 or opt=1 speed when you specify opt=0 for M16C-family. In opt=0 for RX-family, the meaning is different from opt=0 for M16C-family. Optimization is not executed at all. Therefore, the performance of RX-family looks bad. Exsample:
  - ·If O0 for M16C is used, rom size is 0xA2 (main()) -> If optimize=0 for RX is used, rom size is 0xD1 (main()) ...NG
  - •If O0 for M16C is used, rom size is 0xA2 (main()) -> If optimize=1 for RX is used, rom size is 0x95 (main()) ... OK

Please refer to the compiler user's manual for details of the optimization level of the RX compiler.



### Exsample: Sample source

```
#include <stdio.h>
#include <math.h>
#include <stdlib.h>
void main(void);
void sort(long *a);
void change(long *a);
void main(void)
    long a[10];
    long j;
    int i;
    printf("### Data Input ###\forall n");
    for( i=0; i<10; i++ ){
        j = rand();
        if(j<0)\{
            j = -j;
        a[i] = j;
        printf("a[%d]=%ld\n",i,a[i]);
    sort(a);
    printf("*** Sorting results ***\forall n");
    for( i=0; i<10; i++ ){
        printf("a[\%d]=\%ld \\ \\ \textbf{Y}n", \textbf{i}, \textbf{a}[\textbf{i}]);
    }
change(a);
```

```
void sort(long *a)
    long t;
   int i, j, k, gap;
   gap = 5;
    while (gap > 0)
       for( k=0; k<gap; k++){
           for
( i=k+gap; i<10; i=i+gap ){
               for(j=i-gap; j>=k; j=j-gap){
                   if(a[j]{>}a[j{+}gap])\{
                       t = a[j];
                       a[j] = a[j+gap];
                       a[j+gap]=t;\\
                   }else{
                       break;
           }
       gap = gap/2;
}
```

```
void change(long *a)
{
    long tmp[10];
    int i;
    for(i=0; i<10; i++){
        tmp[i] = a[i];
    }
    for(i=0; i<10; i++){
        a[i] = tmp[9 - i];
    }
}</pre>
```



## Web site and support <website and support>

Web site for Renesas Technology

http://japan.renesas.com/

Contact information

http://japan.renesas.com/inquiry

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# Revision history,rh>

		Contents changed			
Rev.	Date issued	Page	Details		
1.00	2009.10.1		Initial edition		