



**DOT MATRIX
LIQUID CRYSTAL DISPLAY
MODULE**

**LMG-S24I12 Serial
USER' MANUAL**

	LMG-SS24I12DLGY		LMG-SS24I12DLYY
	LMG-SS24I12DLGW		LMG-SS24I12DLYW
	LMG-SS24I12DLGB		LMG-SS24I12DLYB
	LMG-SS24I12DLNW		LMG-SF24I12DLNW
	LMG-SF24I12DLGW		LMG-SF24I12DLGB

PROPOSED BY		APPROVED
Design	Approved	

SDEC TECHNOLOGY CORP.

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1. GENERAL AND CHARACTERISTICS

Dot Matrix Format	240 × 128 dots
Controller IC	T6963C or equivalent
Dimensional Outline	91.0W × 74.0H × 10.0D mm
Viewing Area	83.2W × 54.0H mm
Active Viewing Area	79.17W × 46.05H mm
Dot Pitch	0.30W × 0.33H mm
Dot Size	0.33W × 0.36H mm
LMG-SS24I12DLGY	STN, Gray, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is Yellow Green)
LMG-SS24I12DLYY	STN, Yellow Green, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is Yellow Green)
LMG-SS24I12DLGW	STN, Gray, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is White)
LMG-SS24I12DLYW	STN, Yellow Green, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is White)
LMG-SS24I12DLGB	STN, Gray, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is Blue)
LMG-SS24I12DLYB	STN, Yellow Green, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is Blue)
LMG-SS24I12DLNW	STN, Blue, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is White)
LMG-SF24I12DLNW	FSTN, Black, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is White)
LMG-SF24I12DLGW	FSTN, Gray, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is White)
LMG-SF24I12DLGB	FSTN, Gray, 1/128 Duty, 1/12 Bias, 6 O'clock, LED Backlight (Color is Blue)

2. ABSOLUTE MAXIMUM RATINGS:

2.1 Electrical absolute maximum rating

ITEM	SYMBOL	MIN	MAX	UNIT
Logic Circuit Supply Voltage	$V_{DD} - V_{SS}$	0	7.0	V
LCD Driver Circuit Supply Voltage	$V_{DD} - V_{EE}$	0	15.0	V
Input Voltage	V_I	V_{SS}	V_{DD}	V
Operating Temperature	T_{OP}	-20	+70	
Storage Temperature	T_{ST}	-30	+80	

2.2 Environmental absolute maximum ratings

ITEM	OPERATING		STORAGE		COMMENT
	MIN	MAX	MIN	MAX	
Ambient Temperature	0/-20	50/70	-10/-30	60/80	Norm/Extended Note (1)
Humidity	Note (2)		Note (2)		Without condensation
Vibration	--	4.9m/s ²	--	19.6m/s ²	XYZ directions
Shock	--	29.4m/s ²	--	490.0m/s ²	XYZ directions

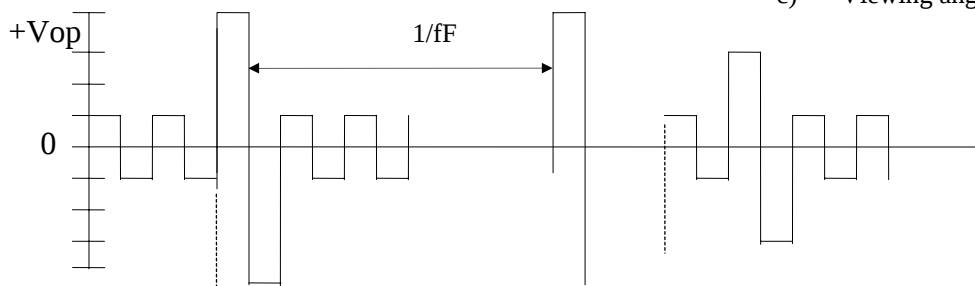
Note (1) Ta at 60 : 50 HR MAX

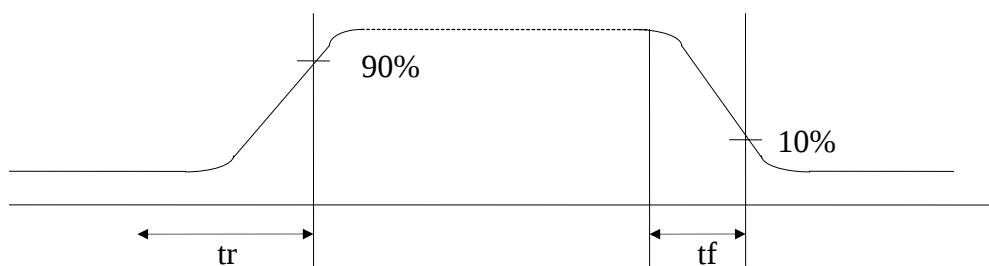
Note (2) Ta 40 : 90% RH MAX

Ta > 40 : Absolute humidity must be lower than the humidity of 90% at 40 .

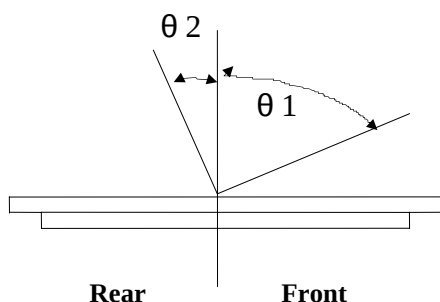
(Note 1) Definition of response time and measuring condition. Response time should be measured at the point of the most smallest response in all segments under the following condition.

- a) Temperature 25
- b) Frame frequency 64 Hz
- c) Viewing angle $\theta = 0^\circ$, $\phi = 0^\circ$

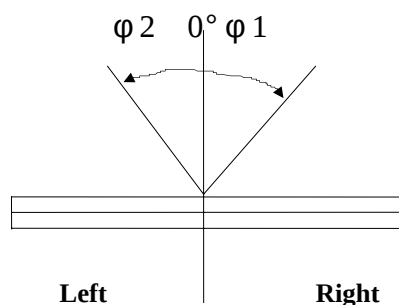




(Note 2) Definition of viewing angle
Front-Rear direction



Right-Left direction



3. ELECTRICAL CHARACTERISTICS

$V_{SS} = 0V$, $T_a = 25$

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Logic Circuit Power Supply	$V_{DD} - V_{SS}$	--	4.75	5.0	5.25	V
LCD Driver Power Supply	$V_{DD} - V_{EE}$	--	--	9.0	--	V
Input Voltage	V_{IH}	$V_{DD} = 5V \pm$	$0.7 \cdot V_{DD}$	--	V_{DD}	V
	V_{IL}	0.25	0	--	$0.3 \cdot V_{DD}$	V
Power Supply Current	I_{DD}	$V_{DD} = 5V$	--	--	12.0	mA
	I_{EE}	$V_{EE} = -10V$	--	--	2.0	mA

4. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REF.
Rise Time	T_r	25	--	396	--	mS	Note 1
Fall Time	T_f	25	--	109	--	mS	Note 1
Contrast	Cr	25	--	12.3	--	--	Note 3
Viewing Angle	θ	25 &	50	--	--	DEG	Note 2
	$\phi 1, \phi 2$	$Cr \ 3$	--	40	--	DEG	Note 2
Frame Frequency	F_f	25	--	64	--	Hz	--

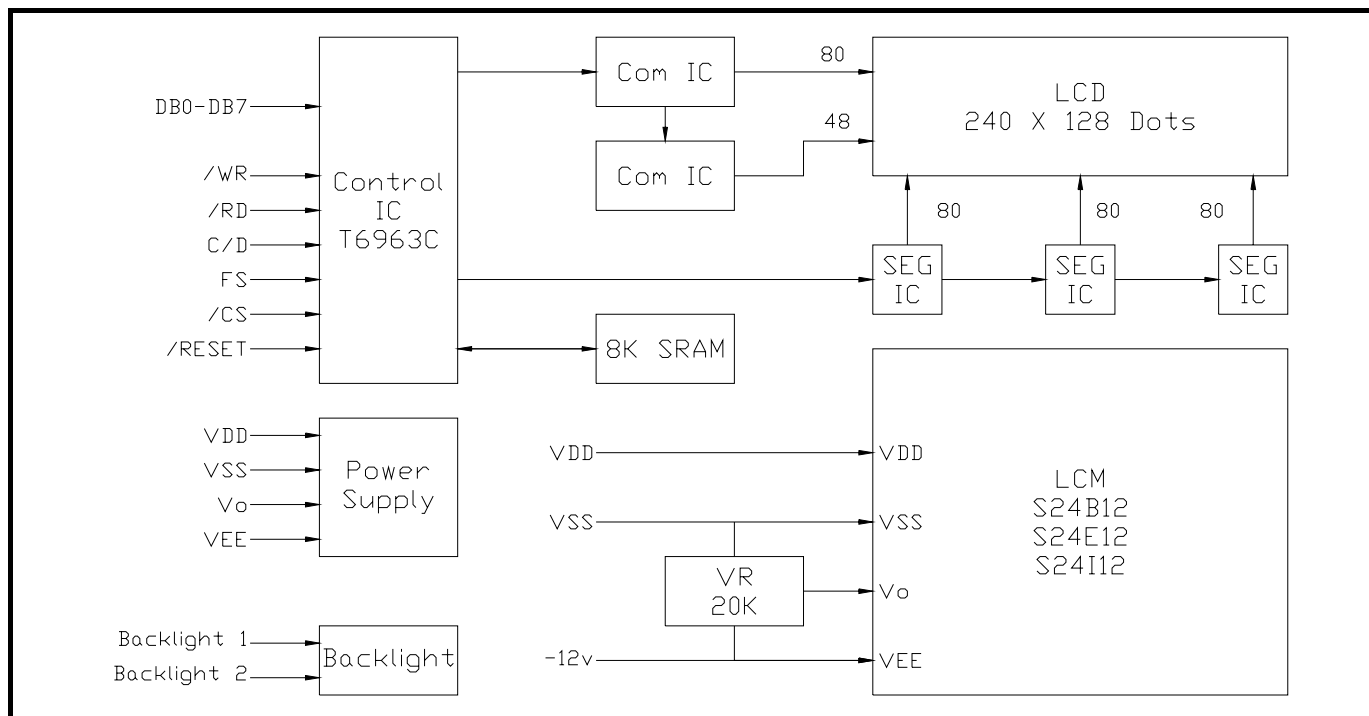
Note 1&2 : See previous page.

Note 3 : Contrast ratio is defined under the following condition.

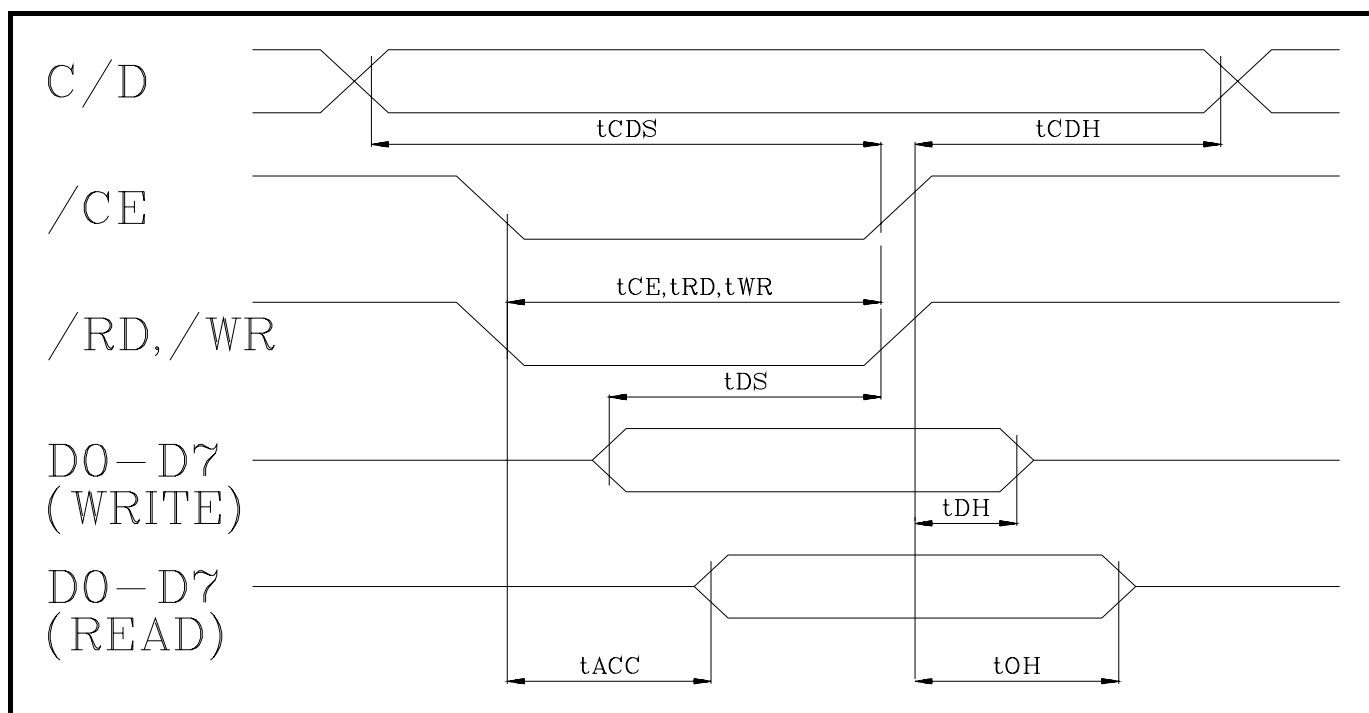
$Cr = \text{reflectance value of non-selected condition} \div \text{reflectance value of selected condition}.$

- (a) Temperature..... 25
- (b) Frame frequency..... 64 Hz
- (c) Viewing angle..... $\theta = 0^\circ, \phi = 0^\circ$
- (d) Operating voltage..... 12.5V

5. BLOCK DIAGRAM AND POWER SUPPLY

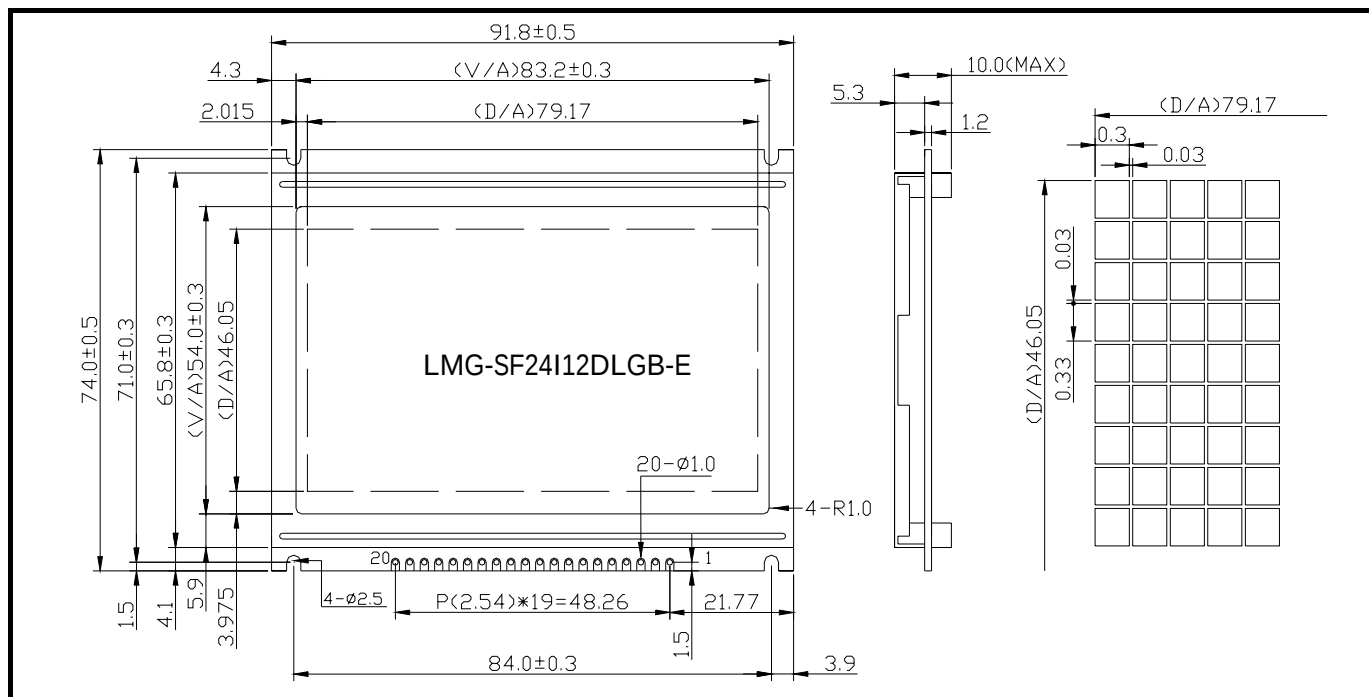


6. TIMING CHARACTERISTICS



ITEM	SYMBOL	MIN	TYP	MAX	UNIT
C/D Set Up Time	tCDS	100	--	--	ns
C/D Hold Time	tCDH	10	--	--	ns
/CE, /RD, /WR Pulse Width	tCE, tRD, tWR	80	--	--	ns
Data Set Up Time	tDS	80	--	--	ns
Data Hold Time	tDH	40	--	--	ns
Access Time	tACC	--	--	150	ns
Output Hold Time	tOH	--	--	50	ns

7. DIMENSION OUTLINE AND PIN CONNECTIONS



NO.	SYMBOL	FUNCTION	NO.	SYMBOL	FUNCTION
1	FGND	Frame ground	11	DB0	Data bus line
2	V _{SS}	Ground potential (Logic)	12	DB1	Data bus line
3	V _{DD}	Power supply for logic circuit	13	DB2	Data bus line
4	V _O	Power supply for LCD circuit	14	DB3	Data bus line
5	/WR	Data write	15	DB4	Data bus line
6	/RD	Data read	16	DB5	Data bus line
7	/CE	Chip enable	17	DB6	Data bus line
8	C/D	Command/Data	18	DB7	Data bus line
9	V _{EE}	Negative voltage input	19	FS	Font select H:6x8 L:8x8
10	/RES	Controller reset	20	LED-K	LED Backlight -

8. POWER SUPPLY FOR BACKLIGHT

ITEM	LED Backlight	LED Backlight	LED Backlight
Use Inverter Type	--	--	--
Backlight Color	Yellow Green	Blue	White
Backlight Input Voltage	DC +4.2 V	DC +3.2 V	DC +3.2 V
Backlight Frequency	--	--	--
Backlight Current	144 mA	144 mA	144 mA
Inverter Input Voltage	--	--	--
Inverter Current Density	--	--	--
Half-Life Time	30,000 HR.	--	--

9. DISPLAY CONTROL INSTRUCTION

9.1 Command list

COMMAND	OCDE	D1	D2	FUNCTION
REGISTER SET	00100001 00100010 00100100	X adrs data Low adrs	Y adrs 00H High adrs	CURSOR POINTER SET OFFSET REGISTER SET ADDRESS POINTER SET
CONTROL WORD SET	01000000 01000001 01000010 01000011	Low adrs Columns Low adrs Columns	High adrs 00H High adrs 00H	TEXT HOME ADDRESS SET TEXT AREA SET GRAPHIC HOME ADDRESS SET GRAPHIC AREA SET
MODE SET	1000X000 1000X001 1000X011 1000X100 10000XXX 10001XXX	- - - - - -	- - - - - -	“OR” mode “EXOR mode “AND” mode “TEXT ATTRIBUTE” mode Internal CG ROM Mode External CG RAM mode
DISPLAY MODE	10010000 1001XX10 1001XX11 100101XX 100110XX 100111XX	- - - - - -	- - - - - -	DISPLAY OFF CURSOR ON, BLINK OFF CURSOR ON, BLINK ON TEXT ON, GRAPHIC OFF TEXT OFF, GRAPHIC ON TEXT ON, GRAPHIC ON
CURSOR PATTERN SELECT	10100000 10100001 10100010 10100011 10100100 10100101 10100110 10100111	- - - - - - - -	- - - - - - - -	1 LINE CURSOR 2 LINES CURSOR 3 LINES CURSOR 4 LINES CURSOR 5 LINES CURSOR 6 LINES CURSOR 7 LINES CURSOR 8 LINES CURSOR
DTAT AUTO READ/WRITE	10110000 10110001 10110010	- - -	- - -	DATA AUTO WRITE SET DATA AUTO READ SET AUTO RESET
DATA READ WRITE	11000000 11000001 11000010 11000011 11000100 11000101	data - data - data -	- - - - - -	DATA WRITE AND ADP INCREMENT DATA READ AND ADP INCREMENT DATA WRITE AND ADP DECREMENT DATA READ AND ADP DECREMENT DATA WRITE AND ADP NONVARIABLE DATA READ AND ADP NONVARIABLE
SCREENPEEK	11100000	-	-	SCREEN PEEK
SCREEN COPY	11101000			SCREEN COPY
BIT SET/RESET	11110XXX 11111XXX 1111X000 1111X001 1111X010 1111X011 1111X100 1111X101 1111X110 1111X111	- - - - - - - - - -	- - - - - - - - - -	BIT RESET BIT SET BIT0 (LSB) BIT1 BIT2 BIT3 BIT4 BIT5 BIT6 BIT7 (MSB)

Note 1: Status check between all commands and data is recommended , though execution time for several commands are specified in above command list.

For the commands with “status check” in execution time, execution time does not specified because it is infuneced by internal situation of controller LSI.

2 : In case of 2 screen mode , Screen copy command cannot be used.

9.2.1 Register set

CODE	FUNCTION	D1	D2
00100001 21H	CURSOR POINTER SET	X address	Y address
00100010 22H	OFFSET REGISTER SET	DATA	00H
00100100 24H	ADDRESS POINTER SET	LOW address	HIGH address

A) CURSOR POINTER SET

The position of cursor is specified by X ADRS, Y ADRS, The cursor position is moved only by this command. The cursor pointer doesn't have the function of increment and decrement. The shift of cursor are set by this command.

X ADRS, Y ADRS are specified following

X ADRS	00H~4FH (lower 7bits are valid)
Y ADRS	00H~1FH (lower 5bits are valid)

1) 1 screen drive

X ADRS 00~4FH

Y ADRS 00H~0FH

2) 2 screen drive

X ADRS 00~4FH

Y ADRS 00H~0FH upper screen

Y ADRS 10H~1FH lower screen

B) OFFSET REGISTER SET

The offset register is used to determine external character generator RAM area.

C) ADDRESS POINT SET

The address point set command is used to indicate the start address for writing (or read) to external RAM.

9.2.2 Control word set

CODE	FUNCTION	D1	D2
01000000 40H	TEXT HOME ADDRESS SET	Low address	High address
01000001 41H	TEXT AREA SET	Columns	00H
01000010 42H	GRAPHIC HOME ADDRESS SET	Low address	High address
01000011 43H	GRAPHIC AREA SET	Columns	00H

The home address and column size are defined by this command.

A) Text home address set

The starting address of external display RAM for Text display is defined by this command. The text home address shows the left end and most upper position.

The relationship of external display RAM address and display position

TH		TH + CL
TH + TA		TH + TA + CL
(TH + TA) + TA		TH + 2TA + CL
(TH + 2TA) + TA		TH + 3TA + CL
TH + (n-1) TA		TH + (n-1) TA + CL

TH: Text Home address

TA: Text Area number (columns)

CL: Columns are fixed by hardware, pin-programmable

ex) Text home address = 0000H Text area = 0020H

MD2=H, MD3=H :32 columns

DUAL=H, MDS=L, MDO=L, MD1=H :4 lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	003FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH

B) Graphic home address set

The starting address of external display RAM for Graphic display is defined by this command.

The graphic home address shows the left end and most upper line.

The relationship of external display RAM address and display position

GH		GH+CL
GH+GA		GH+GA+CL
(GH+GA)+GA		GH+2GA+CL
(GH+2GA)+GA		GH+3GA+CL
GH+(n-1)GA		GH+(n-1)GA+CL

GH: Graphic Home address

GA: Graphic area number (columns)

CL: Columns are fixed by hardware. (pin-programmable)

- ex) Graphic home address = 0000H Graphic area = 0020H
MD2=H, MD3=H :32 columns
DUAL=H, MDS=L, MD0=M, MD1=H :2 lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	003FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH
0080H	0081H		009EH	009FH
00A0H	00A1H		00BEH	00BFH
00C0H	00C1H		00DEH	00DFH
00E0H	00E1H		00FEH	00FFH
0100H	0101H		011EH	011FH
0120H	0121H		013EH	013FH
0140H	0141H		015EH	015FH
0160H	0161H		017EH	017FH
01A0H	01A1H		01BEH	01BFH
01C0H	01C1H		01DEH	01DFH
01E0H	01E1H		01FEH	01FFH

C) Text area set

The columns of display are defined by the hardware setting. This command can be used to adjust columns of display.

- ex) LCD size: 20 columns, 4 lines
Text home address = 0000H Text area = 0014H
MD2=H, MD3=H : 32 columns
DUAL=h, MDS=L, MD0=L, MD1=H :4 lines

0000	0001		0013	0014		001F
0014	0015		0027	0028		0033
0028	0029		003B	003C		0047
003C	003D		004F	0050		005B

→ LCD ←

D) Graphic area set

The columns of display are defined by the hardware setting. This command can be used to adjust columns of graphic display.

- ex) LCD size: 20 columns, 2 lines
Text home address = 0000H Text area = 0014H
MD2=H, MD3=H : 32 columns
DUAL=H, MDS=L, MD0=H, MD1=H : 2 lines

0000	0001		0013	0014		001F
0014	0015		0027	0028		0033
0028	0029		003B	003C		0047
003C	003D		004F	0050		005B
0050	0051		0063	0064		006F
0064	0065		0077	0078		0083
0078	0079		008B	008C		0097
008C	008D		009F	00A0		00AB
00A0	00A1		00B3	00B4		00BF
00B4	00B5		00C7	00C8		00D3
00C8	00C9		00DB	00DC		00E7
00DC	00DD		00EF	00F0		00FD
00F0	00F1		0103	0104		011F
0104	0105		0127	0128		0123
0118	0119		012B	012C		0137
012C	012D		013F	0140		014B

→ LCD ←

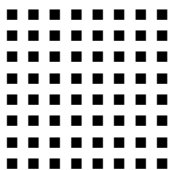
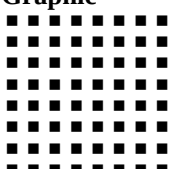
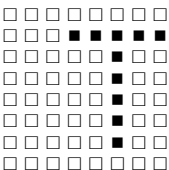
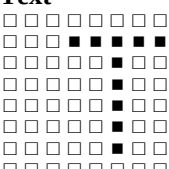
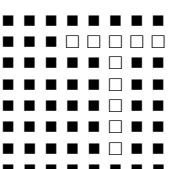
The address in graphic area can be continuous and RAM area can be used without ineffective area, if graphic area is defined the same number as the actual column number of LCD display.

9.2.3 Mode set

CODE	FUNCTION
1000X000	“OR” mode
1000X001	“EXOR” mode
1000X011	“AND” mode
1000X100	“TEXT ATTRIBUTE” mode
10000XXX	internal character generator mode
10001XXX	external character generator mode

The display mode is defined by this command. The display mode doesn't change until this command is sent. Logically “OR”, “EXOR”, “AND” of text and graphic display can be displayed. When internal character generator mode is selected, character code 00H~7FH are selected from built-in character generator RM. The character code 80H~FFH are automatically selected from external character generator RAM.

ex)

 Graphic  “OR”	 Text  “AND”	 “EXOR”
--	--	---

Note: Only text display is attributed, because attribute data is located in graphic RAM area.

A) Attribute function

“Reverse display”, “Character blink” and “Inhibit” are called “Attribute”. The attribute data is written in the graphic area defined by Control word set command. The mode set command selects text display only and graphic display cannot be displayed.

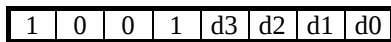
The attribute data of the 1st character in text area is written at the 1st 1 byte in graphic area, and attribute data of n-th character is written at the n-th 1 byte in graphic area. Attribute function is defined as follows:

Attribute RAM 1 Byte				X	X	X	X	d3	d2	d1	d0
d3	d2	d1	d0	FUNCTION							
0	0	0	0	normal display							
0	1	0	1	reverse display							
0	0	1	1	inhibit display							
1	0	0	0	blink of normal display							
1	1	0	1	blink of reverse display							
1	0	1	1	blink of inhibit display							

X: don't care

9.2.4 Display mode

CODE	FUNCTION
10010000	display off
1001XX10	cursor on, blink off
1001XX11	cursor on, blink on
100101XX	text on, graphic off
100110XX	text off, graphic on
100111XX	text on, graphic on



cursor blink	on: 1, off: 0
cursor display	on: 1, off: 0
text display	on: 1, off: 0
graphics display	on: 1, off: 0

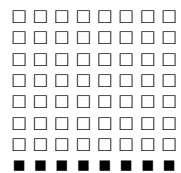
Note: It is necessary to turn on “text” display“ and ”graphic display“ in following case.

- 1) Combination of text/graphic display
- 2) Attribute function

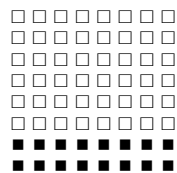
9.2.5 Cursor pattern select

CODE	FUNCTION	CODE	FUNCTION
10100000	1 line cursor	10100100	5 lines cursor
10100001	2 lines cursor	10100101	6 lines cursor
10100010	3 lines cursor	10100110	7 lines cursor
10100011	4 lines cursor	10100111	8 lines cursor

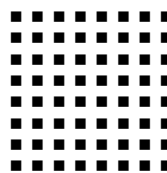
When cursor display is ON, this command selects the cursor pattern from 1 line to 8 lines. The cursor address is defined by cursor pointer set command.



1 line cursor



2 lines cursor



8 lines cursor

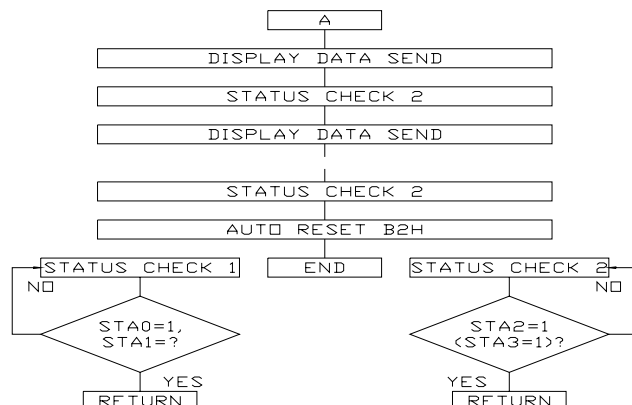
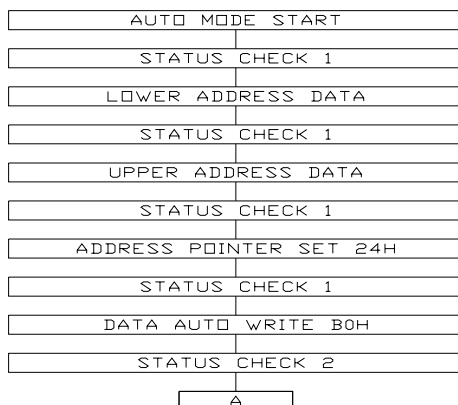
9.2.6 Data auto read/write

CODE	FUNCTION
10110000 B0H	data auto write set
10110001 B1H	data auto read set
10110010 B2H	auto reset

This command is convenient to send full screen data from external display RAM. After setting auto mode, “data write (or read)” command is not necessary between each data. “Data auto write (or read)” command should follow the “Address pointer set” and address pointer is automatically increment by +1 after each data “auto reset” is necessary to return normal operation because all data is regarded “display data” and no command can be accepted in the auto mode.

Note: Status check for auto mode (STA2, STA3 should be checked between each data. Auto reset should be performed after checking STA3=1 (STA2=1).

Refer following flow chart.

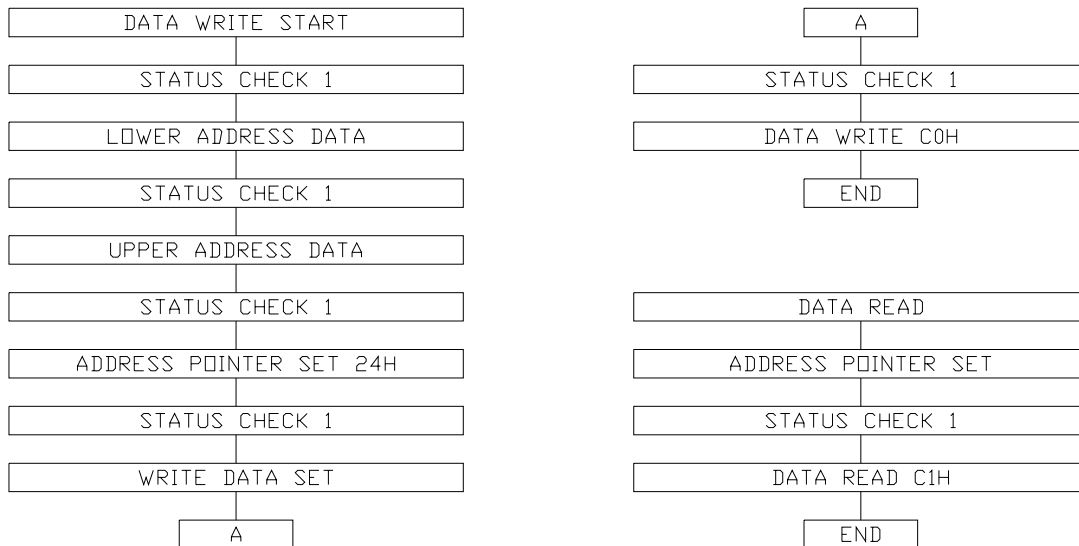


9.2.7 Data read write

CODE	FUNCTION	OPERAND
11000000 C0H	DATA WRITE AND ADP INCREMENT	data
11000001 C1H	DATA READ AND ADP INCREMENT	-
11000010 C2H	DATA WRITE AND ADP DECREMENT	data
11000011 C3H	DATA READ AND ADP DECREMENT	-
11000100 C4H	DATA WRITE AND ADP NONVARIABLE	data
11000101 C5H	DATA READ AND ADP NONVARIABLE	-

This command is used for data write from MPU to external display RAM to MPU. Data write/read should be executed after setting address by address pointer can be automatically increment or decrement by setting this command.

Note: This command is necessary for each 1 byte data. Refer following flow chart.



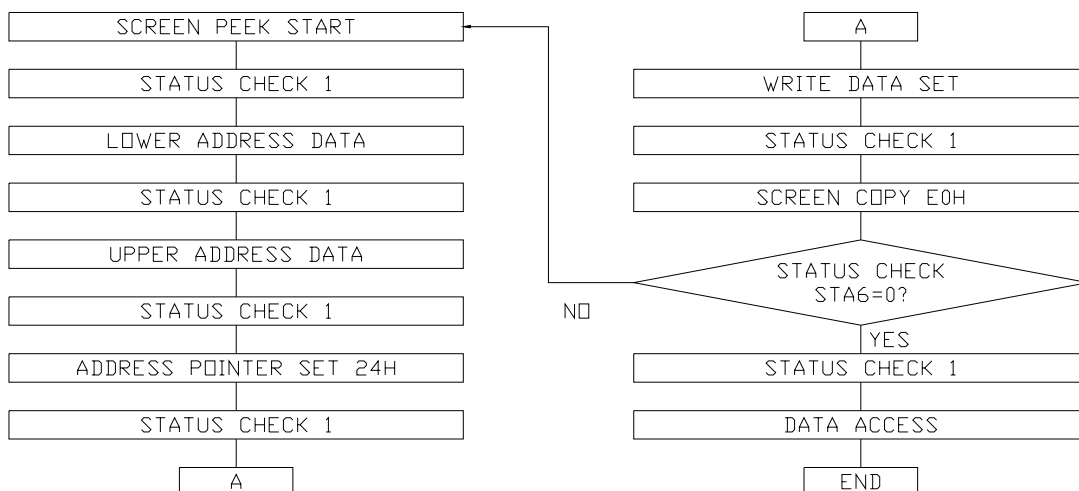
9.2.8 Screen peek

CODE	FUNCTION	OPERAND
11100000 E0H	screen peek	-

This command is used to transfer displayed 1 byte data to data stack, and this 1 byte data can be read from MPU by data access.

The logical combination data of text and graphic display on LCD screen can be read by this command. The status (STA6) should be checked just after “screen peek” command. If the address command is not in graphic area, this command ignored and status flag (STA6) is set.

Refer following flow chart.



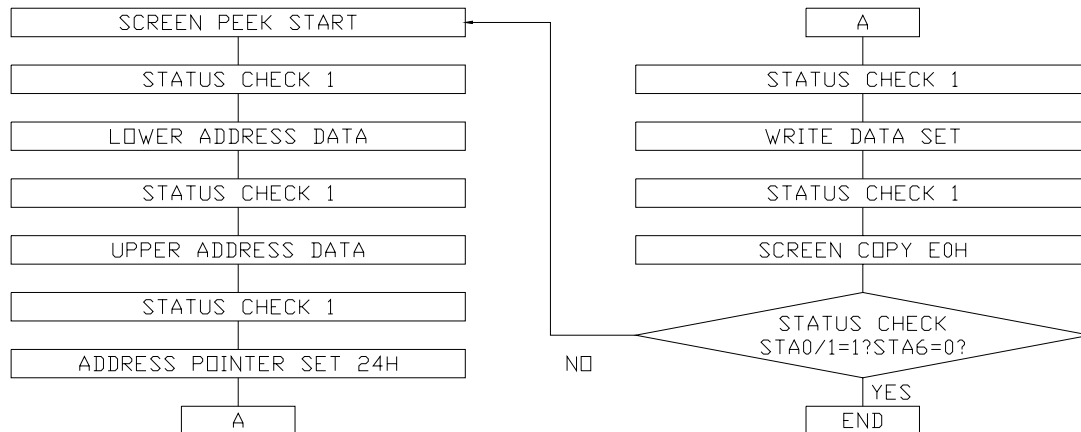
9.2.9 Screen copy

CODE	FUNCTION	OPERAND
11101000 E8H	screen copy	-

This command is used to copy displayed 1 line data to graphic area. The start point of 1 line data in the screen is determined by the address pointer.

Note:a) When the attribute of text is used this command cannot be used. (because attribute data is in the graphic area.)
b) In case of 2 screen drive, this command cannot be used. (because T6963c cannot separate upper screen data and lower screen data.)

Refer following flow chart.

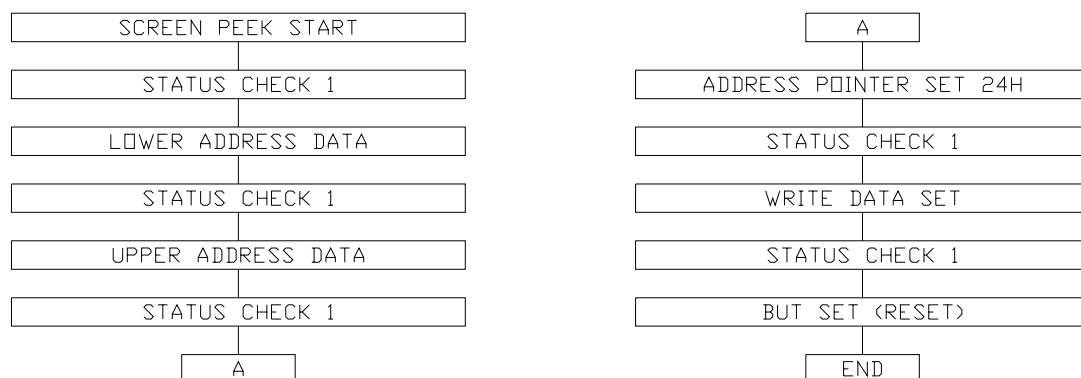


9.2.10 Bit set/reset

CODE	FUNCTION	CODE	FUNCTION
11110XXX	bit reset	1111X011	bit 3
11111XXX	bit set	1111X100	bit 4
1111X000	bit 0 (LSB)	1111X101	bit 5
1111X001	bit 1	1111X110	bit 6
1111X010	bit 2	1111X111	bit 7

This command is used to set or reset a bit of 1 byte is specified by address pointer. Plural bits in the 1 byte data cannot be set/reset at a time.

Refer following flow chart.



9.3 Flowchart of communications with MPU

9.3.1 Status Read

Before sending data (read/write), command it is necessary to check the status.

A) Status Check

Status of T6963C can be read from data lines.

RD = L, WR = H, CE = L, C/D = H, D0~7 -> Status word T6963C. Status word format is following.

MSB					LSB		
STA7	STA6	STA5	STA4	STA3	STA2	STA1	STA0
D7	D6	D5	D4	D3	D2	D1	D0
STA0	check capability of command execution					0: disable	1:enable
STA1	check capability of data read/write					0: disable	1:enable
STA2	check capability of auto mode data read					0: disable	1:enable
STA3	check capability of auto mode data write					0: disable	1:enable
STA4	not use						
STA5	check capability of controller operation					0: disable	1:enable
STA6	error flag. using screen peek/copy command					0: no error	
STA7	check the condition blink					0: display off	1:normal display

- Note:**
1. It is necessary to check STA0 and STA1 at the same time. The error is happened by sending data at executing command.
 2. The status check will be enough to check STA0/STA1.
 3. The STA2/STA3 are valid in auto mode STA0/STA1 are invalid.

Status checking flow



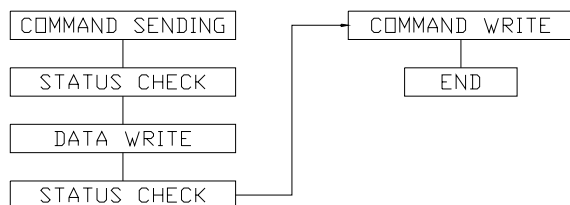
4. It is impossible to save status check in the case of command that is MSB 0. To have the delay time cannot be save status check. The interrupt of hardware is happened at the end of lines. If command of MSB0 is sent in this period, the command executing is waited. The state of waiting is not known without to check status. The sending next command or data is disregarded or rewrites data of waiting command

9.3.2 Data Set

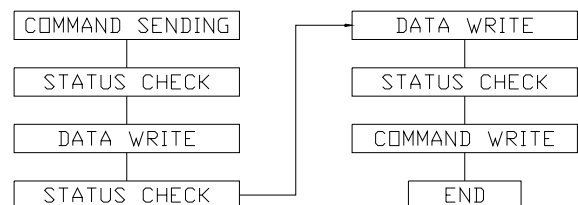
In T6963C, the data have been set and command executes.

The order of procedure of command sending

1) the case of 1 data



2) the case of 2 data



Note: In case of over 2 data sending, the last data (or last 2 data) is valid.

9.4 Initialize

Initialize of controller LSI T6963C is required for “Mode set”, “Control word set” after power on. Following is the one example of initialize procedure of 240x128 dots display (In case of 6x8 dots/font).

Command	C/D	D7 -----D0	Note
Power on	Power on		
Hard reset (use reset terminal)	RESET=“L” (1msec minimum after VDD>4.75V)		
Mode set	1	1 0 0 0 0 0 0 0	“OR” mode
Control word set			
Graphic home position set (Graphic home position 000H)	0 0 1	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 1 0	graphic home address command
Number of graphic area set (Graphic 40x6 dots)	0 0 1	0 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 1 1	number of area command
Text home position set (Text home position 1000H)	0 0 1	0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 1 0 0 0 0 0 0	text home address command
Number of text area set (text 40 column)	0 0 1	0 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 1	number of area command
Initialize end , Data Write			
Address pointer set (address pointer 0000H)	0 0 1	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 1 0 0	graphic home address command
Data Write (graphic)	0 1 0 1 x	0 1 0 1 0 1 0 1 1 1 1 0 0 0 0 0 1 0 1 0 1 0 1 0 1 1 1 0 0 0 0 0 x x x x x x x x	Data command Data command
Address pointer set (address pointer 1000H)	0 0 1	0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 1 0 0 1 0 0	text home address command
Data write (text)	0 1 0 1 x	0 0 1 1 0 1 0 0 1 1 0 0 0 0 0 0 0 0 1 0 1 1 1 1 1 1 0 0 0 0 0 0 x x x x x x x x	Data command Data command
Display Mode Set (text/graphic on)	1	1 0 0 1 1 1 0 0	

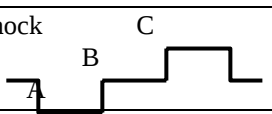
Note 1: “status check” should be inserted between all command and data.

- 2 : Display mode set register is cleared (no display mode) by the hard reset , and no display is appeared on LCD panel. And just after “Display Mode set 9CH”, written data is display on the LCD.

10. CG ROM PATTERN

LSB MSB	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0		!	"	#	\$	%	&	'	(	)	*	+	,	-	.	/
1	0	1	2	3	4	5	6	7	8	9	:	:	<	=	>	?
2	a	b	c	d	e	f	g	h	i	j	k	l	m	n	o	
3	p	q	r	s	t	u	v	w	x	y	z	[	\	]	^	_
4	`	a	b	c	d	e	f	g	h	i	j	k	l	m	n	o
5	p	q	r	s	t	u	v	w	x	y	z	[	\	]	^	_
6	0	1	2	3	4	5	6	7	8	9	:	:	<	=	>	?
7	a	b	c	d	e	f	g	h	i	j	k	l	m	n	o	

11. RELIABILITY CONDITION

		TN Type		STN Type	
		Normal Temp.	Wide Temp.	Normal Temp.	Wide Temp.
Viewing Angle	Horizontal Φ	±30°	±30°	±30°	±30°
	Vertical Θ (mm)	-10°to 30°	-10°to 30°	-10°to 40°	-10°to 40°
Operating Temperature		-10 to 70	-25 to 80	0 to 50	*-20 to 70
Storage Temperature		-20 to 80	-35 to 90	-20 to 70	*-30 to 80
High Temperature (Power Off)		240 Hours @70	240 Hours @90	240 Hours @65	240 Hours @75
Low Temperature (Power Off)		240 Hours @-20	240 Hours @-35	240 Hours @-15	240 Hours @-25
High Temperature (Power On)		240 Hours @70	240 Hours @80	240 Hours @60	240 Hours @70
Low Temperature (Power On)		240 Hours @-10	240 Hours @-25	240 Hours @-10	240 Hours @-20
High Temperature & High Humidity		55 /90%RH 240 Hours	75 /90%RH 240 Hours	45 /90%RH 240 Hours	65 /90%RH 240 Hours
Thermal Shock 5 Cycle		A	60min@-20	60min@-35	60min@-20
		B	5min@25	5min@25	5min@25
		C	60min@70	60min@90	60min@70
Expected Lift		50,000 Hours	50,000 Hours	50,000 Hours	50,000 Hours

*Wide temp. version may not available for some products, Please consult our sales engineer or representative.

12. FUNCTIONAL TEST & INSPECTION CRITERIA

12.1 Sample plan

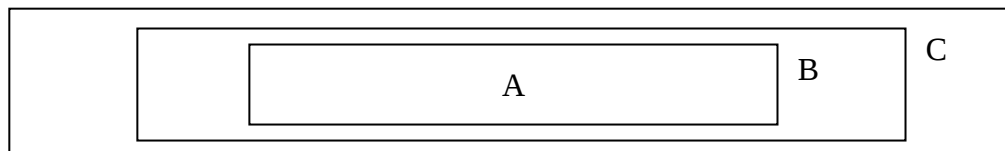
Sample plan according to MIL-STD-105D level 2, and acceptance/rejection criteria is.

Base on : Major defect : AQL 0.65 Minor defect : AQL 2.5

12.2 Inspection condition

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lus (20W) light intensity. All direction for inspecting the sample should be within 45° against perpendicular line.

12.3 Definition of Inspection Zone in LCD



Zone A : Character / Digit area

Zone B : Viewing area except Zone A (Zone A + Zone B = minimum Viewing area)

Zone C : Outside viewing area (invisible area after assembly in customer's product)

Note : As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

12.4 Major Defect

All functional defects such as open (or missing segment), short, contrast differential, excess power consumption, smearing, leakage, etc. and overall outline dimension beyond the drawing. Are classified as major defects.

12.5 Minor Defect

Except the Major defects above, all cosmetic defects are classified as minor defects.

Item No	Item to be Inspected	Inspection Standard					Classification of defects	
1.	Spot defect (Defects in spot from)	Zone size (mm)	Acceptable Qty				Minor	
			A		B			C
		Φ 0.15	Acceptable (clutering of spot not allowed)			Accepta- ble		
		0.15 Φ 0.20	1		2			
		0.20 Φ 0.25	0		1			
		Φ>0.25	0		0			
		Remarks : for dark/white spot, size Φ is defined as Φ=1/2(X+Y)						
2.	Line defect (Defects in line form)	Size (mm)		Acceptable Qty			Minor	
		L Length	W Width	Zone				
				A	B	C		
		Accep- table	W 0.02	Accep- table		Accep- table		
		L 3.0	W 0.03	2				
		L>2.5	W 0.03	0				
		L 3.0	0.03<W 0.05	2				
		L>2.5	0.03<W 0.05	0				
			W>0.05	Counted as spot defect (Follows item 12.5.1)				
Remarks:The total of spot defect and line defect shall not exceed four.								
3.	Orientation defect (such as misalignment of L/C)	Not allowed inside viewing area (Zone A or Zone B)					Minor	
4.	Polarizing	12.5.4.1 Polarizer Position 1.Shifting in Position Should not exceed the glass outline dimension. 2.Incomplete covering of the viewing area due to shifting isn’t allowed					Minor	
		12.5.4.2 Seratches, bubble or dent on Glass/Polarizer/Reflector, Bubble between Polarizer & Reflector/Glass:						
		Size (mm)	Acceptable Qty					
			Zone					
			A	B	C			
		Φ 0.20	Acceptable		Accep- table			
		0.20<Φ 0.50	3					
		0.50<Φ 1.00	2					
		Φ>1.00	0					



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