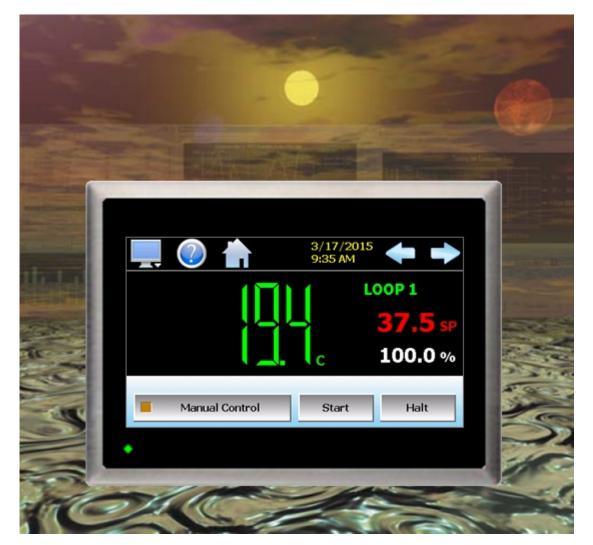


# Orion *nCompass* MC i4.3

1 to 10 Loop Controller User Communications Reference Manual







# **Safety Information in this Manual**

Notes, cautions and warnings appear throughout this book to draw your attention to important operational and safety information.

A "NOTE" marks a short message to alert you to an important detail.

A **"CAUTION**" safety alert appears with information that is important for protecting your equipment and performance.

A "**WARNING**" safety alert appears with information that is important for protecting you, others and equipment from damage. Pay very close attention to all warnings that apply to your application.



This symbol (an exclamation point in a triangle) precedes a general CAUTION or WARNING statement.



This symbol (a lightning bolt in a lightning bolt in a triangle) precedes an electric shock hazard CAUTION or WARNING safety statement.

# **Technical Assistance**

If you encounter a problem with your Orion nCompass controller, review all of your configuration information to verify that your selections are consistent with your application: inputs; outputs; alarms; limits; etc. If the problem persists after checking the above, you can get technical assistance by dialing +1 (866) 342-5332 or by faxing your request to +1 (866) 332-8014, Monday thru Friday, 8:00 a.m. to 5:00 p.m. Eastern Standard Time. You can also email your request to support@futuredesigncontrols.com.

An applications engineer will discuss your application with you.

#### Please have the following information available:

- Complete Model #'s and/or Serial #'s for Component(s) in Question
- Complete Software Version #'s
- All Configuration Information
- All User Manuals

Warranty and return information is on the back cover of this manual.

# **Your Comments**

Your comments or suggestions on this manual are welcome. Please send them to: Future Design Controls, P.O. Box 1196, Bridgeview, Illinois, 60455 Telephone: +1 (888) 751-5444; fax: +1 (888) 307-8014 <a href="https://csr.org/actionalized-send

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## 1 What is nCompass?

The nCompass system combines all of the features of a loop controller, video/chart recorder and data logging system into a single/intuitive device. Email, SMS (text messaging), FTP (file transfer protocol for automated data backup) and remote view/control (Web server/VNC server) are standard with nCompass and can be accessed via LAN/WAN using a PC, tablet or smart phone device.

Future Designs "nCompass" provides a 4.3"color touch screen interface with standard "Smart Device" user interface features for one to ten loop OEM control applications. All loop configuration and runtime user access is configurable at the device with no PC software required. OEM's have the ability to configure runtime features (screen availability, menus, language, etc...) to easily customize the system for their requirements. These configurations can be imported/exported to any other nCompass single/dual loop device for setup (from scratch) within minutes.

Individual high performance board level PID loop control boards (one for each loop) offer up to four control outputs each, powerful profiling capabilities with up to three events and full auto tune functionality with high resolution process inputs.

### 1.1 Features

Each of the nCompass loop control boards provide a single digital input that can be programmed as a automatic program control input for run, hold or abort, a manual mode or failure transfer control input or a program advance to next segment control input.

Each of the loop control boards also provide up to four control outputs which can be used as PID control outputs for heat/cool, direct outputs for controlling external equipment related to the application through software switches called events, or be programmed to act as system alarm outputs.

nCompass can be operated in single set point or automatic program control mode. Program entry is made easy through the use of copy, paste and delete menu selections. Programs can be copied to the external USB memory stick and then imported to another nCompass controller which eliminates the need to enter duplicate programs into multiple systems.

Data file analysis tools make looking at historical data a simple task. Any control variable saved to the data file can be plotted on the historical data chart for any time frame within the data file's total time range.

The built in Ethernet functionality includes a 'Web Server' to provide access to all nCompass data (view only), a VNC interface for remote control and monitoring and an NTS clock, all available via a local Intranet connection (wired or wireless), or the World Wide Web using standard software like Microsoft's Internet Explorer.

nCompass provides a rich set of tools for control interaction and process monitoring. Views include single and all loop views, charts, alarm, automated program status as well as historical data, alarm log and audit trail views. The menu driven interface eliminates screen "clutter" by providing an easy to use "Smart Device" interface for interaction between the user and nCompass.

nCompass can store more than one year of data on its SD memory card. Data logging can be enabled manually or automatically during program operation. Data backup is provided with a USB memory stick for plug and play transfer of files to any PC running Microsoft Windows XP operating systems and via the FTP back-up utility.

nCompass protects system access with 4 level security (user rights based), audit trails that document all user activity and ensures data integrity by digitally signing all data files and audit trails to meet regulatory requirements.



#### The nCompass controller includes the following features:

- · One to ten loop controller models (automatic program operation included).
- Touch screen, "Smart Device" user interface (UI).
- Video recorder mode for view only applications.
- Email, SMS, FTP, VNC and Web functionality standard.

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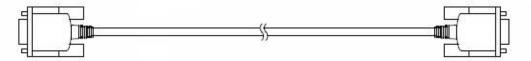
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- Remote View/Control using PC, Tablet or Smartphone.
- Detailed maintenance, alarm monitoring and alarm history.
- User configurable data logging and historical data viewer.
- 4 level security with digitally signed audit trails and data files.
- National time server connectivity with daylight savings.
- Multi-lingual user interface supports over 25 languages.
- 30,000 hour LED display

## 2 Communications Wiring

4 WARNIN	<ul> <li>To avoid potential electric shock and other hazards, all mounting and wiring for nCompass must conform to the National Electric Code (NEC) and other locally applicable codes.</li> <li>Special expertise is required to install, wire, configure and operate the nCompass controller. Personnel without such expertise should not install, wire or operate nCompass.</li> </ul>
	<ul> <li>Prevent metal fragments and pieces of wire from dropping inside the housing of any nCompass component. If necessary, place a cover over the component during installation and wiring. Ingress of such fragments and chips may cause a fire hazard, damage or malfunction of the device.</li> <li>Locate the nCompass touch screen and all related control components away from AC power/motor wiring and sources of direct heat output such as transformers, heaters or large capacity resistors.</li> </ul>

The nCompass touch screen provides an RS232C (COM1) user communications port for connecting nCompass to a PC running software such as FDC's Envision. In order to connect nCompass to a PC, a cable must be made according to the diagram below.



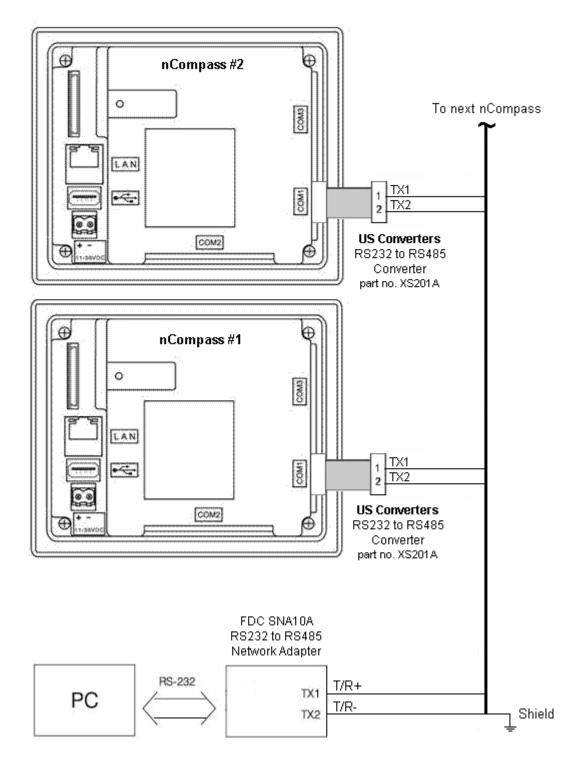
**D-sub 9-pin Female Connector Pinouts** D-sub 9-pin Female Connector Pinouts Description Pin Pin Description Shield Cover Cover Shield RXD Receive Data 2 2 RXD Receive Data TXD Transmit Data 3 3 TXD Transmit Data 5 5 SG SG Signal Ground Signal Ground

**NOTE:** DO NOT use a standard null-modem cable to connect nCompass to a PC. Most computers do not provide a standard serial port and a USB to serial converter must be used. Incompatibilities may exist between nCompass and certain USB to serial adapters which will cause nCompass to malfunction when connected using a standard null-modem cable.

If more than one nCompass controller is to be placed on the communication link with a PC, an RS232/485 converter will be required for each nCompass and the PC in order to convert the RS232 communications port to RS485 multi-drop communications network for connecting multiple nCompass controllers on the PC communications link.







**NOTE:** The connection requires a single twisted-pair cable that is daisy-chained from one nCompass to the next. When using shielded twisted-pair cable, be sure to ground only when end of the cable, preferably at the RS232 to RS485 network adapter. Allowing any other portion of the cable shield to come in contact with ground, or grounding both ends, will cause ground loop currents to flow in that section of the cable which can cause communication errors.

## **3** Communication Basics

The purpose of this document is to provide users interested in using data communications with the nCompass MC, the ability to set up and use a simple network of one or more nCompass control systems by providing a basic understanding of data communications using standard definitions, interfaces and protocols.

In this manual, numbers in the format '0x00' represent values in hexadecimal. Numbers in the format '0' represent values in decimal and finally, numbers in the format '00000000' represent values in binary unless otherwise stated.

### 3.1 Explanation of Terms

#### Machine-to-Machine Communication

In order for machines to communicate with each other, they need a code called a character format or character set. They require rules called protocol to govern their conversation and prevent confusion and errors. Computers need a connecting interface over which to communicate. They may use one pair of wires to send information in one direction and another pair to send in the opposite direction (full duplex), or they may use one pair to send data in both directions (half duplex).

#### **Character Format**

The code or character format for nCompass data communications is shared by virtually everyone in the electronics industry. This code defines a stream of 1's and 0's that are created by varying a voltage signal in a regular manner. This code is the <u>American Standard Code for Information Interchange</u>, called ASCII.

#### Bits and Bytes

The word "bit" is simply the contraction of the words <u>**bi**</u>nary digi<u>t</u>. A bit is the basic unit in ASCII. It is either a "1" or a "0". A byte is a string of eight bits that a computer treats as a single character. ASCII can use a single byte to represent each letter of the alphabet, each digit and each punctuation mark we use.

#### ASCII

The ASCII code defines 128 separate characters, one for each letter, digit and punctuation mark. ASCII also includes control characters similar to those we find on computer keys, such as backspace, shift and return. It also has nine communications control characters for identification, enquiry (inquiry), start of text, end of text, end of transmission, acknowledge, negative acknowledge and escape. The ASCII code is sometimes written in a base 16 number system that is called hexadecimal or "hex" for short. The numbers 0 through 9 represents the first ten digits of this system, and the letters A through F represents the final six digits. The 128 ASCII character codes with the decimal, binary and hexadecimal equivalents are listed in the following table.

#### ASCII Control Codes

ASCII Control Codes are used to give instructions to the remote device and result in specific actions, such as a line feed instruction on a printer. ASCII Control Codes, the first 33 ASCII characters (non printable), are important for the operation of communicating equipment. They give instruction to remote devices that result in specific actions such as a line feed on a printer. Holding down the keyboard control key while pressing the appropriate keyboard key is what sends these values.





### **ASCII Character Chart**

Char         Code         Decimal         Binary         Hex         Char         Code         Decimal         Binary         Hex           SUL         Ciri Q         0         00000001         01         A         Shift A         64         01000001         41           STM         Ciri B         2         00000101         02         Binary         66         01000010         43           ETX         Ciri B         3         00000101         04         Dinary         68         01000101         43           ENG         Ciri F         8         00000101         05         F         Shift C         77         0100011         48           ACM         Ciri F         8         00001010         06         F         Shift 7         73         0100101         48           BEL         Ciri H         7         0000101         06         H         Shift 7         73         0100101         40           F         Ciri J         10         0000101         08         Shift 7         73         0100101         40           F         Ciri J         12         0000110         00         Shift 7         70001001         40						acter C	ΠάΓι			
SOH         Chr A         1         0000001         01         A         Shift A         65         01000010         42           ETX         Chr B         2         00000011         03         C         Shift B         66         01000010         42           ETX         Chr B         4         00000100         04         D         Shift D         65         01000101         43           ENG         Chr E         5         00000111         05         E         Shift F         70         01000111         43           ACK         Chr F         6         00000101         06         F         Shift F         70         01000111         48           BEL         Chr H         8         00001000         06         H         Shift H         73         0100100         44           TAB         00001001         00         J         Shift H         75         0100110         47           Chr H         13         00001010         00         M         Shift N         76         0100110         42           Store Chr M         13         0001000         14         Shift N         78         0100111         42	Char		Decimal	Binary	Hex	Char	Code	Decimal	Binary	Hex
SOH         Chr A         1         0000001         01         A         Shift A         65         01000010         42           ETX         Chr B         2         00000011         03         C         Shift B         66         01000010         42           ETX         Chr B         4         00000100         04         D         Shift D         65         01000101         43           ENG         Chr E         5         00000111         05         E         Shift F         70         01000111         43           ACK         Chr F         6         00000101         06         F         Shift F         70         01000111         48           BEL         Chr H         8         00001000         06         H         Shift H         73         0100100         44           TAB         00001001         00         J         Shift H         75         0100110         47           Chr H         13         00001010         00         M         Shift N         76         0100110         42           Store Chr M         13         0001000         14         Shift N         78         0100111         42	NUL	Ctrl @	0	00000000	00	@	Shift 2	64		40
STX         Cirl B         2         0000010         02         B         Shift B         66         01000101         43           EVT         Cirl D         4         0000010         04         D         Shift D         68         01000101         43           EVN         Cirl E         5         00000101         05         E         Shift D         68         01000101         43           ACK         Cirl F         6         00000101         06         F         Shift E         69         01000111         45           ACK         Cirl B         8         00001000         08         H         Shift G         71         010001101         47           BEL         Cirl J         10         00001100         OA         J         Shift T         73         01001101         48           F         Cirl K         11         00001100         OC         L         Shift T         76         01001111         48           FF         Cirl K         11         00001101         OC         L         Shift T         78         01001111         44           DCI         Cirl K         16         000100101         12	SOH		1	00000001	01		Shift A	65	01000001	41
ETX         Cirl C         3         00000111         03         C         Shift C         67         01000101         44           ENQ         Cirl E         5         00000101         05         E         Shift E         69         01000101         44           ENQ         Cirl F         6         00000101         05         E         Shift F         70         01000110         44           BEL         Cirl G         7         00000100         08         H         Shift H         72         01001001         44           BEL         Cirl J         10         00001000         08         H         Shift H         73         01001001         47           F         Cirl L         12         00001100         00         L         Shift N         76         01001101         48           SIG         Cirl N         14         00001101         0D         M         Shift N         76         01001101         42           SIG         Cirl N         14         00001001         10         M         Shift N         77         01001101         42           SIG         Cirl N         14         00010000         N <th< th=""><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th></th<>										
EOT         Chr D         4         00000100         04         D         Shih D         68         01000100         45           ENO         Chr E         5         00000110         05         F         Shih F         70         01000110         45           ACK         Chr F         6         00000110         06         F         Shih F         70         01000111         47           BS         Chr H         8         00001000         08         H         Shih H         72         01001001         49           LF         Chr J         10         00001010         0A         J         Shih H         73         01001010         49           VT         Chr H         11         00001101         0D         LK         Shih M         76         01001011         48           SC         Chr H         13         00001101         0D         Shih M         77         01001111         47           SD         Chr D         16         000100111         0         Shih M         78         01010100         51           DC4         Chr D         17         00100101         12         Nh         Nh         84										
ENQ         Cirl E         5         0000101         05         E         Shift E         60         0010011         45           BEL         Cirl G         7         0000111         07         G         Shift G         71         01000110         48           BS         Cirl H         8         00001000         08         H         Shift H         72         01001001         48           TAB         Cirl J         10         0000101         0A         J         Shift H         73         01001010         44           VT         Cirl J         10         00001010         0C         L         Shift H         76         01001010         47           SI         Cirl J         10         00001101         0E         N         Shift M         77         01001101         48           SI         Cirl J         13         00001011         0E         N         Shift M         78         01001101         45           DE2         Cirl P         16         00010001         12         R         Shift P         80         01010001         51           DE3         Cirl P         16         0001001         12         R <th>EIX</th> <th></th> <th></th> <th></th> <th></th> <th>-</th> <th></th> <th></th> <th></th> <th></th>	EIX					-				
ACK         Chi G         7         0000110         66         F         Shift F         70         0100110         45           BS         Chi H         8         00001000         08         H         Shift F         71         0100111         47           BS         Chi H         9         00001001         08         H         Shift F         73         01001001         48           LF         Chi L         10         00001101         0A         J         Shift T         73         01001011         48           CH         11         00001100         0C         L         Shift T         75         01001101         48           CR         CHi M         13         00001101         0D         MM         Shift M         77         01001111         47           SO         CHi M         13         00001101         0D         Shift M         77         01001111         48           DC1         CH O         16         00001001         12         R         Shift M         80         01001001         51           DC2         CH R         18         00010001         14         T         Shift M         80					-					
BEL         Chi G         7         0000111         07         G         Shift G         71         0100101         47           BS         Chi H         8         0001000         08         H         Shift I         73         01001001         48           TAB         Chi J         10         00001010         0A         J         Shift I         74         01001010         44           VT         Chi L         12         00001010         0C         L         Shift I         74         01001010         44           GR         Chi L         12         00001101         0D         M         Shift I         76         01001101         42           SI         Chi D         16         00001101         0D         N         Shift I         76         01001111         47           SI         Chi D         16         0001001         11         Q         Shift I         83         0100101         51           DC2         Chi D         16         0001001         12         R         Shift I         84         0101001         52           DC3         Chi D         18         00010011         12         Shift I										45
BS         Cirl H         8         00001000         08         H         Shift H         72         01001001         49           LF         Cirl J         10         0000101         0A         J         Shift J         73         01001001         49           VT         Cirl K         11         00001101         0A         J         Shift J         75         01001011         48           CR         Cirl M         13         00001101         0C         L         Shift J         76         01001101         40           SO         Cirl M         14         00001101         0E         N         Shift M         77         01001011         40           SO         Cirl M         16         00010001         12         R         Shift O         79         01001011         44           DE2         Cirl R         18         00010011         13         S         Shift R         83         01010001         52           DC3         Cirl R         18         00010011         13         S         Shift R         83         0101001         53           DC4         Cirl R         18         00010011         15         U	ACK		6	00000110	06	F	Shift F			46
BS         Cirl H         8         00001000         08         H         Shift H         72         01001001         49           LF         Cirl J         10         0000101         0A         J         Shift J         73         01001001         49           VT         Cirl K         11         00001101         0A         J         Shift J         75         01001011         48           CR         Cirl M         13         00001101         0C         L         Shift J         76         01001101         40           SO         Cirl M         14         00001101         0E         N         Shift M         77         01001011         40           SO         Cirl M         16         00010001         12         R         Shift O         79         01001011         44           DE2         Cirl R         18         00010011         13         S         Shift R         83         01010001         52           DC3         Cirl R         18         00010011         13         S         Shift R         83         0101001         53           DC4         Cirl R         18         00010011         15         U	BEL	Ctrl G	7	00000111	07	G	Shift G	71	01000111	47
TAB         Cril         9         00001001         09         1         Shift         73         0100101         44           VT         Crif K         11         0000101         0A         J         Shift         74         01001010         4A           VT         Crif L         12         0000100         0C         L         Shift         76         01001101         4C           QR         Crif N         14         00001101         0D         M         Shift N         77         01001101         4E           SI         Crif O         15         00001110         0E         N         Shift O         79         01001101         4E           SI         Crif O         16         00010001         12         R         Shift O         81         01010010         52           DC2         Crif R         18         00010010         14         T         Shift O         81         0101001         53           DC4         Crif R         18         0001010         14         T         Shift O         83         01010101         53           DC4         Crif R         18         00010100         14         T	BS		8	00001000	08	н				48
LF         Cirl K         10         00001010         0A         J         Shift J         74         01001010         4A           FF         Cirl K         11         00001100         0C         L         Shift M         75         01001101         4D           CR         Cirl M         13         00001101         0D         M         Shift N         76         01001101         4D           S0         Cirl N         14         00001010         0E         N         Shift N         78         01001110         4E           S1         Cirl O         16         0001000         11         Q         Shift N         78         01010100         51           DE2         Cirl R         18         00010001         12         R         Shift S         83         01010010         51           DC4         Cirl T         20         00010110         13         S         Shift S         83         01011000         53           SYN         Cirl V         22         00010110         14         T         Shift V         88         01011001         59           SUB         Cirl V         23         0001100         14 <t< th=""><th></th><th></th><th></th><th></th><th></th><th>1</th><th></th><th></th><th></th><th></th></t<>						1				
VT         Chrl K         11         00001011         0B         K         Shift K         75         01001011         4B           FF         Cirl M         13         00001101         0D         M         Shift M         77         01001101         4D           S0         Cirl N         14         00001101         0D         N         Shift M         77         01001101         4D           S0         Cirl N         16         00001000         10         P         Shift O         78         01001101         4D           DLE         Cirl P         16         00010000         10         P         Shift O         81         01010100         52           DC3         Cirl S         19         00010101         13         Shift N         83         01010101         52           DC4         Cirl V         20         00011010         16         V         Shift V         86         01010101         55           SYN         Cirl V         22         00011010         18         X         Shift V         86         01011010         56           SWN         Cirl V         23         00011101         18         X						- <u>-</u>				-
FF         Cirl L         12         00001100         0C         L         Shift L         76         01001100         4C           SO         Cirl M         13         00001110         0E         M         Shift N         77         01001110         4D           SI         Cirl O         15         00001110         0E         N         Shift N         78         01001110         4E           SI         Cirl O         16         0001000         10         P         Shift R         80         01010000         50           DC2         Cirl R         18         00010001         12         R         Shift R         83         01010010         51           DC3         Cirl R         18         00010011         13         S         Shift R         83         01010011         53           SYN         Cirl V         21         00010101         14         T         Shift V         84         01010101         55           SWN         Cirl V         22         00010101         16         V         Shift V         85         01010101         55           SUB         Cirl X         24         00011000         18         <										
Chrl M.         13         00001101         OD         M         Shift M.         77         01001101         4D           SI         Chrl O.         15         00001111         OF         N         Shift O.         78         01001111         4F           SI         Chrl P.         16         00010000         10         P         Shift O.         78         01001101         4F           DC1         Chrl P.         16         00010000         12         R         Shift P.         80         01010001         51           DC2         Chrl S.         19         0001010         12         R         Shift P.         83         01010101         52           DC3         Chrl J.         20         00010101         15         U         Shift V.         85         01010101         53           DC4         Chrl J.         22         00010101         16         V         Shift V.         86         01010101         56           STR         Chrl W.         23         000110101         18         X         Shift V.         88         01011010         56           SUB         Chrl J.         26         00011010         1A <t< th=""><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th></t<>										
SO         Ctrl N         14         00001110         OE         N         Shift N         78         01001111         4E           DLE         Ctrl Q         16         00010001         10         P         Shift Q         81         01010001         51           DC1         Ctrl Q         17         00010010         112         R         Shift Q         81         01010010         52           DC3         Ctrl S         19         00010010         12         R         Shift Q         81         01010010         52           DC4         Ctrl T         20         00010100         14         T         Shift N         83         0101010         54           NAK         Ctrl V         21         00010110         16         V         Shift V         86         01010110         56           SYN         Ctrl V         23         0001010         18         X         Shift X         88         01011010         56           Ctrl X         24         0001100         1A         Z         Shift X         88         0101101         56           EM         Ctrl X         24         0001100         1A         Z <td< th=""><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th></td<>										
SI         Crtl Q         15         00001111         OF         Q         Shift Q         79         01011111         4F           DLE         Crtl P         16         00010000         10         P         Shift Q         81         01010000         50           DC2         Crtl R         18         00010001         11         Q         Shift R         82         01010001         51           DC3         Crtl S         19         00010001         13         S         Shift R         83         01010001         53           DC4         Crtl J         20         00010101         15         U         Shift T         84         01010101         53           NKK         Crtl V         22         00010101         15         U         Shift N         86         01010101         55           SYN         Crtl V         22         00011001         1A         Z         Shift N         88         01011001         56           SUB         Crtl Z         26         0001100         1A         Z         Shift A         88         0101100         5A           SUB         Crtl Z         26         00011110         IC						М				
SI         Chrl O         15         00001111         OF         O         Shift O         79         01001111         4F           DLE         Chrl P         16         00010001         11         Q         Shift O         81         01010001         50           DC3         Chrl S         19         00010011         13         S         Shift S         83         0101001         51           DC4         Chrl S         19         00010101         15         U         Shift S         83         01010101         53           DC4         Chrl U         21         00010101         15         U         Shift N         86         01010101         55           SYN         Chrl V         22         00010101         17         W         Shift X         88         0101001         56           SYN         Chrl V         22         00011001         1A         Z         Shift X         88         01011001         56           SUB         Chrl Z         26         0001101         1A         Z         Shift A         80         0101101         56           SUB         Chrl Z         26         00011010         1A         <	SO	Ctrl N	14	00001110	0E	N	Shift N	78	01001110	4E
DLE         Chrl P         16         000100001         10         P         Shift P         80         010100001         51           DC2         Chrl Q         17         00010001         11         Q         Shift R         82         01010001         51           DC3         Chrl S         19         00010101         13         S         Shift R         82         01010101         53           DC4         Chrl T         20         00010101         14         T         Shift R         84         01010101         54           SYN         Chrl V         22         00010110         16         V         Shift W         86         01010111         56           SYN         Chrl V         23         00010101         18         X         Shift W         88         01010101         59           SUB         Chrl Z         26         00011001         18         X         Shift W         89         01011001         59           SUB         Chrl Z         26         00011001         1A         Z         Shift W         89         01011001         59           SUB         Chrl Z         26         00011101         1D	SI		15		0F	0			01001111	4F
DC1         Cht Q         17         00010001         11         Q         Shift Q         81         01010001         52           DC3         Cht R         18         00010011         13         S         Shift S         83         01010011         53           DC4         Cht T         20         00010010         14         T         Shift S         83         01010011         53           DC4         Cht U         21         00010010         14         T         Shift T         84         01010101         55           SYN         Cht V         22         00010011         17         W         Shift V         86         01010101         55           SYN         Cht V         22         00011001         18         X         Shift X         88         01011001         59           SUB         Cht Y         28         00011001         1A         Z         Shift A         89         01011010         56           Cht Y         28         00011100         1A         Z         Shift A         90         01011010         56           SUB         Cht Y         28         00011110         1C         1         91										
DC2         Ctrl R         18         00010010         12         R         Shift R         82         01010010         52           DC3         Ctrl S         19         0001010         14         T         Shift S         83         01010011         53           DC4         Ctrl U         21         0001010         14         T         Shift U         83         01010101         55           SYN         Ctrl V         22         00010101         15         U         Shift V         86         01010101         56           CAN         Ctrl V         23         00011001         16         V         Shift V         86         01011001         56           CAN         Ctrl X         24         00011001         18         X         Shift Y         89         01011001         58           SUB         Ctrl Z         26         00011011         1B         [         90         0101101         58           ESC         Ctrl           28         0001101         1D         ]         93         0101101         55           SS         Ctrl           28         0001100         1C         \         \         92										
DC3         Ctrl S         19         0001011         13         S         Shift S         83         01010111         63           DC4         Ctrl U         21         0001010         14         T         Shift T         84         0101010         54           NAK         Ctrl U         22         0001011         16         V         Shift V         88         0101011         55           SYN         Ctrl V         23         00010111         17         W         Shift V         88         0101000         58           ETB         Ctrl V         23         0001100         18         X         Shift Y         89         01011001         59           SUB         Ctrl Z         26         0001101         1A         Z         Shift Y         89         01011001         59           SUB         Ctrl Z         26         00011101         1A         Z         Shift Y         89         01011010         50           SUB         Ctrl Z         28         00011101         D         1         1         93         01011101         55           SUB         Ctrl Z         30         000101111         1F         - <th></th>										
DC4         Cirl T         20         00010100         14         T         Shift T         84         01010100         54           NAK         Cirl U         21         00010101         16         V         Shift V         86         0101011         55           SYN         Cirl V         23         0001011         17         W         Shift V         86         0101011         57           CAN         Cirl X         24         00011001         18         X         Shift V         89         01011001         58           EM         Cirl X         25         00011001         1A         Z         Shift 7         89         01011001         58           SUB         Cirl Z         26         00011101         1A         Z         Shift 6         94         0101101         55           SUB         Cirl 3         00011101         1D         1         1         93         0101101         55           SS         Shift 3         30         00000110         21         a         A         97         0110000         60           Shift 3         30         00100001         22         b         B         98										
NAK         Cirl U         21         0001010         15         U         Shift V         85         0101010         55           SYN         Cirl V         22         00010110         16         V         Shift V         86         0101010         56           ETB         Cirl W         23         0001011         17         W         Shift V         87         01010111         57           CAN         Cirl X         24         00011001         18         X         Shift Y         89         01011001         58           EM         Cirl Y         25         0001101         1A         Z         Shift Y         89         0101101         5A           ESC         Cirl (         27         00011101         1B         [         [         91         0101101         5D           SC         Cirl A         30         00011101         1E         ^         Shift 6         94         0101110         5E           SP         SPACE         32         0010000         20         `<         Shift 6         94         0101110         5E           SP         SPACE         32         00100011         21         a										
SYN         Cirl V         22         00010110         16         V         Shift V         86         0101010         56           ETB         Cirl X         23         0001001         17         W         Shift X         86         0101000         57           CAN         Cirl X         24         00011001         19         Y         Shift X         86         01011001         59           SUB         Cirl Z         26         00011010         1A         Z         Shift Z         90         01011010         5A           ESC         Cirl Z         26         00011101         1B         I         91         01011010         5A           ESC         Cirl Z         28         00011101         1D         1         93         01011101         5B           FS         Cirl Z         30         00011111         1F         Shift A         94         01011110         5E           US         Cirl Z         31         0001000         20         7         96         0110000         61           Shift 3         35         0010001         21         a         A         97         01100001         61		Ctrl T		00010100					01010100	-
SYN         Ctrl V         22         00010110         16         V         Shift W         86         0101010         56           ETB         Ctrl W         23         0001100         18         X         Shift W         87         01010111         57           CAN         Ctrl X         24         00011000         18         X         Shift W         87         01010101         59           SUB         Ctrl Z         26         00011010         1A         Z         Shift Z         90         01011010         5A           SUB         Ctrl Z         26         00011101         1B         I         91         01011010         5A           SUS         Ctrl Z         20001100         1C         1         1         92         0101110         5B           GS         Ctrl J         29         00011111         1F         Shift A         93         01011110         5E           US         Ctrl J         30         00011111         1F         Shift A         95         01011111         5E           SP         SPACE         32         0010001         21         a         A         97         01100010         61 </th <th>NAK</th> <th>Ctrl U</th> <th>21</th> <th>00010101</th> <th>15</th> <th>U</th> <th>Shift U</th> <th>85</th> <th>01010101</th> <th>55</th>	NAK	Ctrl U	21	00010101	15	U	Shift U	85	01010101	55
ETB         Ctrl W         23         00010111         17         W         Shift W         87         01010111         57           CAN         Ctrl X         24         00011000         18         X         Shift X         87         01011001         58           EM         Ctrl Y         25         00011010         1A         Z         Shift Y         89         01011001         5A           SUB         Ctrl Z         26         00011010         1A         Z         Shift Y         89         01011010         5A           ESC         Ctrl Z         26         00011100         1C         1         91         01011011         5B           FS         Ctrl A         30         00011110         1E         A         Shift 6         94         01011110         5E           US         Ctrl A         30         00011111         1F	SYN			00010110		V				
CAN         Ctrl X         24         00011000         18         X         Shift Y         88         01011000         58           EM         Ctrl Z         25         00011010         1A         Z         Shift Y         89         01011001         59           SUB         Ctrl Z         26         00011001         1A         Z         Shift Z         90         01011010         5A           FS         Ctrl A         28         00011100         1C         V         92         01011010         5D           FS         Ctrl A         30         00011101         1E         A         Shift B         93         01011101         5D           RS         Ctrl A         30         00011110         1E         A         Shift B         93         01011101         5E           SPACE         32         00100000         20         '         '         96         01100001         61           Shift 3         35         00100010         22         b         8         98         01100010         64           Shift 3         35         0010011         23         c         C         C         99         01100010						W				
EM         Ctrl Y         25         00011001         19         Y         Shift Y         89         01011001         59           SUB         Ctrl Z         26         00011010         1A         Z         Shift Y         89         01011001         5A           ESC         Ctrl I         27         00011010         1C         V         V         92         01011011         5B           FS         Ctrl A         28         00011100         1C         V         V         92         01011101         5D           GS         Ctrl A         30         00011110         1E         A         Shift 6         94         01011101         5D           GS         Ctrl A         30         00010001         21         a         A         97         01100001         60           US         Shift 1         33         0010001         22         b         B         98         01100010         64           Shift 3         35         0010010         24         d         D         0100001         63           Shift 4         36         00100110         26         F         102         0110010         64      <										
SUB         Ctrl Z         26         00011010         1A         Z         Shift Z         90         01011010         5A           FS         Ctrl I         27         00011100         1C         V         92         01011101         5B           FS         Ctrl A         28         00011100         1C         V         92         01011100         5C           GS         Ctrl A         30         00011101         1E         A         Shift 6         94         01011111         5E           RS         Ctrl A         30         00011101         1E         A         Shift 6         94         01011110         5E           SPACE         32         00100000         20         Y         Y         96         01100001         61           **         Shift 1         33         00100011         22         b         B         98         01100011         63           *         Shift 3         35         00100101         25         e         E         101         01100010         64           %         Shift 7         38         00100110         26         f         F         102         01101011         6										
ESC         Ctrl [         27         00011011         1B         [         [         91         01011011         58           FS         Ctrl \         28         00011100         1C         \         \         92         01011101         55           GS         Ctrl ^         30         00011101         1D         1         93         01011101         55           US         Ctrl ^         30         00011111         1F					-					
FS         Ctrl 1         28         00011100         1C         1         92         01011100         5C           GS         Ctrl 1         29         00011110         1D         1         93         01011101         5D           RS         Ctrl A         30         00011110         1E         A         Shift 6         94         01011101         5E           US         Ctrl A         31         00011111         1F         A         Shift 6         94         01011110         5E           SPACE         32         00100001         20         *         95         01100001         60           I         Shift 1         33         0010001         22         b         B         98         01100001         62           #         Shift 3         35         0010010         24         d         D         100         01100101         63           Shift 5         37         0010011         25         e         E         101         01100101         65           Shift 5         37         0010011         25         e         E         101         0110010         64           Shift 7         38 <th></th> <th></th> <th></th> <th></th> <th></th> <th>Z</th> <th>Shift Z</th> <th></th> <th></th> <th></th>						Z	Shift Z			
GS         Ctrl 1         29         00011101         1D         1         1         93         01011101         5D           RS         Ctrl ^         30         00011110         1E         ^         Shift 6         94         01011101         5E           US         Ctrl _         31         00010000         20         '         '         96         01100001         60           I         Shift 1         33         00100001         21         a         A         97         01100001         61           "         Shift 1         34         00100010         22         b         B         98         01100010         62           #         Shift 3         35         0010010         24         d         D         100         01100101         63           \$         Shift 4         36         00100100         24         d         D         100         01100101         64           %         Shift 3         37         00100101         25         e         E         101         01100101         66           *         Shift 9         40         00101000         28         h         H         10		Ctrl [				[	[	-		
RS         Ctri ^         30         00011110         1E         ^         Shift 6         94         01011110         5E           US         Ctri _         31         00011111         1F	FS	Ctrl \	28	00011100	1C	١	١	92	01011100	5C
RS         Ctri ∧         30         00011110         1E         ∧         Shift 6         94         01011110         5E           US         Ctri _         31         00011111         1F         _         Shift .         95         01011110         5F           SP         SPACE         32         00100000         20         `         '         96         01100000         60           "         Shift 1         33         00100010         22         b         B         98         01100010         62           #         Shift 3         35         0010010         24         d         D         1000         01100101         62           \$         Shift 5         37         0010011         25         e         E         101         01100101         66           \$         Shift 5         37         00100110         26         f         F         102         0110011         66           \$         Shift 9         40         00101001         29         I         I         104         0110101         67           \$         Shift 9         40         00101010         2C         I         I <t< th=""><th>GS</th><th>Ctrl ]</th><th>29</th><th>00011101</th><th>1D</th><th>1</th><th>1</th><th>93</th><th>01011101</th><th>5D</th></t<>	GS	Ctrl ]	29	00011101	1D	1	1	93	01011101	5D
US         Ctri         31         00011111         1F	RS	Ctrl ^		00011110	1F	^	Shift 6	94		5F
SP         SPACE         32         00100000         20         Y         Y         96         01100000         60           I         Shift 1         33         00100001         21         a         A         97         01100001         61           "         Shift 3         35         00100010         22         b         B         98         01100011         63           \$         Shift 4         36         00100100         24         d         D         100         01100101         63           \$         Shift 5         37         0010010         25         e         E         101         01100101         66           \$         Shift 7         38         00100110         26         f         F         102         01100101         66           *         Shift 9         40         00101000         28         h         H         14         01010010         68           \$         Shift 8         42         0010101         2A         j         J         106         0110101         66           *         Shift 8         42         0010100         2C         I         L         108										
I       Shift 1       33       00100011       21       a       A       97       01100011       61         "       Shift 3       35       00100010       22       b       B       98       01100010       62         #       Shift 3       35       00100010       24       d       D       100       01100101       63         \$       Shift 5       37       00100101       25       e       E       101       01100101       64         %       Shift 5       37       00100101       25       e       E       101       01100101       66         *       38       00100111       27       g       G       103       01100101       66         *       38       00101010       28       h       H       104       0110100       68         >       Shift 0       41       0010100       2A       j       J       106       0110101       68         *       Shift 4       43       00101010       2A       j       J       106       0110101       6A         *       Shift 4       43       00101010       2C       I       L       108 <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th>51111 -</th> <th></th> <th></th> <th></th>							51111 -			
*         Shift '         34         00100010         22         b         B         98         01100010         62           #         Shift 3         35         00100011         23         c         C         99         01100011         63           \$         Shift 4         36         00100100         24         d         D         100         01100100         64           %         Shift 5         37         00100111         25         e         E         101         01100101         66           *         39         00100111         25         e         E         101         01100111         67           (         Shift 9         40         00101000         28         h         H         104         01101010         68           )         Shift 8         42         0010101         29         I         I         105         0110101         68           *         Shift 8         42         0010101         2D         m         M         109         0110101         66           *         .         .44         00101010         2C         I         L         108         0110100										
Shift 3         35         00100010         22         b         b         36         01100010         62           #         Shift 3         35         00100011         23         c         C         99         01100011         63           \$         Shift 5         37         0010010         24         d         D         100         01100100         64           %         Shift 5         37         0010011         25         e         E         101         01100101         65           8         Shift 5         37         00100111         27         g         G         102         01100111         66           '         39         00100100         28         h         H         104         01101011         66           '         39         01010101         29         I         I         105         01101011         66           *         Shift 8         42         00101010         2A         j         J         106         01101011         68           *         Shift 8         42         0010110         2C         I         L         108         01101011         66										
\$         Shift 4         36         00100100         24         d         D         100         01100100         64           %         Shift 5         37         0010010         25         e         E         101         01100101         65           &         Shift 7         38         00100111         27         g         G         102         01100110         66           '         39         00100100         28         h         H         104         0110010         68           )         Shift 9         40         0010100         28         h         H         104         0110100         68           *         Shift 8         42         0010101         29         I         I         105         0110101         69           *         Shift 8         42         0010101         2A         j         J         106         0110101         60           *         Shift 8         42         0010110         2C         I         L         108         0110110         60           *         44         0010110         2E         n         N         110         01101111         66 <th></th> <th></th> <th></th> <th></th> <th></th> <th>b</th> <th></th> <th></th> <th></th> <th></th>						b				
%         Shift 5         37         00100101         25         e         E         101         01100101         65           &         Shift 7         38         00100110         26         f         F         102         01100110         66           '         39         00100110         26         f         F         102         01100111         67           (         Shift 9         40         00101000         28         h         H         104         01101000         68           )         Shift 0         41         0010101         29         I         I         105         0110101         69           *         Shift 8         42         0010101         2A         j         J         106         01101010         6A           *         Shift 1         43         0010101         2D         m         M         107         0110101         6B           .	#	Shift 3	35	00100011	23	С		99	01100011	63
8         Shift 7         38         00100110         26         f         F         102         01100110         66           *         39         00100111         27         g         G         103         01100111         67           (         Shift 9         40         00101000         28         h         H         104         01101000         68           )         Shift 0         41         00101001         29         I         I         105         01101001         68           *         Shift 8         42         0010101         2A         j         J         106         0110101         6A           *         Shift 8         42         0010110         2C         I         L         108         0110110         6E           .         .         44         00101101         2D         m         M         109         0110111         6E           .         .         46         00101101         2E         n         N         110         0110111         6E           .         .         46         00110001         31         q         Q         113         0111000         70 <th>\$</th> <th>Shift 4</th> <th>36</th> <th>00100100</th> <th>24</th> <th>d</th> <th>D</th> <th>100</th> <th>01100100</th> <th>64</th>	\$	Shift 4	36	00100100	24	d	D	100	01100100	64
8         Shift 7         38         00100110         26         f         F         102         01100110         66           *         39         00100111         27         g         G         103         01100111         67           (         Shift 9         40         00101000         28         h         H         104         01101000         68           )         Shift 0         41         00101001         29         I         I         105         01101001         68           *         Shift 8         42         0010101         2A         j         J         106         0110101         6A           *         Shift 8         42         0010110         2C         I         L         108         0110110         6E           .         .         44         00101101         2D         m         M         109         0110111         6E           .         .         46         00101101         2E         n         N         110         0110111         6E           .         .         46         00110001         31         q         Q         113         0111000         70 <th>%</th> <th>Shift 5</th> <th>37</th> <th>00100101</th> <th>25</th> <th>е</th> <th>E</th> <th>101</th> <th>01100101</th> <th>65</th>	%	Shift 5	37	00100101	25	е	E	101	01100101	65
·         ·         39         00100111         27         g         G         103         01100111         67           (         Shift 9         40         00101000         28         h         H         104         01101000         68           )         Shift 0         41         00101001         29         I         I         105         01101001         69           *         Shift 8         42         00101010         2A         j         J         106         01101010         6A           *         Shift =         43         0010110         2A         j         J         106         01101010         6A           -         44         0010110         2C         I         L         108         0110110         6B           -         45         00101101         2D         m         M         109         0110110         6C           -         46         0010111         2F         n         N         110         01101101         6F           0         0         48         00110001         31         q         Q         111         0110011         71           2						f				
(       Shift 9       40       00101000       28       h       H       104       01101000       68         )       Shift 0       41       00101001       29       I       I       105       01101001       69         *       Shift 8       42       00101010       2A       j       J       106       01101001       68         *       Shift =       43       00101010       2A       j       J       106       01101010       6A         *       Shift =       43       00101101       2B       k       K       107       0110101       6B         .       .44       00101101       2D       m       M       109       0110100       6C         .       .46       00101111       2E       n       N       110       01101111       6F         0       0       48       00110001       30       p       P       112       01110000       70         1       49       00110010       32       r       R       114       01110001       71         2       2       50       0011010       34       t       T       116       0111001										
j         Shift 0         41         00101001         29         I         I         105         01101001         69           *         Shift 8         42         0010101         2A         j         J         106         0110101         6A           *         Shift =         43         0010101         2B         k         K         107         0110101         6A           .	1	Shift 0								
*         Shift 8         42         00101010         2A         j         J         106         01101010         6A           +         Shift =         43         0010101         2B         k         K         107         0110101         6B           ,         ,         44         00101100         2C         I         L         108         01101100         6C           -         45         00101101         2D         m         M         109         01101101         6D           .         46         0010110         2E         n         N         110         01101111         6E           /         /         47         0010111         2F         o         O         111         01101111         6E           0         0         48         00110000         30         p         P         112         0110010         70           1         49         0011001         31         q         Q         113         0111001         71           2         2         50         00110011         33         s         S         115         0111001         72           3         3 <th></th>										
+Shift =43001010102Rj3100011010100R,,44001011002CIL108011011006C45001011012DmM109011011016D46001011012EnN110011011016E//47001011112EnN110011011116E//47001011112FoO111011011116E//48001100030pPP11201100007011490011000131qQ11301100017122500011010132rR1140110101723351001100132rR1140110101734452001101034tT1160111010745553001101135uU1170111011756654001101038xX1200111100789957001110038xX12001111007A;;5900111013DYY12101111007A;;5900111013CIShift1240111101 <td< th=""><th>)</th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th></td<>	)									
,,44001011002CIL108011011006C45001011012DmM109011011016D46001011102EnN110011011016E//47001011112FoO111011011116F00480011000030pPP11201110000701149001100131qQ113011100017122500011001032rR11401110101723351001101133sS115011100117344520011010034tT116011101007455530011010135uU11701110101756654001101036vV11801110107677550011011137wW1190111010789957001110038xX120011110078995700111003AzZ12201111007A158001110139yY12101111017A5900111013AzZ12201111017A559 </th <th></th> <th></th> <th></th> <th></th> <th></th> <th>J</th> <th></th> <th></th> <th></th> <th></th>						J				
-         45         00101101         2D         m         M         109         01101101         6D           .         .         46         00101110         2E         n         N         110         01101110         6E           /         /         47         00101111         2F         o         O         111         01101111         6F           0         0         48         00110000         30         p         P         112         01110000         70           1         1         49         0011001         31         q         Q         113         01110001         71           2         2         50         0011001         32         r         R         114         0111001         72           3         3         51         0011001         32         r         R         114         0111001         72           4         4         52         0011001         34         t         T         116         0111010         74           5         53         0011010         36         v         V         118         0111010         76           7 <t< th=""><th>+</th><th>Shift =</th><th></th><th></th><th></th><th>k</th><th>K</th><th>-</th><th></th><th>-</th></t<>	+	Shift =				k	K	-		-
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//47001011112FoO111011011116F00480011000030pP112011100007011490011000131qQ113011100017122500011001032rR114011100107233510011001133sS11501110011734452001101034tT116011101007455530011010135uU117011101017566540011011036vV11801110107677550011010138xX120011110107899570011100139yY12101111001791Shift;58001110103AzZ122011110017899570011100139yY12101111001781;;59001110113B{Shift12301111017A5shift,60001111003CIShift12401111007C==61001111013D\$Shift12601111107D>Shift.62001111103E~Shift <th></th> <th></th> <th>46</th> <th>00101110</th> <th>2E</th> <th>n</th> <th>N</th> <th>110</th> <th>01101110</th> <th>6E</th>			46	00101110	2E	n	N	110	01101110	6E
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:       Shift;       58       00111010       3A       z       Z       122       0111010       7A         ;       ;       59       00111011       3B       {       Shift       123       01111011       7B         <							$\hat{\mathbf{v}}$			
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=         =         61         00111101         3D         }         Shift         125         01111101         7D           >         Shift         62         00111110         3E         ~         Shift         126         01111101         7E	;	;				{				
=         =         61         00111101         3D         }         Shift         125         01111101         7D           >         Shift         62         00111110         3E         ~         Shift         126         01111101         7E	<	Shift ,	60	00111100	3C		Shift \	124	01111100	7C
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## 4 Serial Communication

The user communications interface for nCompass employs serial communication, which is the exchange of data in a one-bit-at-a-time, sequential manner on a single data line or channel. Serial contrasts with parallel communication, which sends several bits of information simultaneously over multiple lines or channels. Not only is serial data communication simpler than parallel, it is also less costly.

#### Baud Rate

The baud unit is named after Jean Maurice Emile Baudot, who was an officer in the French Telegraph Service. He is credited with devising the first uniform-length 5-bit code for characters of the alphabet in the late 19th century. What baud really refers to is modulation rate or the number of times per second that a line changes state. This is not always the same as bits per second (BPS). However, if you connect two serial devices together using direct cables then baud and BPS are in fact the same. Thus, if you are running at 9600 BPS, then the line is also changing states 9600 times per second.

Typical baud rates used for computers are 300, 600, 1200, 2400, 4800, 9600, 14400, 19200, 38400, 57600 and 115200 baud. As the baud rate increases, so does the transmission rate of data. Thus you get more information in a shorter period of time. However, the faster the transmission rate, the more susceptible it is to error due to the quality of the cable and sources of electrical "noise" in the environment. To provide the best balance between speed and noise immunity, nCompass uses a baud rate of 9600 baud. *In order for a device to communicate with nCompass, it must have its serial port set for 9600 baud in order for data communications to work properly.* 

#### Start and Stop Bits

The start bit informs the receiving device that a character is coming, and a stop bit tells it that a character is complete. The start bit is always a 0. The stop bit is always a 1. The human speech equivalent of these bits could be a clearing of the throat to get someone's attention (start bit); and a pause at the end of a phrase (stop bit). Both help the listener understand the message.

A stop bit has a value of 1 - or a mark state - and it can be detected correctly even if the previous data bit also had a value of 1. This is accomplished by the stop bit's duration. Stop bits can be 1, 1.5, or 2 bit periods in length. nCompass uses the default – and most common – length of 1 period for the stop bit. A device used to communicate with nCompass must also have its serial port set to use a stop bit of 1 in order for data communications to work properly.

#### Parity Bit

Besides the synchronization provided by the use of start and stop bits, an additional bit called a parity bit may optionally be transmitted along with the data. A parity bit affords a small amount of error checking, to help detect data corruption that might occur during transmission. There are several defined parity selections available for serial communications. They are even parity, odd parity, mark parity, space parity or none at all can be used.

When even or odd parity is being used, the number of marks (logical 1 bits) in each data byte are counted, and a single bit is transmitted following the data bits to indicate whether the number of 1 bits just sent is even or odd. Mark parity means that the parity bit is always set to the mark signal condition and likewise space parity always sends the parity bit in the space signal condition. Since these two parity options serve no useful purpose whatsoever, they are almost never used.

nCompass offers parity settings of Even, Odd and None. In order for a device to communicate with nCompass, it must have its serial port set to use the same parity setting as nCompass in order for data communications to work properly. nCompass is provided with a default parity setting of Even in order to be compatible with FDC's Envision software.



### 4.1 Interface Standards

An interface is a means for electronic systems to interact. It's a specific kind of electrical wiring configuration. It has nothing to do with how data is sent over that connection. The two most common interfaces used today are RS-232, which provides a simple 1 to 1 connection and RS-485, which provides a multi-drop connection where more than one device can be placed on the same line. The nCompass communications interface is RS-232, but can be changed to RS-485 through the use of external RS232/485 adapters.

#### EIA-232 (Full Duplex)

An EIA-232 (formerly RS-232C) interface uses three wires: a single transmit wire; a single receive wire; and a common line. Only two devices can use an EIA-232 interface. A -3 to -24 volt signal indicates a 1 and a +3 to +24 volt signal indicates a 0. The EIA-232 signal is referenced to the common line rather than to a separate wire, as in EIA-485. Thus, an EIA-232 cable is limited to a maximum of 50 feet, due to noise susceptibility.

#### EIA-485 (Half Duplex)

An EIA-485 interface uses two wires: a T/R+, a T/R- line. A -5-volt signal is interpreted as a 1, a +5-volt signal as a 0. As many as 31 slave devices can be connected to a master on a multi-drop network up to 4000 feet long.

#### <u>Wiring</u>

Most PCs have a standard EIA-232 port (usually referred to as RS-232). In these instances, you must use an interface converter to connect to an EIA-485 multi-drop system. The standards do not specify the wire size and type. Use of 24 AWG twisted pair provides excellent results. If shielded cable is used, terminate the shield at one end only. Always follow the manufacturer's instructions supplied with the interface converter. See Biasing of Buses next.

#### Biasing of Buses

The EIA-485 standard requires the bus to be biased for reliable communication. This requires termination resistors to be placed across the T/R+ and T/R- wires. One resistor is placed at the PC where it connects to the EIA-485 bus. The second resistor is placed at the last controller on the network. Do not place resistors at each controller. The impedance of the wires used for the bus determines the resistor value. For twisted pair, the value is typically 120 ohms. In addition, it may be necessary to have a pull-up and pull-down resistor between the power supply and ground of the interface adapter.

Check the documentation that came with your interface adapter. Biasing the bus reduces reflection of signals sent down the bus. These reflections are sometimes referred to as a standing wave. This condition is most notable when communicating at high baud rates over longer distances.

#### 4.1.1 Interface Converters

The purpose of an interface converter is to allow two different buses to be connected together. Interface converters are required when connecting an EIA-232 port to an EIA-485 bus. The EIA-485 bus is a half duplex bus. This means that it can only send or receive data at any given time. Some interface converters on the market provide the ability to have full duplex with the EIA-485 bus. This is accomplished by using two receivers and transmitters tied in tandem. This type of converter will not work with the nCompass controller. Be sure that the model you purchase is designed for half duplex.

Another consideration when selecting an interface converter is how the converter handles switching between transmit and receive. Typically it is accomplished via a handshake line from the PC. When data flows into the converter from the PC, a handshake line is placed high. When data flows out of the converter to the PC, the handshake line is placed low. In this way, the handshake line controls the direction of information. Another method of achieving this is to use a built-in timer. The converter switches to transmit when a character is sent to it from the PC. After a period of time when the PC has not transmitted, the converter switches to a receive mode.

It is important that you understand how your converter accomplishes this task. You are required to wire this feature or make settings on the converter to enable this function. The PC will not talk to the controller correctly without properly setting this. Your converter may also require settings through dip switches, to set up communications parameters like baud rate, data bits, start bits, stop bits and handshaking. The converter may also require a separate power supply. Some converters get their power from the handshake lines of the PC. If you rely on this method, you will need to wire these additional lines. In addition, your software must set these lines high. A more reliable method is to use an external power supply. This is especially necessary when using a laptop computer. See the documentation that is provided with your converter for more information.

Not all converters are equal in performance. If your chamber operates in a harsh, electrically noisy environment, this can cause less robust converters to work intermittently or not at all. The following converter has been tested and is compatible with nCompass. The converter is equipped with automatic send data control circuits, driver control in the converter hardware, so you don't have to work with software at all. The circuit monitors data flow and enables the driver during transmission and automatically disables it when no data is being sent. There is no need to rework software or install new drivers.

US Converters 405 W. Fairmont Dr. Tempe, AZ 85282 E-mail: <u>mail@usconverters.com</u> www.USconverters.com

Part # XS201A RS232 to RS485 Converter

Future Design Controls 7524 West 98th Place Bridgeview, IL 60455 Phone: 888-751-5444 Fax: 888-307-8014 E-mail: <u>csr@futuredesigncontrols.com</u> www.futuredesigncontrols.com

Part # **SNA10A** Smart Network Adapter Part # **DB9M-DB9F-6ft** (Cable Accessory to connect SNA10A to PC)



### 4.2 Protocol

Protocol describes how to initiate an exchange. It also prevents two machines from attempting to send data at the same time. There are a number of different data communications protocols, just as there are different human cultural protocols that vary according to the situation.

The protocol portion of nCompass communications is very important, because it provides a quality of communication that others often don't have. Protocol-driven communications are more accurate because they are less prone to both operator and noise errors. Protocol maintains system integrity by requiring a response to each message. It's like registered mail — you know that your letter has been received because the post office sends you a signed receipt.

In nCompass data communications, a dialog will continue successfully as long as the messages are in the correct form and responses are returned to the protocol leader. If the operator enters an incorrect message, or interference comes on to the data line, there will be no response. In that case the master must retransmit the message or go to a recovery procedure. If an operator continues to enter an incorrect message or interference continues on the data line, the system will halt until the problem is resolved. nCompass uses Modbus RTU as the protocol of choice. Modbus RTU enables a PC to read and write directly to registers containing the nCompass parameters. With it, you can read all of the controller's parameters with just a single read command.

#### Modbus Remote Terminal Unit (RTU)

Gould Modicon, now called AEG Schneider, created this protocol for process control systems called "Modbus". It has the advantage over other protocols of being extremely reliable in exchanging information. This protocol works on the principle of packet exchanges. The packet contains the address of the controller to receive the information, a command field that says what is to be done with the information and several fields of data. The last item sent in the packet is a field to ensure the data is received intact. This is called a cyclic redundancy check-sum. See the following example for information on how to generate this value. All information is exchanged in hex numbers. nCompass only supports the binary version of Modbus, referenced as RTU. The ASCII version is less efficient and is not supported. Therefore, you must be certain to format all data in hexadecimal.

The CRC (Cyclical Redundancy Checksum) is calculated by the following steps:

- 1. Load a 16-bit register (called CRC register) with 0xFFFF
- 2. Exclusive OR the first 8-bit byte of the command message with the low order byte of the 16-bit CRC register, putting the result in the CRC register.
- 3. Shift the CRC register one bit to the right with MSB zero filling. Extract and examine the LSB.
- 4. If the LSB of the CRC register is zero, repeat step 3, else Exclusive OR the CRC register with the polynomial value 0xA001.
- 5. Repeat steps 3 and 4 until eight shifts have been performed. When this is done, a complete 8-bit byte will have been processed.
- 6. Repeat steps 2 through 5 for the next 8-bit byte of the command message. Continue doing this until all bytes of the command message have been processed. The final contents of the CRC register is the CRC value.

When transmitting the CRC value in the message, the upper and lower bytes of the CRC value must be swapped, i.e. the lower order byte will be transmitted first.

#### Example Cyclical Redundancy Checksum (CRC) Algorithm

```
unsigned int calc_crc(unsigned char *start_of_packet, unsigned char *end_of_packet)
{
unsigned int crc;
unsigned char bit count;
unsigned char *char_ptr;
/* Start at the beginning of the packet */
char_ptr = start_of_packet;
/* Initialize CRC */
crc = 0xFFFF;
/* Loop through the entire packet */
do{
    /* Exclusive-OR the byte with the CRC */
    crc ^= (unsigned int)*char_ptr;
    /* Loop through all 8 data bits */
    bit count = 0;
    do{
        /* If the LSB is 1, shift the CRC and XOR the polynomial mask with the CRC */
        if(crc & 0x0001){
            crc >>= 1;
            crc ^= 0xA001;
        /* If the LSB is 0, shift the CRC only */
        else{
        crc >>= 1;
        ł
    } while(bit count++ < 7);
} while(char_ptr++ < end_of_packet);</pre>
return(crc);
}
```





### 4.3 Creating your own Modbus Application

Listed below are a few of the more common software packages that claim to support the Modbus protocol. This list is provided as informational only. Contact the software manufacturer for more information on applying their software.

LabView by National Instruments 11500 N Mopac Expwy Austin, TX 78759-3504 Phone 800-683-8411 http://www.natinst.com Wonderware by Wonderware 26561 Rancho Pkwy. South Lake Forest, CA 92630 Phone 949-727-3200 http://www.wonderware.com SpecView by SpecView Corporation 13409 53<sup>rd</sup> Ave NW Gig Harbor, WA 98332 Phone 253-853-3199 http://www.specview.com

If you already have a software application that uses Modbus, you can simply skip to the nCompass parameter table in Section 5 for the information your program requires. The rest of this section provides information on writing a software application that uses Modbus.

- 1. You must code messages in eight-bit bytes, with even parity, one stop bit (8, even, 1). nCompass has its parity set to even as default from the factory.
- 2. Negative parameter values must be written in twos' complement format. Parameters are stored in twobyte registers accessed with read and write commands to a relative address.
- 3. Messages are sent in packets that must be delimited by a pause at least as long as the time it takes to send 28 bits (3.5 characters). To determine this time in seconds, divide 28 by the baud rate. In the case of nCompass communications at 9600 baud, this calculates to a minimum period of ~3ms.

In addition, the nCompass timeout period must be added to that in order to properly time the send and receive messages between the host computer and multiple nCompass controllers on the serial link. With a default timeout period in nCompass of 135ms, it makes a total pause of 138ms minimum. Thus, after you receive a response from an nCompass controller at your PC, you must wait a minimum of 138ms before sending the next command.

- 4. Values containing decimal points such as process values and set points, have the decimal point implied, i.e., the data exchange can only be performed using whole numbers. Thus, the value must be scaled appropriately in order to exchange the data correctly. For example, a setpoint of 78.4 degrees must be sent as a value of 784 in order for nCompass to be set correctly. Likewise, a process value read from nCompass with a value of 827 is actually 82.7 degrees. Consult the parameter table for the proper format and allowable range of each value.
- 5. When monitoring a process, try to keep the number of read and write commands to a minimum of 500ms between exchanges to a single controller. Continuously reading data at a faster rate consumes an excess amount of the controller's processor time and does not provided any additional benefits in process monitoring.

#### Handling Communication Errors

Messages with the wrong format or illegal values will receive an exception response. Messages with the wrong CRC or timing will receive no response. It is the user's responsibility to handle the error appropriately within their own software and determine whether to resend the message or halt for operator intervention.

#### User Responsibility

Refrain from reading or writing from/to a register that does not exist or is currently disabled. Writing values to unassigned registers could cause system instability, malfunction or failure. Care must also be taken in that the process can not cause damage to property or injury to personnel if the wrong commands are sent due to operator error or equipment malfunction.



#### 4.3.1 Packet Syntax

Each message packet begins with a one-byte controller address, from 0x01 to 0x1F. The second byte in the message packet identifies the message command: read (0x03); write single (0x06) or write multiple (0x10). The next "n" bytes of the message packet contain register addresses and/or data. The last two bytes in the message packet contain a two-byte Cyclical Redundancy Checksum (CRC) for error detection.

Packet format:	nn	nn	nn nn	nn nn
address	<b>↑</b>	↑	<b>† †</b>	<b>† †</b>
command ———— registers and/or data —				

#### Read Register(s) Command (0x03)

This command returns from 1 to 60 registers. This command is used for reading one or more data locations from nCompass.

controller address (1 byte) f f f f f f f f f f f f f f f f	Packet sent to nCompass:	nn	03	nn nn	00 nn	nn nn
number of registers high byte (0x00)	read command (0x03) starting register high byte starting register low byte number of registers high byte (0x number of registers low byte CRC low byte	 00) —				

Packet returned from nCompass:	nn	03	nn	nn nn nn nn	nn nn
controller address (1 byte) read command (0x03) number of data bytes (1 byte) first register data low byte first register data high byte	_ <b>_</b>				
 register n data high byte ———— register n data low byte ———— CRC low byte —————————— CRC high byte ——————————					



<u>Example:</u> Read registers 35 and 36 (loop 1 process variable and setpoint) of controller at address 1 configured for 1 decimal point.

Sent: 01 03 00 23 00 02 35 C1 Received: 01 03 04 **03 0D 01 F3** 2A 61

Message data: 781 (0x**030D**) = process variable of 78.1 499 (0x**01F3**) = setpoint of 49.9

#### Write Register Command (0x06)

This command writes a value to a single register. This command is to be used for setting control values in nCompass. To set multiple values, repeat the command for each data location.

Packet sent to nCompass: nn	06	nn nn	nn nn	nn nn	
controller address (1 byte) write command (0x06) register high byte register low byte data high byte data low byte CRC low byte CRC high byte					
Packet returned from nCompass:	nn	06	nn nn	nn nn	nn nn
controller address (1 byte) — write command (0x06) — register high byte — register low byte — data high byte — data low byte — CRC low byte — CRC high byte —					

<u>Example:</u> Write register 41 (loop 2 set point) of controller at address one configured with no decimal point to 75 degrees (0x**004B**).

Sent:	01 06 00 29 <b>00 4B</b> 18 35
Received:	01 06 00 29 00 4B 18 35



#### Write Registers Command (0x10)

This command writes values to multiple registers in sequential order. It is used for automatic program download only to transmit program data one step at a time to nCompass. See the Automatic Program Parameters section for the list of registers and their use. If this command is used to write to registers other than the correct program step registers, nCompass will respond with an acknowledgment that the message was received; however, the command will not be executed.

Packet sent to nCompass:	nn	10	nn nn	nn nn	nn	nn nn n	n nn	nn nn
controller address (1 byte) – write command (0x10) — starting register high byte — starting register low byte — number of registers to write I number of registers to write I number of data bytes — data high byte — data low byte —	nigh byte	e —						
•••								
register n data high byte								
register n data low byte —— CRC low byte ——								
CRC low byte								
Packet returned from nCom	Г	nn	1	nn nn	nn nn	nn nn	]	
r acket returned nom nComp	1000.			····	• •		J	
controller address (1 byte) – write command (0x10) — starting register high byte — starting register low byte — number of registers to write l	nigh byt	e —						
number of registers to write I					I			

CRC high byte



#### Exception Responses

When nCompass cannot process a command, it returns an exception response and sets the high bit (0x80) of the command.

0x01 illegal command 0x02 illegal data address 0x03 illegal data value

#### 4.3.2 Error Checking

In Modbus communications, every message sent from the master (your software) receives a response from the slave (nCompass), including write commands. Thus, after each command sent, you must read the controller response before sending the next message. This provides the method of error checking in order to verify that the message you sent was received correctly, and that the controller is operating accordingly. This allows you to then determine the appropriate recovery response in case the message was not received correctly by the controller, and what action is to be taken by an operator and/or the software itself.

The exception responses provide a basic form of error checking. When an exception response is received, the code provided in the response will tell you what the error was in the sent message. However, this is only valid if the controller receives the message you sent, and there was an out-of-range value or simple transmission error in the message. It does not validate incomplete or failed transmissions. To insure that the data you receive from a read command is correct, and that the controller properly received a write command, you must parse the controller's response and validate the return message to insure it is correct.

In order to validate that the message you received is correct, you must calculate the CRC for the received message and compare it with the CRC that the controller appended to the message. This verifies that the data you received was what nCompass sent. If the CRC's do not match, there was an error in the transmission and the entire message should be ignored. This could then be followed by an attempt to resend the failed command, or halt operation and alert an operator.

*Example:* Read registers 35 and 36 (loop 1 process variable and setpoint) of controller at address 1.

Command sent to nCompass:	01 03 00 23 00 02 35 C1
Message received from nCompass:	01 03 04 03 0D 01 F3 2A 61

Calculated CRC: 2A61 (calculated from message 01 03 04 03 0D 01 F3) Received CRC: 2A61

The calculated CRC matches the received CRC, the message is valid. Note that the last two bytes of the received message are not used to calculate the CRC. The last two bytes are the CRC that nCompass appended to the message. Do not include them when calculating the CRC.

#### 4.3.3 Transmitting and Receiving Messages

In order to reliably communicate with nCompass, it is important to develop an efficient means of transmitting and receiving messages. Modbus is a structured protocol and it must be properly followed. It is recommended, if possible, to locate an existing communication driver to incorporate into your software. Developing one from scratch can be challenging. However, if one is not available, or you choose to develop one yourself, the following guidelines may be of assistance.

#### Transmitting Messages

When sending a message to nCompass, it is important to remember that Modbus RTU protocol does not have start-of-transmission or end-of-transmission characters. All messages are "framed" using timeouts between characters. nCompass uses its own fixed timeout setting of 135ms. Thus, if the entire message is not received by nCompass within 135ms, it will discard the data it has received and assume the next data byte received is the start of the next valid message.

The timeout must be considered carefully when developing your application. In creating your message, there are several steps that must be executed in order to build the packet and format the data properly into hexadecimal to send out the serial port of your PC. If you write code in a manner that steps byte by byte through sending the message out the serial port, formatting each piece of data prior to sending it, there is a good possibility that two much time may pass between characters, thus causing a failed transmission.

Therefore, it is recommended that the entire message, including the CRC, be created and assembled prior to being sent to the serial port. By assembling the main body of the message first, you can then pass it to the CRC algorithm which can step sequentially through the message, generate the CRC and append it to the message body. Once the message is completely assembled, it can then be sent out the serial port as a completed packet. This will insure that the message reaches nCompass within the proper time frame.

#### **Receiving Messages**

Due to the fact that Modbus RTU protocol does not have start-of-transmission or end-of-transmission characters, if the serial port driver you are using does not support an interval timeout setting allowing you to automatically terminate a read after a specified time passes between bytes (signaling the end of a message), you must know how long the message will be that you are receiving. That allows you to know how many bytes to read from your serial port and when you have received the entire message. If you rely on a maximum timeout period to terminate the read, depending upon the length of the received message, you will either loose a portion of the message or have to set the timeout period so high, that it will greatly affect the throughput of your code.

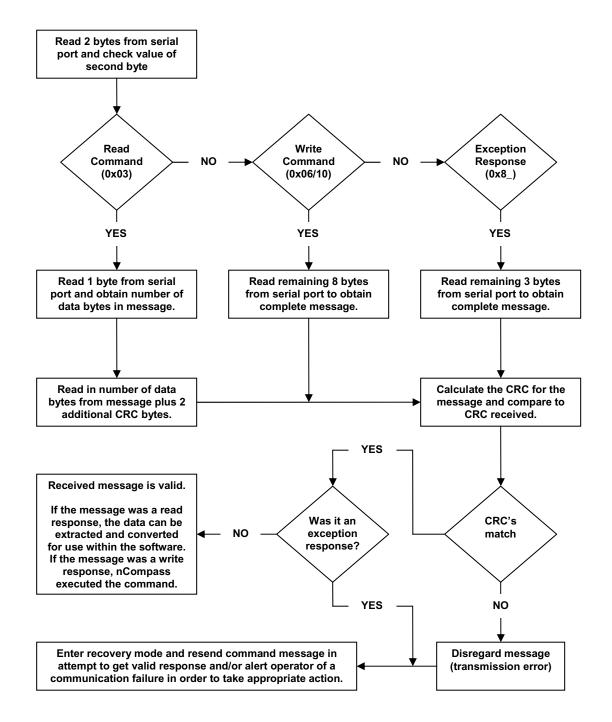
As can be seen from the previous examples for read and write commands in Section 4.3.1, the length of the returned message will very based on the type of command, and for read commands, how many registers are being returned. Response messages can vary in length from as little as 5 bytes for an exception response to as many as 125 bytes for a read command. Therefore, in order to read in the message efficiently, you need to know what type of command it is in response to.

The response messages are always coded with the first two bytes of the message as the controller address and command type. When executing a read, read in only the first 2 bytes of data at the serial port. Examine the second byte and determine what the command is. If it is a write command (0x06 or 0x10), you know the response message is 8 bytes long. You can then read in the next 6 bytes of data from the serial port to complete the message. You can then calculate the CRC for the first 6 bytes of that message, and compare it to the last 2 bytes. If they match, then the communication completed successfully.

If the response is to a read command (0x03), you must then perform a single byte read from your serial port in order to get the next byte of the message. The third byte in a read response message is the number of data bytes in the message. By reading in this value, you then know how many data bytes follow. Note that this value does not include the 2 bytes for the CRC. Thus, when reading in the rest of the message, you will read in the number of data bytes plus an additional two, in order to get the CRC. You can then calculate the CRC for the message and compare it to the last two bytes. If they match, the data you received is valid.





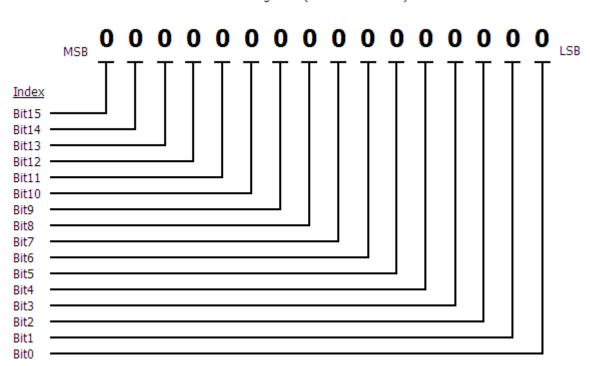


# 5 nCompass MC Loop Controller Data Registers

Some of the values contained in the nCompass loop controller register base contain bit oriented values. This means that each bit of the word indicates an on/off status for a specific setting or condition. In handling these values, it is recommended that the word be converted to its binary equivalent.

By converting the value to its binary equivalent, it produces a Boolean array of true [bit on (1)] and false [bit off (0)] values. This allows each bit to be examined individually. In the same manner, creating a Boolean array of 16 bits produces an equivalent hexadecimal value that can be sent to nCompass in order to set a control register.

For the purpose of this manual, parameters defined as bit oriented will have the function of each bit associated with the bit's index number in the data word. The index number is equal to that of a typical array function. Thus, an index number of zero, selects the first bit in the word (LSB). An index number of 1 selects the second bit in the word, and so on. This helps eliminate offset selection errors that may occur when coding software and using array functions to select which bit in the word that is required for examination.



Data Register (1 word = 16 bits)



Adhere to the following lists of registers and their allowable data ranges. DO NOT attempt to write to any other register number than those listed. DO NOT write to registers that are for options your controller does not have. Failure to adhere to this requirement can result in erratic control and/or damage to equipment.

#### All register numbers are listed in relative values. To convert to absolute values, add 400001.



### 5.1 Control Registers

Register Address		Parameter Description	Data *A	Ran	ge *B	*C
· togit			Туре	Low	High	Unit
0	(0x0000)	Controller Set Point (Static Mode)	W	-32768	32767	*C1
1	(0x0001)	Current Program/Segment Number	R/W	*B1	*B1	-
5	(0x0005)	Set Point for Alarm 1	R/W	-32768	32767	*C1
6	(0x0006)	Set Point for Alarm 2	R/W	-32768	32767	*C1
7	(0x0007)	Set Point for Alarm 3	R/W	-32768	32767	*C1
9	(0x0009)	Input Unit Selection	R	*B2	*B2	-
10	(0x000A)	Decimal Point Selection	R	0	3	-
11	(0x000B)	Operation Mode	R/W	*B3	*B3	-
127	(0x007F)	Manual Event Output	R/W	*B4	*B4	-
128	(0x0080)	Process Value (PV)	R	-32768	32767	*C1
129	(0x0081)	Set Point Value (SV) for Control	R	-32768	32767	*C1
130	(0x0082)	Output 1 Percentage Value (MV1)	R/W	0.00	100.00	%
131	(0x0083)	Output 2 Percentage Value (MV2)	R/W	0.00	100.00	%
132	(0x0084)	Mode and Operational Status	R	*B5	*B5	-
133	(0x0085)	Error Code	R	*B6	*B6	-
134	(0x0086)	Current Program and Segment Running	R	*B1	*B1	-
135	(0x0087)	Total Number of Steps	R	1	64	-
136	(0x0088)	Total Time for Step Running	R	0	99.59	-
137	(0x0089)	Set Point for Current Step	R	-32768	32767	*C1
138	(0x008A)	Time Remaining in Current Step	R	0	99.59	-
139	(0x008B)	Cycles Remaining for Current Loop	R	1	9999 10000=infinite	-
142	(0x008E)	Command Code	W	*B7	*B7	-

#### Notes:

- \*A R/W Specifies readable/writable data, R specifies read only data and W specifies a write only control value.
- \*B The range of certain parameters are dependent upon system options. Consult the following range tables for information regarding the use of these parameters.

#### Reading bit oriented parameters

The value contained in these parameters is dependent upon the combination of "on" bits (1). Therefore, only the individual status of each bit has meaning, not the value of the parameter.



#### Setting bit oriented parameters

The value that must be written to these parameters is dependant upon the combination of "on" bits. Therefore, it is necessary to know the current value of the parameter before setting it so that only the bit status you want to update is changed. Otherwise, sending a value derived from only the bit you wish to set will turn off all other functions related to the other bits in the parameter.

BI					
Parameter Value					
Range Low	9.00				
Range High	9.63				

**Note:** nCompass uses program 9 only in each loop control for automatic ramp/soak program operation. The "whole number" of the value (9) designates program 9. The fractional portion of the value (.00-.63) designates the segment number of the program which corresponds to steps 1 to 64.

#### Example

\* 0 4

Values read from and written to nCompass are integer values (no decimal) so the decimal point is implied. The values read from and written to nCompass will be in the range of 900 to 963. To set the program step number to 3, you would write a value of 902 to nCompass.

902 = 900 (for program number) + 2 (step number of 3 minus 1 to convert step number to segment number)

~	R7

Parameter Value	Description
0	Centigrade
1	Fahrenheit
2	Process Units (linear input type only)

\*B3

Parameter Value	Description
0	Program Run
1	Program Hold
2	Static Mode
3	Automatic Tune
5	Manual Mode

**Note:** Auto tune may not be available depending upon the loop configuration. If auto tune operation is not available, the loop controller will revert back to static mode. When auto tune operation completes normally, the loop controller will revert back to static mode.

Only one mode can be set at time. The program hold mode can not be set unless a program is currently running. The program mode can then be switched between run and hold by setting a value of 0 or 1 to the operation mode register.

When the loop control is in manual mode, the output percentage values for output 1 (MV1) and output 2 (MV2) can be set by writing to the corresponding output register.

\*B4

Parameter Value	Description
Bit0	Event 1 (output 2)
Bit1	Event 2 (output 3)
Bit2	Event 3 (output 4)
Bit3 – Bit15	Not Assigned

**Note:** The events provided by nCompass are a combination of all events provided by all loop controls combined. In order to turn events on and off, the events assigned to each specific control must be accessed. The proper event bits must be turned on in each corresponding loop control.

The first event in nCompass may actually be output 3 of loop controller number 2. This requires bit 1 of the event register to be set on loop controller 2 of nCompass.



#### \*B5

Parameter	Description
Value	Description
Bit0	Automatic Program Run Mode
Bit1	Automatic Program Hold Mode
Bit2	Static Mode
Bit3	Auto Tune Mode
Bit4	Manual Mode
Bit5	Off Mode
Bit6	Failure Transfer
Bit7	Automatic Program Ramp Up
Bit8	Automatic Program Ramp Down
Bit9	Automatic Program Soak
Bit10	Alarm 1 Active
Bit11	Alarm 2 Active
Bit12	Alarm 3 Active
Bit13	Event 1 On
Bit14	Event 2 On
Bit15	Event 3 On

\*B6

Parameter Value	Description
0	No Error
4	Illegal Setup Values
10	Comm Error – Bad Function Code
11	Comm Error – Register Out of Range
14	Comm Error – Write Read Only Data
15	Comm Error – Out of Range Data
25	Holdback Time Out
26	Auto Tune Error
27	Input Type Requires Calibration
29	EEPROM Error
30	Cold Junction Failure
39	Sensor Break
40	A to D Failure

\*B7

Parameter Value	Description
26660	Reset Single Set Point (Static Mode)
26661	Reset Alarms

**Note:** Upon completion of an automatic ramp/soak program, the loop control will not return control to the static set point until the "reset single set point (static mode)" command value is written to the loop control. Even though the static set point can be changed, the loop controller will continue to use the last program set point until the reset command is received.

\*C1 The units of measure and range of a loop input is dependant upon the configuration of the input and/or the units of temperature selection (Celsius or Fahrenheit) of the nCompass loop controller. The decimal point position for the value is an implied value based on the configuration of the input. Thus, a register value of 345 can represent an actual process value of 345, 34.5, 3.45 or 0.345 depending upon the decimal point configuration of the loop.

#### 5.1.1 Communicating with Loop Controllers on nCompass

The loop control register data table provides the list of available data for each loop controller attached to nCompass. In order to read the process value of each loop for example, you must send separate read commands addressed specifically to each controller.

The loop controls are addressed in sequential order starting with the Modbus address entered on the communications settings screen on nCompass. If the Modbus address is set to 3 and there are 5 loop controls attached to nCompass, the loop control addresses would be 3, 4, 5, 6 and 7. Sending the read register command to nCompass to read register 128 with an address of 5 would result in nCompass returning the process value of loop number 3. The third loop of nCompass based on the example, responds to the Modbus address of 5.

For an nCompass system of 1 to 10 loops, simply changing the address of the command allows you to send or receive the same data from the next loop controller. Utilizing a read register command starting at register 127, and reading a total of 13 registers, allows you to retrieve the current operating status of the loop control in a single command. By repeating the command for each loop attached to nCompass, your software can continually monitor the status of all loops attached to nCompass quickly and efficiently.

The design of the serial interface also allows the nCompass MC to connect to FDC'S Envision software. The software is free of charge and provides a host of features and is compatible with all FDC products. Using the P-Series controller selection in EnVision, each loop controller of the nCompass MC can be configured in EnVision as individual P-Series controls. This allows each one to be independently monitored and controlled over the nCompass interface.

### 5.2 Automatic Program Registers

The automatic program parameters are a separate group of registers that are used to download programs to each individual loop controller attached to nCompass. The manner in which the program steps are configured and sent to nCompass is specific and must be followed exactly.

Each program consists of a "header step" of 5 registers and up to 64 steps consisting of 10 data registers. The program must be written one step at a time, using a multiple write command (0x10) to write all the data for each step at once. This allows programs to be stored as two-dimensional arrays, of which code can be written to simply index through the array step-by-step, and transmit the program file to nCompass.

# The first 5 registers of the program (header step) contain specific settings related to the program. These include the program number, hold back band, ramp units and dwell units.

	Register	Parameter Description	Data *D	Rar *E	nge	*F
A	ddress		Туре	Low	High	Unit
86	(0x0056)	Program Selected for View	W	9	9	-
87	(0x0057)	Hold Back Band	W	*E1	*E1	-
88	(0x0058)					
89	(0x0059)	Ramp Units	W	*E2	*E2	-
90	(0x005A)	Dwell Units	W	*E3	*E3	-

#### The following 10 registers of the automatic program contain the data for step 1 of the program.

	egister	Parameter Description	Data D	Ra *E	nge	*F
A	ddress		Туре	Low	High	Unit
91	(0x005B)	Step Number*	W	0	63	-
92	(0x005C)	Step Type	W	*E4	*E4	-
93	(0x005D)	Ramp Target Setpoint	W	-32768	32767	PV
94	(0x005E)	Ramp Time or Ramp Rate	W	0	5999	-
95	(0x005F)	Event Selections 1, 2, 3	W	*E5	*E5	-
96	(0x0060)	Holdback Type	W	*E6	*E6	-
97	(0x0061)	Dwell Time	W	0	9999	-
98	(0x0062)	Jump Step	W	0	63	-
99	(0x0063)	Jump Cycles	W	1	9999	-
100	(0x0064)	Final Setpoint	W	-32768	32767	PV

\*\*The **Step Number** must be offset by 1 when writing step data to nCompass. Steps 1-64 will be sent with the step number as a value of 0-63 (-1 offset).

All remaining steps of the program follow the same format and data structure as is represented for step one above. Each step of the program is downloaded to the same registers (91 to 100). The step number in register 91 designates the step that the data is for on each write command. Since few if any programs will contain the maximum of 64 steps, it is only necessary to write the step data for the number steps used in the automatic program.



#### Notes:

#### \*D W Specifies write only data.

\*E1

Parameter Value	Temperature Units °C	Temperature Units °F	Process Units
Range Low	1	1	1
Range High	555	999	999

#### \*E2

Parameter Value	Description
0	Hours and Minutes
1	Minutes and Seconds
2	Units/Minute
3	Units/Hour

#### \*E3

Parameter Value	Description
0	Hours and Minutes
1	Minutes and Seconds

#### \*E4

Parameter Value	Description	
0	Ramp	
1	Soak	
2	Jump	
3	End	

\*E5

Parameter Value	Description	
Bit0	Event 1	
Bit1	Event 2	
Bit2	Event 3	
Bit3 – Bit15	Not Assigned	

**Note:** The last step of an automatic program must be an end step. If the last step is not an end step, the program will not run correctly and/or a program download error will occur and the program will not operate.

**Note:** The available system events are based on the configuration of the specific loop controller that the program is being downloaded to.

#### \*E6

	20		
Parameter Value	Description		
0	Holdback Disabled		
1	Deviation Low Holdback		
2	Deviation High Holdback		
3	Deviation Band Holdback		

\*F The unit PV means that the unit of the parameter is the same as the unit of PV (the loop configuration).

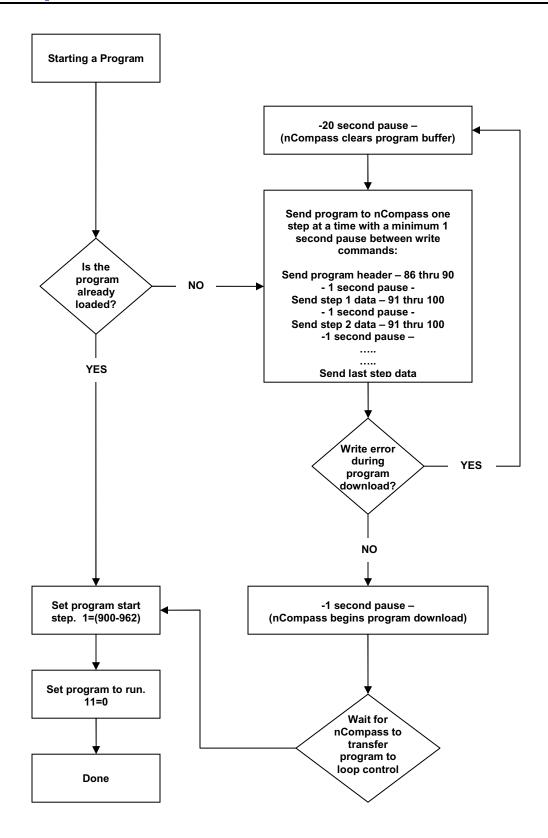
Use caution when loading an automatic program to nCompass. You must insure that the decimal point scaling and units of measurement in the program match the loop setting. Loading a program with a temperature setpoint of 80 will result in a control temperature of 80°F if the nCompass control is in degrees Fahrenheit. However, if the nCompass control is set for degrees Centigrade, it will result in a control temperature of 80°F).

#### 5.2.1 Starting an Automatic Program on nCompass

Automatic programs are sent to nCompass in a step-by-step process. The download sequence must be followed in proper order and must complete without errors to be valid. If a write error is detected during the transfer of a program from a PC to nCompass (no response from nCompass or NACK returned), the program download must be aborted and restarted.

nCompass is automatically placed into program transfer mode when the header step of a program is first sent (registers 86-90). nCompass then begins looking for the following program steps to be sent. Once nCompass sees the end step, it then downloads the program to the loop controller assigned to the Modbus address that the program steps were written to.

If an error occurs during the transfer process from the PC to nCompass, the program transfer process should be stopped at the PC. The data sent to nCompass was either corrupted in transmission or not received properly. It is not possible to resend the failed step because it is not known if any of the previous data was received by nCompass properly. On the transmission error, nCompass will enter a 15 second timeout process. At the end of the timeout period, the buffer will be cleared and the program can be resent. In order to insure that the new download begins properly, induce a 20 second wait period on the host PC after the failed transmission attempt to insure that enough time has elapsed.







# Appendix

# **Terms and Definitions**

**address** – A unique designator for a location of data or a controller that allows each location or controller on a single communications bus to respond to its own message.

**ASCII** (pronounced AS-KEY) – <u>A</u>merican <u>S</u>tandard <u>C</u>ode for <u>Information Interchange</u>. A universal standard for encoding alphanumeric characters into 7 or 8 binary bits.

**Asynchronous** – Communications where characters can be transmitted at an unsynchronized point in time. In other words, it can start and stop anytime. The time between transmitted characters may be of varying lengths. Communication is controlled by "start" and "stop" bits at the beginning and end of each character.

Baud – Unit of signaling speed derived from the number of events per second (i.e., bits per second).

Baud rate – The rate of information transfer in serial communications, measured in bits per second.

Binary – Number based system where only two characters exist, 0 and 1. Counting is 0, 1, 10, 11...

**Bit** – Derived from "<u>Binary digit</u>", a one or zero condition in the binary system.

Byte - A term referring to eight associated bits of information, sometimes called a "character".

**Character** – Letter, numeral, punctuation, control figure or any other symbol contained in a message. Typically this is encoded in one byte.

**Communications** – The use of digital computer messages to link components. (See serial communications and baud rate)

**Converter** – This device will convert from one hardware interface to another such as from EIA-232 to EIA-485. The converter may be transparent to the software, which means you do not have to give any special considerations to software programming.

**CRC** – When data is corrupted during transmission, a method is used to return the data to its correct value. This can be accomplished through several methods: parity, checksum and CRC (cyclic redundancy checksum) are three of these. <u>Cyclic Redundancy Checksum</u> is an error-checking mechanism using a polynomial algorithm based on the content of a message frame at the transmitter and included in a field appended to the frame. At the receiver, it is then compared with the results of the calculation that is performed by the receiver.

**Data** – The information that is transferred across the communications bus. This may be a setpoint, setup parameter, or any character. This information is transferred to an address or register.

**DB-9** – A standardized connector shaped like the letter "D" when viewed on edge. This connector has 9 contacts. It is utilized on most IBM AT compatible PCs as the serial port.

**Decode** – This is the reverse of encode. When a piece of data has information embedded in it, decode is to extract that information. Example: to extract an "A" from 01000001.

**Duplex –** The ability to send and receive data at the same time. "To listen and talk at the same time."

**EIA-232** – <u>E</u>lectronic <u>Industries A</u>ssociation developed this standard hardware interface to allow one device to talk to another device in full duplex mode. This method uses a differential voltage between one wire and ground. Also called an unbalanced system since the ground wire carries the sum of current of all lines. Transmission is limited to about 50 feet.

**EIA-485** – Electronic Industries Association developed this standard hardware interface to allow up to 32 devices to be on a bus at one time. This method uses a differential voltage between two wires. Also called a balanced system since each wire carries the same current value. This has the advantage of being immune to outside electrical disturbances.

**EIA/TIA -232 and -485** – Data communications standards set by the Electronic Industries Association and Telecommunications Industry Association. Formerly referred to as RS- (Recommended Standard). (See EIA-232 and EIA-485)

**Electronic Industries Association (EIA)** – An association in the US that establishes standards for electronics and data communications.

**Encode** – To embed information into a piece of data. This is the reverse of decode. Example: let 01000001 stand for an "A".

**Error Correction** – When an inconsistency is in the data, a method is used to detect and/or return the data to its correct value. This can be done through several methods, parity, checksum and CRC (cyclic redundancy checksum) area three of these.

**Even** – This term is used with parity. See parity.

**Firmware** – Instruction or data stored in an IC (integrated circuit) or on a read only disk. This data is programmed once and cannot easily be changed as software can.

**Full Duplex** – Full is used to mean the duplex's full capability. The ability to send and receive data at the same time. The same as duplex.

#### **GPIB** – See IEEE488

**Half Duplex** – The ability to send or receive data, but not at the same time. "To listen or talk, but not both at the same time."

**Handshake (Handshaking)** – Exchange of predetermined signals between two devices establishing a connection. Using extra wires or software signals to coordinate communications, signals can be sent to tell the transmitter the current status of the other device receiver. Example: Are you busy or are you ready?

**Hex or Hexadecimal** – Number based system where sixteen characters exist, 0 to 9, A to F. Counting is 0..9,A,B,C...

**Integer** – Two bytes make an integer. This contains 16 bits. An integer can represent a decimal value of - 32768 to 32767.

**Logic Level** – A voltage measurement system where only two stable voltage values exist. Example: 0v and 5V, or -3v and +3v.

**Mark** – Represents the transmission of data bit logic 1 (see logic level). Usually this is the most negative voltage value in serial communications.

**Master –** The device on the bus that controls all communications. Only the master can initiate conversation.

**Modbus** – A software protocol developed by Gould Modicon (now AEG) for process control systems. No hardware interface is defined. Modbus is accessed on the master/slave principle, the protocol providing for one master and up to 247 slaves. Only the master can initiate a transaction. This is a half duplex protocol.



Network - When two or more devices share communication lines, the devices are "networked".

**Node** – A point of interconnection to a network.

**Noise Immunity** – The ability of communication lines to ignore electrical noise generated in the lines by nearby magnetic and electrostatic fields.

Odd – This term is used with parity. See parity.

**Parallel** – Communication using this method, transfers eight bits or one byte at a time over eight data wires and one ground wire. This method is eight times faster than using serial but utilizes more hardware.

**Parity** – A bit is assigned at the beginning of a byte to stand for parity. When the '1' bits are counted, the number will be even or odd. A parity bit is used to ensure that the answer is always even if even parity or odd if odd parity. If the receiving end counts the '1' bits and the sum is not the same odd or even, an error is generated. Parity is used to detect errors caused by noise in data transmission.

**Protocol** – A set of rules for communication. This will specify what method to transfer information, packet size, information headers and who should talk when. It is used to coordinate communication activity.

Receive – To accept data sent from another device. The device that receives the data is the receiver.

**Register –** An area of memory that provides temporary storage of digital data.

**Slave** – A device that only responds to commands. This device never starts communication on it's own. Only the Master can do this. (See Master)

**SCADA** – Supervisory Control and Data Acquisition

Serial – To process something in order. First item, second item, etc.

**Serial Communications** – A method of transmitting information between devices by sending all bits serially (see serial) over a single communication channel.

**Software** – Information of data or program stored in an easily changeable format. (RAM, Floppy Disk, Hard Disk)

**Space** – Represents the transmission of a data bit logic 0 (see logic level). Usually this is the most positive voltage value in serial communications.

**Start Bit** – A binary bit or logic level that represents when the serial data information is about to start (at the beginning of a character or byte). This voltage level is positive.

**Stop Bit** – A binary bit or logic level that represents when the serial data information is complete (at the end of a character or byte). This voltage level is negative.

**Synchronous** – When data is transmitted on a data line and a clock signal is used on another line to determine when to check the data line for a logic level. This clock is said to "synchronize" the data. **Transmit** – To send data from one device to another. The device that sends the data is the transmitter.

Word – Two bytes make a word. This contains 16 bits. A word can represent a decimal value of 0 to 65535.





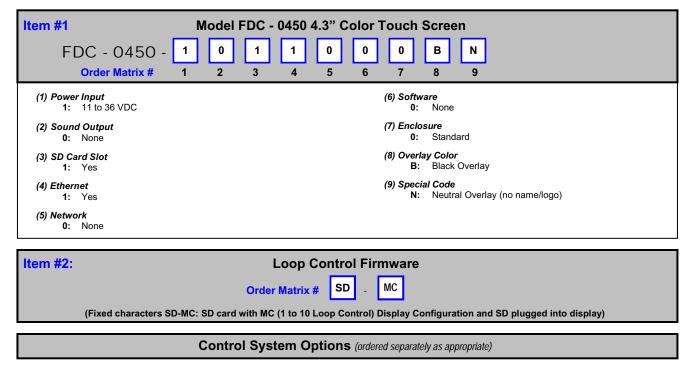
#### How to Order

nCompass MC is ordered as 3 to 12 components dependant upon the number of control loops, one to ten.

#### nCompass MC Sample Part Numbers

(minimum of 3 component part numbers is required; (1) display, (2) firmware, (3) loop #1 )

	<u>ltem #</u> 1	<u>Product</u> Display	<u>Sample Part Number</u> FDC-0450-1011-000BN	<u>Description</u> 4.3" display, MC software, 11-36 VDC power input, SD slot, Ethernet, standard enclosure
	2	MC Firmware	SD-MC	MC Loop Control Firmware (inserted into display SD slot)
	3	Loop #1	B42-4166-11D0	90-250VAC power input, T/C or RTD input, output #1 1A Triac, output #2 1A Triac, output #3 relay, output #4 relay, comms, no display
	12	Loop #10	B42-4366-11D0	90-250VAC power input, mA input, output #1 1A Triac, output #2 1A Triac, output #3 relay, output #4 relay, comms, no display
		<u>Options</u> Power Supply	PS5R-SD24	85-264VAC power input, Output 24VDC 15W (0.6A)
		Cable	CA2011-3D	Cable from display to B42 (DB25 connector (display), twisted pair leads (B42), shielded (10ft)



#### Cable: Display to B42 Control Board

CA2011-8D: Cable (10ft) from Display to B42 controller.

Note: Consult factory for other lengths & options

Display Power Supply (input 100-240VAC / Output 24VDC)DIN Rail Mount:PS5R-SB24:15W power supply (0.6A)Open Frame:PS3X-B24AFC:15W power supply (0.6A)

Note: Either of the above will power the FDC-0450 touch screen display

#### USB Memory Stick

UDF115-2GB: (2GB High Capacity USB Memory Stick (3VDC))

#### System Reset Timer

GE1A-C10MA110/SR2P-06: Reset Timer and socket (DIN Rail)

Note: Timer is recommended for proper system restart due to momentary power interruptions (<500ms) which can erroneous operation.

#### B42 Programming Display Board

 B42 Display Board 300 mm cable:
 3020B42-00300A-00/300

 B42 Display Board 1000mm cable:
 3020B42-00300A-00/1K

#### **Printed Operators Manual**

Part Number (Orion-nCompass MC i4.3 User Manual.pdf) FDC-Orion nCompass MC i4.3 User Manual

#### Printed Configuration Manual

Part Number (Orion-nCompass MC i4.3 Config Manual.pdf) FDC-Orion nCompass MC i4.3 Configuration Manual

#### Printed Communication Reference Manual

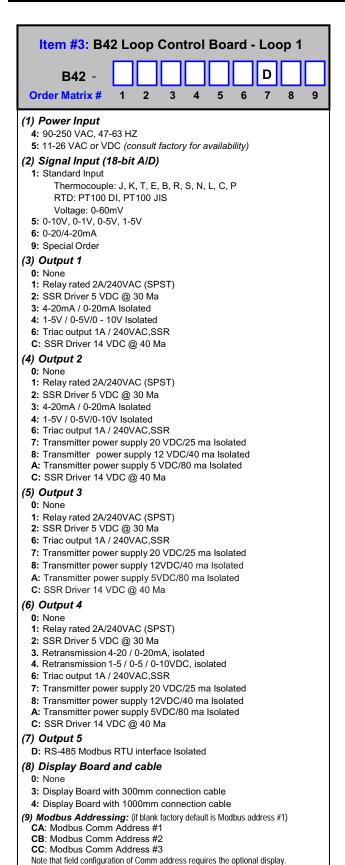
Part Number (Orion-nCompass MC i4.3 Comm Reference.pdf) FDC-Orion nCompass MC i4.3 Communication Reference Manual

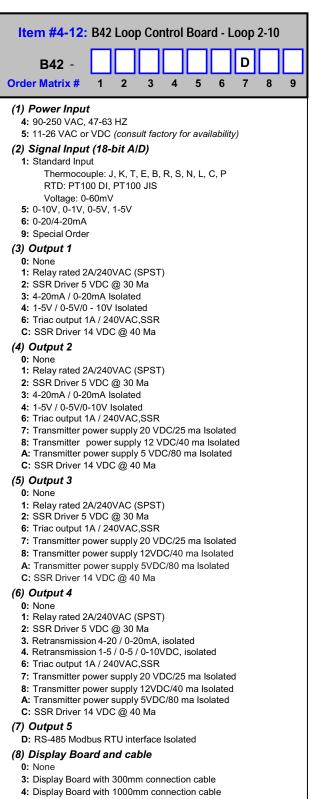
#### **USB Cables & Accessories**

IStick-Panel	USB Panel Mount Adapter
IStick-4X-CVR	USB Panel Mount Adapter-Nema4x

# **FUTURE**DESIGN

# nCompass





(9) Modbus Addressing: (if blank factory default is Modbus address #1)

- CA: Modbus Comm Address #1
- CB: Modbus Comm Address #2
- CC: Modbus Comm Address #3
- Note that field configuration of Comm address requires the optional display.

#### NOTES: SCADA (Supervisory Control & Data Acquisition)

FDC-nCompass Series Graphic User Interface (GUI) touch screen provides a full SCADA feature set providing ease of use with either an icon menu system with finger navigation or traditional Menu bar, data acquisition, alarm manager, operator audit trail, multi-level security with user rights, LAN connections and more.

#### The GUI provides ease of configuration, use & support.

- System Configuration for loop, alarm, input & outputs assignment, Help language selection and more, all without an external device or PC.
- Loop Views: view 1 to 10 controls in single or overall view as well as in a Trend format (trend up to loop controls PV, SP & Percent Out).
- Profile: Virtually unlimited number of profiles with each profile having up to 64 steps with up to 3 events.
- File Management: View, print, copy/move Profile, Alarm, Historical Data (data log files) and operator audit trial files. File transfer via LAN features or USB flash memory.
- LAN: Remote Access & touch screen operation (VNC), email/SMS on alarm, email historical, alarm & audit trail files on-demand, Web Page (view only) and FTP of alarm, audit and historical data files automatically or on-demand.

#### Data Acquisition:

- Data log PV, SP and PID Percent output for 1 to10 loop controls.
- Log interval: configurable 6 seconds to 31 minutes with configurable number of days to auto start & name next file (1 to 31 days).
- File Start/Stop: Configurable; operator on-demand, on system boot, profile ramp-soak start/end or digital inputs
- File Interval: Once started a data log file is configurable to auto end and start new file with the same name as previous file with an appended time/date name. Configurable time interval is from 1 to 31 days.
- File name: Operator entered file name, batch & lot number or if running a profile, file name same as profile name. (all file names appended with date-time to file name)
- Operator Comments/Events: Unlimited operator comments/events linked to each file entered manually or via Bar Code Scanner. - Digital Signatures: full support for user based digital signatures for
- each data file (data encryption).
- Historical Data File: View & print the data directly from the display (auto scale on X & Y axis with each channel selectable for right or left axis values), from a PC after data is copied/moved via LAN (FTP or email) or USB Flash Memory card provided.

#### NOTES: B42 Control Board (1 to 10 may be used)

- B42 Loop Control: http://www.futuredesigncontrols.com/B42.HTM - Analog Input: 200ms scan rate with 18-bit A/D resolution providing high
- accuracy and PID control performance - Outputs: Maximum of 4 control or auxiliary outputs
- PID Control Outputs: up to 2 with Auto Tune capability (heat-cool)
- Relay, SSR driver 5V, SSR driver 14V, 1A SSR, mA\* or VDC\* - Alarm or Event: up to 3 outputs (outputs #2, #3 & #4)
- Relay, SSR driver 5V, SSR driver 14V or 1A SSR
- Auxiliary Outputs: up to 2 outputs, one or two Transmitter power supplies and/or one Retransmission\* output \*Note: Analog mA and VDC PID and retransmit outputs are 15-bit, contributing to high performance PID control and highly accurate retransmitted PV or SP values.
- Alarm Types: Process, Deviation and Deviation Band, all High or Low
- PID Parameters: Two sets of PID values may be configured and
- selected by Profile Step and/or by digital event input. (see next) - Event Inputs: Configurable for one of the following functions.
  - Profile Run (momentary)
  - Profile Hold (closed profile hold, open profile run)
  - Profile Run/Hold (closed profile run, open profile hold)
  - Profile Abort (momentary)
  - Segment Advance (momentary)
  - Manual Mode (closed manual mode, open normal control)
  - Failure Transfer: (closed failure transfer, open normal control) (transfer active %Out is a pre-configured value for each output)

#### **NOTES:** Profile Ramp / Soak Specifications

#### Profile Ramp/Soak:

- Profiles: Virtually an unlimited number of profiles in system memory - Profile Type: System configurable for separate profiles per loop or
- common profile with slave set point operation for other loops. - Profile Start: via touch screen or via Event input (see above).
- Profile Name: Free form 16 character (max) naming convention with auto append time/date stamp
- **Global Profile Configuration** 
  - Profile Time Base: Time Ramp (hh:mm or mm:ss)
- Ramp Rate (degrees/minute or hour) - Starting and Ending SP values (may be individually active or not).
- Guaranteed Soak & Ramp (GS):
  - Range 1 to 999F (555C), configurable per step as
- low, high, band or inactive (disable).
- Power Fail / Recovery when Profile is active:
  - Continue from last SP value.
  - Continue from last PV value.
  - Static mode (abort profile) with last static SP entered.
- Profile Segments: (maximum of 64)
  - Jump To Segment: configurable by segment (also nested looping) - Events: up to 3 events\* per segment.
  - Guaranteed Soak & Ramp: Configurable active or not by segment.
  - \* Note: The number of events available is predicated on the control board hardware, number of control boards and configuration.
- Event Input: (profile specific; refer to B42 Control Board for all options) - Configurable functions below (mutually exclusive to each other)

  - Profile Run (momentary)
  - Profile Hold (closed profile hold, open profile run) - Profile Run/Hold (closed profile run, open profile hold)
  - Step Advance (momentary)
  - Profile Abort (momentary)

Although the Event Input provides a manner to control Profile functions remotely, typically Profile actions would have the Operator using the intuitive easy to use 4.3" color touch screen (operator interface).

#### **NOTES:** Power Supply – Optional

P.O. Box 1196

888.751.5444 Sales

Power Capacity Required (Watts) - A base system requires ~10 watts of 24Vdc power for the nCompass user interface. The 15W power supplies offered are sufficient to power display only.

Note: The power supply should be used to power nCompass components only and not OEM or other end user components or devices



Bridgeview, IL 60455

888.307.8014 Fax

# **FUTURE**DESIGN

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