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8 QzROM

M38234G4FP Receiving Unit

for M34559 evaluation board



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Receiving Unit User's Manual for M34559 Evaluation Board

(M38234G4FP)

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PREFACE

About this manual

This user's manual is written for Renesas QzROM of M38234G4FP receiving unit. Please use this user's manual to understanding on how to operate the receiving unit and also to self-train on how to design M38234G4FP software for LCD front panel with IR receiver function.

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| | Gives an introduction to the configuration of this receiving unit |
| Section 2 | M38234G4FP Receiving Unit Features |
| | Highlights the receiving unit features, specifications and standard circuitry. |
| Section 3 | Quick Overview of How to Use the Board |
| | Gives an overview on how to operate the receiving unit, how to connect the |
| | compact emulator and how to emulator debugger |
| Section 4 | Software Information |
| | Gives a brief description on LCD function and receiving data format |
| Section 5 | Software Overview |
| | Brief introducing demo software three parts function |
| Section 6 | Sample Software flowchart |
| | Gives the detail sample software flowchart |
| Appendix / | ABC |

Some useful information



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Section 1. Overview

The M38234G4FP receiving unit is designed to illustrate the built-in LCD driver MCU of M38234G4FP and M34559 evaluation board transmitting function, the receiving unit package including M38234G4FP receiving board, 12V power supply and user's manual.

This receiving unit can be used for demonstration of the features of M38234G4FP LCD MCU for QzROM promotion to white goods customers, In addition, it can be used by software engineers for self-training on 8-bit software programming

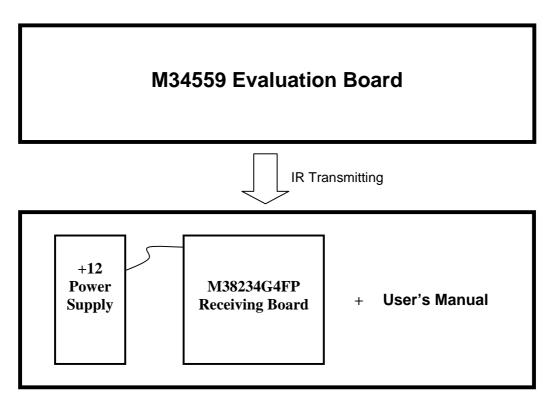


Figure 1.1 M38234G4FP Receiving unit package



Section 2. M38234G4FP Receiving Unit Features

2.1 Key Features

The receiving unit can be used as a demo board or training board

1. Demo board

If the receiving unit assemble QzROM of M38234G4FP with demo software, it can realize following functions:

- LCD display
- IR receiving
- Circumstance temperature display
- Key input
- LED output
- Music buzzer output
- 2. Training board

If the receiving unit connect emulator MCU of M3823AT-RLFS and compact emulator of M38000T2-CPE, it can allow the user to have hand-on experience on 8-bit software programming and software debugging.

The block diagram of the connection of the receiving unit when used as training board is shown in Figure 2.1



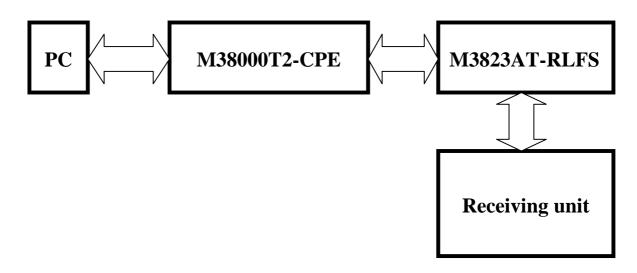


Figure 2.1 Block diagram of the connection of the receiving unit

2.2 Standard Circuitry

The following standard circuitries are included on M38234G4FP receiving unit

- LCD power supply circuit
- IR receiving circuit
- AD converter circuit
- LED output circuit
- Music buzzer circuit
- Reset circuit
- Power supply circuit



2.3 Specification

| Item | Specifications |
|--------------------|--|
| Support MCU | QzROM of M38234G4FP |
| Emulator | M38000T2-CPE Compact emulator |
| Emulator MCU | M3823AT-RLFS |
| Interface Software | M3T-SAR74 |
| | M3T-PD38M |
| | M3T-TM |
| Clock source | 8.0M ceramic oscillator |
| Power supply | + 12 power supply |
| Host PC | IBM PC / At Compatibles |
| | (Windows XP, Windows 2000, Windows Me) |

Table 2.1 Receiving Unit Specification



Section 3. Quick Overview of How to Use the Board

3.1 Demo Board

Connect with +12 power supply and buzzer output one power on music, receiving unit is working

Four Keys Operation:

• K1 Key

- 1. Press this key in power off state, receiving unit enter power on state and LCD display temperature, buzzer output one power on music.
- 2. Press this key in power on state, receiving unit enter power off state, LCD only display "Renesas", buzzer output one power off music and other key are useless
- **K2 Key** Press this key can right rotate LEDs
- K3 Key Press this key can left rotate LEDs
- K4 Key Press this key can flash LEDs

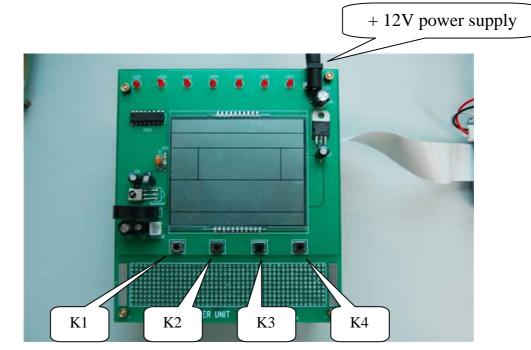


Figure 3.1 Receiving unit operation



3.2 Training Board – Using M38000T2-CPE compact emulator (sample software)

3.2.1 System configuration

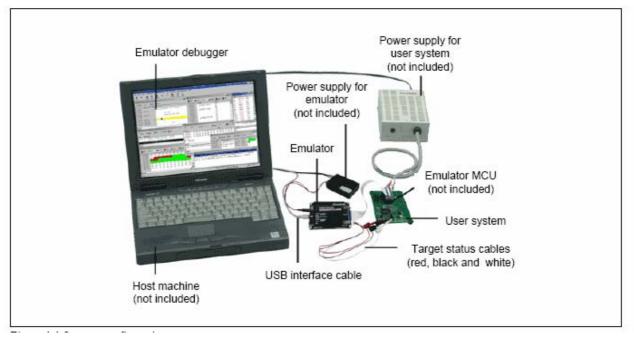


Figure 3.2 System configuration

(1) Compact emulator M38000T2-CPE

This is a compact emulator for the 740 Family with the real-time trace functions

(2) USB interface cable (included)

This is an interface cable for the host machine and the emulator.

(3) Target status cables (included)

This is a cable for checking the status of Vcc and Vss and controlling the RESET signal.

(4) Power supply for emulator

This is a power supply for the emulator. Supply 5.0 V \pm 5% (DC).



(5) Emulator MCU

This is an MCU dedicated for the emulator. Prepare the emulator MCU according to your MCU (M3823AT-RLFS).

(6) User system

This is your application system. This emulator cannot be used without the user system. If you have no user system, you can use a temporary target board (separately available).

(7) Power supply for User system

This is a power supply for the user system. As this emulator cannot supply the power to the user system, supply the power to the user system separately from the emulator.

(8) Host machine

This is a personal computer for controlling the emulator.

(9) Pitch converter board such for connecting user system

This is a pitch converter board for connecting to an MCU foot pattern on the user system. To connect the emulator MCU and user system, a converter board according to the package is required.

3.2.2 M38000T2-CPE

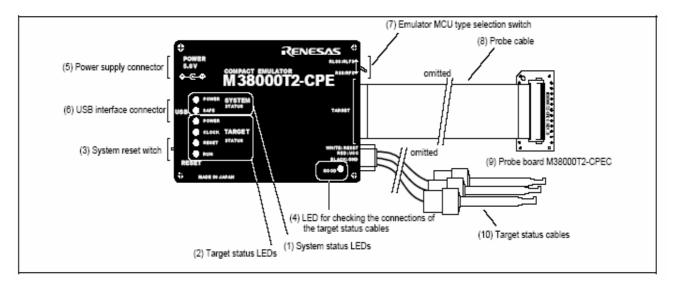


Figure 3.3 Names of the LEDs on the upper panel of the M38000T2-CPE



(1) System Status LEDs

The system status LEDs indicate the emulator main unit's operating status etc.

Table 3.1 lists the definition of the system status LEDs.

| Name | Number | Color | Status | Meaning | |
|-------|--------|--------|--------|---|--|
| POWER | LED1 | Orange | ON | ON Power is supplied to the emulator. | |
| | | | OFF | Power is not supplied to the emulator. | |
| SAFE | LED2 | Green | ON | Emulator system has started normally. | |
| | | | OFF | Emulator system has not started normally. | |

Table 3.1 Definition of the system status LEDs

(2) Target Status LEDs

The target status LEDs indicate the target MCU's power supply and operating status. Table 3.2 lists the definition of each target status LED.

| Name | Number | Color | Status | tatus Meaning | |
|-------|--------|--------|--|---|--|
| POWER | LED3 | Orange | ON | Power is supplied to the emulator MCU. | |
| | | | OFF Power is not supplied to the emulator MCU. | | |
| CLOCK | LED4 | Green | ON | Internal clock ø is output from the emulator MCU. | |
| | | | OFF | Internal clock ø is not output from the emulator MCU. | |
| RESET | LED5 | Red | ON Emulator MCU is being reset. | | |
| | | | OFF | Emulator MCU is not being reset. | |
| RUN | LED6 | Green | ON | User program is being executed. | |
| | | | OFF | User program is not being executed. | |

Table 3.2 Definition of the target status LEDs

(3) System Reset Switch

By pressing the system reset switch, you can initialize the emulator system.

Table 3.3 shows the functions of the system reset switch depending on the state of the emulator.

| State of Emulator | Function |
|---|--|
| When the user's program is being stopped | Initializes the emulator and waits for a command from the emulator debugger. |
| When the user's program is being executed | Stops the user's program, initializes the emulator, and waits for a command from the emulator debugger. |

Table 3.3 Functions of the system reset switch



(4) LED for Checking the Connections of the Target Status Cables

The LED (LED7) for checking the connections of the target status cables lights only when the Vcc and GND of target status cables are connected properly. When this LED does not light after turning on the emulator and user system, turn off the power immediately and check the connections of the target status cables. However, even under normal operating conditions, when the power supply voltage is less than 3.3 V, the GOOD LED will dim or will not turn on.

| Name | Number | Color | Status | s Meaning | |
|------|--------|-------|--------|--|--|
| GOOD | LED7 | Green | ON | VCC and GND of the target status cable are connected properly. | |
| | | | OFF | VCC and GND of the target status cable are not connected | |
| | | | | properly or power is not supplied. | |

Table 3.4 Definitions of the target status LEDs

(5) Power Connector (J1)

This is a connector for connecting the power supply to this product.

(6) USB Cable Connector (J2)

This is a USB cable connector for connecting the host machine to this product

(7) Emulator MCU type selection switch

This is a switch to select the emulator MCU type

(8) Probe cable

This is a cable to connect the emulator MCU. The probe cable is flexible. However, excessive flexing or force may break the cable.

(9) Probe board

This is a board to connect the emulator MCU. Connect this probe board to the upper terminal of the emulator MCU.

(10) Target status cables

These are cables to monitor the VCC, GND and RESET of the user system. Connect these cables to the user system.



3.2.3 Usage (How to Use the Emulator Debugger)

(I) Setup the debugger (PD38M)

| Init | × |
|---------------------------------------|-----------|
| MCU Debug Information Download Resume | |
| MCU: M38000.MCU | Refer |
| Serial No: 11-M38000-3G1001 ► | |
| OK Cancel Help | Next Hide |

Figure 3.4 Init Dialog Box for PD38M

The Init dialog box is provided for setting the items that need to be set when the debugger starts up. The contents set from this dialog box are also effective the next time the debugger starts. The data set in this dialog remains effective for the next start.

(1) Specifying the MCU file



Click the "Refer" button, the file selection dialog is opened, specify the corresponding MCU file, an MCU file is saved under the directory in which PD38M is installed.



(2) Setting of the Communication Interface

USB interface is used for communication with a host computer. It is compliant with USB 1.1.

| Serial No.: | 1-M38000-3G1001 |
|-------------|-----------------|
| | |

(3) Debug Information Tab

The specified content becomes effective when the next being down-load.

| | C On Memory | On Demand | |
|-----------|-------------|-----------|--|
| Temp Dir: | E:¥TEMP | | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |

| On Memory | Allows high-speed process because of use of memory. |
|-----------|---|
| On Demand | Minimizes use of memory. |

Table 3.5 Debug information tab

(4) Download Tab

The specified content becomes effective when the next being start.

| Reset After D | |
|---------------|-----------------|
| Reset Disable | Not Reset. |
| Reset Enable | Reset.(Default) |

Table 3.6 Download tab



(5) Resume Tab

The specified content becomes effective when the next being start.

| Init File: | | Refer |
|------------|--------------|-------|
| | 🔽 Resume | |
| | AutoDownLoad | |
| | | |

• To restore the window status (window position, window size) after the previous debugger program is terminated, check the "Resume" check box. (Resume is ON by default.)



• To re-download a load module (target program), check the "AutoDownLoad" check box. (Redownload is OFF by default.)

🔲 AutoDownLoad

(II) PD38M Window

The PD38M Window is the main window for PD38M. This window displays the main commands on a toolbar. You can click on the buttons on this toolbar to run the target program in normal or one-step mode. The main display area accommodates windows such as the Target Program Window.



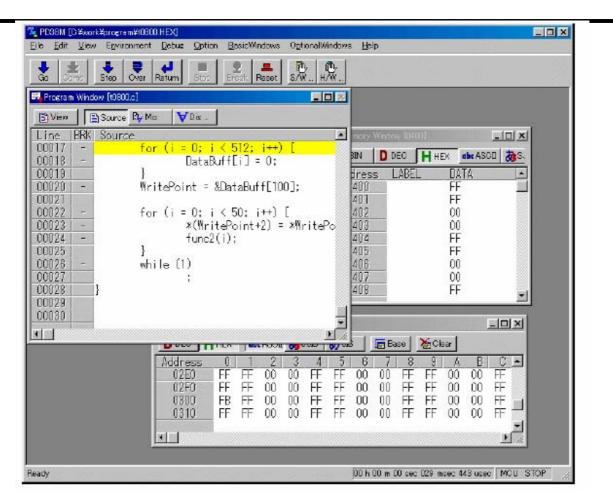


Figure 3.5 PD38M Window

The main commands, such as execution/stop of the target program and step execution, are allocated to the tool bar.

The Option menu is dependent on the active window. When the active window is changed, the Option menu is automatically changed.

The status bar at the bottom of the PD38M window shows the following information:

Explanation/display of menus and buttons

Execution time required from start to end of the target program execution

Execution state of the target program (during execution or execution stopped)



(1) Overview of Source Window

| Source Window | v [t0800.c] |
|---------------|------------------------------|
| View | Source 📴 Mix 💙 Dis |
| Line BRK | Source |
| 00013 | main () { |
| 00014 | char a; |
| 00015 | int i; |
| 00016 | |
| 00017 - | for (i = 0; i < 512; i++) { |
| 00018 - | DataBuff[i] = 0; |
| 00019 | } |
| 00020 - | WritePoint = &DataBuff[100]; |
| | • |
| • | |

Figure 3.6 Source window

(2) Overview of Register Window

| 🕱 Regis | ster Windo | <u> ×</u> |
|---------|------------|-----------|
| Name | Value | Radix |
| PC | 8000 | Hex |
| A | FF | Hex |
| Х | FF | Hex |
| ΙY | FF | Hex |
| Ś | 0FF | Hex |
| | | |
| NV | TBD | IZC |
| 00 | 0 0 0 | 100 |

Figure 3.7 Register window



(3) Overview of RAM monitor Window

| Refresh F | late | | | | Too | Ibar | | | | | | | | | | | | | | |
|------------|----------|--------|--------------|---------|------|-------|------|-------|-----|--------------|-----|----|----|----|----|-----|---------------------|-----|----------|----------|
| 🐝 RAM Moni | lor Wind | 6m [[] | 040 <u>]</u> | l. C | | | | | £ | | | 83 | | | | | | | <u> </u> | |
| | HEX | db | ASCI | i 👌 S | JIS | ar 🖌 | s | 🕞 Ba | se | X ick | ar | | | | | | | | | |
| [102ms | _ | | 2 | 3 | - 4 | - 5 | - 6 | 7 | - 8 | 9 | A | В | C | D | Ε | F | ASCII | | | - |
| 0040 | 95 | -B3 | 80 | 4E | 40 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | . <mark>N0</mark> . | | | |
| 0050 | 95 | 00 | 88 | 00 | 95 | 00 | 95 | 00 | 99 | 00 | 95 | 00 | 95 | 95 | 88 | 95 | | | | |
| 0060 | 95 | 95 | 85 | 0F | -00 | -80 | -00 | 85 | 95 | 85 | 95 | 85 | 95 | 95 | 85 | 99 | <mark></mark> | | | |
| 0070 | 95 | 00 | 95 | 00 | 95 | 00 | 99 | -00 | 95 | 00 | 95 | 00 | 79 | 95 | 95 | -99 | | | y | |
| 0080 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | | | | |
| 0090 | 95 | 00 | 95 | 00 | 99 | 15 | 95 | 00 | 95 | 00 | 99 | 00 | 95 | 00 | 95 | -00 | | | | |
| 00A0 | 14 | 0B | -83 | E0 | .94 | - 16 | 80 | 95 | 88 | 95 | 95 | 95 | 95 | 95 | 95 | 95 | | | | |
| 00B0 | 95 | 00 | 99 | -00 | 95 | 00 | FF | 05 | 99 | 00 | 95 | 00 | 95 | 00 | 99 | -00 | | | | |
| 0000 | 99 | 00 | 95 | 00 | 95 | 95 | 99 | 95 | 95 | 99 | 95 | 95 | 99 | 95 | 95 | -99 | | | | |
| 00D0 | 95 | 00 | 88 | 15 | 95 | -00 | 95 | 5F | 99 | 00 | 95 | 00 | 95 | 00 | 88 | -00 | | | | |
| 00E0 | 95 | 00 | 95 | 00 | 99 | 95 | 95 | 99 | 95 | 95 | 99 | 95 | 7C | 99 | 95 | 95 | | | | |
| 00F0 | 95 | 00 | 95 | 00 | 99 | 95 | 95 | 99 | 95 | 95 | 99 | 95 | 7C | 04 | 00 | -80 | | | | |
| 0100 | 01 | 91 | 6A | 01 | 91 | 6A | 01 | 91 | 6Å | 01 | 91 | 6A | 01 | 95 | 6A | 01 | jj | | .j.j. | |
| <u></u> | | - | | - | | | | | | | - | | | | | | | - | | <u> </u> |
| Addres | s | | W | rite | (Rec | l) Re | ad(0 | ìreer | 0 | | Dat | a | | | | | ASC11 | Cod | e | |

Figure 3.8 RAM monitor window



Section 4. Software Information

4.1 LCD function

The 3823 Group has the build-in Liquid Crystal Display (LCD) drive control circuit. When the LCD enable bit is set to "1" after data is set in the LCD mode register, the segment output enable register and the LCD display RAM, the LCD drive control circuit starts reading the display data automatically, performs the bias control and the duty ratio control, and displays the data on the LCD panel.

A maximum of 32 segment output pins and 4 common output pins can be used, up to 128 pixels can be controller for LCD display. In this sample software, 1/4 duty and 1/3 bias are selected for displaying data on the LCD, 4 common signal output pins and 17 segments signal output pins are used to drive the LCD.

The LCDCK timing frequency (LCD drive timing) is generated internally and the frame frequency can be determined with the following equation;

f(LCDCK) = (frequency of count source for LCDCK) (divider division ratio for LCD)

Frame frequency = <u>f(LCDCK)</u> (duty ratio)

To the LCD power input pins (VL1–VL3), apply the voltage shown in Table 4.1 according to the bias value. Select a bias value by the bias control bit (bit 2 of the LCD mode register).

| Bias value | Voltage value | | | | | |
|------------|------------------|--|--|--|--|--|
| | VL3=VLCD | | | | | |
| 1/3 bias | VL2=2/3 VLCD | | | | | |
| | VL1=1/3 VLCD | | | | | |
| 4/21: | VL3=VLCD | | | | | |
| 1/2 bias | VL2=VL1=1/2 VLCD | | | | | |

Table 4.1 Bias control and applied voltage to VL1–VL3

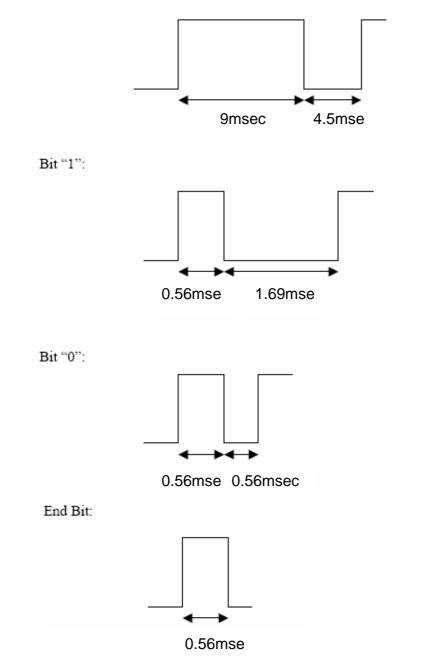
Refer to the 3823 Group datasheet for more information on LCD Function.



4.2 Receiving Format

The receiving infra-red signal format for the receiving program is described as follows:

Header/Separator:



- 29 -



The receiving data consists of 9 bytes. Each data as following:

User code (A) + Function code (B+C+D+E+F+G+H) +Checksum (I)

IR definition:

| А | User code : 1byte 10001000 (88) |
|---|-------------------------------------|
| | D3 D2 D1 D0 : setting temperature |
| | 0 0 0 0 |
| В | 0 0 0 1 |
| | |
| | 1 1 1 132 |
| | D5 D4 fan speed |
| | 1 1high |
| | 1 0middle |
| | 0 1low |
| | 0 0auto |
| | D2 D1 D0: function |
| С | 0 0 0auto |
| | 0 0 1cold |
| | 0 1 0dry |
| | 0 1 1heat |
| | 1 0 0fan |
| | D4 D3 D2 D1 D0: real time hour |
| | 0 0 0 0 000 hour |
| D | 0 0 0 101 hour |
| | 1 0 1 1 123 hour |
| | D5 D4 D3 D2 D1 D0: real time minute |
| | 0 0 0 0 0 00 minute |
| Е | 0 0 0 0 11minute |
| | |
| | 1 1 1 0 1 159minute |
| | |
| | D4 D3 D2 D1 D0: timer hour |
| | |



| | 0 0 0 0 000 hour |
|---|--|
| F | 0 0 0 101 hour |
| | |
| | 1 0 1 1 123 hour |
| | |
| | D5 D4 D3 D2 D1 D0: timer minute |
| | 0 0 0 0 0 00 minute |
| G | 0 0 0 0 0 11 minute |
| 9 | |
| | |
| | 1 1 1 0 1 159 minute |
| | |
| | D1 D0: timer status |
| | 0 0no timer on/off |
| | 0 1timer off |
| | 1 0timer on |
| | |
| | D2: power on/off |
| | 1power on |
| н | 0power off |
| | |
| | D3: sleep |
| | 1sleep |
| | 0no sleep |
| | |
| | D4 : swing |
| | 1swing |
| | 0 no swing |
| | |
| 1 | checksum: A+B+C+D+E+F+G+H low 8 bit (ignore carry) |
| | |



Section 5. Software Overview

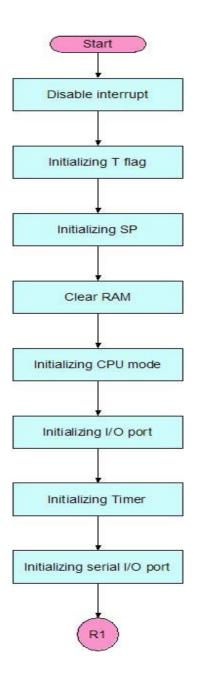
This software including three parts:

One: receive.a74 (main part) Two: m3823sfr.inc (M38234G4FP special function register definition) Three: ram.inc (program ram definition)

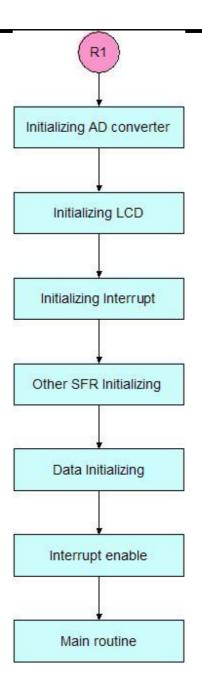


Section 6. Sample software flowchart

6.1 Initialization

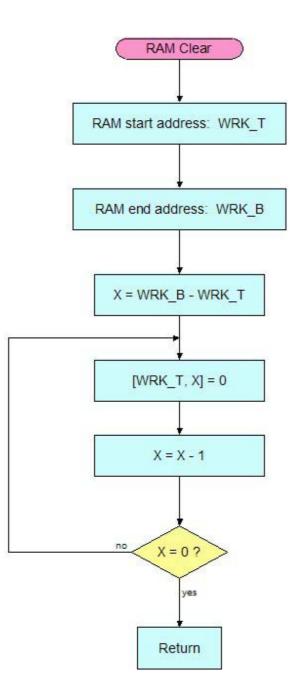






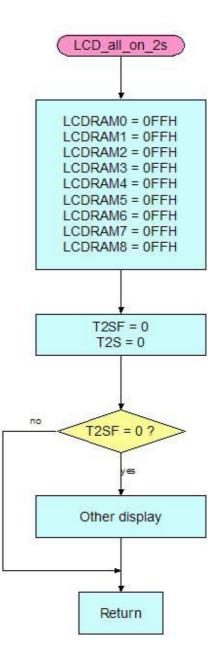


6.2 RAM clear



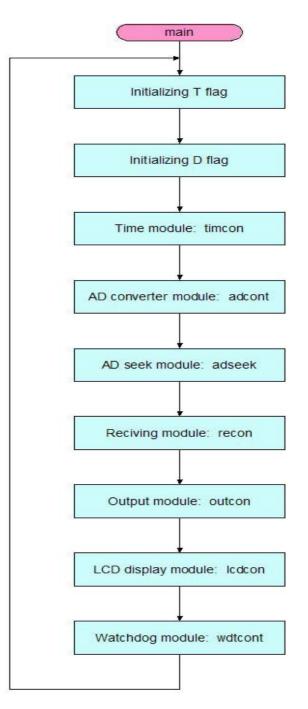


6.3 All LCD On 2S





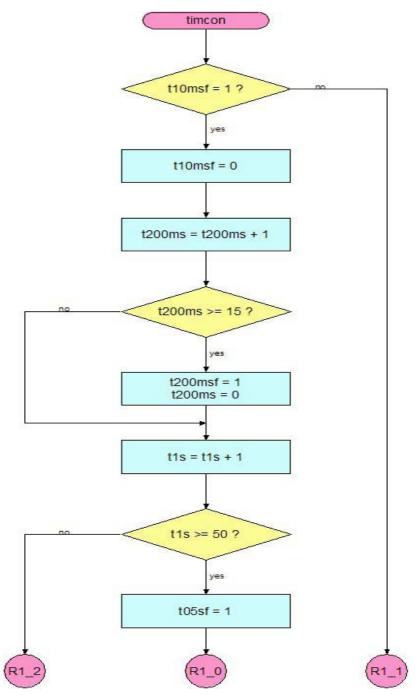
6.4 Main routine



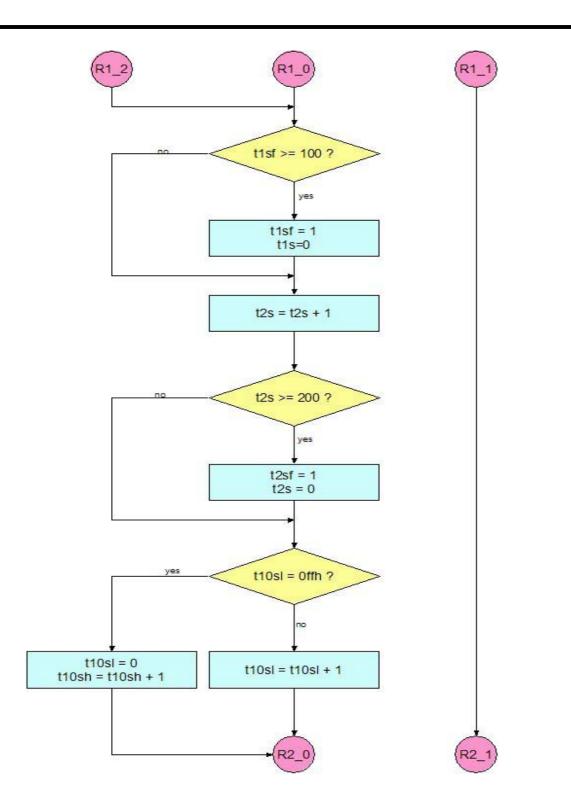
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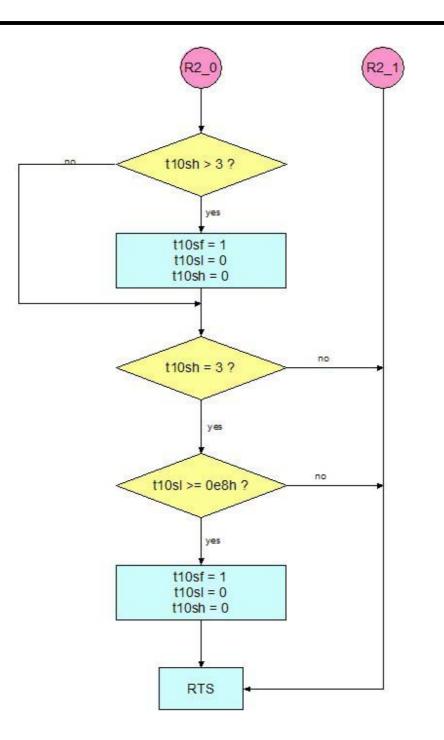
6.5 Timer control subroutine





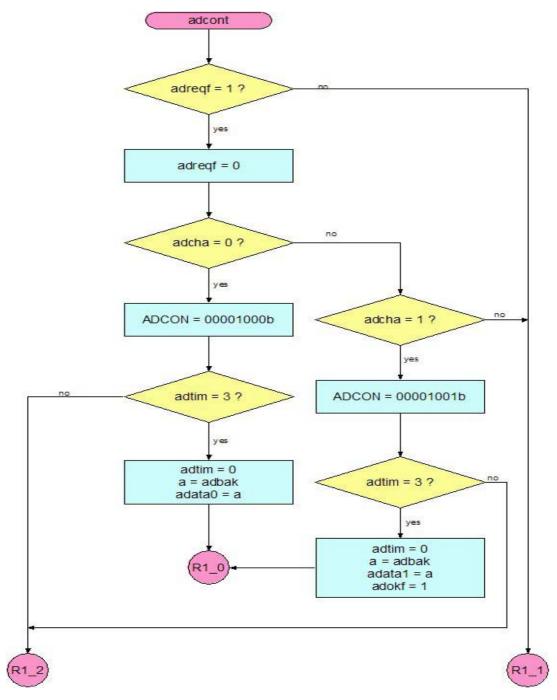




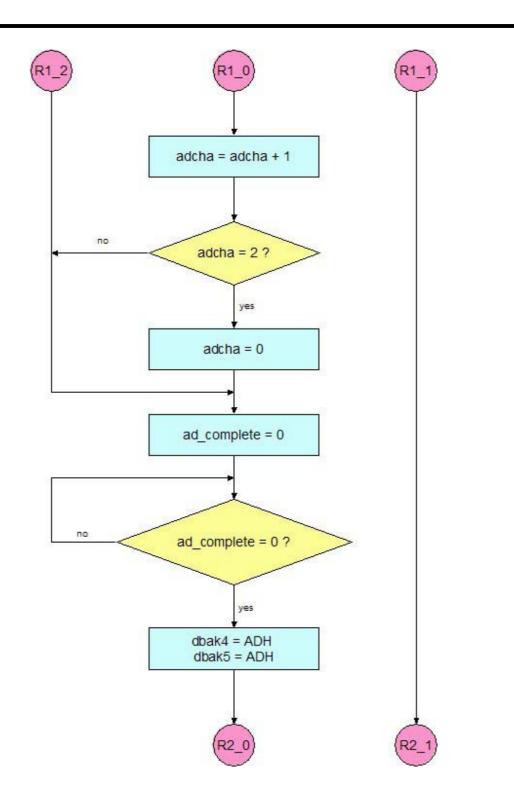




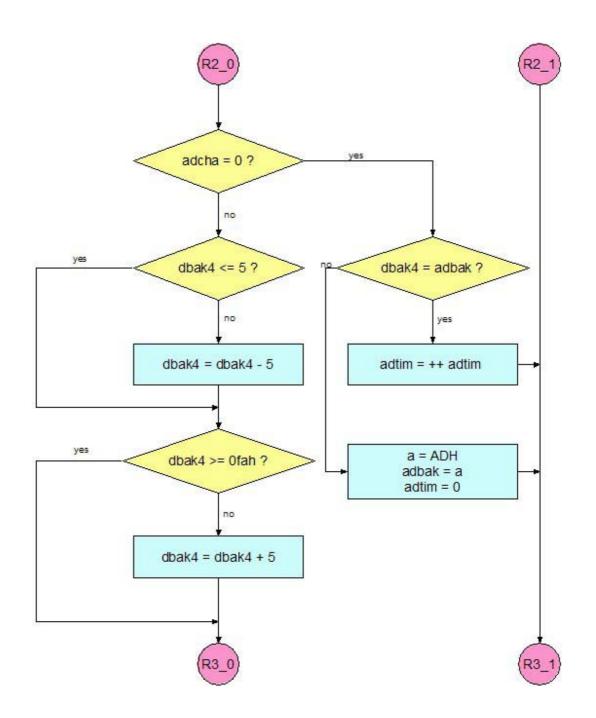
6.6 AD converter control subroutine



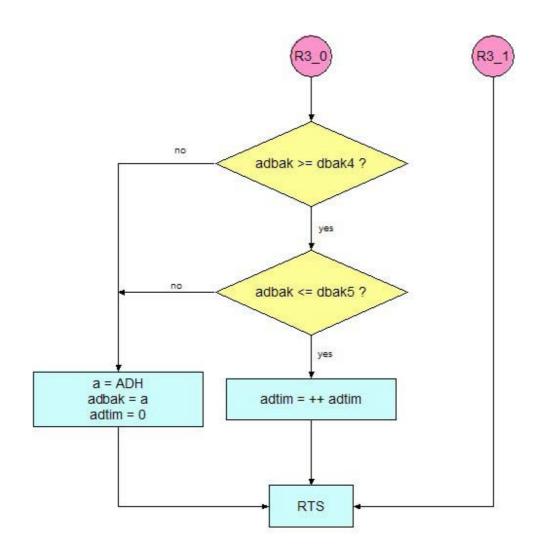






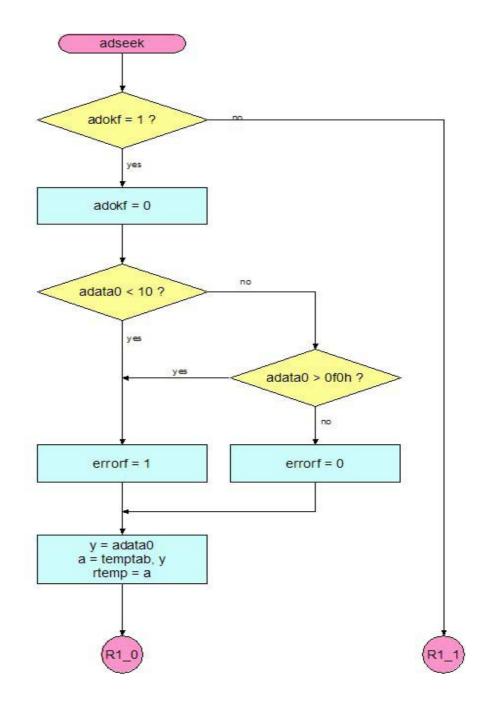




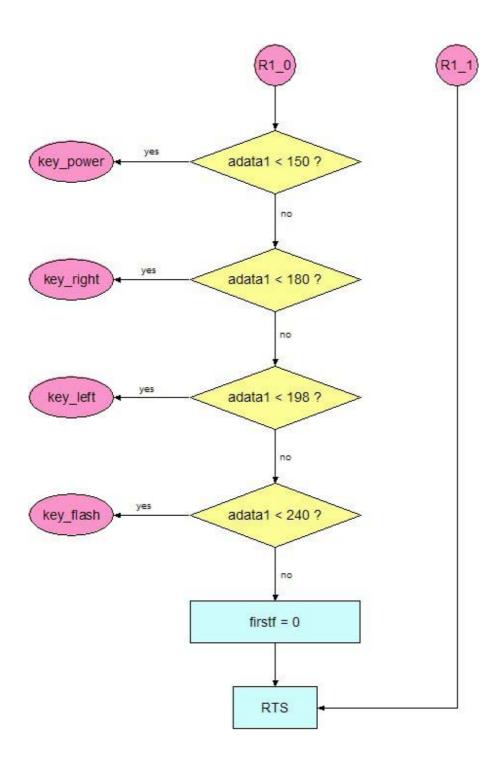




6.7 AD seek table subroutine

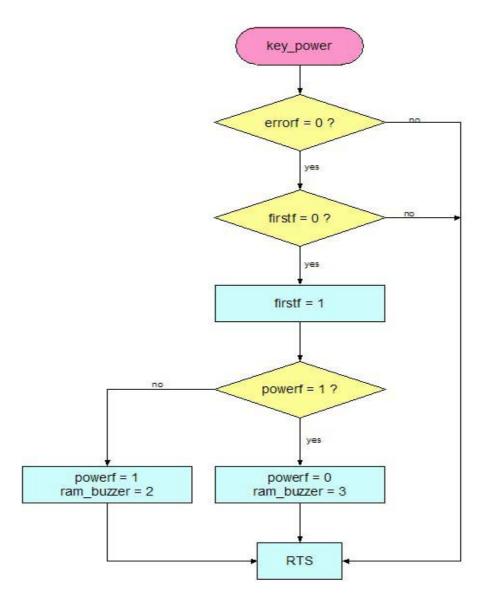






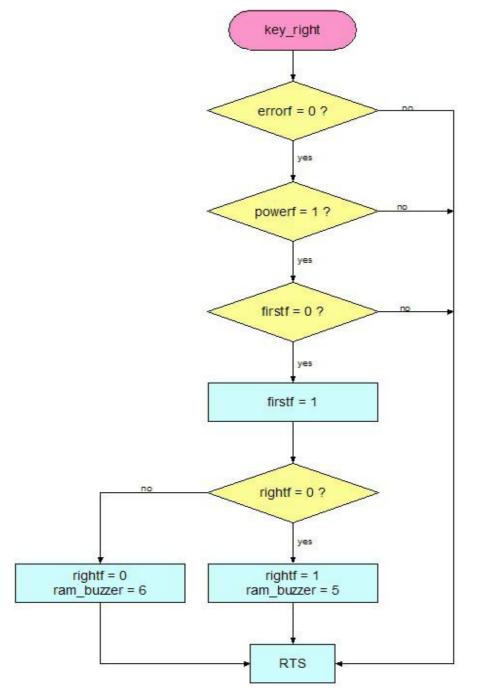


6.7.1 Power key subroutine



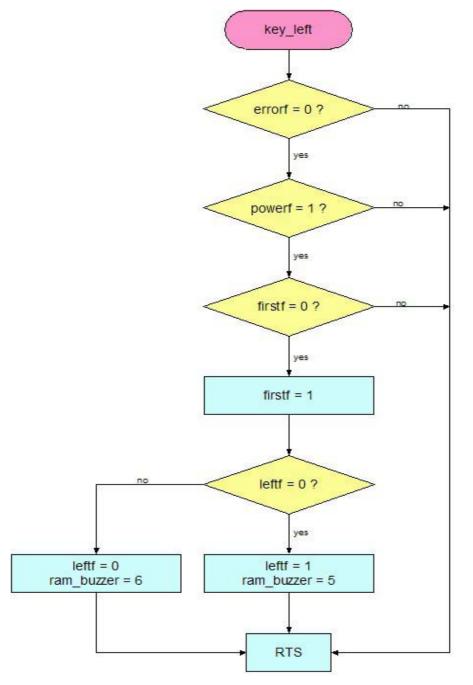


6.7.2 Right key subroutine



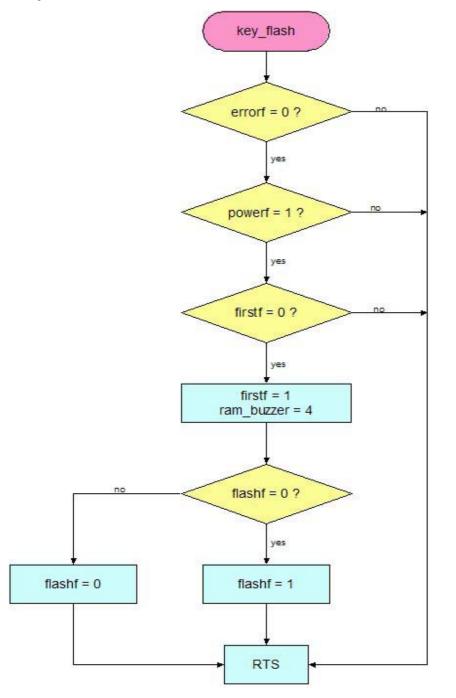


6.7.3 Left key subroutine



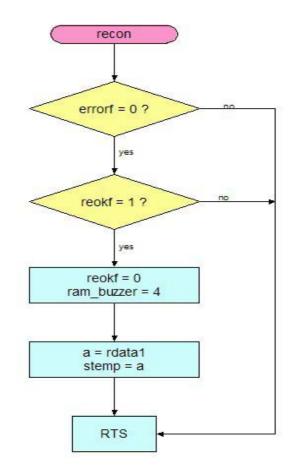


6.7.4 Flash key subroutine



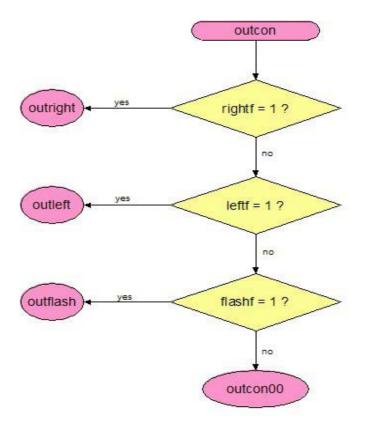


6.8 Receiving control subroutine



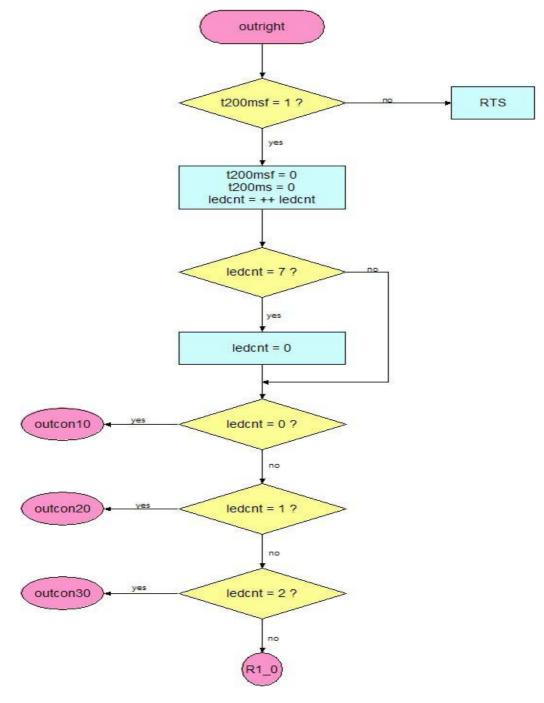


6.9 Output control subroutine

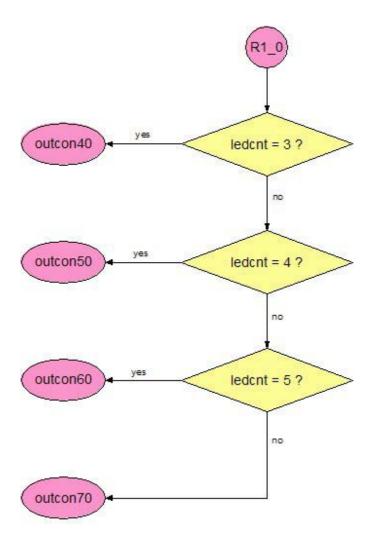




6.9.1 Right output control subroutine

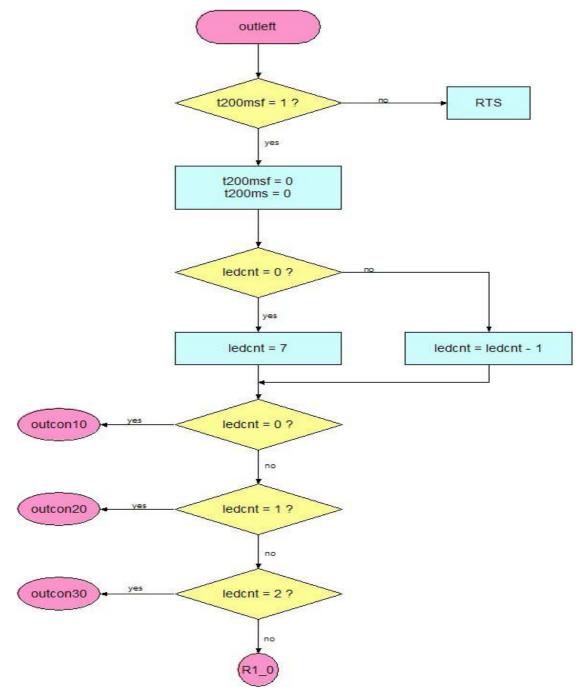




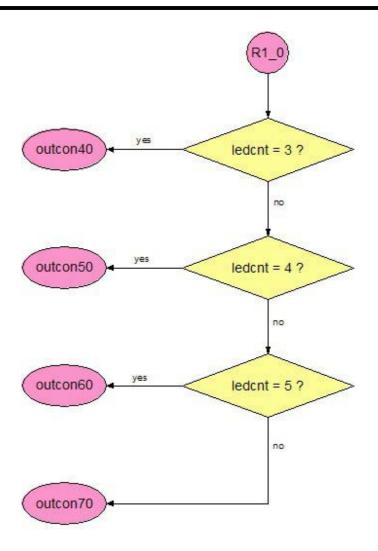




6.9.2 Left output control subroutine

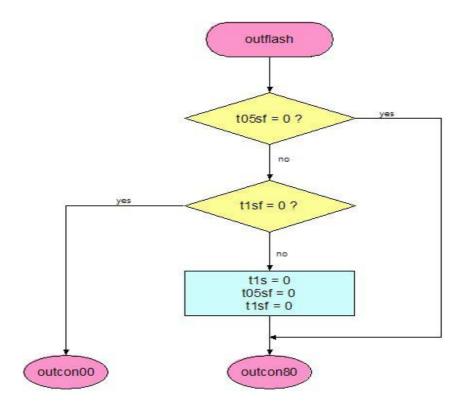






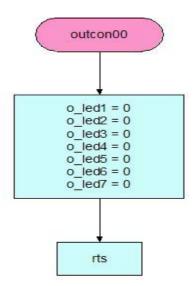


6.9.3 Flash output control subroutine



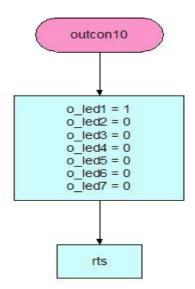


6.9.4 LEDs control subroutine (1)



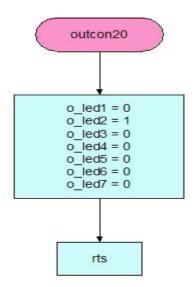


6.9.5 LEDs control subroutine (2)



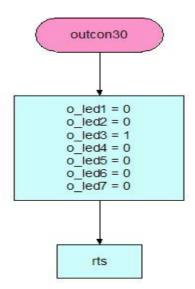


6.9.6 LEDs control subroutine (3)



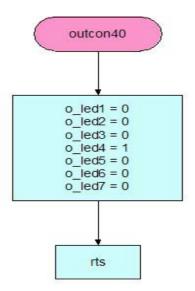


6.9.7 LEDs control subroutine (4)



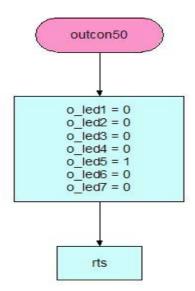


6.9.8 LEDs control subroutine (5)



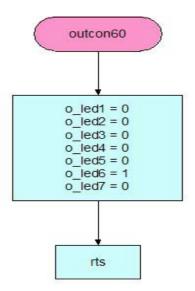


6.9.9 LEDs control subroutine (6)



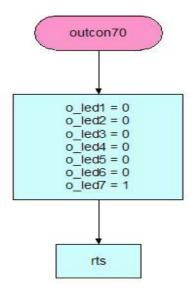


6.9.10 LEDs control subroutine (7)



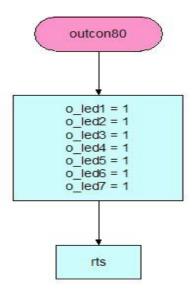


6.9.11 LEDs control subroutine (8)



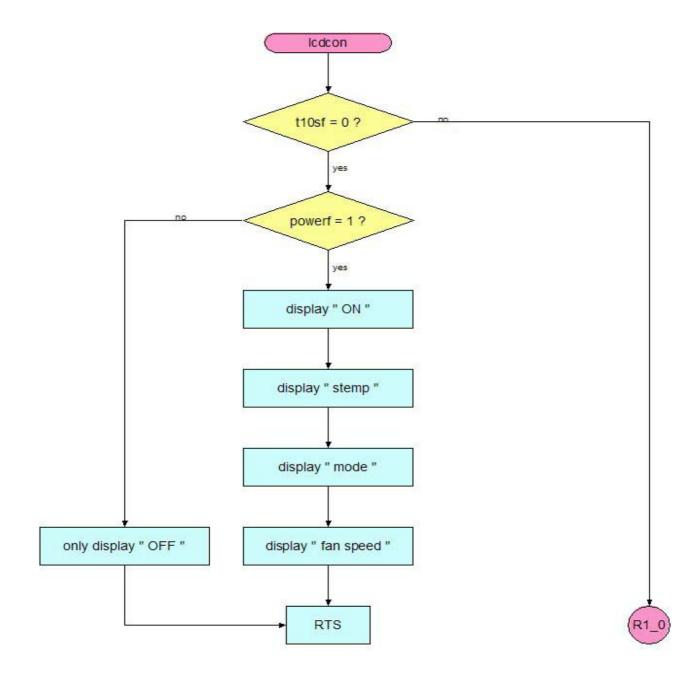


6.9.12 LEDs control subroutine (9)

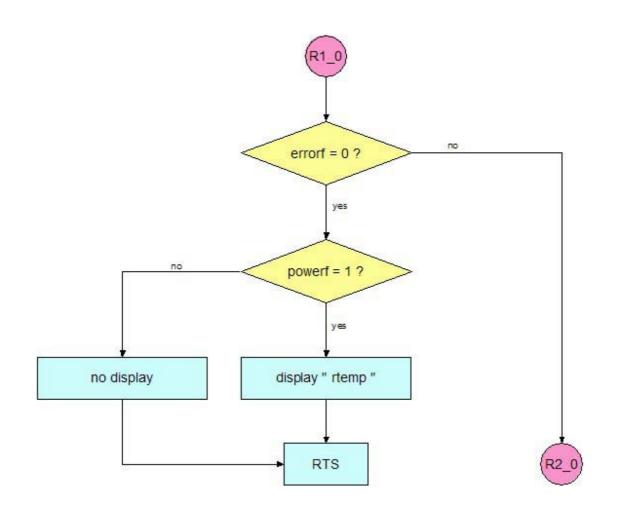




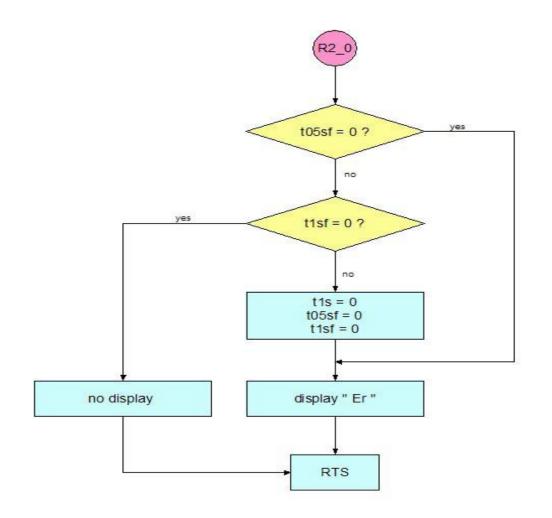
6.10 LCD display subroutine





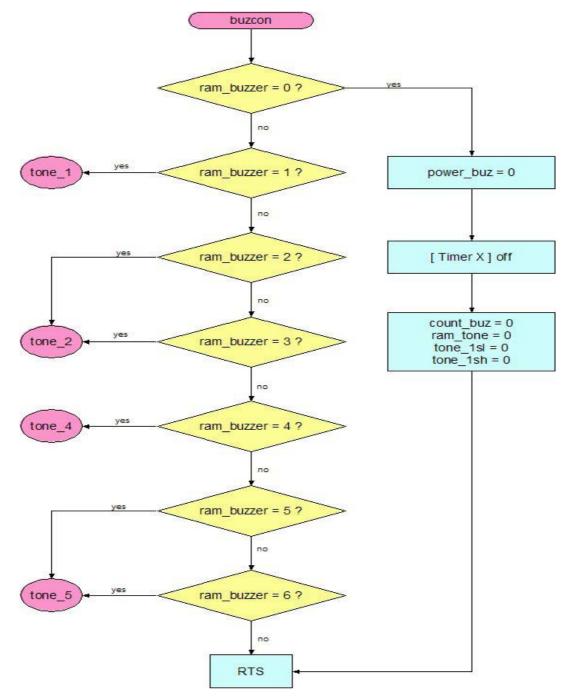




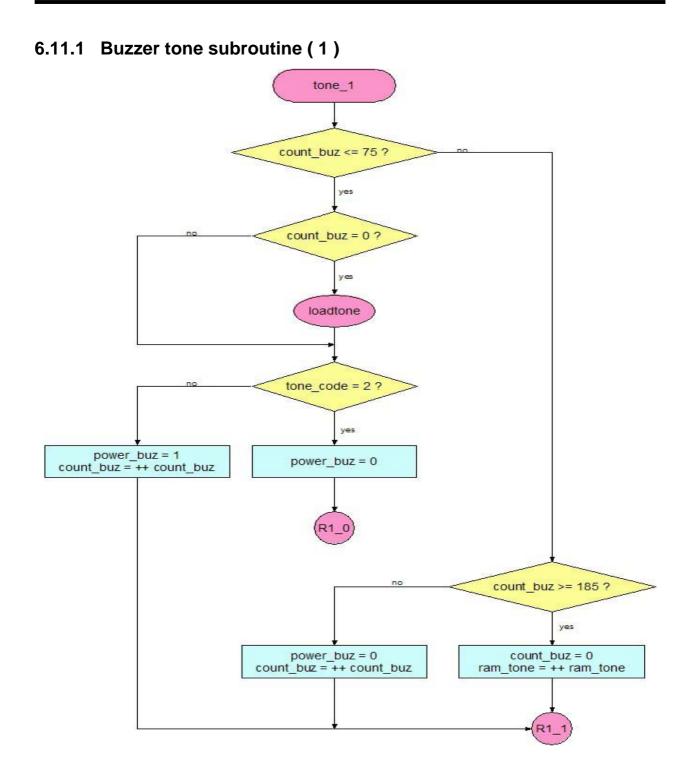




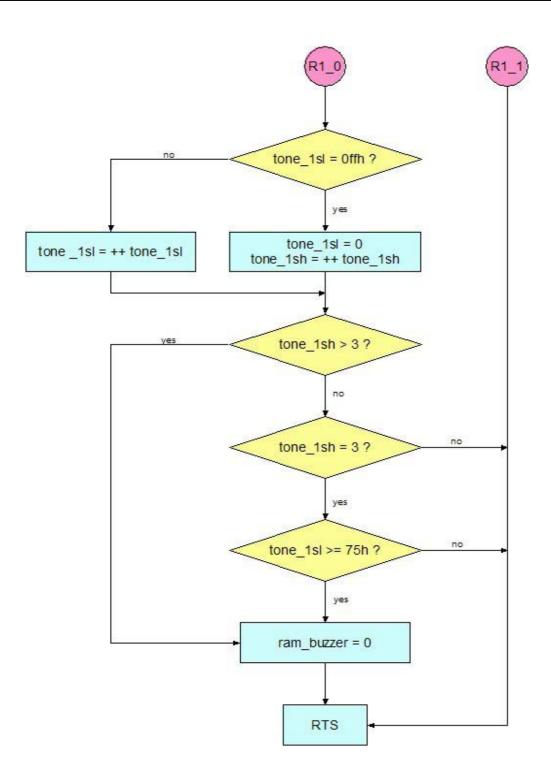
6.11 Buzzer control subroutine



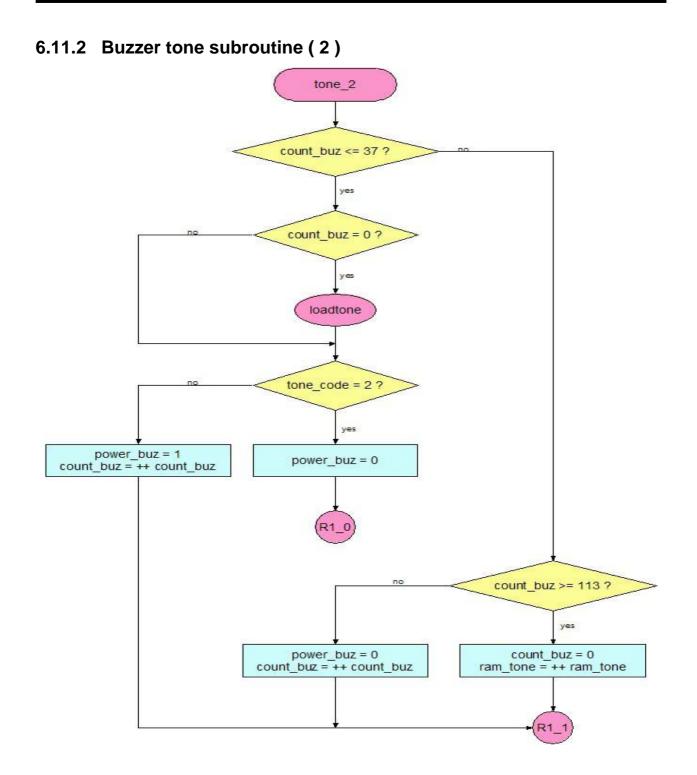




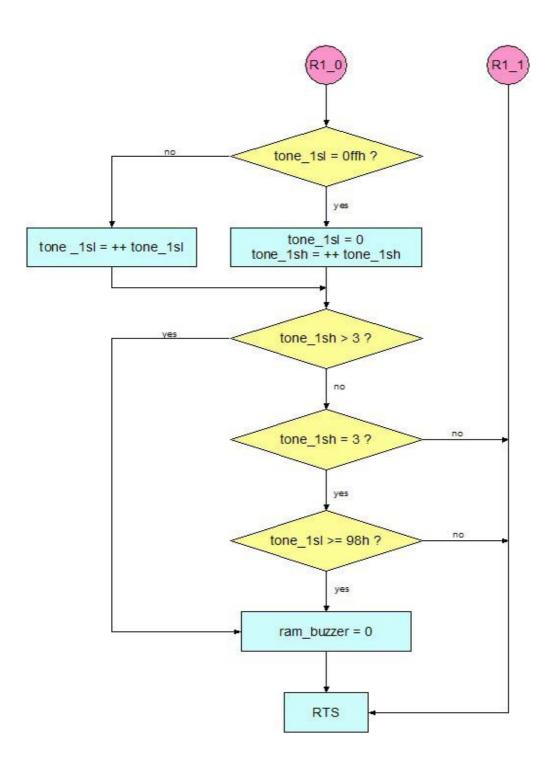




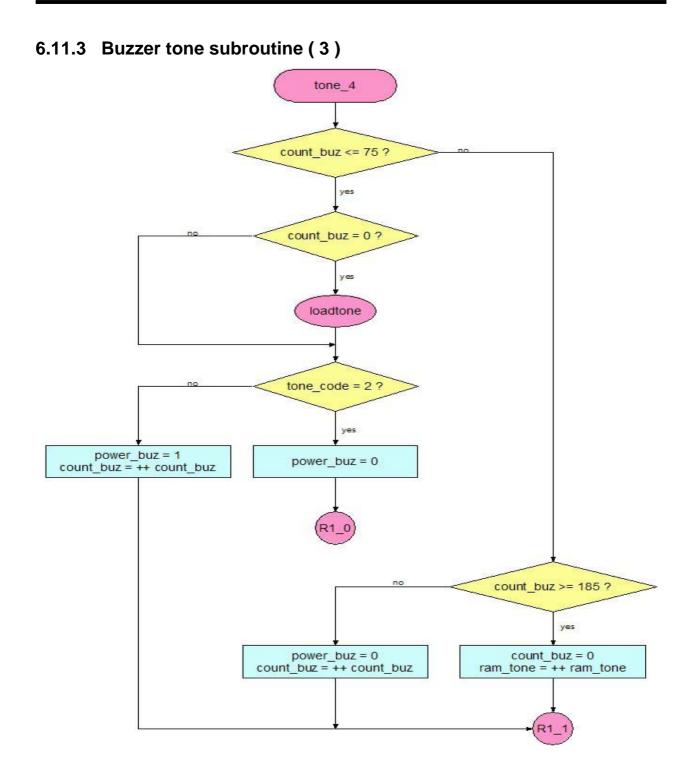




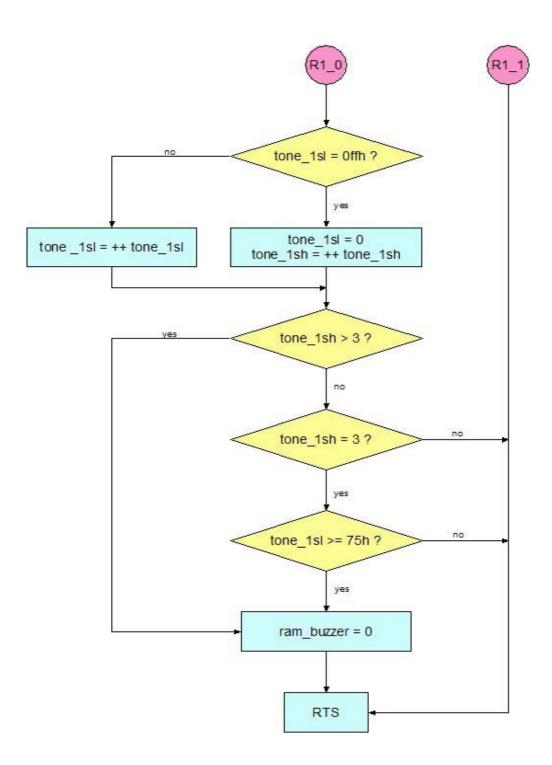




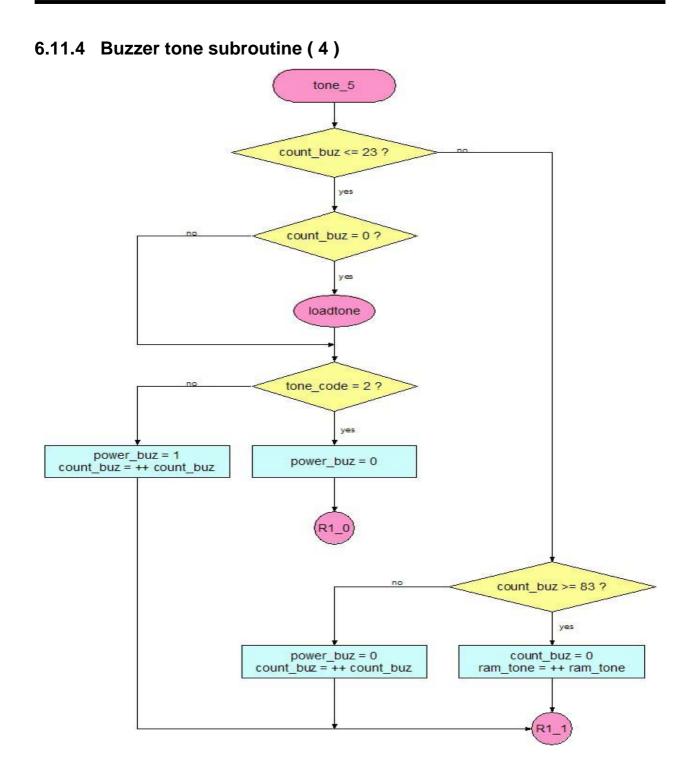




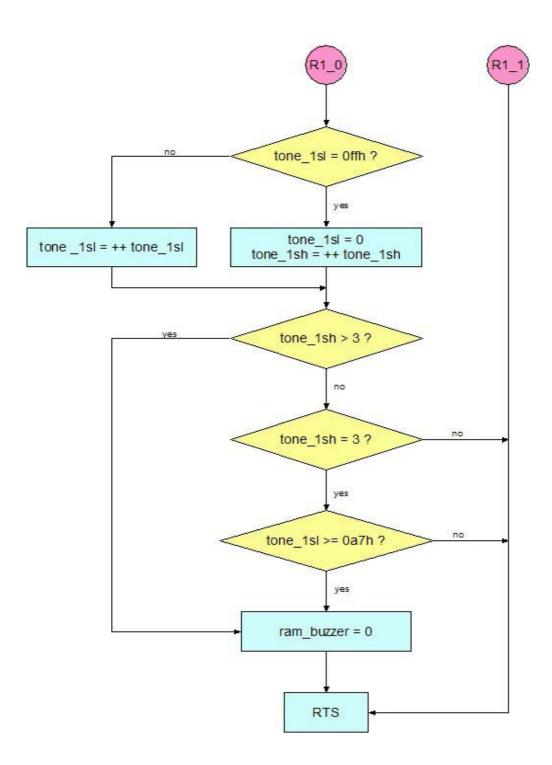






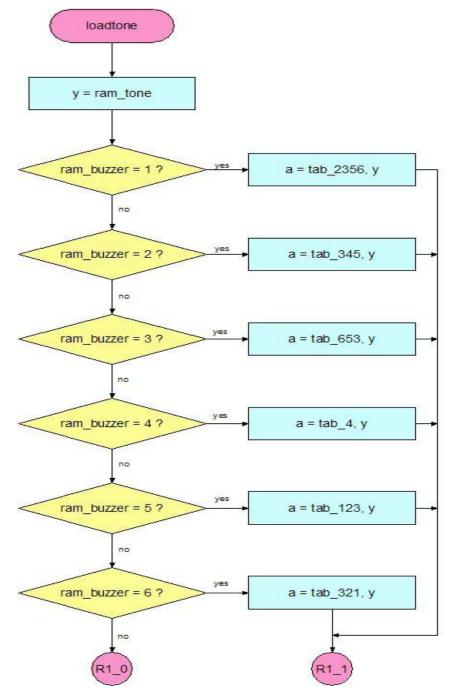




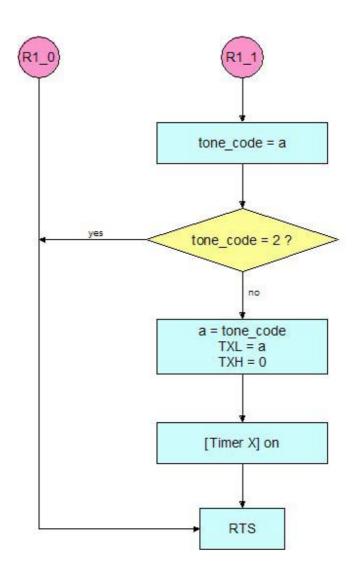




6.11.5 Buzzer tone subroutine (5)

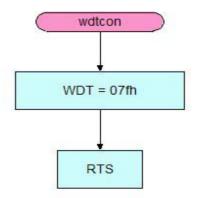








6.12 Watchdog control subroutine





Appendix A Photo of Receiving Unit PCB Board



Photo 1: Top layout of PCB board



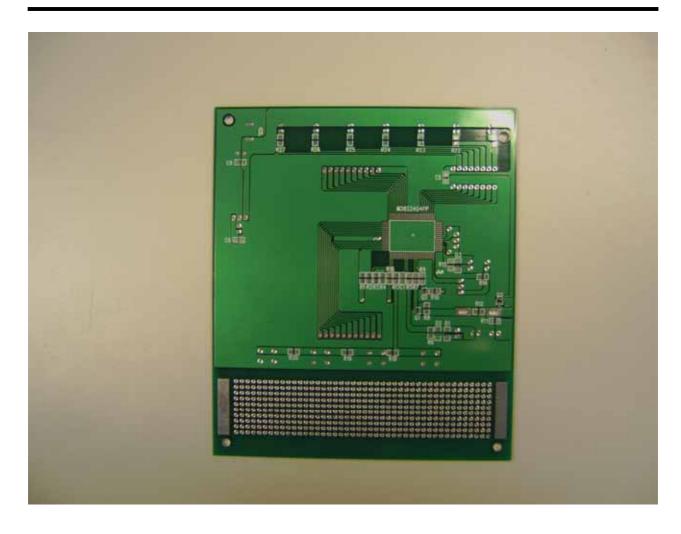


Photo 2: Bottom layout of PCB board

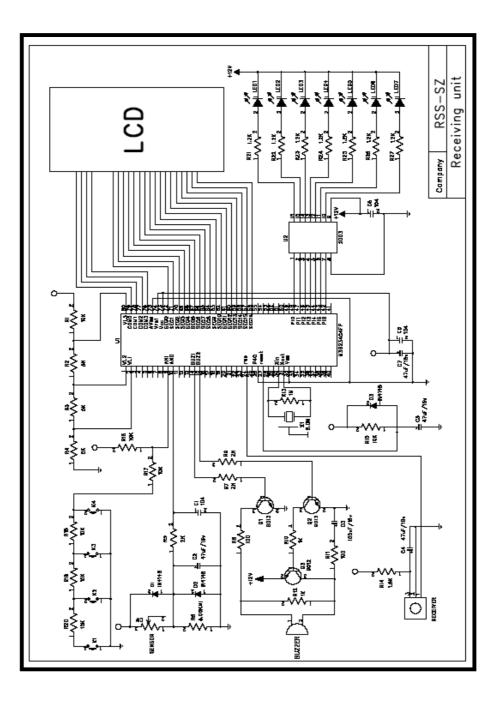


Appendix B Photo of Receiving Unit





Appendix C Schematic of Receiving Unit



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