

SIMULTANEOUS

High Performance  
Simultaneous Data  
Acquisition USB Modules

DT9836 Series:

- 6 or 12 simultaneously captured analog input channels with 16-bit resolution @ 225kHz per channel.

DT9832 Series:

- 4 simultaneously captured analog input channels with 16-bit resolution @ 1.25MHz per channel.

DT9832A Series:

- 2 simultaneously captured analog input channels with 16-bit resolution @ 2.0MHz per channel.

In addition to the simultaneous channels, all modules feature two deglitched waveform analog outputs, 32 digital I/O lines, two 32-bit user counter/timers, and three quadrature decoders. All functions can be synchronously triggered on the same clock.

DT9836, DT9832 & DT9832A

BUS: USB

Type: Simultaneous Multifunction Series

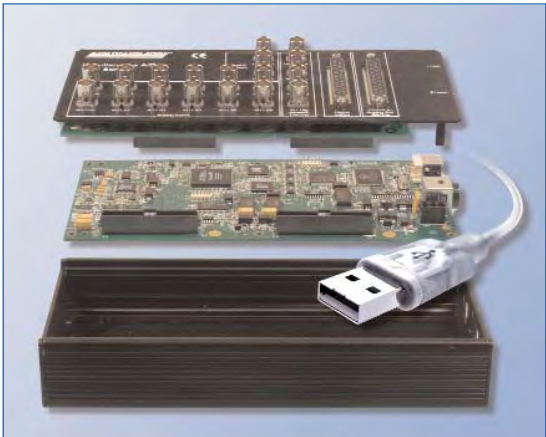


Figure 1. The High Performance Simultaneous Series modules are available in two configurations: BNC connection box and OEM embedded version. Both have 500V galvanic isolation.

Simultaneous and Synchronous USB Series

|         | Simultaneous A/D Throughput per Channel | Highest Signal Frequency for Measurement | Signal Bandwidth | D/A Channels @500 kHz | Quad Decoders/ C/T | 32 High Speed DIO Lines | Applications   |
|---------|---|--|------------------|-----------------------|--------------------|-------------------------|--|
| DT9836  | 6 or 12 ch @ 225kHz                     | 112.5kHz                                 | >1.125MHz        | 0 or 2                | 3/2                | 225kHz                  | Semiconductor Device Characterization, Nanotechnology Testing, Scientific Analysis, Drug Discovery, Automotive Testing |
| DT9832  | 4 ch @ 1.25MHz                          | 625kHz                                   | >6.25MHz         | 0 or 2                | 3/2                | 1.25MHz                 |  |
| DT9832A | 2 ch @ 2.0MHz                           | 1.0MHz                                   | >10.0MHz         | 0 or 2                | 3/2                | 2.0MHz                  |  |

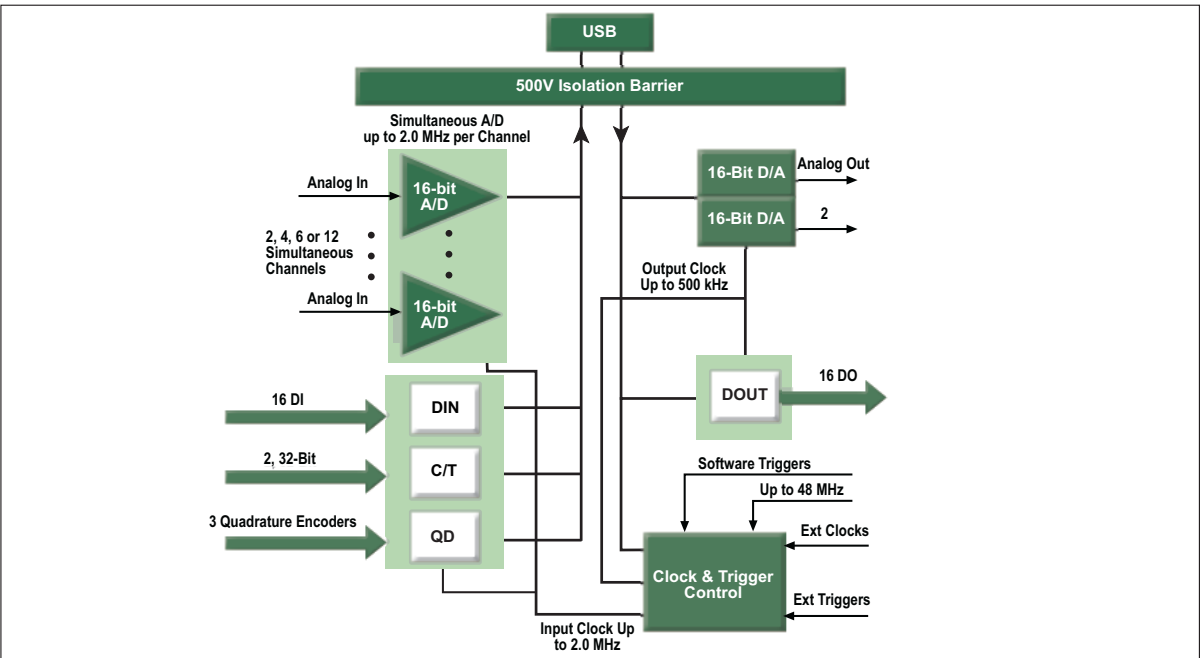


Figure 2. The DT9836 and DT9832 Series provides USB 2.0 multifunction modules for simultaneous acquisition of 2 to 12 analog inputs. Each series differs only in the number of simultaneous inputs and the throughput speed. All functions: A/D, D/A, DIO, Quad Decoder, and C/T can be synchronously triggered. For more detailed block diagrams, click here.

## Simultaneous, High Accuracy Performance

The High Performance Simultaneous Series provides simultaneous analog input operation. Each analog input has its own A/D converter to eliminate phase shift between channels - a problem with multiplexed architectures where all inputs share one A/D converter. As a result, this Series allows you to correlate simultaneous measurements.

### Precision Measurements...

True 16-bit A/D at up to 2.0MHz throughput per channel for measuring multiple input signals simultaneously

### No Limits ...

Full simultaneous and synchronous operation of all subsystems

### Designed for Low Noise ...

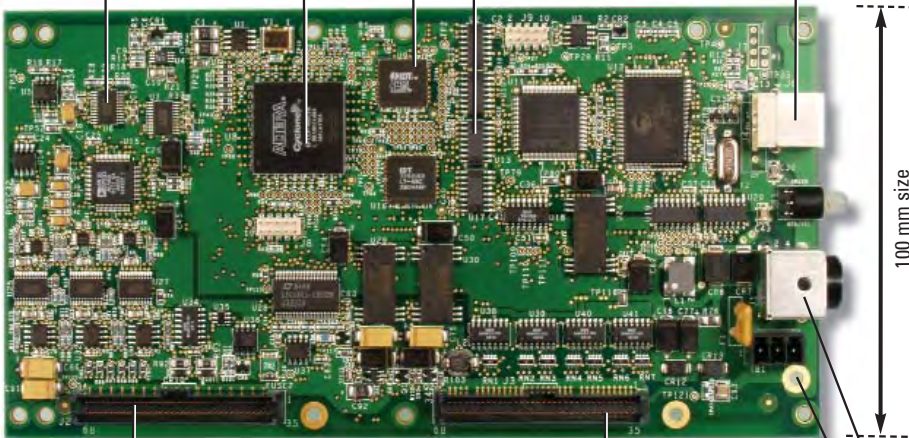
12-layer PCB provides optimal grounding and shielding to maintain signal integrity

### Fully Protected ...

500V galvanic isolation protects your computer and maintains signal integrity

### High-Speed USB 2.0 ...

USB 2.0 connector for data transfer at up to 480Mbps



### Simultaneous Analog Inputs ...

2, 4, 6 or 12 simultaneously sampled analog input channels



The Simultaneous Series is available in the same package size for all models.

### Ultra Digital I/O ...

Full digital I/O flexibility for time stamping, pattern recognition, & synchronizing with external events

### Full-Featured Counter/Timers ...

Two 32-bit counter/timers ideal for testing applications.

### Three Quadrature Decoders ...

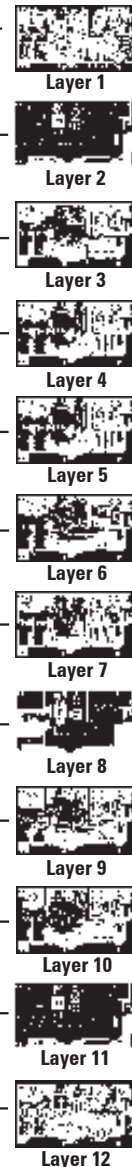
For X/Y positioning and rotation (tachos)

### External Control ...

Flexible clocks and triggers

### Flexible Power Connections ...

+5 V connector; a secondary +5 V connector is provided for embedded applications



## Selection Criteria: Nyquist and Bandwidth Limits Determine Model Choices

Each model has been designed to accurately measure higher bandwidth signal components. To accurately measure 16-bit accuracy, the front-end input amplifier has a bandwidth of ten times the Nyquist limit. Below are examples of these design characteristics for each board.

### DT9836

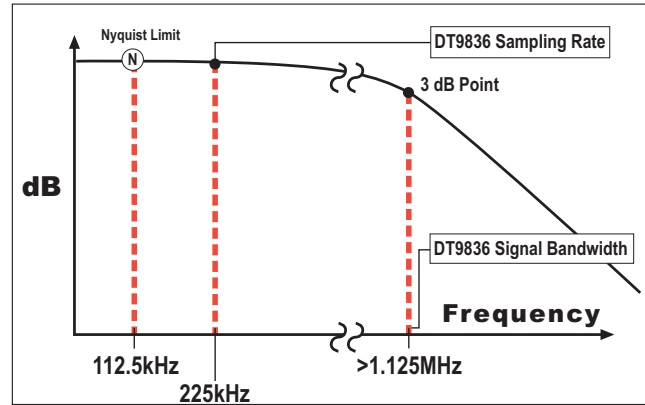


Figure 3. The DT9836 has a sampling rate for each channel of 225 kHz. This means that the Nyquist limit allows signal frequencies up to 112.5 kHz to be adequately measured. The analog input components have a signal bandwidth that is ten times the Nyquist limit or in this case, greater than 1.125 MHz to minimize roll-off and phase errors.

### DT9832

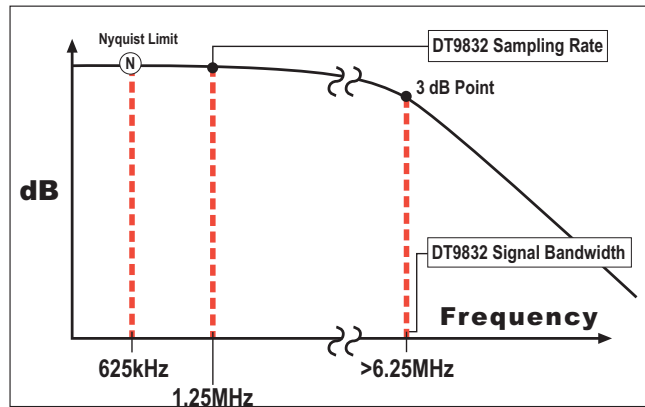


Figure 4. The DT9832 has a sampling rate for each channel of 1.25 MHz. This means that the Nyquist limit allows signal frequencies up to 625 kHz to be adequately measured. The analog input components have a signal bandwidth that is ten times the Nyquist limit or in this case, greater than 6.25 MHz to minimize roll-off and phase errors.

### DT9832A

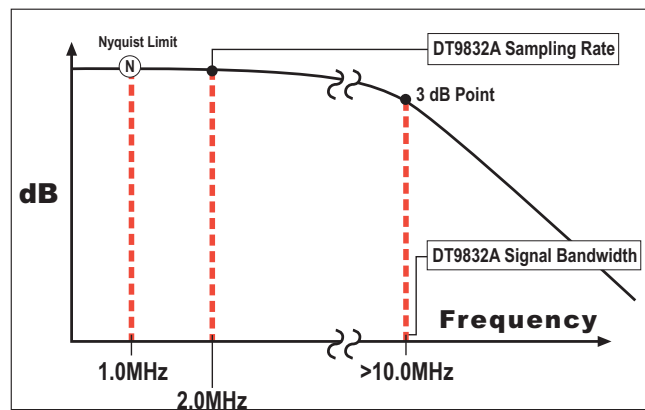


Figure 5. The DT9832A has a sampling rate for each channel of 2.0 MHz. This means that the Nyquist limit allows signal frequencies up to 1.0 MHz to be adequately measured. The analog input components have a signal bandwidth that is ten times the Nyquist limit or in this case, greater than 10.0 MHz to minimize roll-off and phase errors.

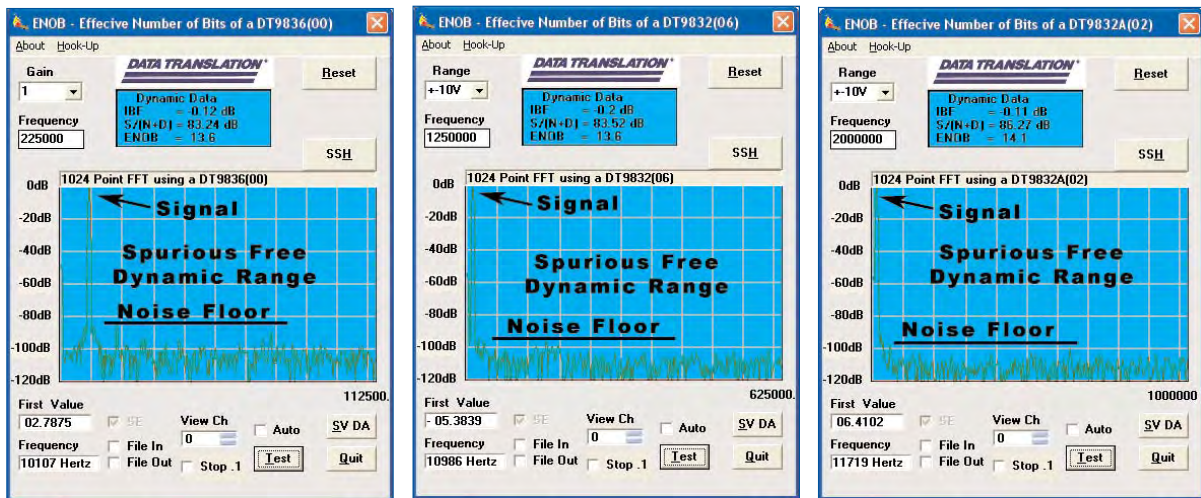


Figure 6. These graphs show the outstanding quality of the DT9836, DT9832, and DT9832A for all error sources... with ENOB (Effective Number Of Bits) ratings of 13.6, 13.6, and 14.1 bits respectively and an SFDR (Spurious Free Dynamic Range) of 86dB.

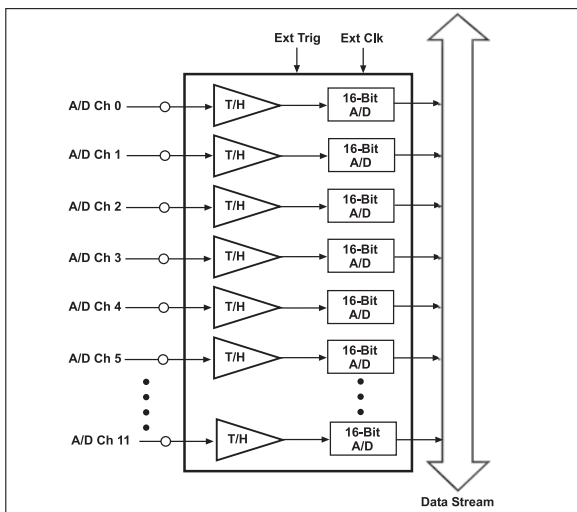


Figure 7. The DT9836 Series features 6 or 12 independent, successive-approximation A/D converters with track-and-hold circuitry. Each converter uses a common clock and trigger for simultaneous sampling of all analog inputs at up to 225kS/s per channel. The DT9832 Series features 2 or 4 simultaneous A/D converters with sampling rates up to 2.0 MHz.

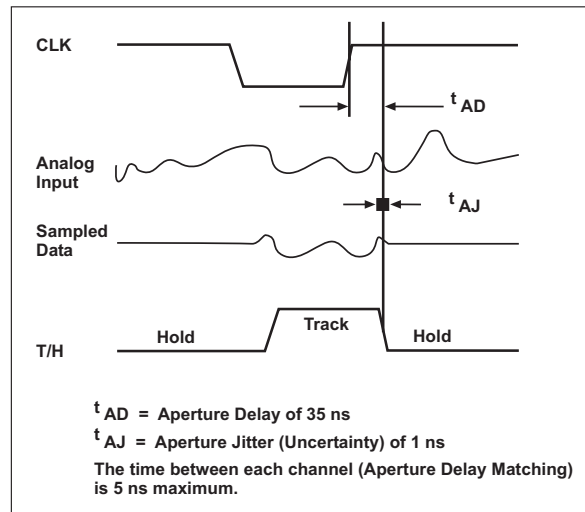


Figure 8. The Simultaneous Series A/D design features built-in accuracy. A maximum aperture delay of 35ns (the time that it takes the A/D to switch from track to hold mode) is well matched at 5ns across all track-and-hold circuits, virtually eliminating the channel-to-channel skew that is associated with multiplexed inputs. A maximum aperture uncertainty of 1ns (the jitter or variance in aperture delay), virtually eliminates phase noise in data.



## Overview

The High Performance Simultaneous Series features simultaneous analog inputs, deglitched waveform analog outputs, 32 digital input/output lines, 2 counter/timers, and 3 quadrature decoders. All these subsystems can be run synchronously.

## Synchronous Operation

All functions of the data acquisition modules (All A/D, D/A, DIO, Counter Timers, and Quadrature Decoders) can be simultaneously triggered internally or externally. The data can then be streamed synchronously to host memory. This can be done via external trigger or by the internal clock of the module.

The synchronous operation allows all I/O data to be processed and correlated for all inputs and outputs. This is very valuable in determining the response across a device-under-test (DUT) to stimuli at the same exact instant.

## Simultaneous, High-Resolution Analog Inputs

This Series of modules is available in various analog input channel configurations:

### DT9836 Series:

- 12-channel or 6-channel.
- Each analog input signal has its own analog-to-digital converter with sampling rates of up to 225kHz and 16-bit resolution.

### DT9832 Series:

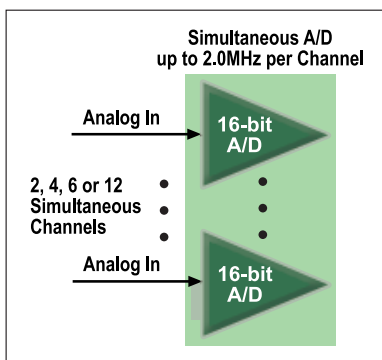
- 4-channel.
- Each analog input signal has its own analog-to-digital converter with sampling rates of up to 1.25MHz and 16-bit resolution.

### DT9832A Series:

- 2-channel.
- Each analog input signal has its own analog-to-digital converter with sampling rates of up to 2.0MHz and 16-bit resolution.

## Closely Matched Analog Inputs

The isolated analog inputs of the simultaneous series have been designed to match each other with high precision. Each input has its own separate high impedance 16-bit A/D converter. The impedance has been carefully matched for each of the inputs so that one looks exactly like the other. The data acquisition board has 12-layers in its make-up to adequately shield and protect each signal path etch from the high speed transitions of the digital lines.



**Figure 9. Separate high-speed A/D converters for each input offer parallel signal acquisition with high accuracy.**

The slightest mismatch would result in DC and AC errors in measurement when trying to correlate readings at the same instant in time.

The SFDR plot in Figure 6 gives the best indication of the match of these DC and AC characteristics. The AC dynamic performance at high switching speeds for all channels shows overall accuracy to be better than 13.6 bits. This is worst case with all errors shown in the FFT ENOB plot. This performance is beyond any other data acquisition system available.

## High-Speed, High-Resolution Analog Outputs

There are 2 simultaneous 16-bit analog outputs comprising separate high speed, deglitched, waveform D/A converters. This design allows highly accurate arbitrary waveforms to be generated at throughput speeds of 500kHz each. Standard waveforms such as sine, triangle, and square waves are easily produced by loading the output memory and triggering them synchronously or separately. Great care has been used in design to minimize the glitch energy for any major or minor carry. This results in extremely smooth waveforms.

You can update the analog output channels as you are acquiring analog input data for gap-free simultaneous stimulus and response. In addition, you can update the digital output lines with the analog output channels at the analog output rate.

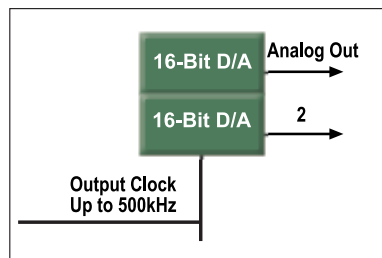
## Flexible Output Modes

Using the Simultaneous Series, you can output a single value from a single analog output channel or multiple values from multiple analog output channels. An output channel list gives you the flexibility of updating only the analog output channels you want or

updating the digital output lines with specified analog output channels at the D/A clock rate. You can update analog output channels at up to 500 kSamples/s.

Both Series feature the following output modes:

- Continuous output mode – Choose this mode if you want to accurately control the period between conversions of individual output channels in the output channel list.
- Waveform mode – Use this mode if you want to output waveforms repetitively from an output FIFO on the module, minimizing communication overhead with the host computer. If you specify only one channel in the output-channel list, you can load a waveform containing up to 128 kSamples into the output FIFO. If you specify all the analog output channels and the digital output lines in the output-channel list, you can load a waveform containing up to 24 kSamples into the output FIFO. Using waveform mode, you can update multiple channels at up to 500 kSamples/s.



**Figure 10. Two deglitched, 16-bit D/A converters for pure waveform generation.**

## High-Speed Digital I/O Lines

The Simultaneous Series modules feature 32 digital I/O lines dedicated as 16 in or 16 out. The first eight digital input lines can also be used for interrupt on change. You can read all the digital input lines simultaneously with the analog input channels at the A/D clock rate. The digital input lines can also be clocked separately as the only channel in the channel-gain list at up to 225 kHz on the DT9836, 1.25 MHz on the DT9832, and 2.0 MHz on the DT9832A.

For digital output operations, you can update all the digital output lines with the analog output channels at the D/A output clock rate.

All lines are EMI protected to minimize interference from transient signals.

## Options for Solution Development



Figure 11. There are many software choices available for application development. Each option offers development capability at different levels. Choose from ready-to-measure applications to full graphical programming with DTMeasure Foundry.

### Multifunction Counter/Timers

All Simultaneous Series modules feature two 32-bit user counter/timers. If you wish, you can read the value of the counter/timer channels with the analog input channels and digital input lines at the A/D clock rate. The following counter/timer functions are supported: event counting, frequency measurement, pulse width measurement, and period measurement.

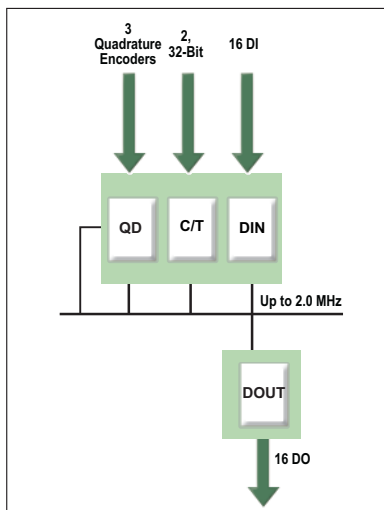


Figure 12. All digital functions can triggered or clocked synchronously along with the analog I/O functions.

### Quadrature Decoder

The Quadrature Decoder module contains three quadrature decoders which allow simultaneous decoding of three quadrature encoded inputs. The quadrature decoders may be used to provide relative or absolute position or, by calculating the difference between samples, the rotational speed. Each quadrature decoder supports 'A', 'B', and 'Index' inputs. The index input may be used to zero out the positional count and the A and B input relationships are used to increment or decrement the positional count.

Each decoder features a digital input filter that is programmable from 27ns to 7µs for the DT9836 and from 20ns to 5µs for the DT9832 and DT9832A. This unique filtering capability helps remove ringing edges and unwanted noise.

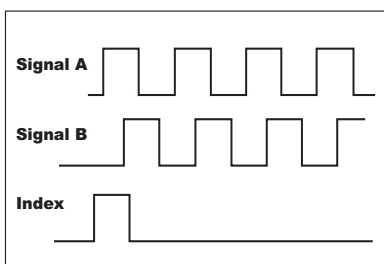


Figure 13. A quadrature decoder takes the output signals (A, B, and Index) for the quadrature encoder as inputs and converts these signals into a numerical value that can be used to determine position, distance, velocity, and other functions.

### Flexible Clocks and Triggers

For maximum flexibility, all Simultaneous Series modules provide independent clocks and triggers for the A/D and D/A subsystems. This allows you to trigger and clock the analog output subsystem synchronously with, or independent of, the analog input subsystem. Each subsystem supports an internal clock and external clock input, as well as the following trigger types: software command, analog threshold, and external digital input trigger.

### Software

These modules ship with the Data Acquisition OMNI CD, which includes DT-Open Layers device drivers for Windows 2000/XP, Ready-to-Measure applications that allow you to take data immediately upon setup, and an evaluation version of our test and measurement builder, DT Measure Foundry. For maximum flexibility, these modules operate under all prominent software applications, including LabVIEW, Visual Basic, MATLAB and more. These software choices allow users of all levels – from programmers to application users – the ability to access the functionality of these modules.

## Flexible Packaging Configurations

The Simultaneous Series modules are available in two packaging configurations: a BNC connection box and an OEM embedded version. The BNC configurations are enclosed in metal boxes with standard BNC and DSUB connectors, 2 BNCs for connecting analog outputs, and 4 BNCs for connecting external clocks and triggers. The BNC configuration ships with a +5 V galvanically isolated power supply and power cable, USB 2.0 cable, and Data Acquisition OMNI CD.

The OEM configuration, ideal for embedding in test systems, provides all the functionality of the Simultaneous Series in PC-board form. This configuration ships with a USB 2.0 cable and Data Acquisition OMNI CD.

## Power

The BNC connection box option includes a separate +5V power supply and power cable for quick setup. OEMs can purchase these options separately as EP361. A secondary power connector is also provided for OEMs to allow custom power wiring.

## USB 2.0 Compatibility

These modules are fully compatible with USB 2.0 and USB 1.1. USB 2.0 extends the speed of connection to up to 480 Mbps. For optimal performance, it is recommended that you use the series with a USB 2.0 port. They can be used with a USB 1.1 port, but at USB 1.1 performance.

Click here for full specifications:

- DT9836 Series
- DT9832 Series

Click here for pin assignments:

- DT9836 Series
- DT9832 Series

## 500V Galvanic Isolation Protects Your Data

Computers are susceptible to ground-spikes through any external port. These spikes can cause system crashes and may even cause permanent damage to your computer. These modules feature 500 Volts of galvanic isolation to protect your computer from ground-spikes and to ensure a reliable stream of data.

## EMI and ESD Design Criteria

The simultaneous series has been designed to perform with the lowest noise characteristics. Damping resistors in series with every I/O line minimize ringing and EMI and provide current limits that protect against transient signals.

## Accessories for OEM Configurations

For applications where you want to embed a DT9836 or DT9832/DT9832A Series module inside other equipment, use the OEM packaging configuration (no enclosure) and our optional accessories.

## Cross-Series Compatibility Saves Programming Time, Protects Your Investment

Virtually all Data Translation data acquisition boards, including the Simultaneous Series, are compatible with the DT-Open Layers software standard. This means that if your application was developed with one of Data Translation's software products, you can easily upgrade to a new Data Translation board. Little or no

reprogramming is needed. For example, if you are currently using a DT3016 board on a PCI bus, upgrading to a DT9836 Series module on the USB bus is simple – just load and configure the new driver and you're done.

## User Manuals

The Simultaneous Series includes a comprehensive user's manual. Manuals are provided in electronic (PDF) format on the Data Acquisition OMNI CD provided with the module. You can also purchase hard copies.

## Technical Support

As you develop your application, application engineers are available during normal business hours to discuss your requirements. Extensive information, including drivers, example code, pinouts, a searchable Knowledgebase, and much more, is available 24 hours a day on our web site at [www.datatranslation.com](http://www.datatranslation.com). Support is also available from your point of purchase. Telephone support is free for the first 45 days. After that time, support is available for a small fee.

Click here to see available accessories:

- DT9836 Series
- DT9832 Series

Click here for full block diagrams:

- DT9836 Series
- DT9832 Series

## DT9836, DT9832/DT9832A

All DT9836 and DT9832/DT9832A Series modules are shipped with a USB cable and the Data Acquisition OMNI CD, which includes DT-Open Layers-compliant device drivers for Microsoft Windows 2000/XP, an evaluation version of DT Measure Foundry. Ready-to-Measure software, and comprehensive manuals in PDF form. The EP361 (power supply) is included with the BNC box configuration.

### DT983X

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-X

-XXX

#### Analog Input

##### DT9836

06 = 6 single-ended channels  
12 = 12 single-ended channels

##### DT9832

04 = 4 single-ended channels

##### DT9832A

02 = 2 single-ended channels

#### Analog Output

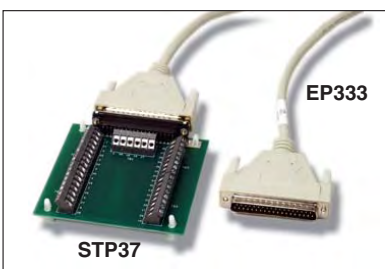
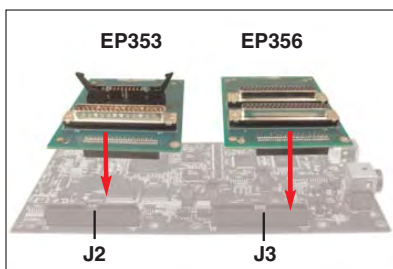
##### DT9836, DT9832, or DT9832A

0 = 0  
2 = 2

#### PAK = Package Configuration

OEM = Board-level embedded version for maximum flexibility (no power supply).

BNC = A metal box enclosure has BNCs for analog inputs. If you select a model with analog outputs, 2 BNCs are provided for connecting analog output signals. The BNC box configuration provides 4 BNCs for connecting external clocks and triggers. (EP361 power supply and power cable included).



## Ordering Summary

### Accessories (Sold Separately)

- EP361 — A +5 V power supply (included with BNC configurations).
- EP353 — Accessory panel with 1, 37-pin DSUB connector and 1, 26-pin signal conditioning connector for attaching analog input signals (for OEM configurations only).
- EP355 — Screw terminal panel for attaching analog I/O and digital I/O signals (for OEM configurations only).
- EP356 — Accessory panel with 2, 37-pin DSUB connectors for attaching analog output, counter/timer, trigger, clock signals, and digital I/O signals (for OEM configurations only).
- EP333 — Cable with two 37-pin male DSUB connectors between STP37 and EP356 or BNC box.
- EP360 — Cable with one 37-pin female and one 37-pin male DSUB connector between STP37 and EP353 or BNC box.
- STP37 — 37-pin screw terminal panel that connects to the EP356, EP353, or BNC box via an EP333 or EP360 cable.

### Software

The Omni CD includes:

- DT-Open Layers device drivers for Windows 2000/XP
- Evaluation copy of DT Measure Foundry test and measurement application builder for Windows 2000/XP.
- Quick DataAcq — ready-to-measure software application, source code included
- DT-LV Link to access the power of our boards through LabVIEW.
- DTx-EZ to access visual programming tools for Microsoft Visual Basic and Visual C++.

### Free Software Downloads

Data Translation now offers free downloads on the Web for:

- Scope — chart recording/oscilloscope function application.
- DAQ Adaptor for MATLAB®— software interface to MATLAB.

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Prices, availability, and specifications subject to change without notice.  
2/2006

## Module Ordering Summary

| Board            | Analog In Simultaneous | Analog Out | Throughput | Digital In | Digital Out | Counter/Timers | Quadrature Encoders | Packaging            |
|------------------|------------------------|------------|------------|------------|-------------|----------------|---------------------|----------------------|
| DT9836-06-2-BNC  | 6SE                    | 2          | 225 kHz    | 16         | 16          | 2              | 3                   | BNC Box              |
| DT9836-06-2-OEM  | 6SE                    | 2          | 225 kHz    | 16         | 16          | 2              | 3                   | OEM Embedded Version |
| DT9836-06-0-BNC  | 6SE                    | 0          | 225 kHz    | 16         | 16          | 2              | 3                   | BNC Box              |
| DT9836-06-0-OEM  | 6SE                    | 0          | 225 kHz    | 16         | 16          | 2              | 3                   | OEM Embedded Version |
| DT9836-12-2-BNC  | 12SE                   | 2          | 225 kHz    | 16         | 16          | 2              | 3                   | BNC Box              |
| DT9836-12-2-OEM  | 12SE                   | 2          | 225 kHz    | 16         | 16          | 2              | 3                   | OEM Embedded Version |
| DT9836-12-0-BNC  | 12SE                   | 0          | 225 kHz    | 16         | 16          | 2              | 3                   | BNC Box              |
| DT9836-12-0-OEM  | 12SE                   | 0          | 225 kHz    | 16         | 16          | 2              | 3                   | OEM Embedded Version |
| DT9832-04-2-BNC  | 4SE                    | 2          | 1.25 MHz   | 16         | 16          | 2              | 3                   | BNC Box              |
| DT9832-04-2-OEM  | 4SE                    | 2          | 1.25 MHz   | 16         | 16          | 2              | 3                   | OEM Embedded Version |
| DT9832-04-0-BNC  | 4SE                    | 0          | 1.25 MHz   | 16         | 16          | 2              | 3                   | BNC Box              |
| DT9832-04-0-OEM  | 4SE                    | 0          | 1.25 MHz   | 16         | 16          | 2              | 3                   | OEM Embedded Version |
| DT9832A-02-2-BNC | 2SE                    | 2          | 2.0 MHz    | 16         | 16          | 2              | 3                   | BNC Box              |
| DT9832A-02-2-OEM | 2SE                    | 2          | 2.0 MHz    | 16         | 16          | 2              | 3                   | OEM Embedded Version |
| DT9832A-02-0-BNC | 2SE                    | 0          | 2.0 MHz    | 16         | 16          | 2              | 3                   | BNC Box              |
| DT9832A-02-0-OEM | 2SE                    | 0          | 2.0 MHz    | 16         | 16          | 2              | 3                   | OEM Embedded Version |

All modules feature 16-bit resolution with input ranges of +/-10, 5V.



## DT9836 Series Hardware Specifications (At +25° C and Rated Voltage, Unless Otherwise Specified)

### Analog Inputs

|                  |                         |
|------------------|-------------------------|
| Number of inputs | 6 or 12 SE Simultaneous |
| Resolutions      | 16 bits                 |
| Range            | +/-10, +/-5 V           |

### A/D Throughput

|                   |                        |
|-------------------|------------------------|
| Per channel       | 225 kS/s               |
| Channel bandwidth | 2.5 MHz to -3 dB point |

### Sample and Hold

|                      |           |
|----------------------|-----------|
| Aperture uncertainty | 1 ns      |
| Aperture delay       | 35 ns     |
| Aperture match       | 5 ns      |
| Gain match           | +/-0.015% |
| Zero match           | +/-1.5 mV |

### System Accuracy (% of FSR)

|                     |                 |
|---------------------|-----------------|
| Gain=1              | +/-0.015%       |
| Bipolar Input Range | +/-10 V, +/-5 V |
| Output Coding       | Offset binary   |

### Maximum Input Voltage without Damage

|           |          |
|-----------|----------|
| Power On  | +/- 30 V |
| Power Off | +/-20 V  |

### Input Impedance

|                           |                    |
|---------------------------|--------------------|
| Bias Current              | +/-10 nA           |
| Integral Nonlinearity     | +/-0.015%          |
| Differential Nonlinearity | +/-0.003%          |
| Inherent Quantizing Error | +/-1/2 LSB         |
| A/D Zero Drift(/ ° C)     | +/-25 $\mu$ V/ ° C |
| Gain Drift (of FSR/ ° C)  | +/-50 ppm/ ° C     |

### Analog Outputs

|                                      |                                |
|--------------------------------------|--------------------------------|
| Number of DACs                       | 2                              |
| Resolution                           | 16-bits                        |
| <b>Settling Time to 0.01% of FSR</b> |                                |
| 10 V Step                            | 5 $\mu$ s                      |
| 100 mV Step                          | 2 $\mu$ s                      |
| Throughput                           | 500 kS/s                       |
| Slew Rate                            | 10 V/ $\mu$ s                  |
| Glitch Energy                        | 12 nV-s typical                |
| Output Range                         | +/-10 V                        |
| Data Coding                          |                                |
| Bipolar                              | Offset Binary                  |
| Output Current                       | +/-5 mA maximum                |
| Output Impedance                     | 0.1 $\Omega$                   |
| Capacitive Driver Capability         | 0.004 $\mu$ F                  |
| Protection Against                   | Short Circuit to analog ground |
| Nonlinearity                         | 1 LSB                          |
| Differential Nonlinearity            | 1 LSB                          |
| Inherent Quantizing Error            | +/-1/2 LSB                     |
| Gain Error                           | Adjustable to Zero             |
| Zero Error                           | Adjustable to Zero             |
| Gain Drift                           | +/-30 ppm of FSR/ ° C          |
| Zero Drift (Bipolar)                 | +/-10 ppm of FSR/ ° C          |

## DT9836 Series Hardware Specifications (At +25° C and Rated Voltage, Unless Otherwise Specified) - continued.

### Digital I/O Subsystem (All models)

|                               |   |
|-------------------------------|---|
| Number of DIO                 | 32 (16 in/16 out), 1 dynamic digital output               |
| Number of Ports               | 2, 16-bit   |
| Logic Family                  | LVTTL   |
| Logic Sense                   | Positive true   |
| Input Type                    | Level sensitive   |
| Input Termination             | Inputs tied to +3.3 V with 15 k $\Omega$ pullup resistors |
| Input Logic Load              | 1 LVTTL load  |
| Logic High Input Voltage      | 2.0 V minimum   |
| Logic Low Input Voltage       | 0.8 V maximum   |
| Logic Low Input Current       | -0.4 mA maximum   |
| Fan-out                       | 12 mA   |
| Logic High Output Voltage     | 2.0 V minimum   |
| Logic Low Output Voltage      | 0.8 V maximum   |
| Logic High Output Current     | -12 mA maximum  |
| Logic Low Output Current      | 12 mA maximum   |
| Interrupt on Change           | Yes   |
| Clocked with the sample clock | Yes   |
| Software I/O Selectable       | No  |

### Counter Timer\* (All models)

|            |  |
|------------|--|
| Channels   | 2 user counter/timers: 3 quadrature decoders |
| Resolution | 32 bits/channel                              |

### External A/D and D/A Triggers (All Models)

#### Triggering Sources:

|                          |                                 |
|--------------------------|---------------------------------|
| Internal                 | Software initiated              |
| External                 | Software selectable             |
| Input Type               | Edge sensitive                  |
| Logic Family             | LVTTL                           |
| Logic Load               | 1 LVTTL load                    |
| Input Termination        | 2.2 k $\Omega$ pullup to +3.3 V |
| Logic Low Input Voltage  | 0.8 V maximum                   |
| Logic High Input Current | 25 $\mu$ A maximum              |
| Logic Low input Current  | -0.25 mA maximum                |

#### Minimum Pulse Width:

|            |       |
|------------|-------|
| Clock High | 25 ns |
| Clock Low  | 25 ns |

#### Triggering Modes:

|                 |     |
|-----------------|-----|
| Single Scan     | Yes |
| Continuous Scan | Yes |
| Triggered Scan  | Yes |

### Onboard A/D Clocks

|                |                       |
|----------------|-----------------------|
| Base Frequency | 36 MHz                |
| Divisor Range  | 3 to 4,294,967,295    |
| Usable Range   | 225 kHz to 0.00838 Hz |

### Onboard D/A Clocks

|                |                       |
|----------------|-----------------------|
| Base Frequency | 36 MHz                |
| Divisor Range  | 3 to 4,294,967,295    |
| Usable Range   | 500 kHz to 0.00838 Hz |

\* Has same logic high and low voltage and current specifications as the digital I/O lines.

## DT9836 Series Hardware Specifications (At +25° C and rated Voltage, Unless Otherwise Specified)- continued.

### External A/D and D/A Clocks

|                            |  |
|----------------------------|--|
| Input Type                 | Edge sensitive, rising-edge or falling-edge programmable |
| Logic Family               | LVTTL  |
| Logic Load                 | 1 LVTTL load   |
| Input Termination          | 2.2 k $\Omega$ pullup to +3.3 V                          |
| Logic High Input Voltage   | 2.0 V  |
| Logic Low Input Voltage    | 0.8 V  |
| Logic Low Input Current    | 1.2 mA   |
| Oscillator Frequency       | DC to 225 kHz maximum (A/D); DC to 500 kHz maximum D/A   |
| <b>Minimum Pulse Width</b> |  |
| Clock High                 | 25 ns  |
| Clock Low                  | 25 ns  |

### Interface Characteristics

|                          |  |
|--------------------------|--|
| Compatible Bus           | USB 2.0 or 1.1   |
| Interface Type           | Bulk   |
| Windows                  | DT-Open Layers Drivers                                 |
| Plug 'N Play             | USB Windows  |
| OEM Board I/O Connectors | 2, 68-pin connectors                                   |
| Fully packaged           | Enclosure with BNC and D-Sub connectors, or board-only |

### Power Requirements

|          |                      |
|----------|----------------------|
| +5 Volts | +/-5%, @ 2 A Maximum |
|----------|----------------------|

### Physical /Environmental

|                                   |   |
|-----------------------------------|---|
| Dimensions (OEM Embedded Version) | 190 mm x 100 mm                         |
| Dimensions (BNC Box Version)      | 215.9 mm (L) x 105.9 mm (W) x 50 mm (H) |
| Weight (OEM Embedded Version)     | 4.6 oz.                                 |
| Operating Temperature Range       | -0 to +55 ° C                           |
| Storage Temperature Range         | -25 to 85 ° C                           |
| Relative Humidity                 | to 95% non-condensing                   |