



Rev 1.1 – December 2007



Table of Contents

Chapter 1	Introduction	3
Chapter 2	Module Accessories	4
Chapter 3	Module Operation	5
Chapter 4	Installing the Module	6
Chapter 5	Configuring the Module	7
Chapter 6	I/O Address Map	10
Chapter 6	I/O Address Map	10

Appendix A	UDT Example	.13
Appendix B	Specifications	.17



CHAPTER 1 INTRODUCTION

The 1756HP-KeyID is the KeyID Master module for the Allen-Bradley ControlLogix PLC system that communicates with up to 30 KeyID slave modules. This enables the detection of the KeyID status at each slave.

This document serves to describe the functionality, installation, configuration and use of the module.



CHAPTER 2 MODULE ACCESSORIES

The module interfaces to the network via the RJ45 port located on the front of the module. The required cable is supplied with the module.

Each 1756HP-KeyID package includes the following components:

- 1756HP-KeyID module
- 3m Cable
- 1756HP-KeyID user manual





CHAPTER 3 MODULE OPERATION

The 1756HP-KeyID module is designed to operate within the Allen-Bradley ControlLogix PLC system. All power required for the module's operation is derived from the 1756 backplane.

The 1756HP-KeyID master module will communicate by broadcasting a packet to all of the slaves. The slaves will then reply in turn, according to address, with all the necessary information. Should a slave not reply, a flag will be set indicating that it is offline. Should the slave reply in future transmissions, it will be marked as online again.

The current status of the module is conveyed to the user by means of the 3 bi-color Status LED's and the alphanumeric LED display.



CHAPTER 4

INSTALLING THE MODULE

The module is equipped with a RIUP (Removal and Insertion Under Power) circuitry enabling the module to be installed or removed from the chassis while power is applied.

Attach the cable to the module via the RJ45 connector. Connect the communication wires Blue (A), White/Blue (B), Brown (Ground) to the correct bus terminals of the 4-wire communications bus that runs to the slaves.

Once the module powers up, it will immediately look at the parameters defined and start searching for the KeyID slaves as set out in the parameters.



CONFIGURING THE MODULE

5.1. Establishing the Direct Connection

This section describes the procedures necessary to configure the 1756HP-KeyID module within the ControlLogix system. Each 1756HP-KeyID module must be owned by a single ControlLogix controller.

The 1756 Generic Module is used in RSLogix5000 to configure the module. The configuration of the module is detailed in the table below.

Data Format								
CommFormat	Data – INT							
Connection parameters								
Description	Instance		Size					
Input	1		192					
Output	2		38					
Configuration	4		0					
RPI								
Min	5 msec	Max		750.0 msec				

Table 5.1 : 1756HP-KeyID connection parameters.

The steps required to add a new 1756HP-KeyID module are detailed below.



Figure 5.1 : Right-click on I/O Configuration and select New Module



elect Module Type		3
Гуре:	Major Revision:	
1756-MODULE	1	
Туре	Description	
1756-L1	ControlLogix5550 Controller	
1756-L53	ControlLogix5553 Controller	
1756-L55	ControlLogix5555 Controller	
1756-L63	ControlLogix5563 Controller	
1756-M02AE	2 Axis Analog/Encoder Servo	
1756-M08SE	8 Axis SERCOS Interface	
1756-MODULE	Generic 1756 Module	
1756-0A16	16 Point 74V-265V AC Output	
1756-0A16l	16 Point 74V-265V AC Isolated Output	
1756-0A8	8 Point 74V-265V AC Output	
1756-0A8D	8 Point 74V-132V AC Diagnostic Output	
1756-0A8E	8 Point 74V-132V AC Electronically Fused Output	-
Show		
Vendor: All	💌 🔽 Other 🔽 Specialty I/O 🛛 Sel	ect All
🔽 Analog 🔽	Digital 🔽 Communication 🔽 Motion 🔽 Controller 📃 Cle	ar All
	OK Cancel H	telp

Figure 5.2 : Select Generic 1756 Module (1756-MODULE)

📑 Module Prop	perties: Local:4 (1756-MODULE 1.1)				×
General Con	nection Module Info Backplane				
Туре:	1756-MODULE Generic 1756 Module				
Parent:	Local	- Connection Pa	rameters Assembly Instance:	Size:	
Na <u>m</u> e:	KeylD	<u>I</u> nput:	1	192	÷ (16-bit)
Description:	A	O <u>u</u> tput:	2	36	÷ (16-bit)
	v	Configuration:	4	0	→ (8-bit)
Comm <u>F</u> ormat	: Data - INT 📃	<u>S</u> tatus Input:			-
Sl <u>o</u> t:	4	S <u>t</u> atus Output:		,	
Status: Offline	OK	Cancel	Apply	,	Help

Figure 5.3 : Configure module's parameters



Module Properties: Local:2 (1756-MODULE 1.1)
General Connection Module Info Backplane
<u>R</u> equested Packet Interval (RPI): 5.0 <mark>→</mark> ms (0.2 - 750.0 ms)
Major Fault On Controller If Connection Fails While in Run Mode
Les Scheduled Connection over ControlNet
Module Fault
Status: Offline OK Cancel Apply Help

Figure 5.4 : Configure module's RPI (Requested Packet Interval)

Once a modules configuration data has been downloaded to the controller, it will attempt to establish a connection with the module. A connection will fail if there is inappropriate configuration data.



CHAPTER 6

I/O ADDRESS MAP

The input and output image of the 1756HP-KeyID module is defined in the following sections. Appendix A provides an example UDT that can be used to extract and view the data.

6.1			Inp	out	lma	ge										
Bit	15	14	12	12	11	10	٩	Q	7	6	5	4	2	2	1	0
Int	15	Ŧ	13	12		10	3	0	'	0	5	Ŧ	3	2		U
0		Reserved														
1								Rese	erved							
2							C	nline	(Int	1)						
3							C	nline	(Int	2)						
4								Rese	erved							
5								Rese	erved							
6							SI	ave 1	Stat	us						
7							Sla	ave 1	Volta	ige						
8							Slave	e 1 Te	mpei	rature	Э					
9							Slave	1 Ke	yID S	Statu	5					
10						Sla	ive 1	Anal	ogue	Inpu	ıt 1					
11						Sla	ive 1	Anal	ogue	Inpu	it 2					
12							SI	ave 2	Stat	us						
13							Sla	ave 2	Volta	ige						
14							Slave	e 2 Te	mper	atur	e					
15						5	Slave	2 Ke	yID S	Statu	5					
16						Sla	ive 2	Anal	ogue	Inpu	it 1					
17						Sla	ive 2	Anal	ogue	Inpu	ıt 2					
12							SI	ave 3	Stat	us						
•																
•																
174							Sia	ave 3	J Sta	tus						
175							Sia	Ve 30	VOIT	age	-					
176						8	lave	30 10	empe	ratur	e					
177						5	lave	30 K	eyiD :	Statu	S					
178						Sia		Ana	logue	e inp						
1/9						51a	ve su	Ana	logue	e inp	ut 2					
180								Rese	erved							
•																
•																
•																
								D								
191								Rese	erved							

Table 6.1 : Input image.



- Int 0: Reserved
- Int 1: Reserved
- Int 2: Online Bit 0 indicates if the slave with address 1 is online (1 online, 0 offline); Bit 1 indicates if the slave with address 2 is online; this continues to Bit 15, which holds the online status of the slave with address 16.
- Int 3: Online Same as above, with Bit 0 providing the online status of the slave with address 17 and ends with Bit 13 for slave address 30.
- Int 4: Reserved
- Int 5: Reserved
- Int 6: Slave 1 Status bits.
- Int 7: Slave 1 Voltage Indicates the supply voltage on the bus.
- Int 8: Slave 1 Temperature Indicates the temperature of the slaves enclosure.
- Int 9: Slave 1 KeyID Status Bit 0 to 7 indicates the KeyID input status in their respective order. Bit 8 indicates the output 1 and Bit 9 the output 2 status.
- **Int 10:** Slave 1 Analogue Input 1 Gives the analogue 1 input voltage (0-10V) as a 10bit number (10V = 1023).
- **Int 11:** Slave 1 Analogue Input 2 Gives the analogue 2 input voltage (0-10V) as a 10bit number (10V = 1023).
- Int 12 179: Repetition of Int 6 to 11 for each slave as shown in table 6.1.
- Int 180 191: Reserved



Bit	45		40	40		40	•	•	-	~	-	•	_		•
Int	1 10 14 13 12 11 10 9 0 7 6 5 4 3 2 1									U					
0								Rese	erved						
1								Rese	erved						
2								Rese	erved						
3							Max	cimur	n Sla	ves					
4							C	ontro	l (Int	1)					
5		Control (Int 2)													
6								Slav	ve 1						
7								Slav	ve 2						
8								Slav	ve 3						
9								Slav	ve 4						
35								Slav	e 30						
36								Rese	erved						
37								Rese	erved						

6.2. Output Image

Table 6.2 : Output image.

- Int 0: Reserved
- Int 1: Reserved
- Int 2: Reserved
- Int 3: Maximum Slaves Used to enter the address of the last slave on the line. Slaves with an address higher than this address will not be scanned.
- Int 4: Control Bit 0 indicates if the slave with address 1 must be polled again if removed from the scan list as indicated in Int 2 and 3 of the input image (1 – scan again, 0 – ignore); Bit 1 indicates the scan status for slave 2 and this continues to Bit 15, which holds the scan status for slave 16.
- Int 5: Control Same as above, with Bit 0 providing the scan status of the slave with address 17 and this ends with Bit 13 for slave address 30.
- Int 6: Slave 1 Used to send the output status that slave 1 should have. Bit 0 will change the output status of Output 1 and Bit 1 will change the output status of Output 2.
- Int 7: Slave 2 Used to send the output status that slave 2 should have. Bit 0 will change the output status of Output 1 and Bit 1 will change the output status of Output 2.
- Int 8 35: Repetition of Int 6 and 7 for all the slaves (to address 30) as shown in table 6.2 above.

Int 36: Reserved

Int 37: Reserved



APPENDIX A UDT EXAMPLE

See Figure A.1 below on how to create a new User Defined Type.



Figure A.1 : Creating a new User Defined Data Type

Create the first UDT called Slaves. This UDT consists of 32 bits with each Slave being represented by a bit. This will be used to indicate the online status of the slaves. This is detailed in Figure A.2 below. The last 2 bits will not have slave address and can be named not used.



1011 D	ata Type: Slaves	;			
Na	ime:	Slaves			
De	escription:			×	
Men	nbers:			Data Ty	pe Size: 4 byte(s)
	Name	Data Type	Style	Description	
	Slave_1	BOOL	Decimal		
	Slave_2	BOOL	Decimal		
	Slave_3	BOOL	Decimal		
	Slave_4	BOOL	Decimal		
	Slave_5	BOOL	Decimal		
L_	Slave_6	BOOL	Decimal		_
	Slave_7	BOOL	Decimal		_
	Slave_8	BOOL	Decimal		_
_	Slave_9	BOOL	Decimal		_
	Slave_10	BOOL	Decimal		_
_	Slave_11	BOOL	Decimal		_
	Slave_12	BOOL	Decimal		_
_	Slave_13	BOOL	Decimal		_
_	Slave_14	BUUL	Decimal		_
-	Slave_15	BUUL	Decimal		_
-	Slave_16	BUUL	Decimal		_
-	Slave_17	BUUL	Decimal		_
<u> </u>	Slave_18	BUUL	Decimal		_
H	Slave_13	POOL	Decimal		_
<u> </u>	J Slave_20	DOOL	Decimal		
h	love Up Mov	e <u>D</u> own OI	K Can	cel Apply	Help
I _					>

Figure A.2 : UDT called Slaves. Used to indicate the online status of each slave.

The next UDT is created to set out each individual slave's values. The UDT is shown in Figure A.3.

111 D	ata Type: Slave_Values				<u> </u>
Na	ame: Slave V	alues			
····					
De	escription:			<u> </u>	
				~	
Mer	nbers:			Data Type Size: 1	2 byte(s)
	Name	Data Type	Style	Description	
	Status	INT	Binary		
	Voltage	INT	Decimal		
	Temperature	INT	Decimal		
	KeyID_Status_In	SINT	Binary		
	KeyID_Status_Out	SINT	Binary		
	Analogue_1	INT	Decimal		
	Analogue_2	INT	Decimal		
10f ^e 010					
					·
11					
11					
11					
11					
11					
Π					
	Nove Up Nove Down	ОК	Cancel	Apply	Help

Figure A.3 : UDT called Slave_Values. Shows the values obtained from the slave.



Next an UDT called KeyID_Inputs is created that uses all of the defined UDT's. The copy instruction will be used to copy the entire Input Image of the 1756HP-KeyID module into a Tag of type KeyID_Inputs. This will make accessing the values and information of each slave much easier. Figures A.4 and A.5 detail the KeyID_Inputs UDT.

199 D.	ata Type: KeyID_	Inputs			
Na	me:	KeyID_Inputs			
De	scription:			×	
Mem	ibers:			Data Typ	e Size: 516 byte(s)
	Name	Data Type	Style	Description	
	Reserved_1	DINT	Binary		
	Slaves_Online	Slaves			
	Reserved_2	DINT	Binary		
	Slave_1_Values	Slave_Values			
	Slave_2_Values	Slave_Values			
	Slave_3_Values	Slave_Values			_
	H Slave_4_Values	Slave_Values			_
	H Slave_5_Values	Slave_Values			_
	H Slave_6_Values	Slave_Values			_
	H Slave_7_Values	Slave_Values			_
	Slave_8_values Clave_8_Values	Clave_Values			-
	Slave_3_values Slave_10_Values	Slave_Values			-
	Slave_TU_Value Slave_T1_Value	s pidve_values			-
	Slave_11_value Slave_12_Value	s Slave_values			-
	Slave_12_value Slave_12_value	s Slave_values			-
	Slave_15_value	s Slave Values			-
	Slave 15 Value	s Slave Values			
	Slave 16 Value	s Slave Values			
	Slave 17 Value	s Slave Values			-
	⊞ Slave 18 Value	s Slave Values			
M	love <u>Up</u> Move	Down 0	K	Cancel Apply	Help

Figure A.4 : UDT called KeyID_Inputs. Shows the first half of the UDT.

111 D	ata Type: KeyID_Input	5			_ 🗆 ×
Na	ame: KeylD_	Inputs			
De	escription:			×	
Men	nbers:			Data Type Size: 3	84 byte(s)
	Name	Data Type	Style	Description	
	∃ Slave_21_Values	Slave_Values			
	⊞ Slave_22_Values	Slave_Values			
	⊞ Slave_23_Values	Slave_Values			
	⊞ Slave_24_Values	Slave_Values			
		Slave_Values			
	⊞ Slave_26_Values	Slave_Values			
	⊞ Slave_27_Values	Slave_Values			
		Slave_Values			
		Slave_Values			
	⊞ Slave_30_Values	Slave_Values	-		
	Reserved_3	INT	Decimal		
	Heserved_4	INT	Decimal		
	Heserved_5	INT	Decimal		
	Heserved_b	INT	Decimal		
100	heserved_/	119 1	Decimal		
010					-
h	Move <u>Up</u> Move <u>D</u> own	ОК	Cancel	Apply	Help

Figure A.5 : UDT called KeyID_Inputs. Shows the last half of the UDT.



Next an UDT called KeyID_Outputs is created. The copy instruction will be used to copy the entire Output Image of the 1756HP-KeyID module into a Tag of type KeyID_Outputs. This will make accessing the values and information of each slave much easier. Figures A.6 and A.7 detail the KeyID_Outputs UDT.

🛗 Data Type: KeyID_Outputs 📃 🗌 🗵							
Name:	KevID 0	KevID Dutputs					
provid_							
Description:		<u>A</u>					
				x			
Members:				Data Type Size: 7	6 byte(s)		
Name		Data Type	Style	Description			
Reserved	£1	DINT	Decimal				
SlaveTim	SlaveTime		Decimal				
Maximum	Maximum_Slaves		Decimal				
Control	Control		Decimal				
Slave_1	Slave_1		Decimal				
Slave_2	Slave_2		Decimal				
Slave_3	Slave_3		Decimal				
Slave_4	Slave_4		Decimal				
Slave_5	Slave_5		Decimal				
Slave_6	Slave_6		Decimal				
Slave_7	Slave_7		Decimal				
Slave_8		INT	Decimal				
Slave_9	Slave_9		Decimal				
Slave_10	Slave_10		Decimal				
Slave_11	Slave_11		Decimal				
Slave_12	Slave_12		Decimal				
Slave_13	Slave_13		Decimal				
Slave_14		INT	Decimal				
Slave_15		INT	Decimal				
Slave_16		INT	Decimal				
Slave_17		INT	Decimal		-		
India 20 Million Carlos Apply Help							
4) · · · · · · · · · · · · · · · · · · ·							

Figure A.6 : UDT called KeyID_Outputs. Shows the first half of the UDT.

Bota Type: KeyID_Outputs				<u>- 0 ×</u>
Name: KeylD_0	utputs			
Description:			×	
Members:	Data Tupe	Stule	Data Type Size: 7	'6 byte(s)
Slave 17	INT	Decimal	Description	
Slave 18	INT	Decimal		
Slave 19	INT	Decimal		
Slave 20	INT	Decimal		
Slave 21	INT	Decimal		
Slave 22	INT	Decimal		
Slave 23	INT	Decimal		
Slave 24	INT	Decimal		
Slave_25	INT	Decimal		
Slave_26	INT	Decimal		
Slave_27	INT	Decimal		
Slave_28	INT	Decimal		
Slave_29	INT	Decimal		1
Slave_30	INT	Decimal		
Reserved_3	INT	Decimal		
Reserved_4	INT	Decimal		
100				-
Move Up Move Down OK Cancel Apply Help				
<u> </u> ∙				

Figure A.7 : UDT called KeyID_Outputs. Shows the last half of the UDT.



APPENDIX B SPECIFICATIONS

Parameter	Specification			
General				
Module Location	Any Slot			
Electrical				
Backplane Current	515mA @ 5.1V			
	3mA @ 24V			
Schedules Connection Parameters				
RPI	5ms to 750ms			

(...../// end of document)

