

USER NOTE: PCA-6775 Manual

Edition 1
PN: 200K677511
Oct 2007

Please replace the following pages in your PCA-6775 User Manual edition 1 with the attached pages from this user note.

We apologize for any inconvenience. Advantech will address these issues as soon as possible. If you have any further questions or concerns, feel free to contact your Advantech sales representative at any time.

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PCA-6775 User Note

Packing List

Before you begin installing your card, please make sure that the following materials have been shipped:

- 1 PCA-6775 Series Half-sized single board computer
- 1 Startup manual
- 1 CD for utility, drivers, and manual (in PDF format)
- 1 Power cable p/n:1700002343
- 1 Y cable for PS/2 Keyboard, PS/2 Mouse p/n:1700060202
- 1 FDD cable p/n:1701340603
- 1 Printer port cable p/n:1700260250
- 1 COM port cable(RS232/422/485) p/n:1701140201
- 1 EIDE (HDD) cable p/n:1701400452
- 1 SIDE (HDD) cable p/n:1701440500
- USB cable with 2 ports p/n:1703100260
- Jumper Pack p/n:9689000002

If any of these items are missing or damaged, contact your distributor or sales representative immediately.

Model No. List	Description
PCA-6775F-L0ALE	ISA VIA Mark 533 MHz Slot SBC w/1LAN/128 MB on board Memory/LVDS/CFC
PCA-6775F-Q0A1E	ISA VIA Mark 800 MHz Slot SBC w/1LAN/LVDS/CFC
PCA-67753F-L0A1E	ISA VIA Mark 533 MHz Slot SBC w/3LAN/LVDS/CFC
PCA-67753F-Q0A1E	ISA VIA Mark 800 MHz Slot SBC w/3LAN/LVDS/CFC

2.1 Jumpers

The PCA-6775 has a number of jumpers that allow you to configure your system to your application. The table below lists the functions of the various jumpers.

<i>Table 2.1: Jumpers</i>	
Label	Function
J1	LVDS Panel Power Select
J2	COM2 function selection
J3	Power
J4	Clear CMOS Jumper

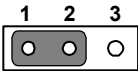
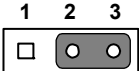
2.6 Clear CMOS (J4)

Warning! *To avoid damaging the computer, always turn off the power supply before setting “Clear CMOS.” Before turning on the power supply, set the jumper back to “3.0 V Battery On.”*

This jumper is used to erase CMOS data and reset system BIOS information. The procedure for clearing CMOS is:

- 1. Turn off the system.
- 2. Short pins 2 and 3 briefly.
- 3. Return jumper to pins 1 and 2.
- 4. Turn on the system. The BIOS is now reset to its default setting

Table 2.3: Clear CMOS (J4)

Clear CMOS	
(Default) BAT	Clear CMOS
	

2.7 COM2 RS-232/422/485 Select (J2)

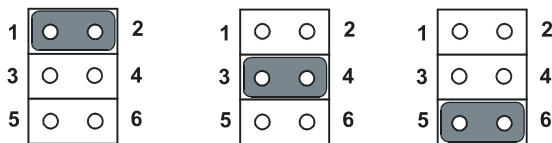


Table 2.4: COM2 RS-232/422/485 Select

1-2 (default)	RS-232
3-4	RS-422
5-6	RS-485

2.8 Power (J3)



Table 2.5: Power

Pin	Function
1-2	Reserved. Do not use.
2-3 (default)	Enabled.

2.9 LVDS Panel Power Select (J1)



Table 2.6: LVDS Panel Power Select

Pin	Function
1-2 (default)	+5 V
2-3	+3.3 V

```

OUT DX,AL
MOV DX,2FH
MOV AL,??H ; Put the output value into AL
OUT DX,AL

```

```

-----
Exit extended function mode |
-----

```

```

MOV DX,2EH
MOV AL,AAH
OUT DX,AL

```

A.2 Watchdog programming

The PCA-6775 is equipped with a watchdog timer that resets the CPU or generates an interrupt if processing comes to a standstill for any reason.

This feature ensures system reliability in industrial standalone or unmanned environments.

Below is a sample of programming code for controlling the Watchdog Timer function.

A.2.1 Example Program

```

;-----
MOV DX , 440h ; I/O port 440h for Watchdog
MOV AL , 0AAh ; Initialize Watchdog by outputting 0AAh to Watchdog
I/O port 440h two times

OUT DX , AL ;
OUT DX , AL ;
OUT DX , 0xh ; Set 0xh as the Watchdog time period
; Set 00h to disable Watchdog
;-----

```

Page 55 intentionally blank.

