TS-RDS5W

Description

TS-RDS5W is a USB2.0 Portable SD/MMC Card
Reader. It is a small and thin device specifically
designed for fast, easy data transfer and exchange using
multiple types of Memory Cards.

Features

- Color: White
- Fully Compliant with the Hi-Speed USB 2.0 specification
- Hi-Speed Data transfer rates of up to 480Mb/s
- USB powered (no external power or battery needed)
- LED indicates card insertion and data traffic
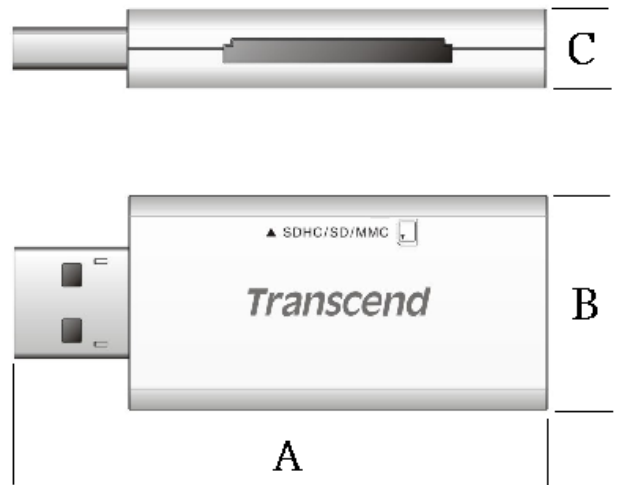- Compatible Cards：

  Directly:

  - o **SD (Secure Digital)**
  - o **SDHC (Secure Digital High Capacity)**
  - o **MMC (MultiMediaCard)**
  - o **MMCplus**
  - o **MMCmobile**
  - o **RS-MMC**

  Adapter Required：

  - o **miniSD**
  - o **miniSDHC**
  - o **microSDHC**
  - o **microSD** / **TransFlash**
  - o **MMCmicro**

Placement

Dimensions

Side	Millimeters	Inches
A	57.90 ± 1.0	2.28 ± 0.04
B	23.20 ± 1.0	0.91 ± 0.04
C	7.60 ± 1.0	0.30 ± 0.04

System Requirements

- Desktop or notebook computer with a working USB port
- One of the following Operating Systems:
  - o Windows® Me
  - o Windows® 2000
  - o Windows® XP
  - o Windows Vista™
  - o Mac™ OS 9.x, or later
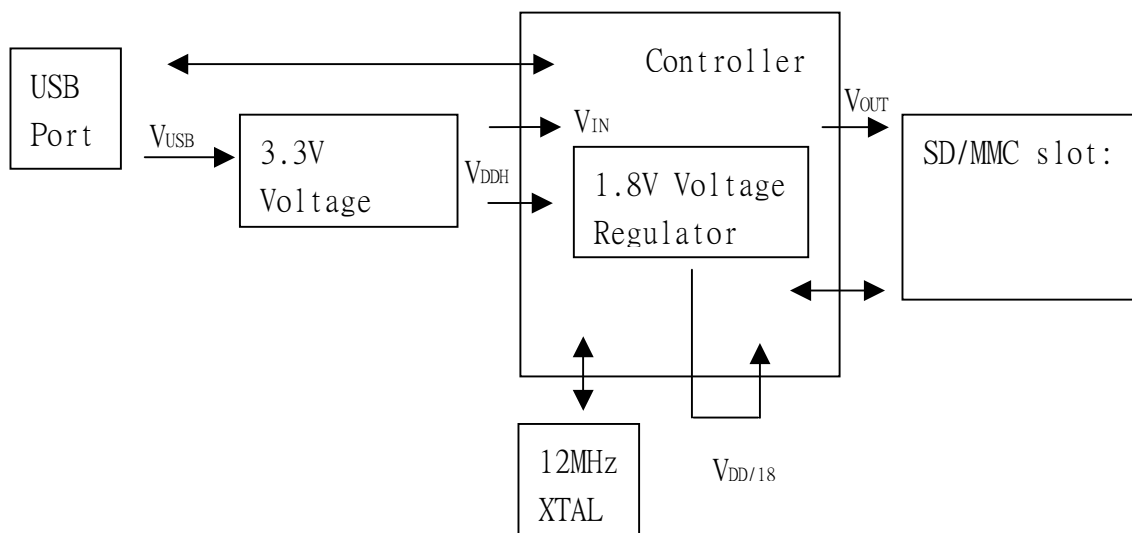 - o Linux™ Kernel 2.4.2, or later

TS-RDS5W

Pinouts

Pin No.	Pin Name
01	V_{USB}
02	USB-
03	USB+
04	VSS

Pin Identification

Symbol	Function
USB- USB+	USB differential signal: The pairs are used to transmit Data/Address/Command
VSS	Ground
V_{USB}	USB Power Input

Block Diagram

Absolute Maximum Ratings

SYMBOL	PARAMETER	RATING	UNITS
V_{DDH}	Power Supply	-0.3 to V_{DDH}+0.3	V
V_{IN}	Input Voltage	-0.3 to 3.6	V
V_{OUT}	Output Voltage	-0.3 to V_{DDH}+0.3	V
T_{STG}	Storage	-40 to 150	^{O}C

Recommended Operating Conditions

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS
V_{DDH}	Power Supply	3.0	3.3	3.6	V
$V_{DD/18}$	Digital Supply	1.62	1.8	1.98	V
V_{IN}	Input Voltage	0	3.3	3.6	V
T_{OPR}	Operating	0		70	^{O}C

DC Characteristics:

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
I_{IN}	Input current	no pull-up or pull-down	-10	±1	10	μA
I_{OZ}	Tri-state leakage current		-10	±1	10	μA
C_{IN}	Input capacitance	Pad Limit		2.8		ρF
C_{OUT}	Output capacitance	Pad Limit		2.8		ρF
C_{BID}	Bi-directional buffer capacitance	Pad Limit		2.8		ρF

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