# MALS180X

### Silicon planar type

#### For ESD protection

#### Overview

MALS180X is optimal for cell phones and AV application, all types of I/O circuits.

It is possible to protect against forward and reverse surges.

#### Features

- High resistance to surge voltages: 15 kV guaranteed
- Low terminal capacitance C<sub>t</sub> for low loss, low distortion, and good retention of signal waveforms.



### SSMini2-F1

- Pin Name
- - 1: Cathode
  - 2: Cathode

Marking Symbol: SX

#### Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Repetitive peak forward current	I <sub>FRM</sub>	200	mA	
Total power dissipation *1	P <sub>T</sub>	150	mW	
Electrostatic discharge *2	ESD	±15	kV	
Junction temperature	Tj	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	

Note) \*1:  $P_T = 150 \text{ mW}$  achieved with a printed circuit board.

\*2: Test method: IEC61000-4-2 (C = 150 pF, R = 330  $\Omega$ , Contact discharge: 10 times)

#### Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Zener voltage *	VZ	$I_Z = 5 \text{ mA}$	17.5		20.0	V
Zene operating resistance	R <sub>Z</sub>	$I_Z = 5 mA$			60	Ω
Reverse current	I <sub>R</sub>	$V_{\rm R} = 13.0  {\rm V}$			15	nA
Terminal capacitance	Ct	$V_R = 0 V$ , $f = 1 MHz$		4		pF

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

2. The temperature must be controlled 25°C for  $V_Z$  mesurement.

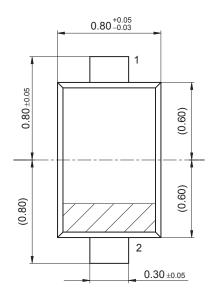
 $V_Z$  value measured at other temperature must be adjusted to  $V_Z$  (25°C)

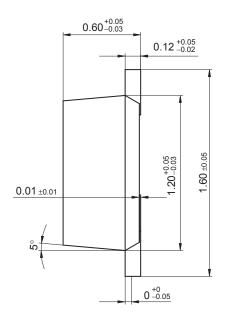
3. \*:  $V_Z$  guaranted 20 ms after current flow.

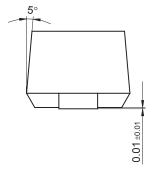
## **Panasonic**

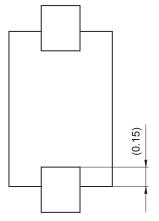
### SSMini2-F1

Unit: mm









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