## MA2YJ50

### Silicon epitaxial planar type

#### For rectification

#### ■ Features

- Forward current (Average)  $I_{F(AV)} = 3.0 \text{ A}$  rectification is possible.
- Low forward voltage V<sub>F</sub>: 0.55 V (max.)

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Reverse voltage	$V_R$	40	V	
Forward current (Average) *1	I <sub>F(AV)</sub>	3.0	A	
Non-marking and Company	$I_{FSM}$	50 *2	A	
Non-repetitive peak forward surge current		15 *3	A	
Junction temperature	T <sub>j</sub>	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	

- Note) \*1: Lead temperature:  $Tl = 60^{\circ}C$ , DC wave on
  - \*2: Rectangle wave 1 cycle (Pulse width =  $50 \mu s$ , non-repetitive peak current)
  - \*3: 50 Hz sine wave 1 cycle (Non-repetitive peak current)

#### ■ Package

• Code

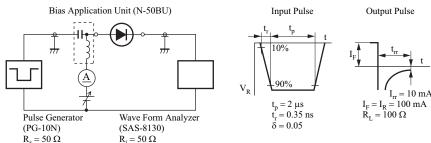
Mini2-F1

- Pin Name
  - 1: Anode
  - 2: Cathode
- Marking Symbol: 3D

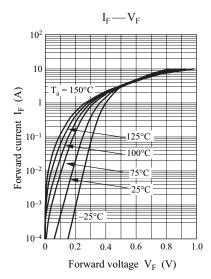
#### ■ Electrical Characteristics $T_a = 25$ °C±3°C

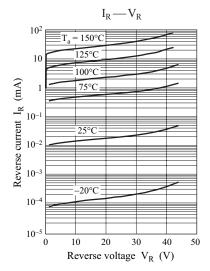
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	$V_{F1}$	$I_F = 1.0 A$		0.35	0.44	V
	$V_{F2}$	$I_F = 3.0 A$		0.47	0.55	
Reverse current	$I_R$	$V_R = 40 \text{ V}$		40	200	μΑ
Terminal capacitance	C <sub>t</sub>	$V_R = 10 \text{ V, } f = 1 \text{ MHz}$		70		pF
Reverse recovery time *	t <sub>rr</sub>	$I_F = I_R = 100 \text{ mA}, I_{rr} = 10 \text{ mA},$ $R_L = 100 \Omega$		25		ns
Thermal resistance (j-a)	$R_{\text{th(j-a)}}$	Mounted on an alumina PC board		110		°C/W
		Mounted on a glass epoxy PC board		160		
Thermal resistance (j-l)	R <sub>th(j-l)</sub>			60		°C/W

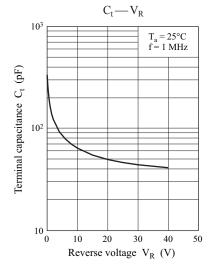
- Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.
  - 2. This product is sensitive to electric shock (static electricity, etc.). Due attention must be paid on the charge of a human body and the leakage of current from the operating equipment.
  - 3. \*: t<sub>rr</sub> measurement circuit



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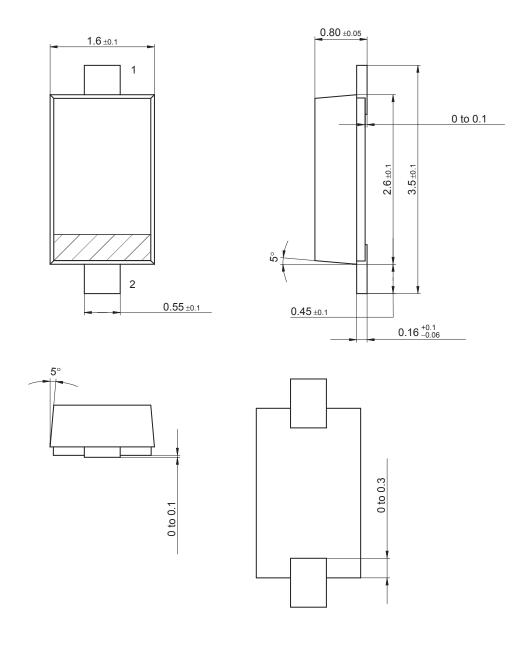




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Panasonic MA2YJ50

Mini2-F1 Unit: mm



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