

IFSYS-7000 IrDA™ SIR Endec Chip (SOP8)

Product Brief

The IFSYS -7000 is designed to be used as an add on peripheral IC for any SIR IrDA™ compliant optoelectronic transceiver. It is ideally suited to applications requiring low cost and low power communication based on the IrDA™ standard. IFSYS-7000 does not require an external crystal; it utilizes a 16X clock from the host system to provide the 16X baud rate clock for both encoding and decoding, using infrared pulse widths of 3/16th the bit time. iFoundry's IFSYS-7000 supports the full complement of IrDA™ V1.1 baud rates, from 9,600 bps right up to 115,200bps.

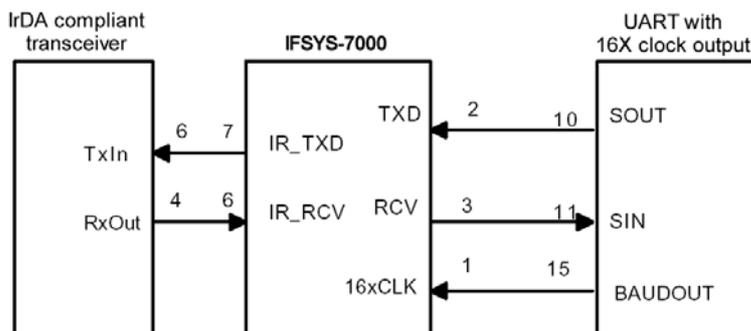


IFSYS-7000

Key features

- ❑ Wide voltage operating range (2.7V – 5.5V). Single power supply input.
- ❑ Small form factor 8-pin SOP package.
- ❑ Operates in 3/16th mode transmit pulse width.
- ❑ Operates from an external 16X clock input from the host processor or UART. Supports the following speeds:
 - 9,600bps
 - 19,200bps
 - 38,400bps
 - 57,600bps
 - 115,200bps

Sample Block Diagram



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SPECIFICATIONS

Hardware features

- 8-pin SOP (surface mount) package
- Single power supply (2.7V – 5.5V supply)

Supported speeds

- Using external 16X clock from UART:
 - 9,600bps
 - 19,200bps
 - 38,400bps
 - 57,600bps
 - 115,200bps

Electrical characteristics

- Temperature:
 - Operating: -40°C to +85°C
 - Storage: -65°C to +150°C
- Current consumption:
 - 1.5mA (typ.), 6mA (max.) [2MHz on 16X clock input]

Dimensions

- SOP8 package: 6.65mm (W) x 5.60mm (L) x 1.55 mm (H)
- Weight: ~0.2g

Packaging options

- Tape and reel
- Tube

Ordering Information

| Order Number | Order Description |
|------------------|-------------------------------|
| IFSYS-7000A:6101 | IC in Tube packaging |
| IFSYS-7000A:7101 | IC in tape and reel packaging |
| | |

IrDA™ is a trademark of the Infrared Data Association.

iFoundry Systems Singapore

67 Ayer Rajah Crescent
Unit 03-20/22
Singapore 139950

For more information:

Email: sales@ifoundrysys.com
Web: www.ifoundrysys.com