



Delphi Series S48SA, 33W Family DC/DC Power Modules: 48V in, 5V/6.6A out

The Delphi Series S48SA, surface mountable, 48V input, single output, isolated DC/DC converter is the latest offering from a world leader in power system and technology and manufacturing — Delta Electronics, Inc. This product family provides up to 33 watts of power or up to 12A of output current (for output voltage 1.8V or under). With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions. All models are protected from abnormal input/output voltage and current conditions.

FEATURES

- High efficiency: 90.5% @ 5V/ 6.6A
- Size: 47.2mm x 29.5mm x 8.35mm
 (1.86" x 1.16" x 0.33")
- Low profile: 0.33"
- Industry standard footprint and pin out
- Surface mountable
- Fixed frequency operation
- Input UVLO, Output OCP, OVP
- No minimum load required
- 2:1 input voltage range
- Basic insulation
- ISO 9001, TL 9000, ISO 14001, QS 9000, OHSAS 18001 certified manufacturing facility
- UL/cUL 60950 (US & Canada) recognized, and TUV (EN60950) certified
- CE mark meets 73/23/EEC and 93/68/EEC directive

OPTIONS

- Positive on/off logic
- Through hole pin

APPLICATIONS

- Telecom/DataCom
- Wireless Networks
- Optical Network Equipment
- Server and Data Storage
- Industrial/Test Equipment



TECHNICAL SPECIFICATIONS

 $(T_A=25$ °C, airflow rate=300 LFM, $V_{in}=48$ Vdc, nominal Vout unless otherwise noted.)

Input Voltage Max. Typ. Max. Min. Typ. Max. Min. Typ. Max. Input Voltage Continuous	RFA
Input Voltage	Units
Continuous Refer to Figure 18 for measuring point -40 115	
Transient (100ms)	Vdc
Department	Vdc
Storage Temperature	°C
Input/Output Isolation Voltage 1 minute 1500 Input Voltage 38	°C
INPUT CHARACTERISTICS	Vdc
Operating Input Voltage	
Turn-On Voltage Threshold 30.5 32. 33.4 35.5 32. 33.5 34. 35.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32. 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33.5 32.5 33	V
Turn-Off Voltage Threshold	
Lockout Hysteresis Voltage 1 2 3	V
Lockout Hysteresis Voltage 1 2 3	V
No-Load Input Current	V
Off Converter Input Current Inrush Current(I*) 7 Inrush Current(I*) 0.01 Input Refected-Ripple Current P-P thru 12µH inductor, 5Hz to 20MHz 5 Input Voltage Rejple Current 120 Hz 50 OUTPUT CHARACTERISTICS Vin=48V, lo=50%lo.max, Ta=25°C 4.92 5 5.08 Output Voltage Reputation Vin=48V, lo=50%lo.max, Ta=25°C 4.92 5 5.08 Over Line Vin=36V to75°V ±2 ±5 5.08 Over Temperature Ta=40°C to 85°C ±0 ±0 ±0 ±0 300 <t< td=""><td>Α</td></t<>	Α
Innush Current(f1)	mA
Input Voltage Ripple Rejection P-P thru 12µH inductor, 5Hz to 20MHz 5 100	mA
Input Voltage Ripple Rejection 120 Hz	A ² s
OUTPUT CHARACTERISTICS Vin=48V, lo=50%lo.max, Ta=25°C 4.92 5 5.08 Output Voltage Regulation lo=lo,min to lo,max ±2 ±10 ±2 ±10 ±10 300 Over Line Vin=36V to75V ±2 ±5 ±5 Cover Temperature Ta=40°C to 85°C 100 300 Total Output Voltage Ripple and Noise Full Load, lipe and line and temperature 4.85 5.15 5.15 Output Voltage Ripple and Noise Full Load, 1pe ceramic, 10pe tantalum 30 75 75 78 78 78 75 7	mA
Output Voltage Regulation Vin=48V, lo=50%lo.max, Ta=25°C 4.92 5 5.08 Output Voltage Regulation lo=lo,min to lo,max ±2 ±10 Over Line Vin=36V to75V ±2 ±5 Over Temperature Ta=40°C to 85°C 100 300 Total Output Voltage Range Over sample load, line and temperature 4.85 5.15 Output Voltage Ripple and Noise 5Hz to 20MHz bandwidth 30 75 RMS Full Load, lip* ceramic, 10µF tantalum 8 20 Operating Output Current Range 0 6.6 6.0 Output DC Current-Limit Inception Output Voltage 10% Low 7.26 8.5 9.9 DYNAMIC CHARACTERISTICS Output Voltage 20ment Transient 48V, 10µF Tan & 1µF Ceramic load cap, 0.1A/µs 50 100 Positive Step Change in Output Current 50% lo,max to 75% lo,max 50 100 Settling Time to 1% of Final value 75% lo,max to 50% lo.max 50 100 Start-Up Time, From Input 6 12 Maximum Output Capacitance Full load; 5% overshoot of Vout at startup <td>dB</td>	dB
Output Voltage Regulation	
Cover Load	V
Over Line Vin=36V to 75V ±2 ±5 Over Temperature Ta=40° to 85°C 100 300 Total Output Voltage Range Over sample load, line and temperature 4.85 5.15 Output Voltage Ripple and Noise SHz to 20MHz bandwidth	
Over Temperature Ta=40℃ to 85℃ 100 300 Total Output Voltage Range Over sample load, line and temperature 4.85 5.15 Output Voltage Ripple and Noise 5Hz to 20MHz bandwidth 5Hz to 20MHz bandwidth Peak-to-Peak Full Load, 1µF ceramic, 10µF tantalum 30 75 RMS Full Load, 1µF ceramic, 10µF tantalum 8 20 Operating Output Current Range 0 6.6 0.6 0.6 0.0 0.6 6 0.6 0.0 0.6 6 0.0 0.0 6.6 6 0.0 0.6 6 0.0 0.6 6 0.0 0.6 6 0.0 0.6 6 0.0 0.6 6 0.0 0.0 6.6 6 6 0.0 0.0 0.6 6 0.0 0.0 0.6 6 0.0 0.0 0.6 6 0.0 0.0 0.0 0.6 6 0.0 0.0 0.0 0.0 0.0 0.0 0.0 0.0 0.0 0.0 <td>mV</td>	mV
Total Output Voltage Range	mV
Output Voltage Ripple and Noise Full Load, 1μF ceramic, 10μF tantalum 8	ppm/°C
Peak-to-Peak Full Load, 1μF ceramic, 10μF tantalum 30 75	V
RMS	
Operating Output Current Range	mV
Output DC Current-Limit Inception Output Voltage 10% Low 7.26 8.5 9.9 DYNAMIC CHARACTERISTICS 48V, 10μF Tan & 1μF Ceramic load cap, 0.1A/μs 50 100 Positive Step Change in Output Current 50% lo,max to 75% lo,max 50 100 Negative Step Change in Output Current 75% lo,max to 50% lo.max 50 100 Settling Time to 1% of Final value 200 100 Turn-On Transient 6 12 Start-Up Time, From On/Off Control 6 12 Start-Up Time, From Input 6 12 Maximum Output Capacitance Full load; 5% overshoot of Vout at startup 2200 EFFICIENCY 88.5 90.5 1SOLATION CHARACTERISTICS 88.5 90.5 Isolation Voltage 1500 Isolation Capacitance 1500 FEATURE CHARACTERISTICS 1500 Switching Frequency 330 ON/OFF Control, (Logic Low-Module ON) Von/off at lon/off=1.0mA 0 0.8 Logic High Von/off at lon/off=0.0V 1 1 Leakage Curr	mV
DYNAMIC CHARACTERISTICS	A
Output Voltage Current Transient Positive Step Change in Output Current So% lo,max to 75% lo,max So 100 Negative Step Change in Output Current Som lo,max to 55% lo,max So 100 Settling Time to 1% of Final value Turn-On Transient Start-Up Time, From On/Off Control Start-Up Time, From Input Maximum Output Capacitance Full load; 5% overshoot of Vout at startup EFFICIENCY 100% Load Solation Voltage Isolation Resistance Isolation Resistance Switching Frequency ON/OFF Control, (Logic Low-Module ON) Logic Low Logic High ON/OFF Current Leakage Current 48V, 10µF Tan & 1µF Ceramic load cap, 0.1A/µs 50 100 100 100 100 100 100 100 100 100	Α
Positive Step Change in Output Current 50% lo,max to 75% lo,max 50 100	
Negative Step Change in Output Current 75% lo,max to 50% lo.max 50 100	
Settling Time to 1% of Final value	mV
Turn-On Transient 6 12 Start-Up Time, From Input 6 12 Maximum Output Capacitance Full load; 5% overshoot of Vout at startup 2200 EFFICIENCY 88.5 90.5 ISOLATION CHARACTERISTICS 88.5 90.5 Isolation Voltage 1500 1500 Isolation Capacitance 10 1500 FEATURE CHARACTERISTICS 330 0 Switching Frequency 330 0 ON/OFF Control, (Logic Low-Module ON) Von/off at lon/off=1.0mA 0 0.8 Logic High Von/off at lon/off=0.0 μA 15 0 ON/OFF Current lon/off at Von/off=0.0V 1 1 Leakage Current Logic High, Von/off=15V 50	mV
Start-Up Time, From On/Off Control 6 12 Start-Up Time, From Input 6 12 Maximum Output Capacitance Full load; 5% overshoot of Vout at startup 2200 EFFICIENCY 100% Load 88.5 90.5 ISOLATION CHARACTERISTICS 1500 Isolation Voltage 1500 Isolation Capacitance 10 Isolation Capacitance 1500 FEATURE CHARACTERISTICS 330 ON/OFF Control, (Logic Low-Module ON) Conjument of the property of	μs
Start-Up Time, From Input 6 12 Maximum Output Capacitance Full load; 5% overshoot of Vout at startup 2200 EFFICIENCY 88.5 90.5 100% Load 88.5 90.5 ISOLATION CHARACTERISTICS 1500 Isolation Resistance 10 1500 Isolation Capacitance 1500 1500 FEATURE CHARACTERISTICS 330 00 Switching Frequency 330 00 ON/OFF Control, (Logic Low-Module ON) Von/off at Ion/off=1.0mA 0 0.8 Logic High Von/off at Ion/off=0.0 µA 15 0 ON/OFF Current Ion/off at Von/off=0.0V 1 1 Leakage Current Logic High, Von/off=15V 50	
Maximum Output Capacitance Full load; 5% overshoot of Vout at startup 2200 EFFICIENCY 88.5 90.5 1SOLATION CHARACTERISTICS 1500 Isolation Resistance 10 Isolation Capacitance 1500 FEATURE CHARACTERISTICS 330 Switching Frequency 330 ON/OFF Control, (Logic Low-Module ON) Von/off at Ion/off=1.0mA 0 Logic High Von/off at Ion/off=0.0 µA 15 ON/OFF Current Ion/off at Von/off=1.5V 50	ms
## State	ms
100% Load 88.5 90.5 ISOLATION CHARACTERISTICS 1500 Isolation Voltage 1500 Isolation Resistance 10 Isolation Capacitance 1500 Iso	μF
ISOLATION CHARACTERISTICS ISOlation Voltage 1500	0/
Isolation Voltage 1500 Isolation Resistance 10 Isolation Capacitance 1500 FEATURE CHARACTERISTICS 330 Switching Frequency 330 ON/OFF Control, (Logic Low-Module ON) Von/off at Ion/off=1.0mA 0 0.8 Logic High Von/off at Ion/off=0.0 µA 15 ON/OFF Current Ion/off at Von/off=0.0V 1 Leakage Current Logic High, Von/off=15V 50	%
Isolation Resistance	V
Isolation Capacitance	
FEATURE CHARACTERISTICS Switching Frequency 330 ON/OFF Control, (Logic Low-Module ON) 0 Logic Low Von/off at lon/off=1.0mA 0 Logic High Von/off at lon/off=0.0 μA 15 ON/OFF Current lon/off at Von/off=0.0V 1 Leakage Current Logic High, Von/off=15V 50	MΩ pF
Switching Frequency 330 ON/OFF Control, (Logic Low-Module ON) Von/off at Ion/off=1.0mA 0 0.8 Logic Low Von/off at Ion/off=0.0 µA 15 15 ON/OFF Current Ion/off at Von/off=0.0V 1 1 Leakage Current Logic High, Von/off=15V 50	ρг
ON/OFF Control, (Logic Low-Module ON) Von/off at Ion/off=1.0mA 0 0.8 Logic High Von/off at Ion/off=0.0 μA 15 ON/OFF Current Ion/off at Von/off=0.0V 1 Leakage Current Logic High, Von/off=15V 50	kHz
Logic Low Von/off at Ion/off=1.0mA 0 0.8 Logic High Von/off at Ion/off=0.0 μA 15 ON/OFF Current Ion/off at Von/off=0.0V 1 Leakage Current Logic High, Von/off=15V 50	KHZ
Logic High Von/off at Ion/off=0.0 μA 15 ON/OFF Current Ion/off at Von/off=0.0V 1 Leakage Current Logic High, Von/off=15V 50	V
ON/OFF Current Ion/off at Von/off=0.0V 1 Leakage Current Logic High, Von/off=15V 50	V
Leakage Current Logic High, Von/off=15V 50	mA
	uA
Output voltage Thin Mange Across Thill Pill & ±VO 01 =V0, Pout ≥ Illax Tateu -10	WA
Output Over-Voltage Protection(Hiccup) Over full temp range; % of nominal Vout 115 125 140	%
GENERAL SPECIFICATIONS Over full temp range, % of norminal yout 115 125 140	70
MTBF Io=80% of Io, max; Tc=40°C 4.81	M hrs
Weight 18	
Over-Temperature Shutdown Refer to Figure 18 for measuring point 115	grams

ELECTRICAL CHARACTERISTICS CURVES

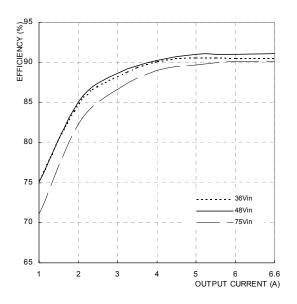


Figure 1: Efficiency vs. load current for minimum, nominal, and maximum input voltage at 25°C.

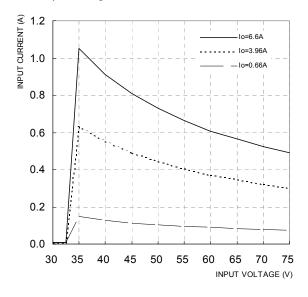


Figure 3: Typical input characteristics at room temperature.

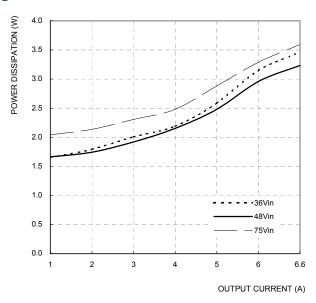


Figure 2: Power dissipation vs. load current for minimum, nominal, and maximum input voltage at 25°C.

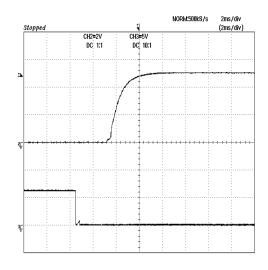


Figure 4: Turn-on transient at full rated load current (2 ms/div). Top Trace: Vout (2V/div); Bottom Trace: ON/OFF Control (5V/div).

ELECTRICAL CHARACTERISTICS CURVES

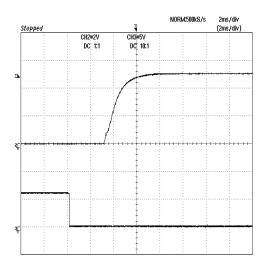
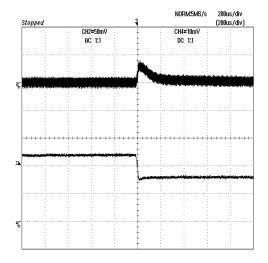


Figure5: Turn-on transient at zero load current (2 ms/div). Top Trace: Vout (2V/div); Bottom Trace: ON/OFF Control (5V/div).

Figure 6: Output voltage response to step-change in load current (50%-75% of Io, max; di/dt = 0.1A/μs). Load cap: 10μ F, $100 \text{ m}\Omega$ ESR tantalum capacitor and 1μ F ceramic capacitor. Top Trace: Vout (50mV/div), Bottom Trace: lout (2A/div).



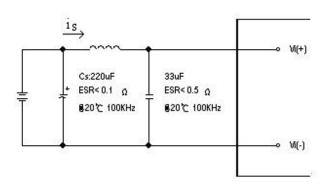


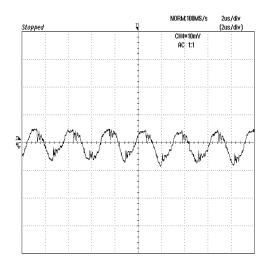
Figure 7: Output voltage response to step-change in load current (75%-50% of Io, max; di/dt = 0.1A/ μ s). Load cap: 10μ F, 100 m Ω ESR tantalum capacitor and 1μ F ceramic capacitor. Top Trace: Vout (50mV/div), Bottom Trace: lout 2A/div).

Figure 8: Test set-up diagram showing measurement points for Input Reflected Ripple Current (Figure 9).

Note: Measured input reflected-ripple current with a simulated

Note: Measured input reflected-ripple current with a simulated source Inductance (L_{TEST}) of 12 μ H. Capacitor Cs offset possible battery impedance.

ELECTRICAL CHARACTERISTICS CURVES



Copper Strip

Vo(+)

10u

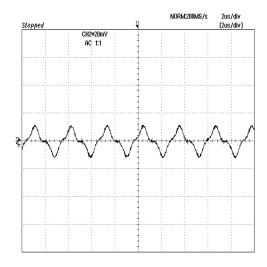
The strip

Vo(-)

RESISTIVE LOAD

Figure 9: Input Reflected Ripple Current, i_S , at full rated output current and nominal input voltage with $12\mu H$ source impedance and $33\mu F$ electrolytic capacitor (2 mA/div).

Figure 10: Output voltage noise and ripple measurement test setup. Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.



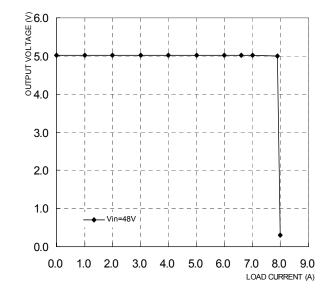


Figure 11: Output voltage ripple at nominal input voltage and rated load current (20 mV/div). Load capacitance: $1\mu F$ ceramic capacitor and $10\mu F$ tantalum capacitor. Bandwidth: 20 MHz.

Figure 12: Output voltage vs. load current showing typical current limit curves and converter shutdown points.

DESIGN CONSIDERATION

Input Source Impedance

The impedance of the input source connecting to the DC/DC power modules will interact with the modules and affect the stability. A low ac-impedance input source is recommended. If the source inductance is more than a few $\mu H,$ we advise adding a 10 to 100 μF electrolytic capacitor (ESR < 0.7 Ω at 100 kHz) mounted close to the input of the module to improve the stability.

Layout and EMC Considerations

Delta's DC/DC power modules are designed to operate in a wide variety of systems and applications. For design assistance with EMC compliance and related PWB layout issues, please contact Delta's technical support team. An external input filter module is available for easier EMC compliance design. Application notes to assist designers in addressing these issues are pending release.

Safety Considerations

The power module must be installed in compliance with the spacing and separation requirements of the enduser's safety agency standard if the system in which the power module is to be used must meet safety agency requirements.

When the input source is 60Vdc or below, the power module meets SELV (safety extra-low voltage) requirements. If the input source is a hazardous voltage which is greater than 60 Vdc and less than or equal to 75 Vdc, for the module's output to meet SELV requirements, all of the following must be met:

- The input source must be insulated from any hazardous voltages, including the ac mains, with reinforced insulation.
- One Vi pin and one Vo pin are grounded, or all the input and output pins are kept floating.
- The input terminals of the module are not operator accessible.
- A SELV reliability test is conducted on the system where the module is used to ensure that under a single fault, hazardous voltage does not appear at the module's output.

Do not ground one of the input pins without grounding one of the output pins. This connection may allow a non-SELV voltage to appear between the output pin and ground.

This power module is not internally fused. To achieve optimum safety and system protection, an input line fuse is highly recommended. The safety agencies require a normal-blow fuse with 3A maximum rating to be installed in the ungrounded lead. A lower rated fuse can be used based on the maximum inrush transient energy and maximum input current.

Soldering and Cleaning Considerations

Post solder cleaning is usually the final board assembly process before the board or system undergoes electrical testing. Inadequate cleaning and/or drying may lower the reliability of a power module and severely affect the finished circuit board assembly test. Adequate cleaning and/or drying is especially important for un-encapsulated and/or open frame type power modules. For assistance on appropriate soldering and cleaning procedures, please contact Delta's technical support team.

FEATURES DESCRIPTIONS

Over-Current Protection

The modules include an internal output over-current protection circuit, which will endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the modules will automatically shut down (hiccup mode).

The modules will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

Over-Voltage Protection

The modules include an internal output over-voltage protection circuit, which monitors the voltage on the output terminals. If this voltage exceeds the over-voltage set point, the module will shut down (Hiccup mode). The modules will try to restart after shutdown. If the fault condition still exists, the module will shut down again. This restart trial will continue until the fault condition is corrected.

Over-Temperature Protection

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the over-temperature threshold the module will shut down.

The module will try to restart after shutdown. If the overtemperature condition still exists during restart, the module will shut down again. This restart trial will continue until the temperature is within specification.

Remote On/Off

The remote on/off feature on the module can be either negative or positive logic. Negative logic turns the module on during a logic low and off during a logic high. Positive logic turns the modules on during a logic high and off during a logic low.

Remote on/off can be controlled by an external switch between the on/off terminal and the Vi(-) terminal. The switch can be an open collector or open drain.

For negative logic if the remote on/off feature is not used, please short the on/off pin to Vi(-). For positive logic if the remote on/off feature is not used, please leave the on/off pin to floating.

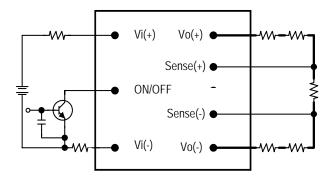


Figure 13: Remote on/off implementation

Remote Sense (Optional)

Remote sense compensates for voltage drops on the output by sensing the actual output voltage at the point of load. The voltage between the remote sense pins and the output terminals must not exceed the output voltage sense range given here:

$$[Vo(+) - Vo(-)] - [SENSE(+) - SENSE(-)] \le 10\% \times Vout$$

This limit includes any increase in voltage due to remote sense compensation and output voltage set point adjustment (trim).

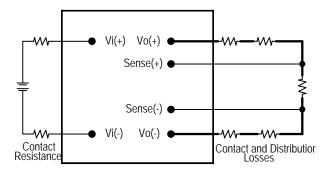


Figure 14: Effective circuit configuration for remote sense operation

If the remote sense feature is not used to regulate the output at the point of load, please connect SENSE(+) to Vo(+) and SENSE(-) to Vo(-) at the module.

The output voltage can be increased by both the remote sense and the trim; however, the maximum increase is the larger of either the remote sense or the trim, not the sum of both.

When using remote sense and trim, the output voltage of the module is usually increased, which increases the power output of the module with the same output current.

Care should be taken to ensure that the maximum output power does not exceed the maximum rated power.

Output Voltage Adjustment (TRIM)



To increase or decrease the output voltage set point, the modules may be connected with an external resistor between the TRIM pin and either the Vo+ or Vo -. The TRIM pin should be left open if this feature is not used.

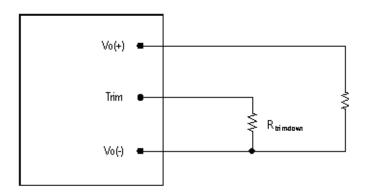


Figure 15: Circuit configuration for trim-down (decrease output voltage)

If the external resistor is connected between the TRIM and Vo- pins, the output voltage set point decreases. The external resistor value required to obtain a percentage of output voltage change \triangle Vo% is defined as:

$$Rtrim - down = \frac{1690}{\Delta Vo\%} - 73.1 [K\Omega]$$

Ex. When trim-down -10% (5V X 0.9 = 4.5V)

$$Rtrim - down = \frac{1690}{10} - 73.1 = 95.9 [K\Omega]$$

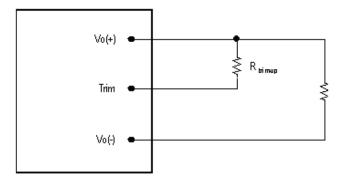


Figure 16: Circuit configuration for trim-up (increase output voltage)

If the external resistor is connected between the TRIM and Vo+the output voltage set point increases. The external resistor value required to obtain a percentage output voltage change △Vo% is defined as:

$$Rtrim - up = \frac{69(100 + \Delta Vo\%) - 1690}{\Delta Vo\%} - 73.1 [\text{K}\Omega]$$

Ex. When trim-up +10% (5V X 1.1 = 5.5V)

$$Rtrim - up = \frac{69(100 + 10) - 1690}{10} - 73.1 = 516.9 [K\Omega]$$

Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

THERMAL CONSIDERATIONS

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

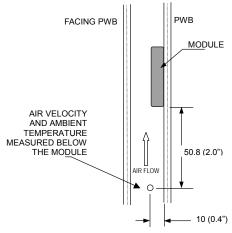
Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module or a heat sink is 6.35mm (0.25").

Thermal Derating

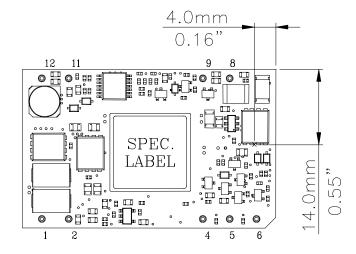
Heat can be removed by increasing airflow over the module. The module's maximum hot spot temperature is 115° C. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 17: Wind Tunnel Test Setup

THERMAL CURVES



Fiture 18: Hot spot temperature measured point \star The allowed maximum hot spot temperature is defined at 115 $^{\circ}$ C

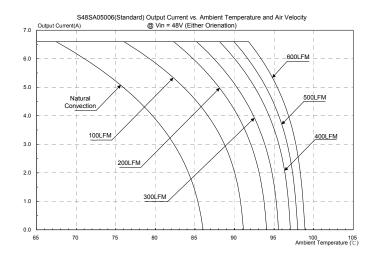


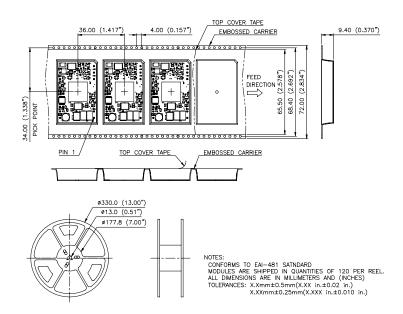
Figure 19: Output current vs. ambient temperature and air velocity @V_{in}=48V

PICK AND PLACE LOCATION

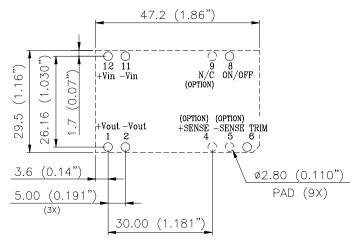
47.2 (1.86") 23.40 (0.921") (1.91) (2.91) (2.91) (2.91) (2.91) (2.91) (2.91) (3.91) (3.91) (47.2 (1.86") (47.2 (1.

NOTES:
ALL DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

SURFACE-MOUNT TAPE & REEL



RECOMMENDED PAD LAYOUT (SMD)

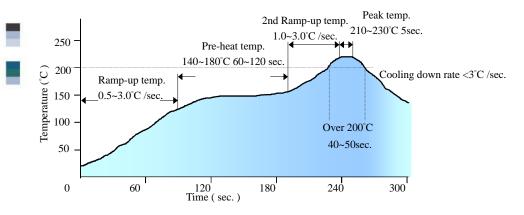


RECOMENDED P.W.B PAD LAYOUT

NOTES:

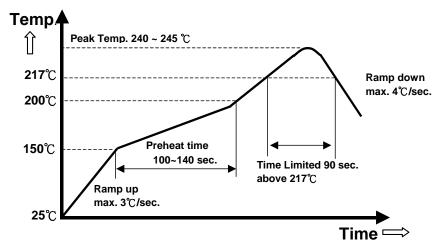
DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

LEADED (Sn/Pb) PROCESS RECOMMEND TEMP. PROFILE



Note: The temperature refers to the pin of S48SA, measured on the pin 1 (+Vout) joint.

LEAD FREE (SAC) PROCESS RECOMMEND TEMP. PROFILE

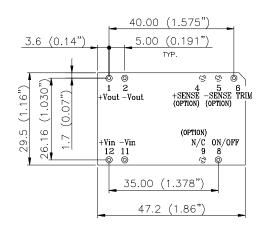


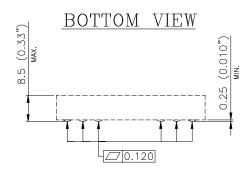
Note: The temperature refers to the pin of S48SA, measured on the pin 1 (+Vout) joint.

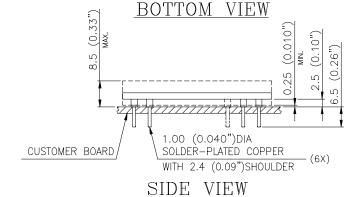
MECHANICAL DRAWING

SMD

Through-Hole







SIDE VIEW

NOTES:

DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

Pin No.	Name	Function
1	+Vout	Positive output voltage
2	-Vout	Negative output voltage
6	Trim	Output voltage trim
8	ON/OFF	ON/OFF logic
11	-Vin	Negative input voltage
12	+Vin	Positive input voltage
Optional Pin	Name	Function
4	+Sense (Option)	Positive sense pin
5	-Sense (Option)	Negative sense pin
9	NC	No connection

PART NUMBERING SYSTEM

S	48	S	Α	050	06	N	R	F	A *
Form	Input	Number of	Product	Output	Output	ON/OFF	Pin Type		Option
Facto	Voltage	Outputs	Series	Voltage	Current	Logic			Code
S- Sma Powe	_	S- Single				N- Negative P- Positive	R- SMD T- Through hole		A- 9 pin, no sense B- 6 pin, no sense C- 9 pins with sense (12V has option B only)

^{*} Option code A includes 9 pins. Pins 4, 5, and 9 have no connection.

Option code B excludes pin 4, 5, and 9 (total 6 pins).

Option code C features 9 pins with sense function.

MODEL LIST

MODEL NAME	INPUT		DEL NAME INPUT OUTPUT		PUT	EFF @ 100% LOAD
S48SA1R212NRFA	36V~75V	0.85A	1.2V	12A	84.0%	
S48SA1R512NRFA	36V~75V	0.85A	1.5V	12A	88.0%	
S48SA1R812NRFA	36V~75V	0.85A	1.8V	12A	88.0%	
S48SA2R510NRFA	36V~75V	1.3A	2.5V	10A	88.5%	
S48SA3R310NRFA	36V~75V	1.3A	3.3V	10A	90.5%	
S48SA05006NRFA	36V~75V	1.3A	5.0V	6.6A	90.5%	
S48SA12003NRFB	36V~75V	1.3A	12V	3.0A	90.0%	

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